



Spec No.: DS-55-94-0001Effective Date: 04/07/2000

Revision: A

LITE-ON DCC

RELEASE

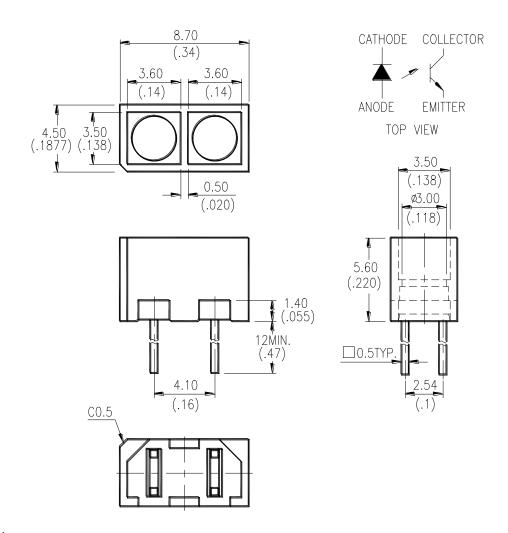
BNS-OD-FC001/A4

Property of LITE-ON Only

FEATURES

- * NON-CONTACT SWITCHING.
- * FOR DIRECT PC BOARD OR DUAL-IN-LINE SOCKET MOUNTING.
- * FAST SWITCHING SPEED.

PACKAGE DIMENSIONS



NOTES:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ± 0.25 mm(.010") unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.

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Property of Lite-On Only

ABSOLUTE MAXIMUM RATINGS AT TA=25℃

PARAMETER	SYMBOL	MAXIMUM RATING	UNIT						
INPUT DIODE									
Power Dissipation	P _D	90	mW						
Peak Forward Current (300 pps , 10 μ S pulse)	I_{CP}	1	A						
Continuous Forward Current	I_{F}	60	mA						
Reverse Voltage	V_R	5	V						
OUTPUT PHOTOTRANSISTOR									
Power Dissipation	$P_{\rm C}$	100	mW						
Collector-Emitter Voltage	V_{CEO}	30	V						
Emitter-Collector Voltage	V _{ECO}	5	V						
Collector Current	$I_{\rm C}$	20	mA						
Operating Temperature Range	$T_{ m opr}$	-25°C to + 85°C							
Storage Temperature Range	T_{stg}	-40°C to + 100°C							
Lead Soldering Temperature [1.6mm (.063") Form Case]	T_{S}	260°C for 5 Seconds							

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Property of Lite-On Only

ELECTRICAL OPTICAL CHARACTERISTICS AT TA=25°C

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	Bin No.			
INPUT DIODE										
Forward Voltage	V_{F}		1.2	1.6	V	$I_F = 20 \text{mA}$				
Reverse Current	I_R			100	μ A	V _R =5V				
OUTPUT PHOTOTRANSISTOR										
Collector-Emitter Dark Current	I_{CEO}			100	nA	V _{CE} =10V				
COUPLER										
Collector-Emitter Saturation Voltage	V _{CE(SAT)}			0.4	V	I_{C} =0.2mA I_{F} =20mA				
On State Collector Current	I _{C(ON)}	200		400	uA	VCE=5V	BIN A			
		300		600		IF=20mA	BIN B			
		500		1000		d=3.5mm (90% Reflective	BIN C			
		800		1600		White Paper)	BIN D			

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Property of Lite-On Only

TYPICAL ELECTRICAL / OPTICAL CHARACTERISTICS CURVES

(25°C Ambient Temperature Unless Otherwise Noted)

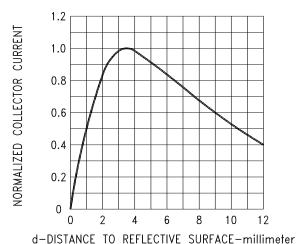


Fig.1 NORMALIZED COLLECTOR CURRENT VS. OBJECT DISTANCE

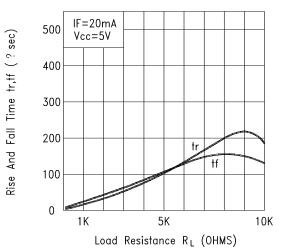


Fig.3 RISE AND FALL TIME VS. LOAD RESISTANCE

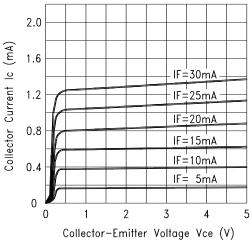


Fig.2 COLLECTOR CURRENT VS. COLLECTOR VOLTAGE

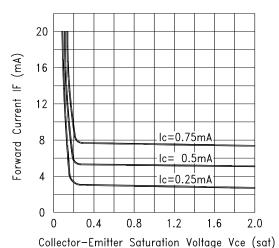


Fig.4 FORWARD CURRENT VS.

Collector-Emitter Saturation Voltage

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