

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

General purpose & High capacitance

Class 2, Y5V

6.3 V TO 50 V 10 nF to 47 μF

RoHS compliant & Halogen Free



YAGEO Phícomp



SCOPE

This specification describes Y5V series chip capacitors with leadfree terminations.

<u>APPLICATIONS</u>

- Consumer electronics, for example:
 - Tuners
 - Television receivers
 - Video recorders
 - All types of cameras
 - Mobile telephones

FEATURES

- · Supplied in tape on reel
- Nickel-barrier end termination
- RoHS compliant
- Halogen Free compliant

ORDERING INFORMATION-GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

XXXX X X Y5V X BB XXX (1) (2) (3)

(I) SIZE – INCH BASED (METRIC)

0201 (0603)

0402 (1005)

0603 (1608)

0805 (2012)

1206 (3216)

1210 (3225)

(2) TOLERANCE

 $M = \pm 20\%$

Z = -20% to +80%

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

K = Blister taping reel; Reel 7 inch

P = Paper/PE taping reel; Reel 13 inch

F = Blister taping reel; Reel 13 inch

C = Bulk case

(4) RATED VOLTAGE

5 = 6.3 V

6 = 10 V

7 = 16 V

8 = 25 V

9 = 50 V

(5) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $103 = 10 \times 10^3 = 10,000 \text{ pF} = 10 \text{ nF}$

3 14

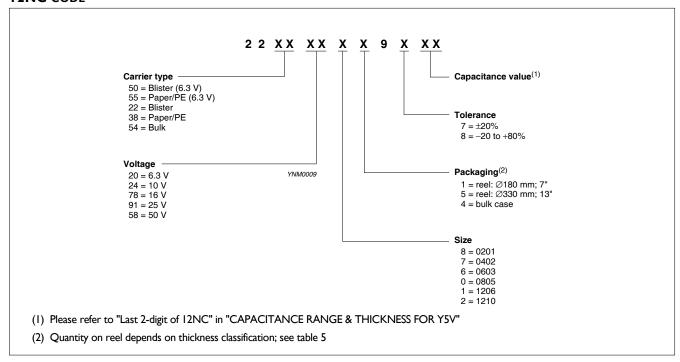
PHYCOMP BRAND ordering codes

GLOBAL PART NUMBER (preferred), PHYCOMP CTC (for North America) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

GLOBAL PART NUMBER (PREFERRED)

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

12NC CODE



PHYCOMP CTC code (for north america)

● Example: 12062F105M8BB0D

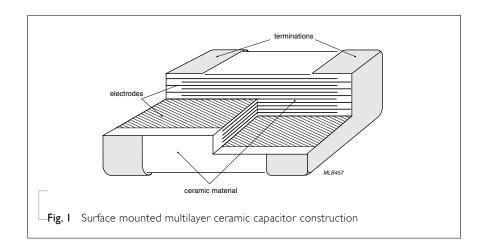
1206	2F	105	М	8	В	В	0	D
Size code	Temp. Char.	Capacitance in pF	Tolerance	Voltage	Termination	Packing	Marking	Range identifier
0201 0402 0603 0805 1206 1210	2F = Y5V	the third digit signifies the multiplying factor: $0 = \times 1$ $1 = \times 10$ $2 = \times 100$ $3 = \times 1,000$ $4 = \times 10,000$ $5 = \times 100,000$ $6 = \times 1,000,000$	$M = \pm 20\%$ $Z = -20\%$ to $+80\%$	5 = 6.3 V 6 = 10 V 7 = 16 V 8 = 25 V 9 = 50 V		2 = 180 mm 7" Paper/PE 3 = 330 mm 13" Paper/PE B = 180 mm 7" Blister F = 330 mm 13" Blister P = Bulk case	0 = no marking	D = Class 2 MLCC



CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

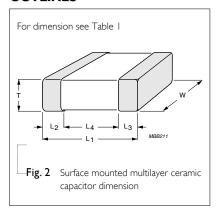


DIMENSION

Table I For outlines see fig. 2

TVDE	TYPE L _I (mm) W (mm)		T (MM)	L ₂ / L ₃	(mm)	L ₄ (mm)
IIFE			1 (11111)	min.	max.	min.
0201	0.6 ±0.03	0.3 ±0.03	_	0.10	0.20	0.20
0402	1.0 ±0.05	0.5 ±0.05	_	0.20	0.30	0.40
0603	1.6 ±0.10	0.8 ±0.10		0.20	0.60	0.40
0805	2.0 ±0.10 ⁽¹⁾	1.25 ±0.10 ⁽¹⁾		0.25	0.75	0.55
	2.0 ±0.20 ⁽²⁾	1.25 ±0.20 ⁽²⁾	- 5 .	0.23	0.75	0.55
1206	3.2 ±0.15 ⁽¹⁾	1.6 ±0.15 ⁽¹⁾	Refer to table 2 to 4	0.25	0.75	1.40
	3.2 ±0.30 ⁽²⁾	1.6 ±0.20 ⁽²⁾	-	0.23	0.75	1,10
1210	3.2 ±0.20 ⁽¹⁾	2.5 ±0.20 ⁽¹⁾		0.25	0.75	1.40
1210	3.2 ±0.40 ⁽²⁾	2.5 ±0.30 ⁽²⁾	_	0.23	0.73	1.40
1812	4.5 ±0.20 ⁽¹⁾	3.2 ±0.20 ⁽¹⁾		0.25	0.75	2.20
1012	4.5 ±0.40 ⁽²⁾	3.2 ±0.40 ⁽²⁾		0.23	0.73	2,20

OUTLINES



NOTE

- 1. Dimension for size 0805 to 1812, $C \le 100 \text{ nF}$
- 2. Dimension for size 0805 to 1812, C > 100 nF

CAPACITANCE RANGE & THICKNESS FOR Y5V

Table 2 Sizes from 0201 to	0402
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CAP.	Last 2-digit of I2NC		0201		0402				
	≤ 25 V	50 V	6.3 V	25 V	6.3 V	10 V	16 V	25 V	50 V
10 nF	36	05		0.3±0.03					0.5±0.05
22 nF	41	07						0.5.10.05	
47 nF	45	09					0.5.10.05	0.5±0.05	
100 nF	49	12	0.3±0.03			0.5±0.05	0.5±0.05		
220 nF	52	14			05.005				
470 nF	58	16			0.5±0.05				
1.0 μF	63	18							
2.2 µF		67							
4.7 µF		72							
ΙΟ μΕ		76							
22 µF		81							
47 µF		85							

Table 3 Sizes from 0603 to 0805

CAP.	-	git of I2NC	0603					0805				
	≤ 25 V	50 V	6.3 V	10 V	16 V	25 V	50 V	6.3 V	10 V	16 V	25 V	50 V
10 nF	36	05										
22 nF	41	07					0.8±0.1				0.6±0.1	0.6±0.1
47 nF	45	09				0.8±0.1	U.0±U.1				0.6±0.1	0.0±0.1
100 nF	49	12			0,6±0.1							
220 nF	52	14								0.6±0.1		0.05.10.1
470 nF	58	16			0.8±0.1						0.85±0.1	0.85±0.1
Ι.0 μF	63	18		00101						0.85±0.1		1.25±0.2
2.2 µF		67	0.8±0.1	0.8±0.1					0.85±0.1		1.25±0.2	
4.7 µF		72						0.85±0.1	0.85±0.1 1.25±0.2	1.25±0.2		
ΙΟ μF		76						125102	125102			
22 µF		81						1.25±0.2	1.25±0.2			
47 µF		85										

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-3 series is on request



CAPACITANCE RANGE & THICKNESS FOR Y5V

Table 4	Sizes from	1206 to	1210	
CAP.	Last 2-digit of	of I2NC		Ľ

CAP.	Last 2-di	git of I2NC	1206					1210			
	≤ 25 V	50 V	6.3 V	10 V	16 V	25 V	50 V	6.3 V	10 V	16 V	25 V
10 nF	36	05									
22 nF	41	07									
47 nF	45	09				0.6±0.1	0.6±0.1				
100 nF	49	12									
220 nF	52	14									
470 nF	58	16					0.85±0.1				
1.0 μF	63	18				0.85±0.1	U.03±U.1				
2.2 µF		67			0.85±0.1						
4.7 µF		72		0.85±0.1	0.65±0.1						
10 μF	:	76	0.85±0.1		1.15±0.1	1.6±0.2			1.5±0.1	1.5±0.1	1.5±0.1
22 µF		81	1.6±0.2	1.6±0.2	1.6±0.2	1.6±0.2			1.6±0.2	1.6±0.2	
47 µF		85						2.0±0.2			

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-3 series is on request

THICKNESS CLASSES AND PACKING QUANTITY

-	_			_
	la	b	le	5

CITE	THICKNIECC	TAREVAURTU	Ø180 MM	/ 7 INCH	Ø330 MM	/ 13 INCH	OLIANITITY
SIZE CODE	THICKNESS CLASSIFICATION	TAPE WIDTH – QUANTITY PER REEL	Paper	Blister	Paper	Blister	QUANTITY PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
_	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		10,000	
_	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
_	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
_	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			
_	1.15 ±0.15 mm	I2 mm		3,000			
_	1.25 ±0.2 mm	I2 mm		3,000			
1808	1.35 ±0.15 mm	I2 mm		2,000			
1000	1.5 ±0.1 mm	I2 mm		2,000			
_	1.6 ±0.2 mm	I2 mm		2,000			
	2.0 ±0.2 mm	I2 mm		2,000			
_	0.6 / 0.85 ±0.1 mm	I2 mm		2,000			
_	1.15 ±0.1 mm	I2 mm		1,000			
	1.25 ±0.2 mm	I2 mm		1,000			
1812	1.5 ±0.1 mm	I2 mm		1,000			
	1.6 ±0.2 mm	I2 mm		1,000			
	2.0 ±0.2 mm	I2 mm		1,000			
	2.5 ±0.2 mm	I2 mm		500	50,000		

ELECTRICAL CHARACTERISTICS

Y5V DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all test and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

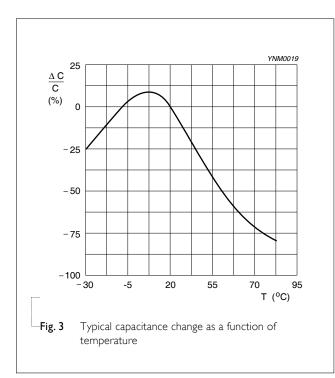
- Temperature: 15 °C to 35 °C - Relative humidity: 25% to 75% - Air pressure: 86 kPa to 106 kPa

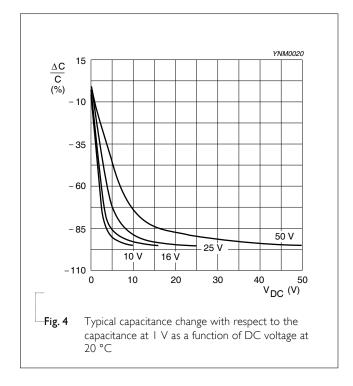
Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

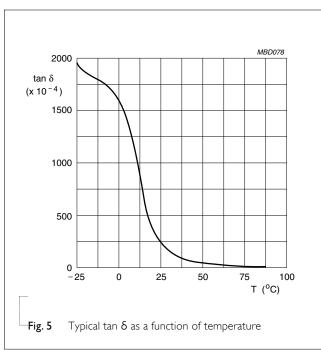
The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

Table 6				
DESCRIPTION				VALUE
Capacitance range				IO nF to 47 μF
Capacitance tolerance				±20% -20% to +80%
Dissipation factor (D.F.)				
	≤ 6.3 V			≤ 15%
		Exception: 0805 ≥ 22 µF		≤ 20%
	10 V			≤ 12.5%
		Exception: 0402 ≥ 680 n	F ; 0603 ≥ 2.2 μF;	≤ 15%
		0805 ≥ 10 μF	1206 ≥ 10 μF	≤ 20%
	16 V			≤ 12.5%
		Exception: 0603 ≥ 4.7 µF		≤ 15%
	≥ 25 V			≤ 9%
		Exception: 0201 ≥ 10 nF		≤ 12.5%
Insulation resistance after	r I minute at	U _r (DC)	$R_{\rm ins} \ge 10~{\rm G}\Omega$ or $R_{\rm ins} \times C_{\rm r} \ge 500$ seconds w	hichever is less
Maximum capacitance ch	ange as a fun	ction of temperature		
(temperature characteris	tic/coefficien	t):		+22% to -82%
Operating temperature r	ange:		_30	°C to +85 °C









SOLDERING RECOMMENDATION

Table 7

SOLDERING	SIZE				
METHOD	0402	0603	0805	1206	≥ 1210
Reflow	≥ 0.1 µF	≥ 1.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave	< 0.1 µF	< 1.0 µF	< 2.2 µF	< 4.7 µF	



Surface-Mount Ceramic Multilayer Capacitors | General Purpose & High Cap. | Y5V | 6.3 V to 50 V

TESTS AND REQUIREMENTS

Table 0 Test Di Ocedules and Teduli en lents	Table 8	Test procedures	and requirements
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TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual inspection and dimension check	4.4 Any applicable meth		Any applicable method using × 10 magnification	In accordance with specification
Capacitance (1)	At 20 °C, 24 hrs after annealing $f = 1$ KHz for $C \le 10$ μ F, rated voltage > 6.3 V, meas voltage 1 V _{rms} at 20 °C $f = 1$ KHz, for $C \le 10$ μ F, rated voltage ≤ 6.3 V, meas voltage 0.5 V _{rms} at 20 °C		At 20 °C, 24 hrs after annealing f = 1 KHz for C \leq 10 μ F, rated voltage $>$ 6.3 V, measuring at voltage 1 V _{rms} at 20 °C f = 1 KHz, for C \leq 10 μ F, rated voltage \leq 6.3 V, measuring at	Within specified tolerance
Dissipation 4.5.2 factor (D.F.) (I)		4.5.2	Class 2: At 20 °C, 24 hrs after annealing $f = 1 \text{ KHz for C} \leq 10 \mu\text{F, rated voltage} > 6.3 \text{ V, measuring at voltage } 1 \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = 1 \text{ KHz, for C} \leq 10 \mu\text{F, rated voltage} \leq 6.3 \text{ V, measuring at voltage } 0.5 \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = 120 \text{ Hz for C} > 10 \mu\text{F, measuring at voltage } 0.5 \text{ V}_{rms} \text{ at } 20 \text{ °C}$	In accordance with specification
Insulation 4.5.3 resistance		4.5.3	At U_r (DC) for I minute	In accordance with specification
Y5V: -30 °C to		4.6	Class 2: Between minimum and maximum temperature Y5V: -30 °C to +85 °C Normal Temperature: 20 °C	<pre><general purpose="" series=""> ΔC/C Class 2: Y5V: 22% to -82% </general></pre> <pre><high capacitance="" series=""> ΔC/C Class 2: Y5V: 22% to -82%</high></pre>
Adhesion 4.7		4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N size = 0402: 2.5N size = 0201: 1N

NOTE:

1. For individual product specification, please contact local sales.

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS	
Bond strength of plating on end face	IEC 60384- 21/22	4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage	
			Conditions: bending I mm at a rate of I mm/s, radius jig 340 mm	<pre><general purpose="" series=""> $\Delta C/C$ Class2: Y5V: $\pm 10\%$ <high capacitance="" series=""> $\Delta C/C$ Class2: Y5V: $\pm 10\%$</high></general></pre>	
Resistance to soldering heat		4.9	Precondition: $150 \pm 0/-10$ °C for I hour, then keep for 24 ± 1 hours at room temperature Preheating: for size ≤ 1206 : 120 °C to 150 °C for I minute Preheating: for size > 1206 : 100 °C to 120 °C for I minute and 170 °C to 200 °C for I minute Solder bath temperature: 260 ± 5 °C Dipping time: 10 ± 0.5 seconds Recovery time: 24 ± 2 hours	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned	
		410			
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds. Test conditions for lead containing solder alloy Temperature: 235 ±5 °C Dipping time: 2 ±0.2 seconds Depth of immersion: 10 mm Alloy Composition: 60/40 Sn/Pb Number of immersions: 1 Test conditions for leadfree containing solder alloy Temperature: 245 ±5 °C Dipping time: 3 ±0.3 seconds Depth of immersion: 10 mm Alloy Composition: SAC305 Number of immersions: 1	rne solder should cover over 95% of the critical area of each termination	

TEST METHOD		PROCEDURE	REQUIREMENTS	
IEC 60384- 21/22	4.11	Preconditioning; 150 +0/–10 °C for 1 hour, then keep for 24 ±1 hours at room temperature	No visual damage	
			<general purpose="" series=""></general>	
			ΔC/C	
		·	Class2:	
		30 minutes at lower category temperature 30 minutes at upper category temperature	Y5V: ±20%	
			<high capacitance="" series=""></high>	
		Recovery time 24 ±2 hours	ΔC/C	
			Class2:	
			Y5V: ±20%	
		-	D.F. meet initial specified value	
			R _{ins} meet initial specified value	
	4.13	1. Preconditioning, class 2 only:	No visual damage after recovery	
		·	<general purpose="" series=""></general>	
		· ·	ΔC/C	
			Class2:	
			Y5V: ±30%	
			D.F.	
		90 to 95% R.H. 1.0 $U_{\rm r}$ applied	Class2:	
		4. Recovery:	Y5V: ≤ 15%	
		Class 2: 24 ±2 hours	R _{ins}	
		5. Final measure: C, D, IR	Class2:	
			Y5V: \geq 500 M Ω or R _{ins} × C _r \geq 25s	
		P.S. If the capacitance value is less than the minimum value permitted, then after the other	whichever is less	
		measurements have been made the capacitor shall	<high capacitance="" series=""></high>	
			ΔC/C	
		then the requirement shall be met.	Class2:	
			Y5V: ±30%	
			D.F.	
			Class2:	
			Y5V: 2 x initial value max	
			R _{ins}	
			Class2:	
			Y5V: 500 M Ω or $R_{ins} \times C_r \ge 25s$	
			whichever is less	
		IEC 60384- 4.11 21/22	IEC 60384- 21/22 Preconditioning; 150 +0/-10 °C for I hour, then keep for 24 ± I hours at room temperature 5 cycles with following detail: 30 minutes at lower category temperature 30 minutes at upper category temperature Recovery time 24 ± 2 hours 4.13 I. Preconditioning, class 2 only: 150 +0/-10 °C /I hour, then keep for 24 ± I hour at room temp 2. Initial measure: Spec: refer initial spec C, D, IR 3. Damp heat test: 500 ± 12 hours at 40 ± 2 °C; 90 to 95% R.H. I.O U _r applied 4. Recovery: Class 2: 24 ± 2 hours 5. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other	

Phicomp Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS	
Endurance	IEC 60384- 4. 21/22	1.14	 Preconditioning, class 2 only: 150 +0/-10 °C /I hour, then keep for 24 ± I hour at room temp Initial measure: Spec: refer initial spec C, D, IR Endurance test: Temperature: Y5V: 85 °C Specified stress voltage applied for I,000 hours:	No visual damage	
Voltage proof	IEC 60384-1 4	1.6	Specified stress voltage applied for I minute $U_r \le 100 \text{ V}$: series applied 2.5 U_r $100 \text{ V} < U_r \le 200 \text{ V}$ series applied (1.5 $U_r + 100$) $200 \text{ V} < U_r \le 500 \text{ V}$ series applied (1.3 $U_r + 100$) $U_r > 500 \text{ V}$: 1.3 U_r I: 7.5 mA	No breakdown or flashover	

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 5	Jul 29, 2010	-	- Modify the last 2-digit of I2NC
Version 4	Jun 24, 2010	-	- Dimension on 1206 case size updated
Version 3	Apr 22, 2010	-	- Dimension updated
Version 2	Feb 04, 2010	-	- The statement of "Halogen Free" on the cover added
Version I	Nov 04, 2009	-	- Ordering code updated
			- Dimension updated
Version 0	Apr 15, 2009	-	- New datasheet for general purpose and high capacitance Y5V series with RoHS compliant
			- Replace the "6.3V to 50V" part of pdf files: Y5V_6.3V_10V_9_Preliminary, Y5V_10V-to-50V_10_Preliminary, Y5V_16V_25V_50V_11
			- Combine 0201 from pdf files: UP-NP0X5RX7RY5V_0201_6.3-to-50V_2 and UY-NP0X5RX7RY5V_0201_6.3-to-50V_2
			- Define global part number
			- Description of "Halogen Free compliant" added
			- Test method and procedure updated