

JIANGSU CHANGJIANG ELECTRONICS TECHNOLOGY CO., LTD

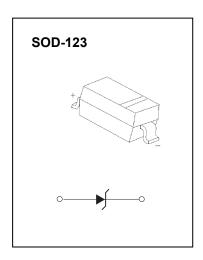
SOD-123 Plastic-Encapsulate Diodes

CESD5V0D1

ESD Protection Diode

DESCRIPTION

The CESD5V0D1 is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.



FEATURES

- Stand-off Voltage:5V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 KV) Per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb-Free Devices

Maximum Ratings @Ta=25℃

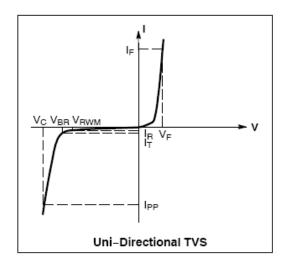
Parameter	Symbol	Limit	Unit	
IEC61000-4-2(ESD)	Air		±30	KV
	Contact		±30	ICV
ESD Voltage	per human body model		30	KV
Total Power Dissipation on FR-5 Board (Note 1)		P _D	250	mW
Thermal Resistance Junction-to-Am	$R_{\Theta JA}$	500	°C/W	
Lead Solder Temperature - Maximun	T∟	260	${\mathbb C}$	
Junction and Storage Temperature R	$T_{j,} T_{stg}$	-55 ~ +150	°C	

Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only. Functional operation above the recommended. Operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

1. $FR-5 = 1.0 \times 0.75 \times 0.62$ in.

ELECTRICAL CHARACTERISTICS (Ta= 25°C unless otherwise noted)

Symbol	Parameter				
I _{PP}	Maximum Reverse Peak Pulse Current				
Vc	Clamping Voltage @ IPP				
V _{RWM}	Working Peak Reverse Voltage				
I _R	Maximum Reverse Leakage Current @ V _{RWM}				
V _{BR}	Breakdown Voltage @ I _T				
I _T	Test Current				
I _F	Forward Current				
V _F	Forward Voltage @ I _F				
P _{pk}	Peak Power Dissipation				
С	Max. Capacitance @V _R =0 and f =1MHz				



ELECTRICAL CHARACTERISTICS (Ta = 25 $^{\circ}$ C unless otherwise noted, V_F = 0.9 V Max. @ I_F = 10mA)

Device*	Device Marking	V _{RWM} (V)	I _R (μΑ) @ V _{RWM}	V _{BR} (V) @ I _τ (Note 2)		Ι _τ	Vc(V) @Max IPP*	I _{PP} (A) ⁺
		Max	Max	Min	Max	mA	Max	Max
CESD5V0D1	KE	5	1	6.2	7.3	1.0	21.7	15

^{*}Other voltages available upon request.

^{2.} $\mbox{V}_{\mbox{\footnotesize BR}}$ is measured with a pulse test current $\mbox{I}_{\mbox{\footnotesize T}}$ at an ambient temperature of 25°C.