



ORDERING INSTRUCTIONS:

(ADD THESE COMMENTS WHEN UPLOADING GERBERS.)

JLCPCB JLC7628 4-Layer 0.8mm Impedance Control
4-Layer Board
0.8mm Thickness
1oz Copper (Outer layers)
ENIG Finish
Castellated Holes

When running the panelizer, add a vertical gap of 0.2"
to allow the castellated holes to be routed out

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DESIGNED BY Paul Clark

Sheet:			
File: kicadBoard.kicad_pcb			
Title:			
Size: A4	Date:		Rev:
KiCad E.D.A.	pcbnew (6.0.9)		Id: 1/1