

Diagram illustrating a control system structure. The system consists of a reference input REF^{**} entering a summing junction. The output of this junction passes through a block labeled $FIDISA$. The output of $FIDISA$ is then processed by a series of blocks: a gain of 20, a block labeled F , a block labeled 3, a block labeled 90, and a block labeled m . The output of the m block is fed back through a block labeled m to the initial summing junction. Additionally, there is a direct feedback path from the output of the final m block back to the initial summing junction.