



\*2.375V < VCC < 3.465V

\*The LSB bit of the 7 bit address is determined by the logic level on pin ADO.  
Default Address = 0x68 (pin ADO is logic low)  
Alternative Address = 0x69 (pin ADO is logic high)

\*\*Optional external reference clock input or synchornization digital input.  
Connected to GND by default. Cut trace for external control



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TITLE: \${PROJECTNAME}					
Design by:				REV:	
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