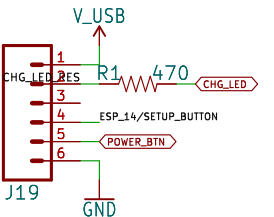
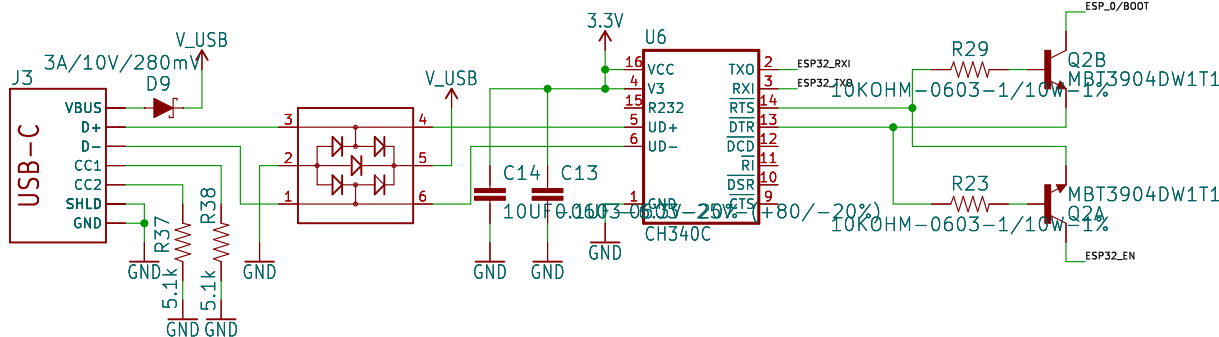


To Membrane Overlay

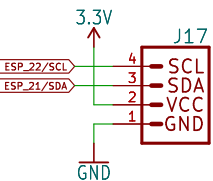
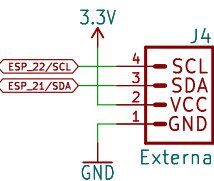
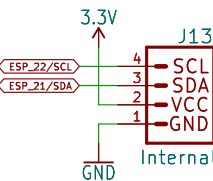
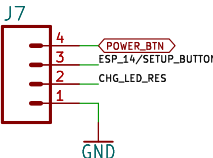


Overlay ribbon is 1mm trace width/spacing.
We use 12pin 0.5mm connector with doubled up pins.
The connector has contacts on both top and bottom.

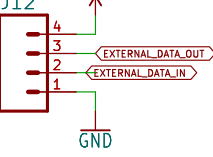
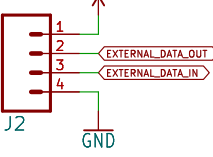
USB-to-Serial
For ESP32



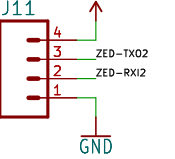
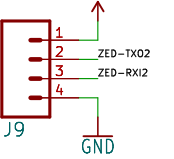
Connections



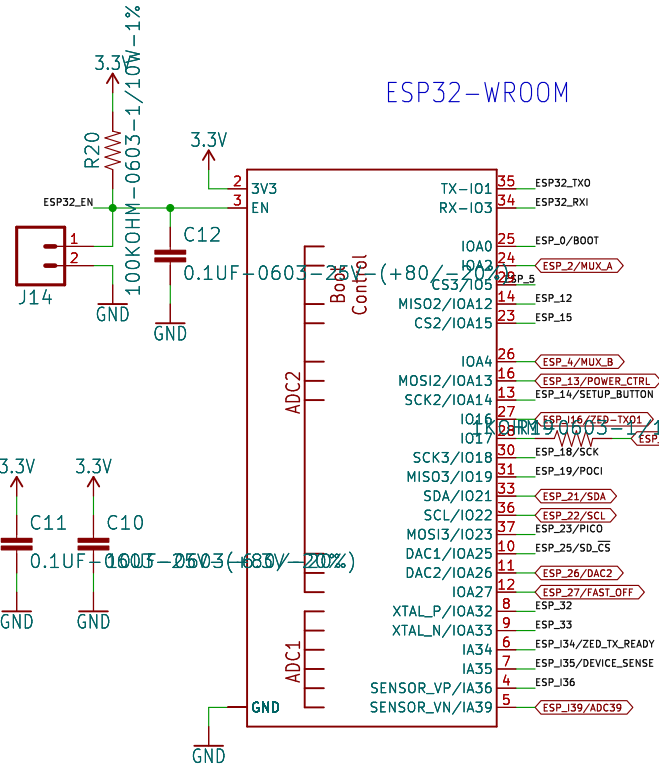
External Data Port
3.3V Signals Only



External Radio
RTCM In/Out
5V is not guaranteed
May be 3.5-5V



ESP32-WROOM

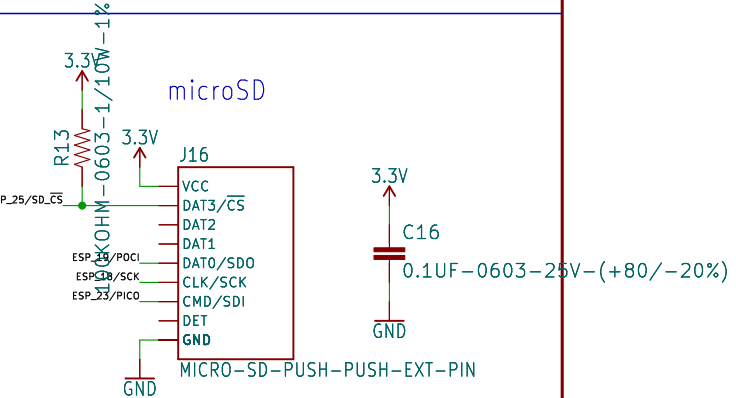


IDs:
Surveyor: N/A
Express: 10/3.3
Express+: 3.3/10
Facet: 10/10
Facet LBand: 10/20

Use boot control pins with caution: 0, 2, 5, 12, 15
I00: Avoid device connections. Can be used as a stat LED.
I02: Avoid external pullups - will cause bootloader fail.
I05: Has builtin pullup at POR.
I012: Avoid external pullups - will cause bootloader fail.
I015: Has builtin pullup at POR.

ADC2 has problems when WIFI is enabled.

R19 prevents potential serial contention with Data Port



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TITLE: \${PROJECTNAME}

Design by: A. DESIGNER

Date: \${CURRENT DATE}

Sheet: \${#}/\${##}

Size: User
KiCad E.D.A. eeschema 7.0.2

Rev: 1/1