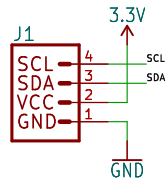


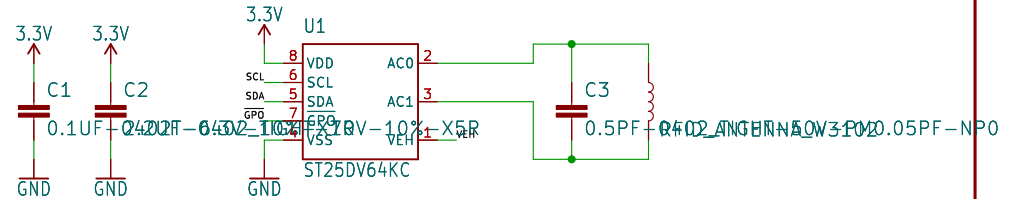
Qwiic Connector

QWIIIC_RIGHT_ANGLE



ST25DV64KC

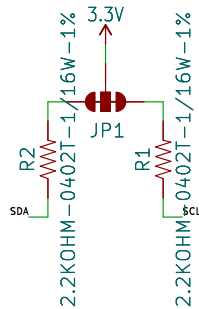
Notes on antenna tuning:
The ST25DV64KC has an internal tuning capacitance of 28.5pF
The W3102 antenna has an inductance of 4.7μH
To tune the antenna circuit to 13.56MHz, the total capacitance needs to be 29.1pF
C3 may need to be tuned for best performance



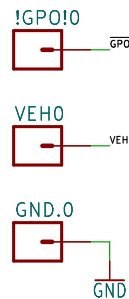
Default I2C Address (User Memory) (Unshifted): 0x53
Default I2C Address (System Memory) (Unshifted): 0x57

I2C Pull-ups

Open jumper to disable I2C pull-up resistors



Breakouts



Special Instructions	
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TITLE: \${PROJECTNAME}	
Design by: Paul Clark	REV: >REV
Date: \${CURRENT_DATE}	Sheet: \${#}/\${##}

Size: User	Date:	Rev:
KiCad E.D.A.	eeschema 7.0.2	Id: 1/1