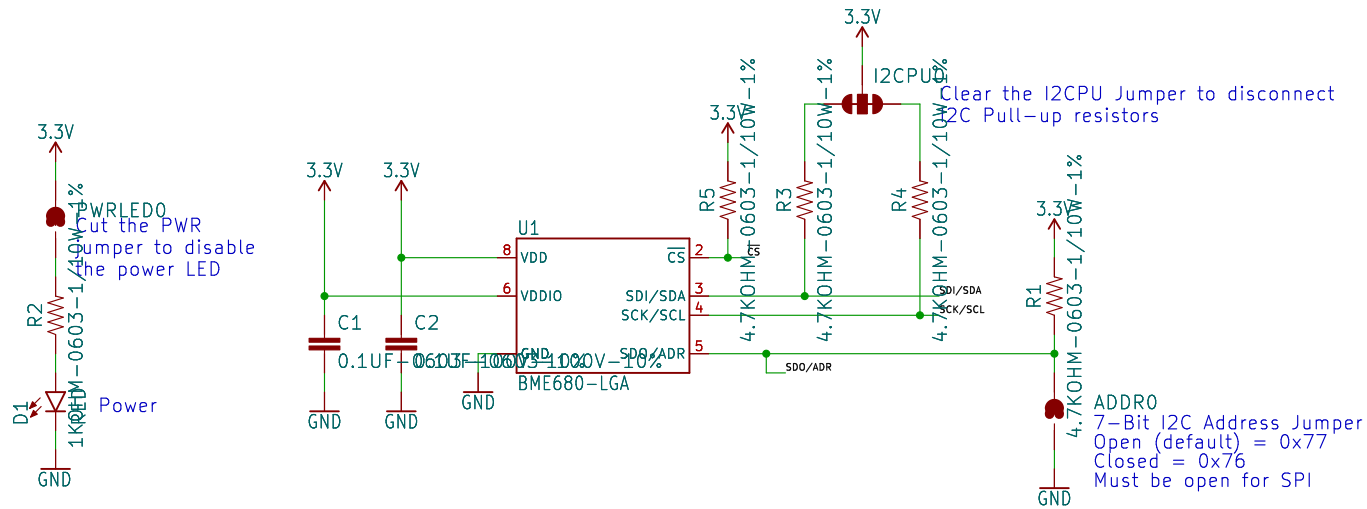
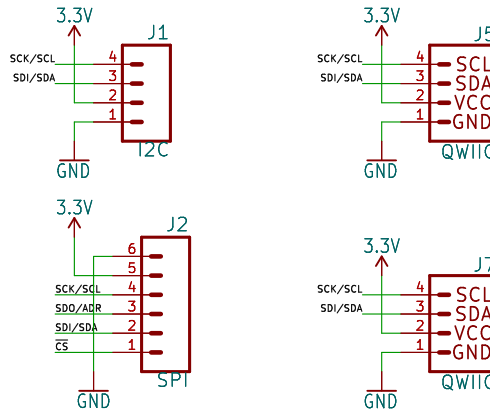


BME680
VDD: 1.71–3.6 V
VDDIO: 1.2–3.6 V



Connections



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TITLE: \${PROJECTNAME}

Design by: N. S. DESIGNER

Revision By: A. England

REV:

>10

Date: \${CURRENT_DATE}

Sheet: \${#}/\${##}

Size: User

Date:

Rev:

KiCad E.D.A. eeschema 7.0.2

Id: 1/1