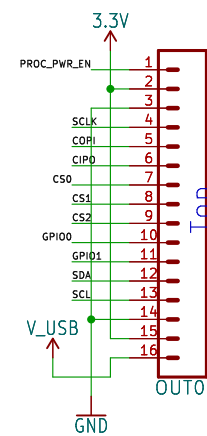
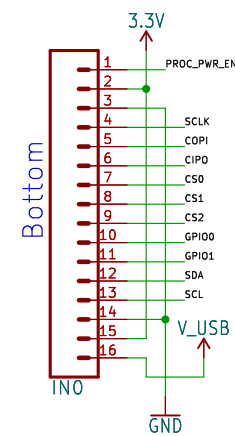
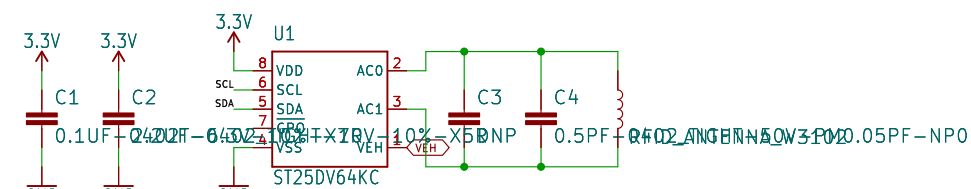


Bottom

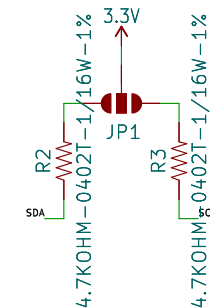


Notes on antenna tuning:
The ST25DV64KC has an internal tuning capacitance of 28.5pF
The W3102 antenna has an inductance of 4.7μH
To tune the antenna circuit to 13.56MHz, the total capacitance needs to be 29.1pF
C3 and C4 may need to be tuned for best performance



Default I2C Address (User Memory) (Unshifted): 0x53
Default I2C Address (System Memory) (Unshifted): 0x57

Close jumper to enable I2C pull-up resistors



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TITLE: \${PROJECTNAME}			
Design by: >DESIGNER Paul Clark		REV: >REV 1	
Date: \${CURRENT_DATE}		Sheet: \${#}/\${##}	
Size: User KiCad E.D.A. eeschema 7.0.2		Rev: 1/1	