linetime_mains MAINS_WAVE ________MAINS_WAVE MAINS_ZC_ ____JMAINS_ZC MAINS_5V 3v3 VDDe17 VDDe30 VDDe39 VDDe52 VDDe62 3v3 | C101 6 | VBAT | VDD | 39 | VDD | 52 | VDD | 62 | VDD | 72 | VDD | 84 | VDD | 84 | VDD | 100 | V 3, VDD=72 VDD=84 VDD=108 VDD=131 CND VDD=144 CND VDD=1 File: ../../../tmp/linetime/hardware/line linetime_battery PGOOD OPGOOD
CHG OCHG I_CHG_______I_CHG V_BATT_MON_____QV_BATT_MON File: ../../../tmp/linetime/ linetime_gps VDDA & VREF+ PC10
PC11
112
PC12
113
PC13
PC14
PC15

8
9

X GPS_RXD______GPS_RXD GPS_TXD GPS_TXD

GPS_RESET SGPS_RESET GPS_PPS___GGPS_PPS CS2100_CLK___CCS2100_CLK 71 VCAP1 CS2100_SDA OCS2100_SDA PD0 114 PD1 115 PD2 116 uSD_CMD PD3 117 PD4 118 GPS_RESET 106 VCAP2
106 VSS
38 VSS
61 VSS
94 VSS
120 VSS
130 VSS
31 VSS SD2100_SCL___GCS2100_SCL 106 VCAP2 File: ../../../tmp/linetime/hardware/linelinetime_ethernet RXD1____QRXD1 PD4 118 GPS_REST PD5 119 GPS_RXD PD6 122 GPS_TXD PD7 123 PD8 77 PD8 77 PD10 80 PD11 81 BACKLIGHT PD12 81 BACKLIGHT RXD0_____RXD0 CRS_DV____CRS_DV REF_CLK____GREF_CLK TXEN____TXEN TXD0____TXD0 TXD1_____TXD1 MDIO___OMDIO 31 VSSA MDC ____MDC GND 143 PDR_ON 138 BOOTO PHY_RST PHY_RST File: ../../../tmp/linetime/hardware/line PD12 PD13 PD14 PD15 82 85 86 PD15 nRST 25 NRST PEO 141
PEO 14 CS2100_CLK 23 PH0 PH1 REF_CLK 35 PA1
MDIO 36 PA2
B5 37
VSYNC 40
SOUNDER 41 SOUNDER 41 PA5 G2 42 PA6 CRS_DV 43 PA7 SD2100_SCL 100 PA8 BACKLIGHT BACKLIGHT TOUCH SD TOUCH SD TOUCH SD TOUCH SC TOUCH SD SOUNDER SOUNDE File: ../../../tmp/linetime/ R3 46 PB0 R6 47 PB1
SOUNDER-SD 48 PB2
GPS_PPS 133 PB3 LED1 R101 LED 1k D101 GND GPS_PS 1.33 PB3 | PB4 | 1.345 PB4 | 1.345 PB5 | 1.345 PB6 | 1.347 PB7 | 1.345 PB6 | 1.345 PB6 | 1.345 PB6 | 1.345 PB8 | 1.345 PB1 | 1.345 SHIELD 9 LED2 R102 LED GND GND CD_L 10 3v3 6 1 5 6 ND 11 6 ND LED3 R103 LED The D103 GND Indicator LEDs VCC 7 IC102 3x3

7 IN OUT 1 2 1 7 7 8 IN OUT 6 5D OUT 5 121 6 5D OUT 6 5D O 3_y3 PG00D 74 PB13
CE 75 PB14
PHY_RST 76 PB15 PG0 56 PG1 57 PG2 87 PG3 88 PG4 99 PG5 91 PG7 92 PG7 92 PG8 93 G7 3v3 P101

VTref SWDIO/TMS 2 SWDIO

GND SWDCLK/TCK 4 SWCLK

GND SWO/TDO 6 SWCLK

KEY TDI 6 SWD

GND SWD 0 RESET 10 RRST 3v3 LDO for digital supply ROM_CS 5 CS VDD 6 C CS VDD 80 SCK 1 SCK VSS ROM_SO 4 SO GND GND 254402F48 Debug/programming Interface ROM with MAC address STM32F7xxZxTx Drawn By: Adam Greig Sheet: / File: working.kicad_sch Title: Linetime Size: A3 Date: 2016-12-14 KiCad E.D.A. eeschema 7.0.6









