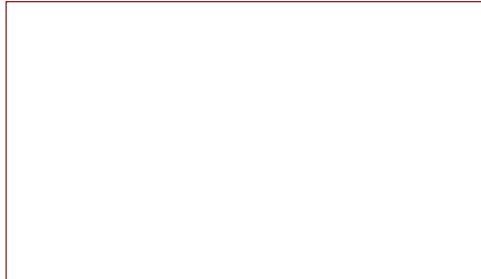
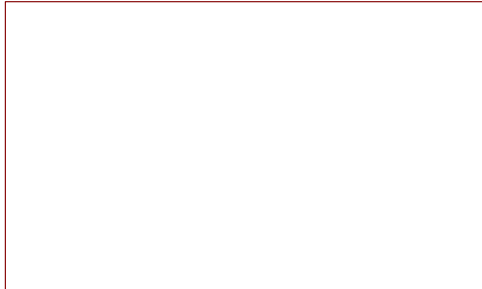
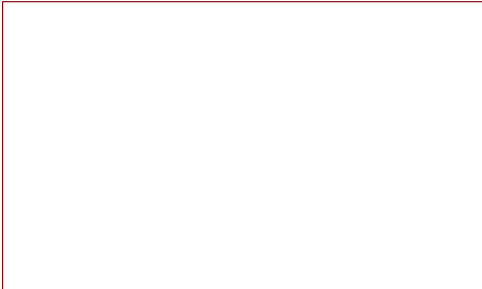





	1	2	3	4	5	6	
A	Linux SiP – OSD3358–512M–BSM		FPGA – LFE5U–85F–6BG381C		Memory – DDR3 Memory Modules		
							
B	File: ../../../../tmp/Akita/PCB/Akita/LinuxSiP.sch		File: ../../../../tmp/Akita/PCB/Akita/FPGA.sch		File: ../../../../tmp/Akita/PCB/Akita/Memory.sch		
	IO – PMOD & Hight Speed Interface		Power Supplies		FPGA Power		
							
C	File: ../../../../tmp/Akita/PCB/Akita/IO.sch		File: ../../../../tmp/Akita/PCB/Akita/Power.sch		File: ../../../../tmp/Akita/PCB/Akita/FPGA_Power.sch		
D							
	1	2	3	4	5	6	

Sheet: /  
File: working.kicad\_sch

Title:

Size: A4	Date:	Rev:
KiCad E.D.A.	eeschema 7.0.6	Id: 1/7

Sheet: /		
File: working.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. eeschema 7.0.6	Id: 1/7	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /Linux SiP - OSD3358-512M-BSM/ File: LinuxSiP.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. eeschema 7.0.6	Id: 2/7	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /IO - PMOD & Hight Speed Interface/ File: IO.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. eeschema 7.0.6		Id: 3/7

1		2		3		4		5		6	
A											
B											
C											
D											
Sheet: /Power Supplies / File: Power.sch											
Title:											
Size: A4						Date:			Rev:		
KiCad E.D.A. eeschema 7.0.6									Id: 4/7		

The figure shows a blank schematic sheet with a grid. The grid has 6 columns labeled 1 to 6 and 4 rows labeled A to D. A title block is located in the bottom right corner.

Sheet: /FPGA - LFE5U-85F-6BG381C/ File: FPGA.sch	
<b>Title:</b>	
Size: A4	Date:
KiCad E.D.A. eeschema 7.0.6	Rev: Id: 5/7

The figure shows a blank schematic sheet with a grid. The grid has 6 columns labeled 1 to 6 and 4 rows labeled A to D. A title block is located in the bottom right corner.

Sheet: /Memory – DDR3 Memory Modules /	
File: Memory.sch	
<b>Title:</b>	
Size: A4	Date:
KiCad E.D.A. eeschema 7.0.6	Rev: Id: 6/7

123456					
A					
B					
C					
D					
123456					

Sheet: /FPGA Power/ File: FPGA_Power.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. eeschema 7.0.6	Id: 7/7	