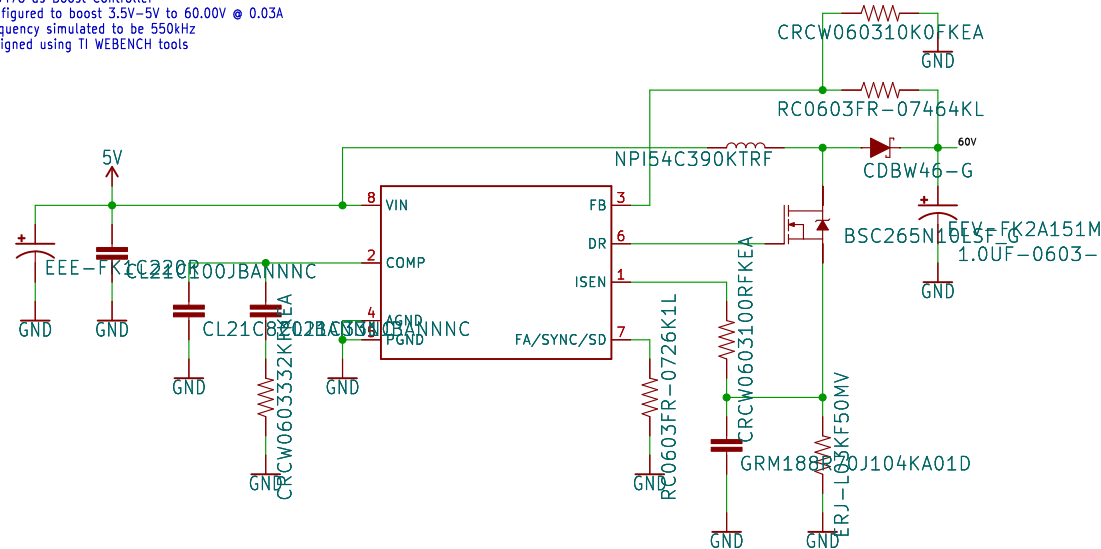
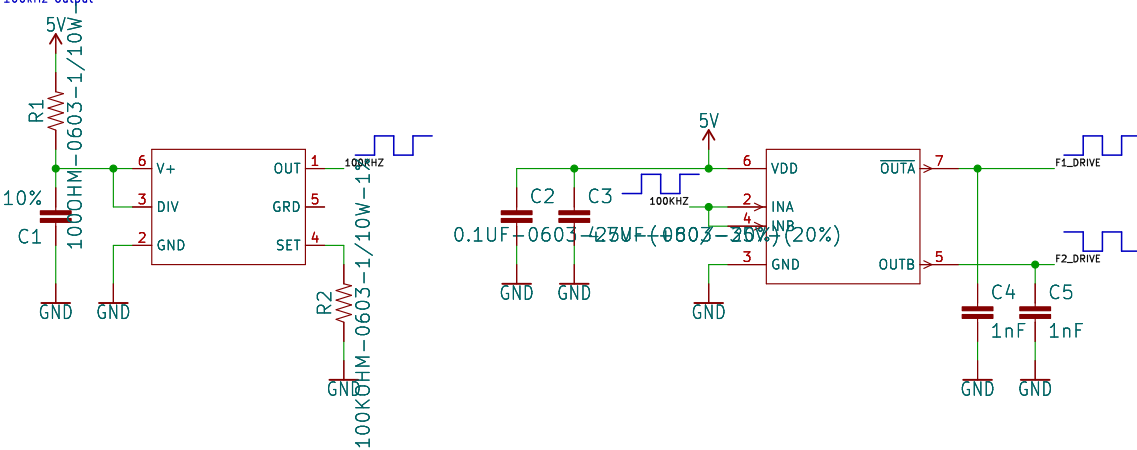


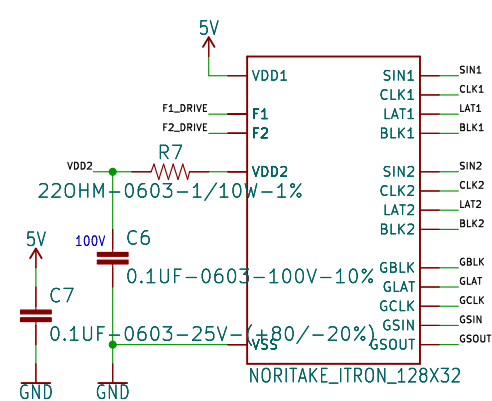
LM3478 as Boost Controller  
Configured to boost 3.5V-5V to 60.00V @ 0.03A  
Frequency simulated to be 550kHz  
Designed using TI WEBENCH tools



MAX628 Power MOSFET Driver  
WAY overspec for this job but has complementary outputs we need  
Inputs tied together and fed from LTC6906 clock oscillator configured  
for 100kHz output

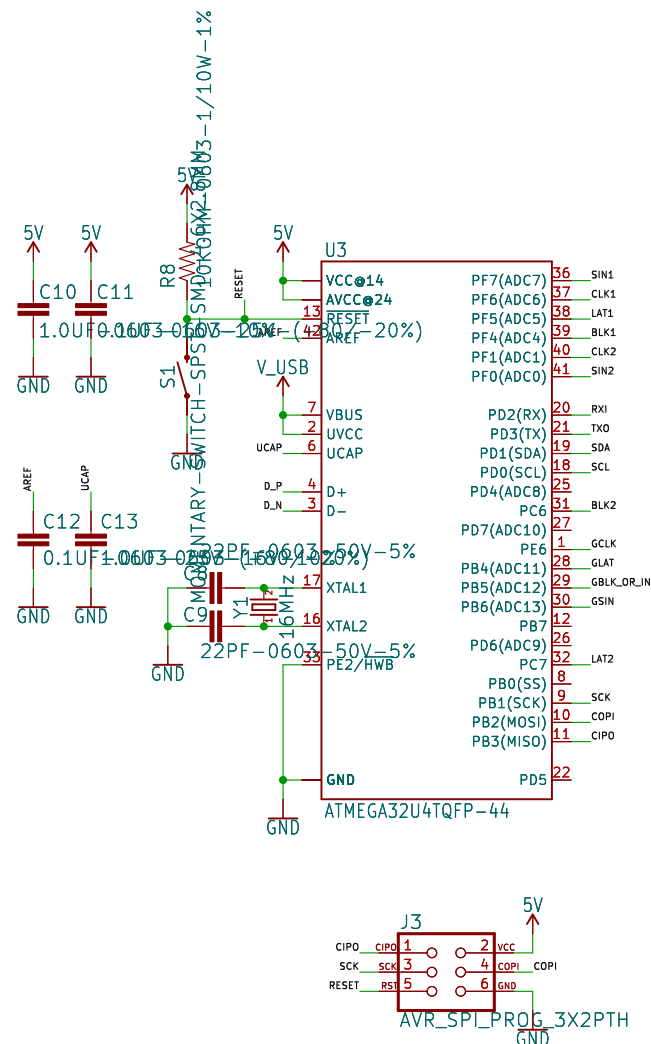
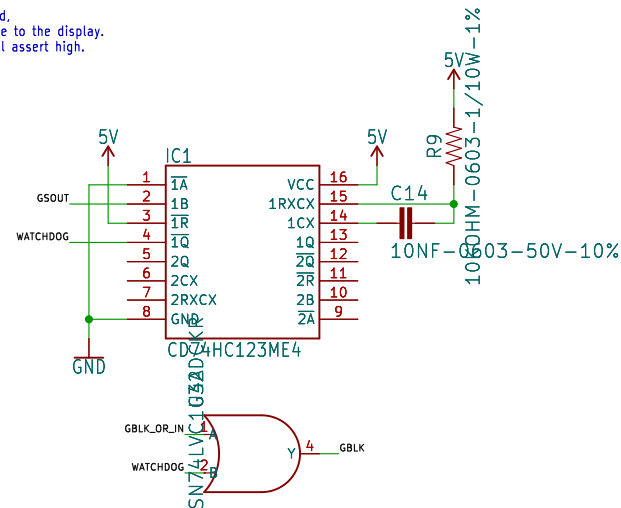


This VF glass includes 3 x 64 bit serial shift register, latched drivers which connect to the anode and grid electrodes. An external host is required to provide a multiplexing data stream to refresh the display. The signal inputs can be connected to the ports of a CMOS microprocessor.

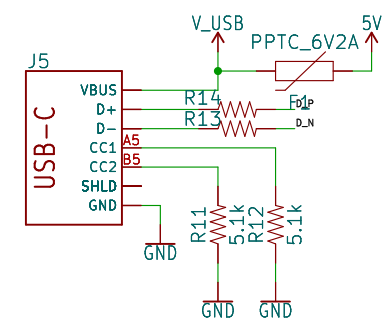
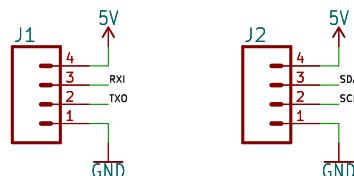
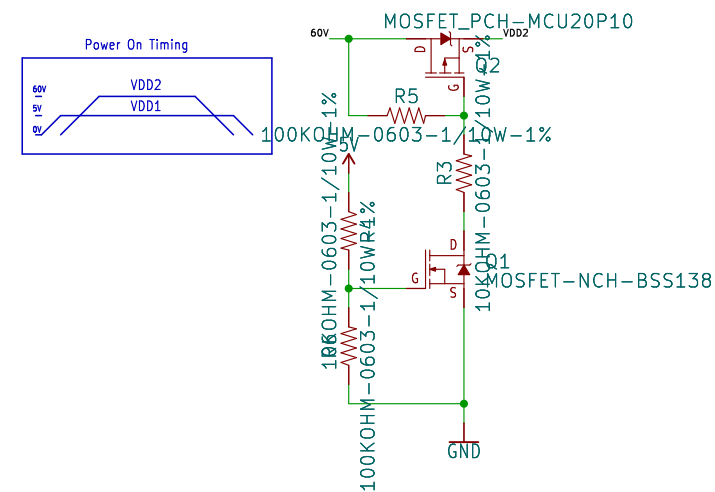


In case the CPU is interrupted for some reason during multiplex driving, the grid scan may stop. This will cause permanent damage to the CIG VFD.

If scanning of the display stops with VDD2 applied, the BLK input must be set high to prevent damage to the display. If the HC123 is not retriggered every 100ns it will assert high. This signal is OR'd to the Grid Blanking input.



From Datasheet:  
"While the display power is being supplied, if the logic supply voltage "VDD1" is floating, or is kept at less than "+4.5V", the driver chips may be PERMANENTLY DESTROYED."  
This switch should ensure that VDD2 is never high while VDD1 is floating or low.



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