

Place C1 as close as possible to PVDD.

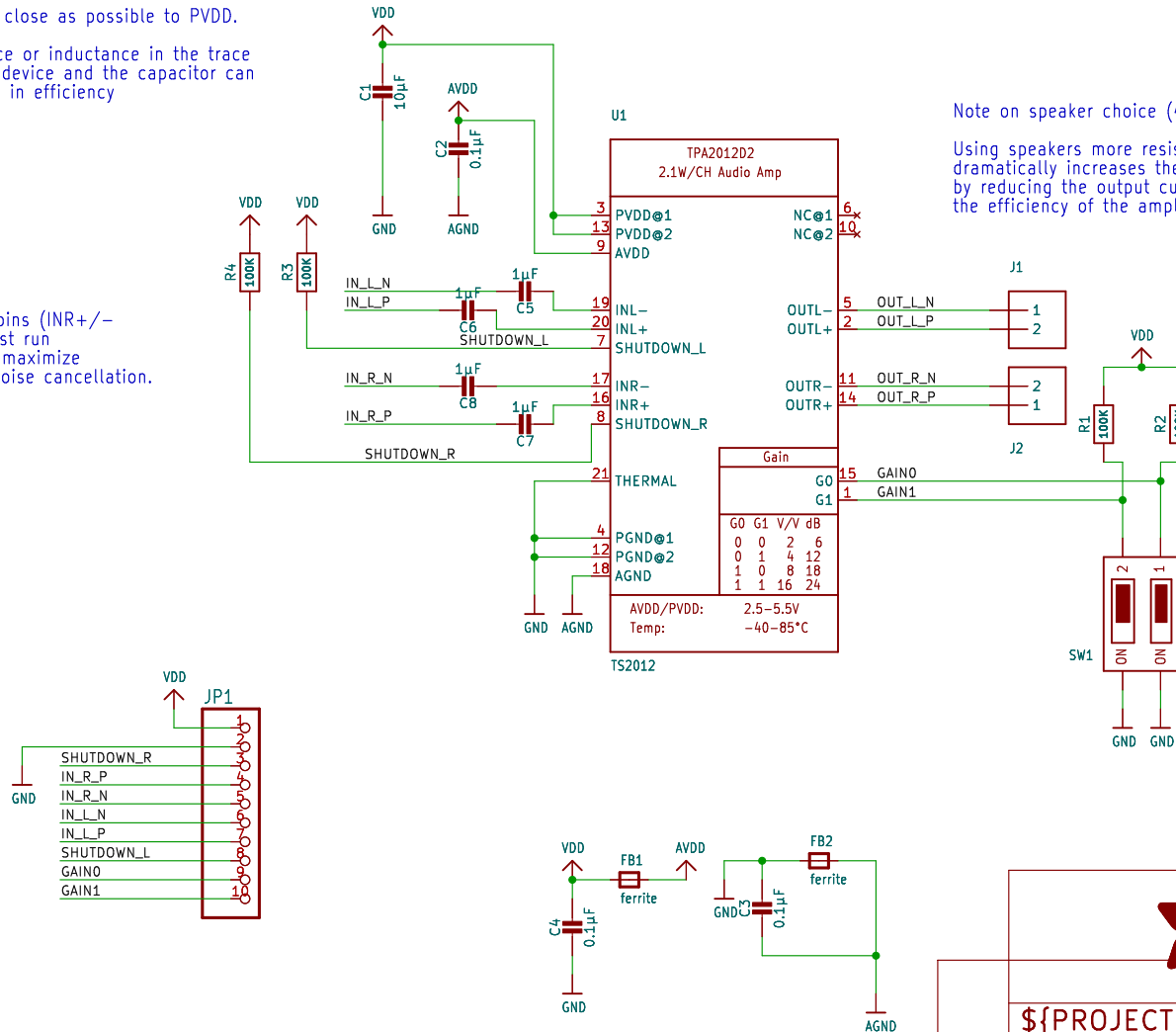
Any resistance or inductance in the trace between the device and the capacitor can cause a loss in efficiency

The audio input pins (INR+/- and INL+/-) must run side-by-side to maximize common-mode noise cancellation.

Note on speaker choice (4/8/16/32 ohm, etc.):

Using speakers more resistive than 4-Ω dramatically increases the thermal performance by reducing the output current and increasing the efficiency of the amplifier.

To control gain in SW set both pins to OFF and control the pin state in SW



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Drawing: {AUTHOR}

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