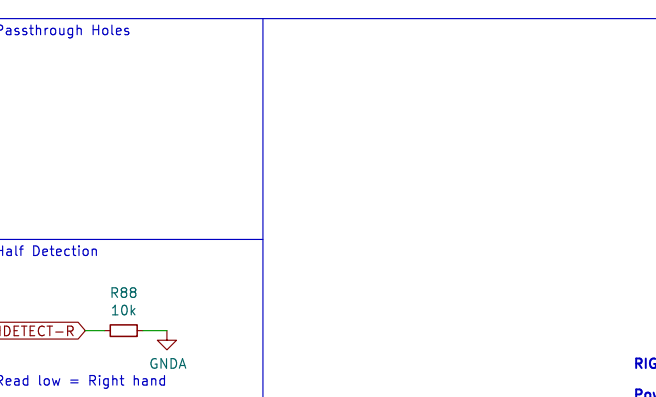
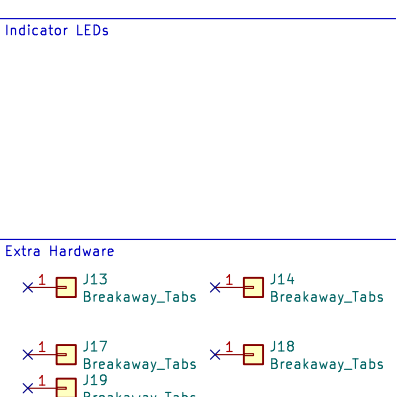
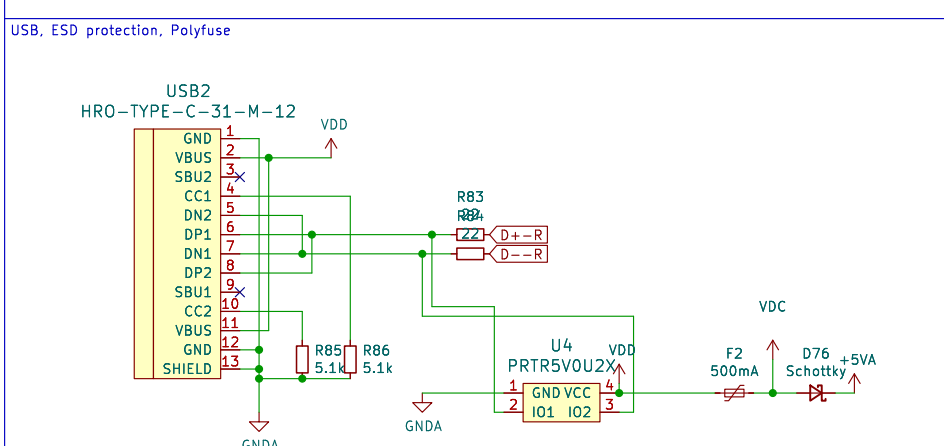
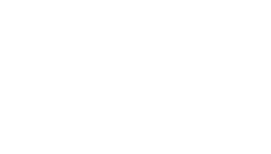
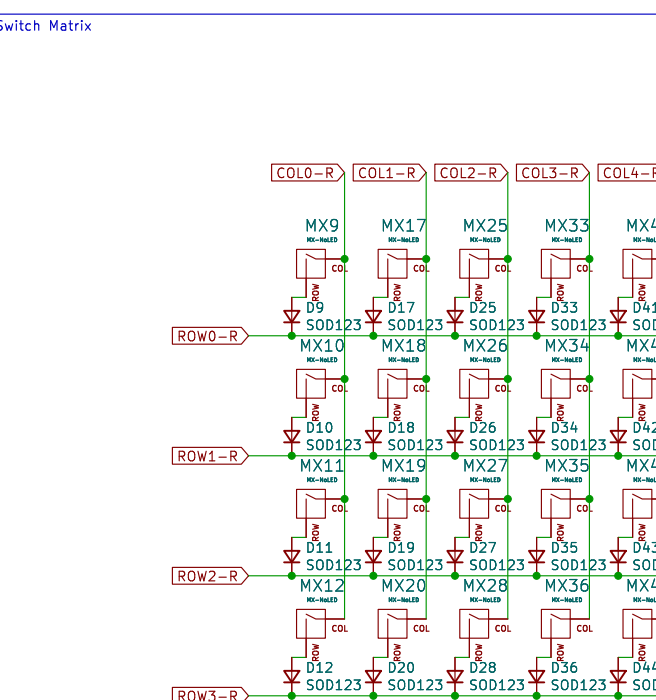
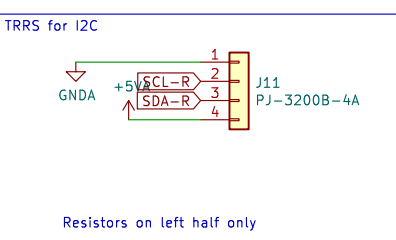
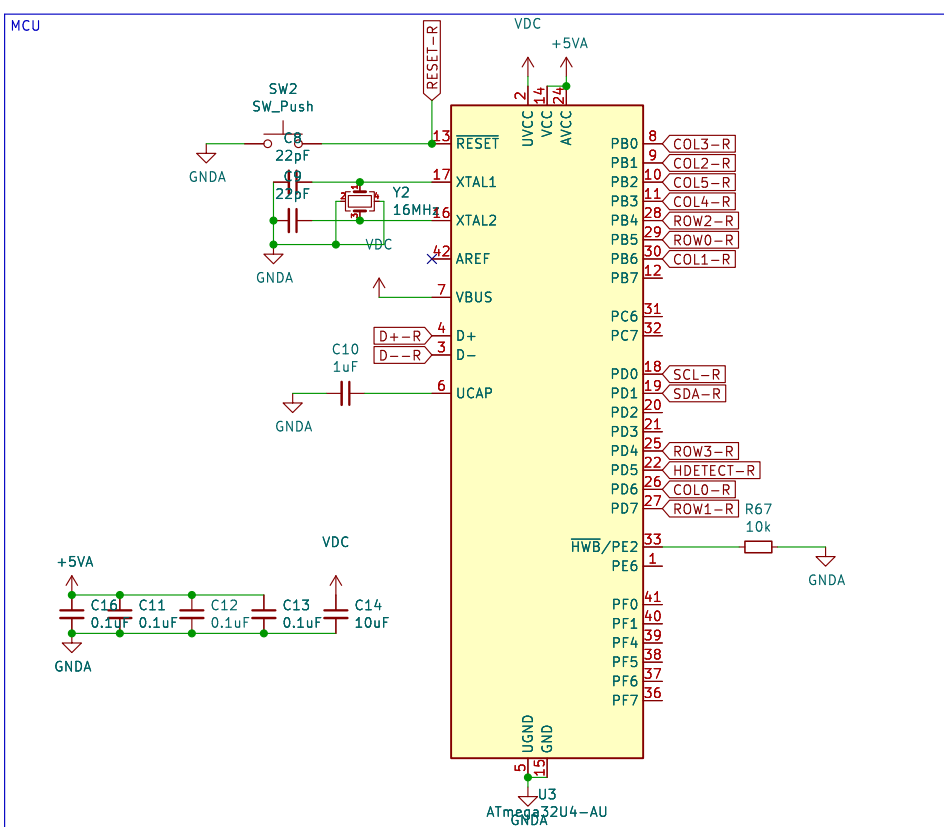


LEFT HAND SCHEMATIC



RIGHT HAND SCHEMATIC

Power net changes:
VCC -> VDD
VBUS -> VDC
GND -> GNDA
+5V -> +5VA
to prevent kicad from complaining about no connections between halves

Credits to
crkdb by foosten - Design inspiration and fork base
Iris by Bakingspy - Schematic reference