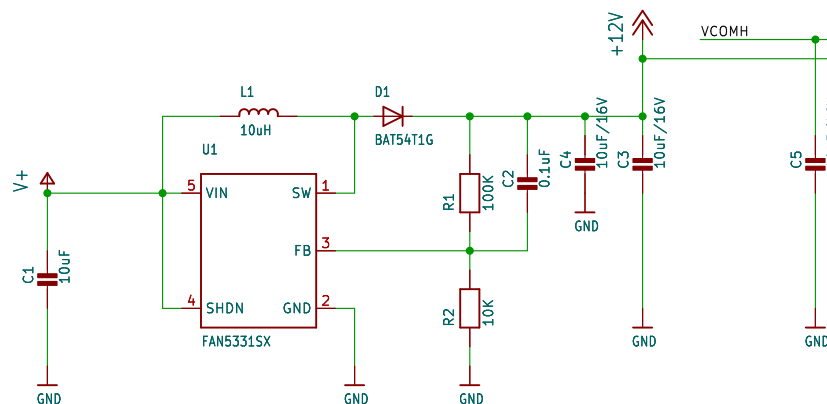
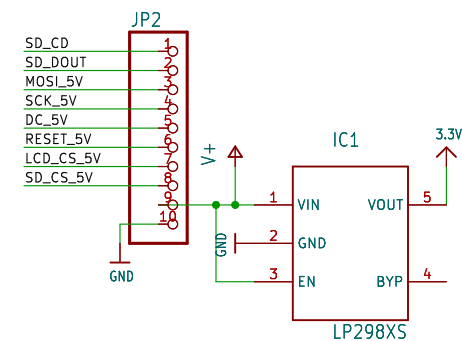
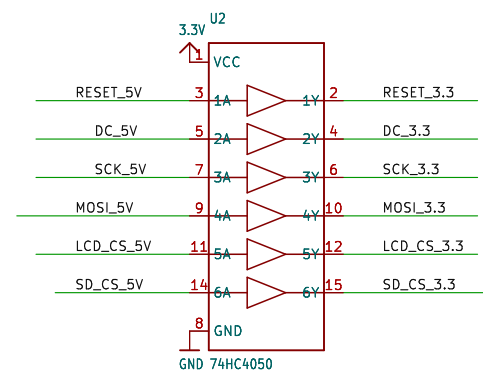
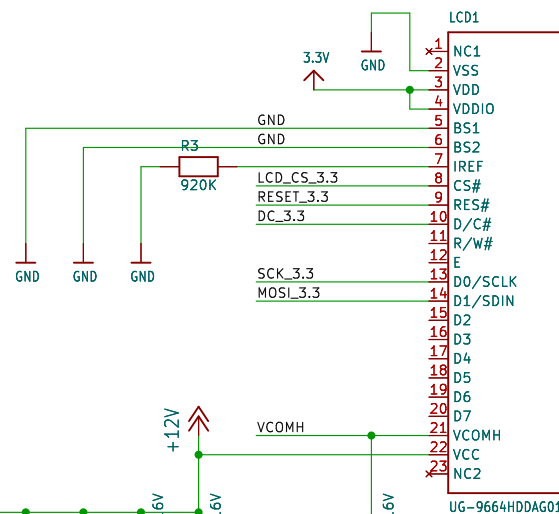


BUS is set to SPI by default (R4/R5)



For C2, see p.7 of DS (R1xCF)

$$VOUT = 1.23V (1 + R1/R2)$$

$$VOUT = 13.53V$$

This is 0.5V below the recommended voltage, but uses common resistors for easy assembly

Supply Voltage (VDD): 2.4–3.5V  
Logic (VDDIO): 1.8V–VDD  
Supply Voltage (VCC): 14V

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