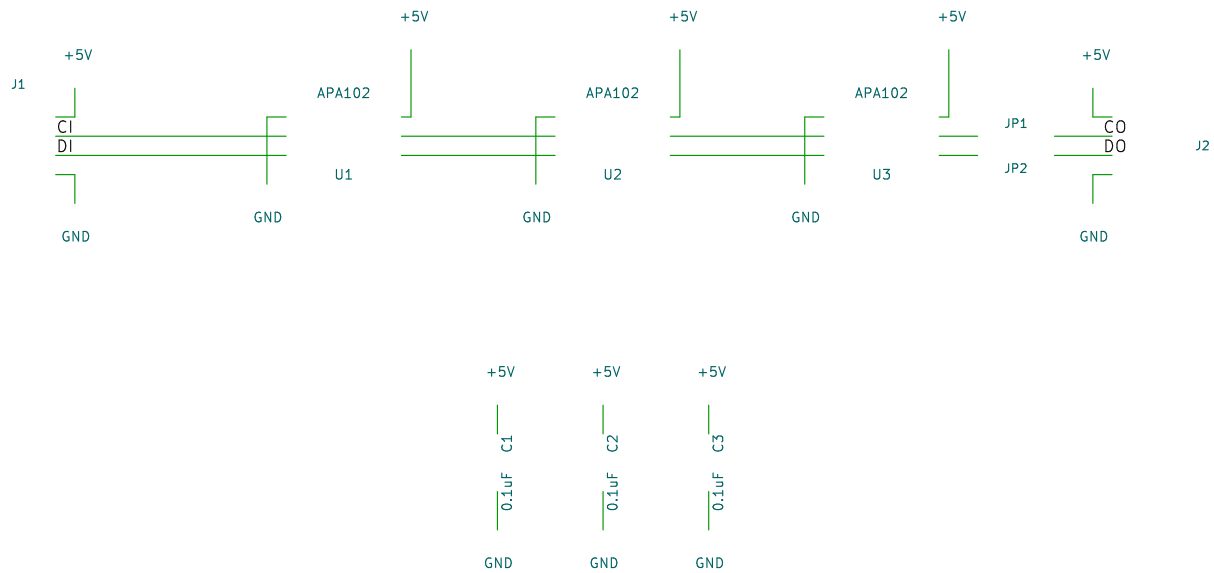


Note: JP1 and JP2 are present so the user can disconnect clock and data signals from subsequent boards, allowing you to make a closed loop design without having the logic from the last board interfere with the logic of the first. They are normally closed, so open them if you're making a closed loop design.



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Designed by Pete Dokter

Sheet:1/1
File: working.kicad_sch

Title: Lumenati 90L

Size: A4	Date: 7/5/17	Rev: v10
KiCad E.D.A.	eeschema 7.0.6	Id: 1/1