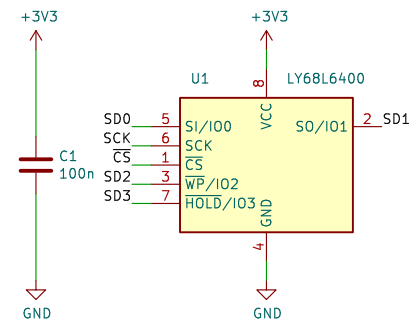
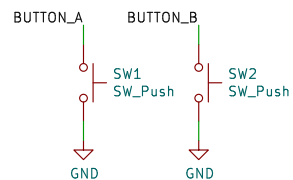


Want data bus to be on consecutive ascending GPIOs
to make things easier on the PIO side



Yup that's it



Luke Wren

Sheet: /
File: working.kicad_sch

Title: **QSPI PSRAM PMOD breakout**

Size: A4 Date: 2020-08-19

KiCad E.D.A. eeschema 7.0.6

Rev: 1

Id: 1/1