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This directory includes regular Electrical and Computer Engineering Department faculty, adjunct faculty, and faculty with joint appointments. Click on faculty names to see their personal web pages.

Regular Faculty | EE

In integrated circuit design, dynamic logic (or sometimes clocked logic) is a design methodology in combinatory logic circuits, particularly those implemented in MOS technology. It is distinguished from the so-called static logic by exploiting temporary storage of information in stray and gate capacitances. It was popular in the 1970s and has seen a recent resurgence in the design of high ...

Dynamic logic (digital electronics) - Wikipedia

ISSN: 2277-3754 ISO 9001:2008 Certified International Journal of Engineering and Innovative Technology (IJEIT) Volume 2, Issue 1, July 2012 252

Volume 2, Issue 1, July 2012 Design of CMOS Schmitt Trigger

This session presents RF-inspired emerging technologies in the areas of silicon photonics, cryogenic CMOS and heterogeneous integration to enable new photonic systems, quantum computing, and cross-technology optimized circuits.

Technical Sessions | RFIC Symposium

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ISSCC Award Winners - 2019 ISSCC

Both the Symposium on VLSI Technology and the Symposium on VLSI Circuits recognize outstanding student papers with a Best Student Paper Award.

Best Student Paper Award - VLSI 2019

No. Title: Publication: Authors: 2018: 75: Dual Receiver Coils Wireless Power Transfer System with Interleaving Switching : IEEE PELS Kye-Seok Yoon, Sang-Han Lee, In-Kui Cho, Ho-Jin Lee, Gyu-Hyeong Cho

KAIST CNSL

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Tzi-Dar Chiueh (蔡志達) - 蔡志達

Computers and electronics play an enormous role in today's society, impacting everything from communication and medicine to science. Although computers are typically viewed as a modern invention involving electronics, computing predates the use of electrical devices. The ancient abacus was perhaps ...

Category:Computing and electronics - Engineering and ...

Interfacing mm-wave ICs with antennas remains a critical challenge for emerging mm-wave communication, sensor, and radar transceivers. This workshop will focus on the integration of antenna, antenna-arrays and antenna interfaces for microwave and mm-wave sensors and communications applications.

Workshops and Short Courses | IMS2019

Worldwide utility patents. Inventors with 200 or more worldwide utility patents are shown in the following table. While in many cases this is the number of utility patents granted by the United

States Patent and Trademark Office, it may include utility patents granted by other countries, as noted by the source references for an inventor.

List of prolific inventors - Wikipedia

Thin film materials have many advantages over their bulk counterparts, in terms of properties, applications, and economical considerations. One example is mechanical flexibility, i.e., bendability, when they are in freestanding forms or integrated with other thin film materials.

Stretchable Thin Film Materials: Fabrication, Application ...

Direct printing of thin-film transistors has enormous potential for ubiquitous and lightweight wearable electronic applications. However, advances in printed integrated circuits remain very rare ...

Three-dimensional monolithic integration in flexible ...

By Dick James. IEDM this year was its usual mixture of academic exotica and industrial pragmatics (to use a very broad-brush description), but the committee chose to keep us all waiting until the Wednesday morning before we got to the CMOS platform papers.

Chipworks - semimd.com

The IEEE A-SSCC 2018 (Asian Solid-State Circuits Conference) is an international forum for presenting the most updated and advanced chips and circuit designs in solid-state and semiconductor fields. The conference is supported by the IEEE Solid-State Circuits Society and will be held in Asia.

A-SSCC 2018 | IEEE Asian Solid-State Circuits Conference 2018

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www.icce-asia2018.org

This Review looks at recent progress in the development and understanding of memristive devices, and examines the performance requirements for computing with such devices.

Memristive devices for computing | Nature Nanotechnology

Digital Signal Processing (DSP) Return to www.101science.com home page. DSP a crash course. Digital signal processing is still a new technology and is rapidly developing.

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