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This site is dedicated to providing resources for students and professionals in the area of analog integrated circuit design using CMOS and BiCMOS technologies. The resources are designed to help understand the principles, concepts, and techniques of analog integrated circuit design.

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I.1 - INTRODUCTION. GLOBAL OBJECTIVES • Teach the analysis, modeling, simulation, and design of analog circuits implemented in CMOS technology. • Emphasis will be on the design methodology and a hierarchical approach to the subject. SPECIFIC OBJECTIVES. 1. Present an overall, uniform viewpoint of CMOS analog circuit design. 2. Achieve an understanding of analog circuit design.

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Allen and Holberg - CMOS Analog Circuit Design Page III.4-4. Typical Model Parameters Suitable for SPICE Simulations Using Level-3 Model (Extended Model). These Values Are Based upon a 0.8 μ m Si-Gate Bulk CMOS n-

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Chapter 1 presents the material necessary to introduce CMOS analog circuit design. This chapter gives an overview of the su.bject of CMOS analog circuit design. defines notation and convention, mak~ a brief! Iwvey of analog signal processing, and gives an example of analog CMOS design with emphasis OII the hierarchial aspect of the design.

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