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IC Applications and HDL Simulation Lab; Updated on October 14, 2016 VIKRAM 18525 Prerequisite: Know the theory about the experiment. Study the pin diagram and functioning of each pin of IC 741. Study how an integrator and a differentiator work. ... Viva Questions: 1. Define integrator.

IC Applications and HDL Simulation Lab Notes: Integrator ...

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IC Applications Lab Manual... - 4 - Lab Exam Question Paper 1. a) Design a three input adder circuit to have a voltage gain of 3. Calculate the resistor values, currents and output voltage when all the input voltages are 1V. b) A substractor is to be designed to amplify the difference between two voltages by a factor of 10.

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VIVA QUESTIONS: 1. Draw the characteristics of ideal comparator and that of a commercially available comparator 2. What are the other application op amp 3. List the different types of comparator 4. What is meaning of voltage limiting ?Show how it is obtained ? 5. Draw the circuit for converting a square wave in to a series of positive pulses? 6.

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IC Applications and HDL Simulation Lab; ... Viva Questions: What is the full form of VHDL? ... IC 565 - PLL Applications. Voltage Regulator using IC 723, Three Terminal Voltage Regulators - 7805, 7809, 7912. Measrement of IC741 Op-Amp parameters. Study and analyze standard digital ICs.

IC Applications and HDL Simulation Lab Notes: HDL code to ...

EE0313-INTEGRATED CIRCUITS LAB 1 TABLE OF CONTENTS 1. Syllabus 2. Mapping of Program Outcomes with Instructional Objectives ... Study and application of PLL IC's 6. Design of binary adder and subtractor. 7. Design of counters. ... 3 Pre viva questions 5

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ANALOG ELECTRONIC CIRCUITS (AEC) LABORATORY MANUAL II / IV ...

Logic Design Laboratory Manual 3 _____ VIVA QUESTIONS: 1. Why NAND & NOR gates are called universal gates? 2. Realize the EX – OR gates using minimum number of NAND gates. 3. Give the truth table for EX-NOR and realize using NAND gates? 4. What are the logic low and High levels of TTL IC's and CMOS IC's? 5. Compare TTL logic family with ...

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Q.1 Give some application of 555 timers IC? Q.2 What are the basic elements of a 555 timer IC? Q.3 Give important features of 555 timer IC? Q.4 What do you mean by the term duty cycle of an astable multivibrator? ... DIGITAL ELECTRONICS LAB VIVA QUESTIONS VIVA QUESTIONS Q.1 What do you mean by Logic Gates?

Engineers: DIGITAL ELECTRONICS LAB VIVA QUESTIONS

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Electronic Devices And Circuits Lab Viva Questions

Every viva examination is different, so it is not possible to know in advance exactly what the examiners will ask you. However, there are some common questions which you may like to practice as part of your own preparations. Generally, the questions that are asked in viva examinations can be grouped ...

Practice Viva Questions — University of Leicester

Viva Questions: 1.Define and give the expression for its relation with . 2.Expres Ic in terms Ic E o and Ic B o. Ic= E + Ic B o and Ic= B + Ic E o 3.What is the arrow in the transistor symbol indicates? It indicates the direction of the emitter current when the E-B junction is forward biased.

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