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ANALYSIS AND DESIGN - U-Cursos

He joined the University of California, Berkeley in 1971 with the Department of Electrical Engineering and Computer Sciences.Gray's research interests include bipolar and MOS circuit design, electro thermal interactions in integrated circuits, device modeling, telecommunications circuits, and analog-digital interfaces on analog integrated circuits.

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ANALYSISANDDESIGN OF ANALOGINTEGRATED CIRCUITS

Paul R. Gray received the B.S., M.S., and Ph.D. degrees in Electrical Engineering from the University of Arizona, Tucson, in 1963, 1965, and 1969, respectively. ... While there, he was involved in the application of new technologies for analog integrated circuits, including power integrated circuits and data conversion circuits. In 1971, he ...

Paul R. Gray | EECS at UC Berkeley

A selection of topics from these chapters can be chosen as follows. Chapter 10 deals with nonlinear analog circuits, and portions of this chapter up to Section 10.3 could be covered in a first course. Chapter 11 is a comprehensive treatment of noise in integrated circuits, and material up to and including Section 11.4 is suitable.

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Course Outline MOS Device Structure and Circuit Models Single-Stage and Differential Amplifiers Passive and Active Current Mirrors Frequency Response of Amplifiers Noise Feedback Op Amp Design Stability and Frequency Compensation Bandgap References Introduction to Switched-Capacitor Circuits Simulation Platform: HSPICE or Cadence.

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For EE240a: Gray, Hurst, Lewis, Meyer, Analysis and Design of Analog Integrated Circuits, 5th Edition, John Wiley & Sons, 2009. NOTE: This text was used for previous renditions of this course, so is still very relevant as supplemental reading, especially for MOS circuit design. Various material to be distributed throughout the course. ...

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