
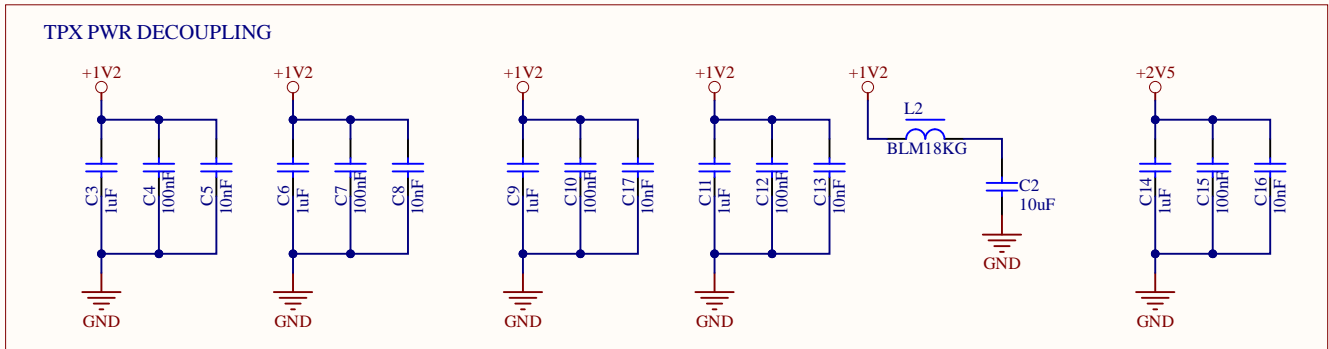
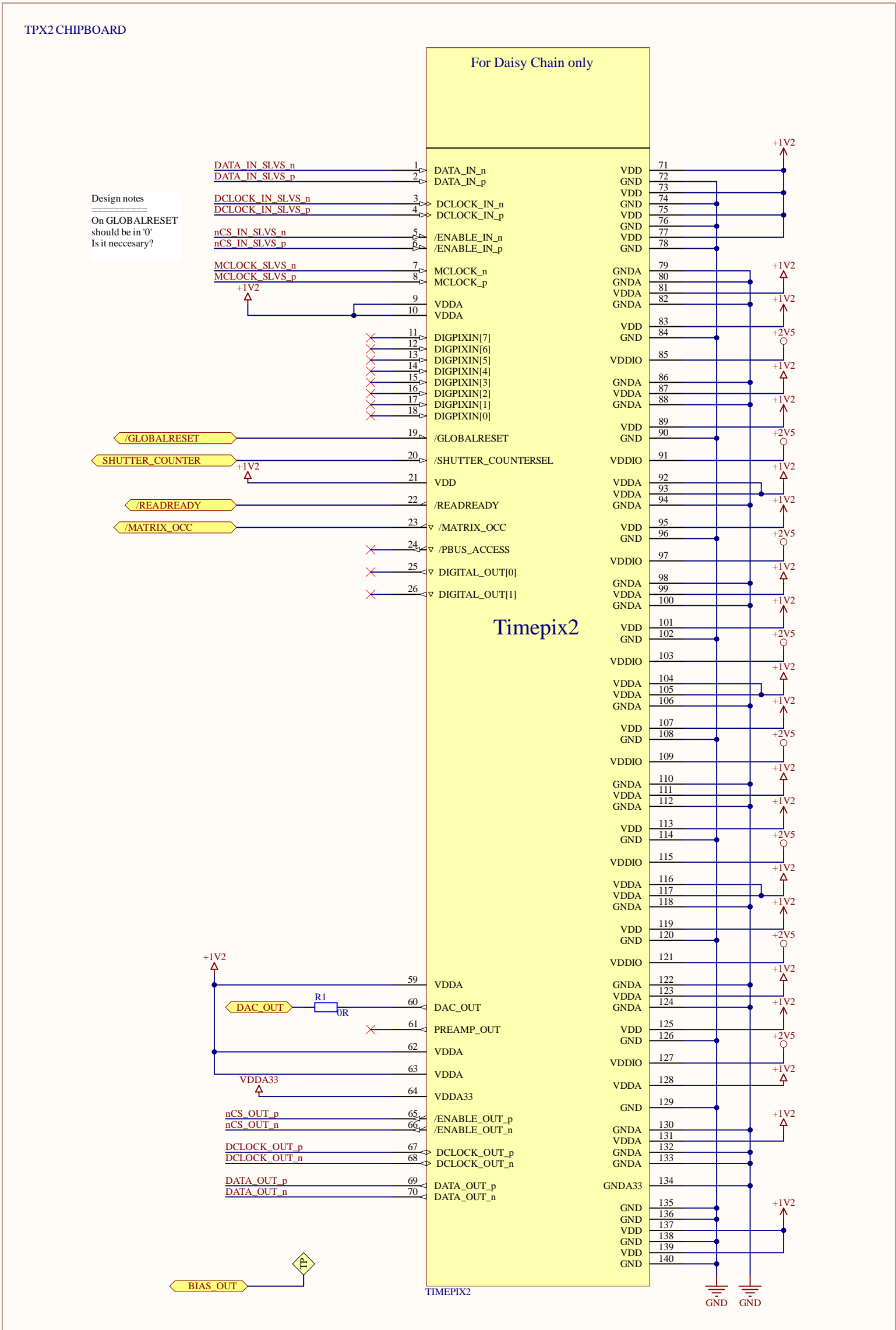
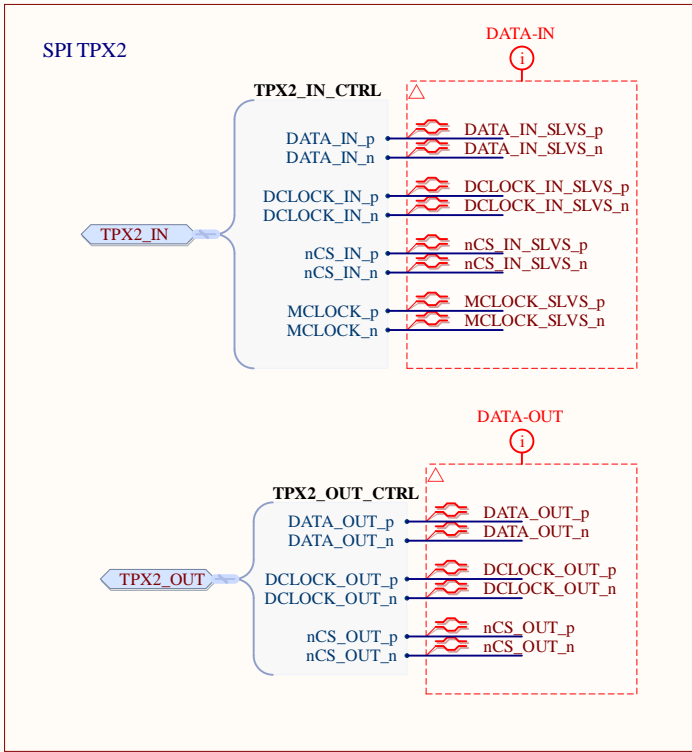
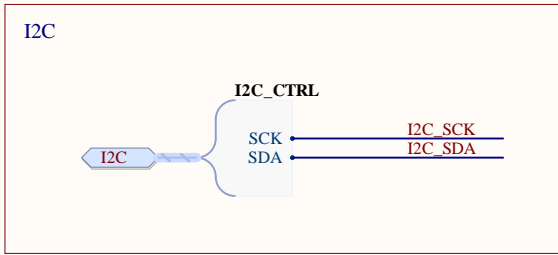
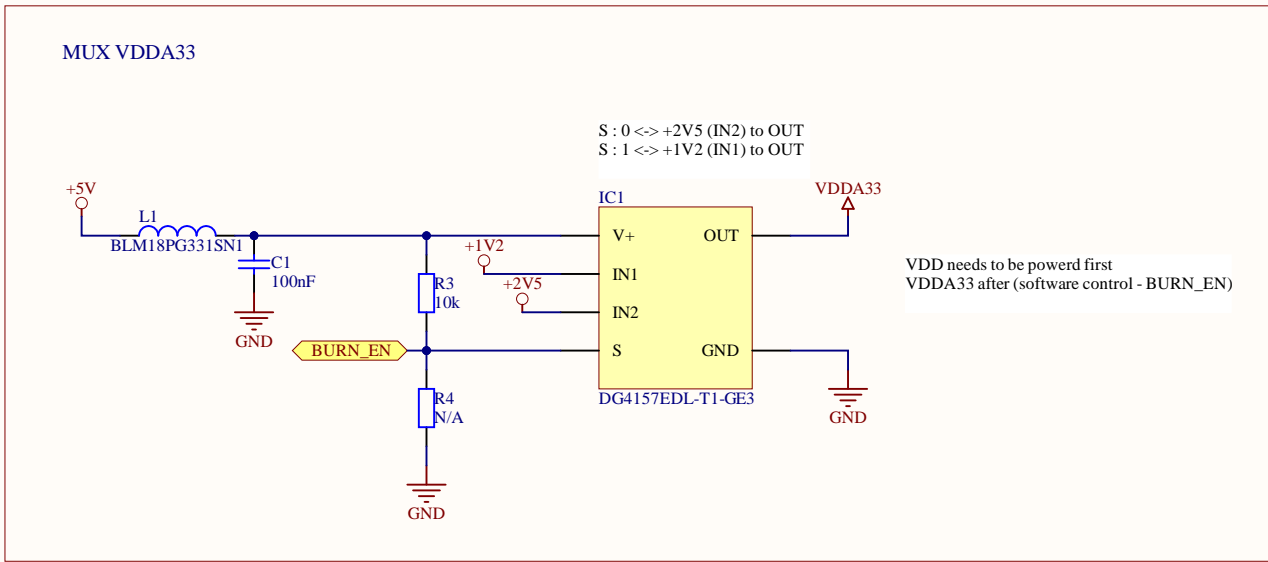
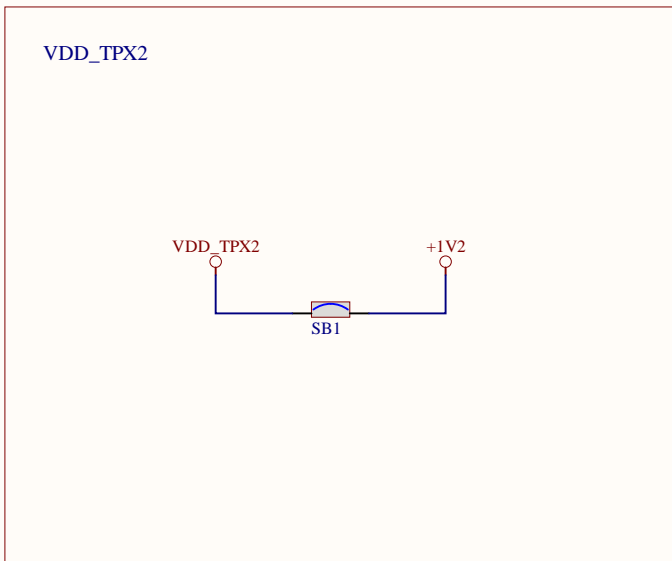
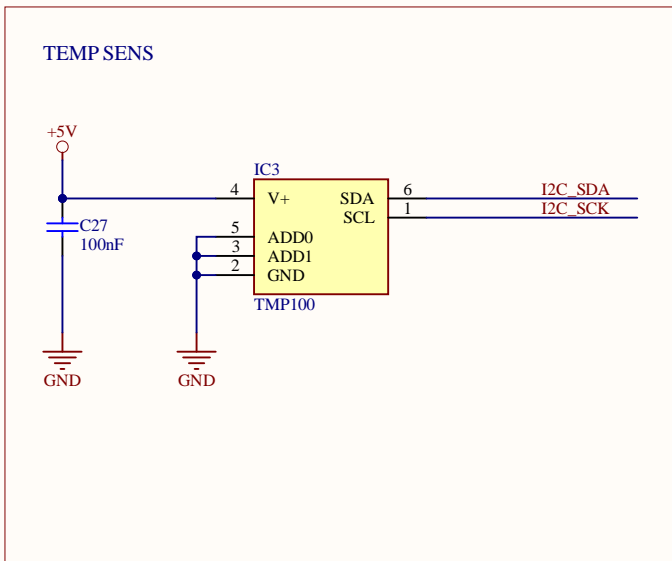
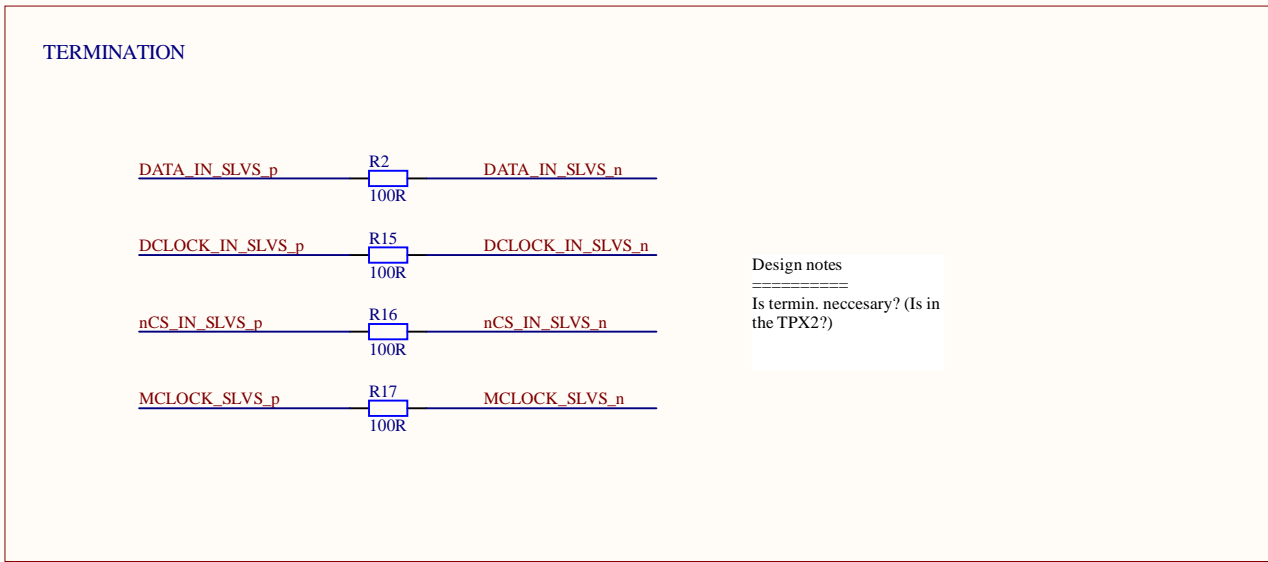
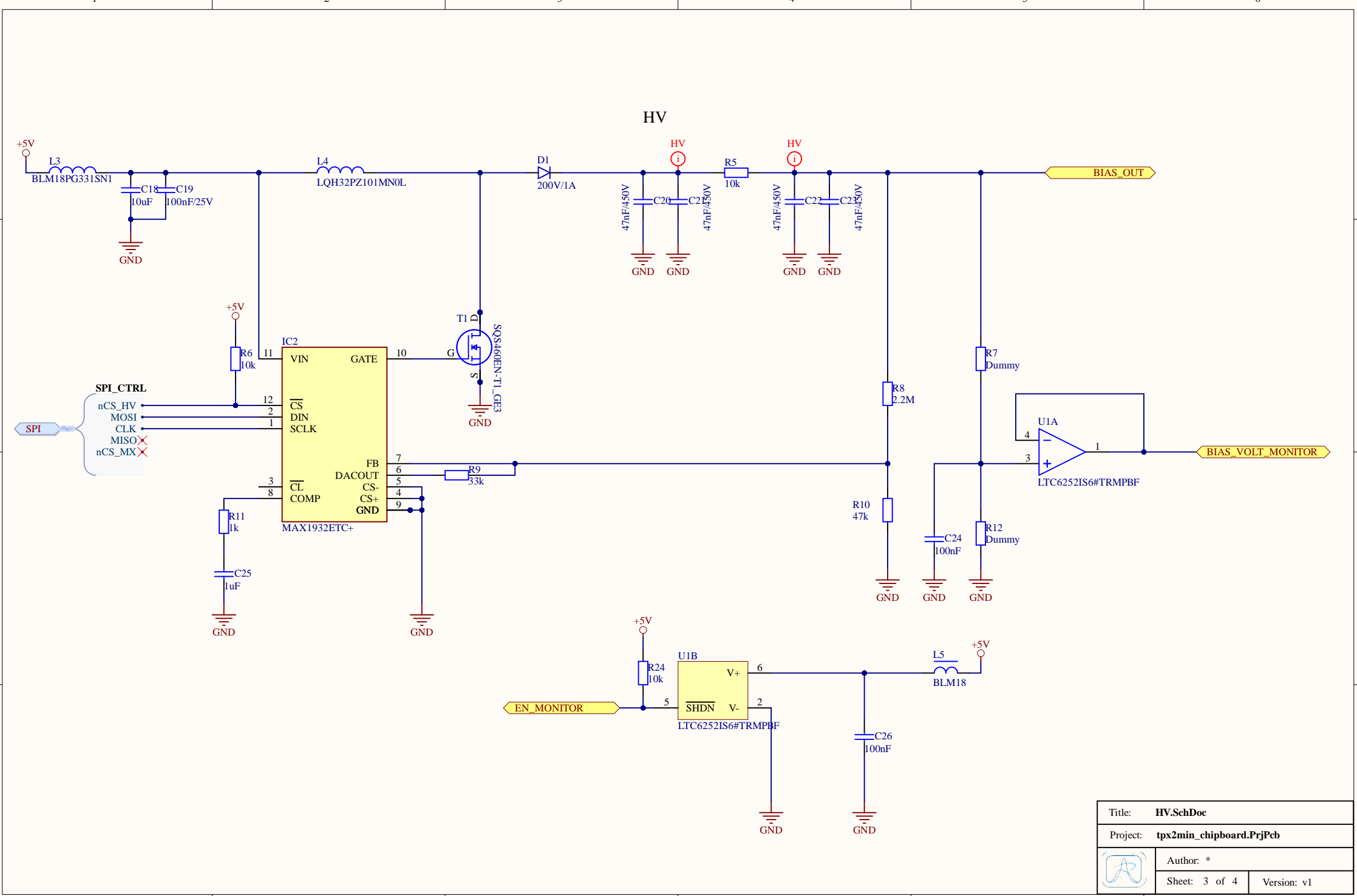


Title: <b>Top Level.SchDoc</b>		
Project: <b>tpx2min_chipboard.PrjPcb</b>		
	Author: *	
	Sheet: 1 of 4	Version: v1



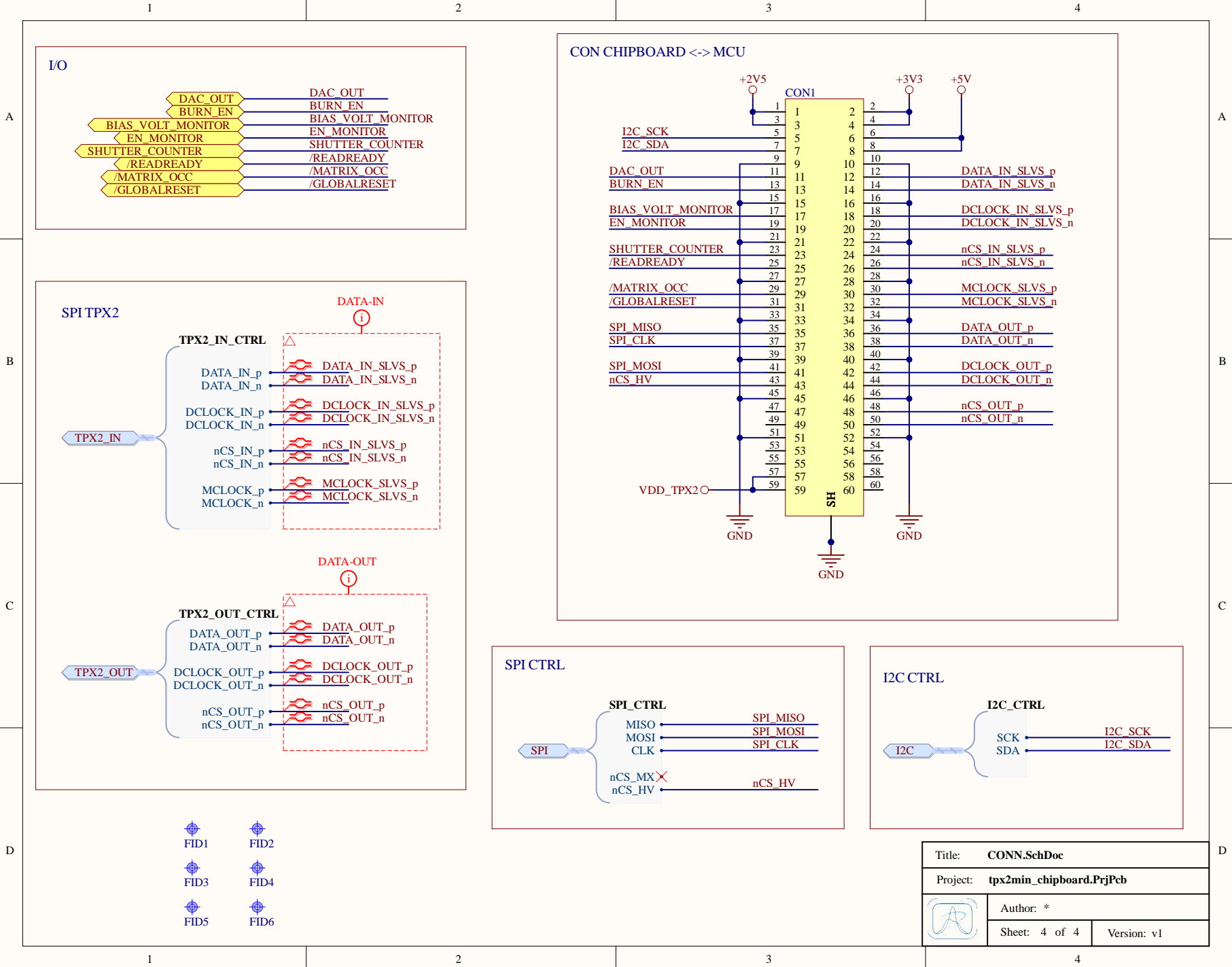


A

B

C

D



1

2

3

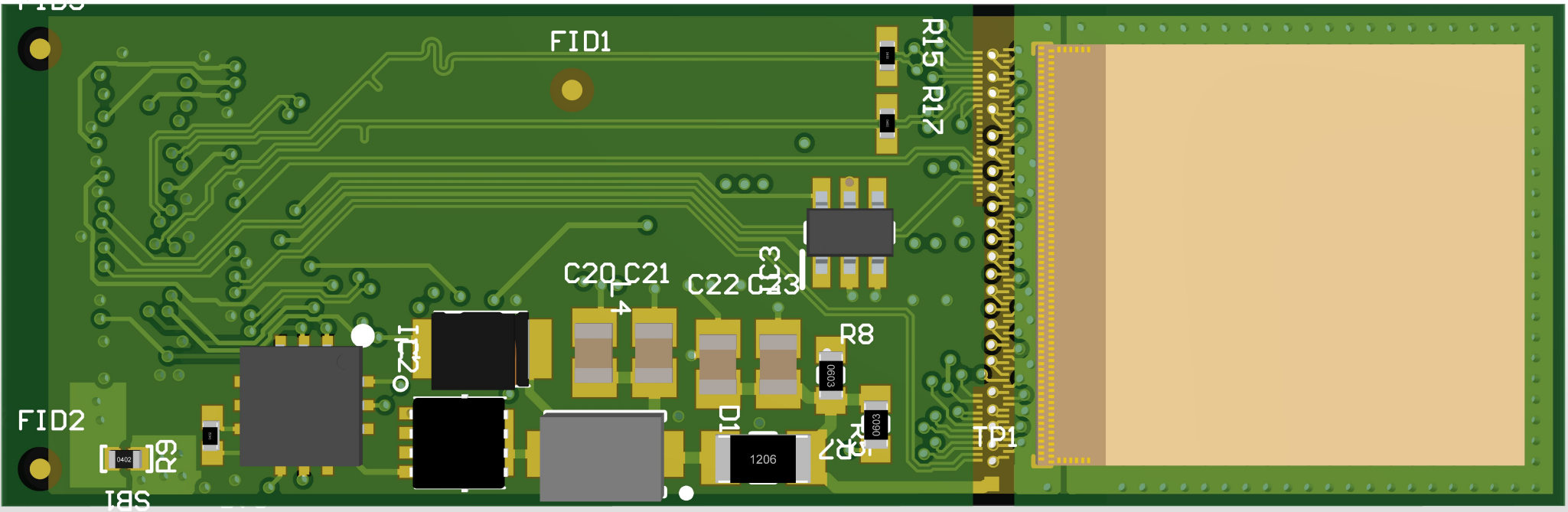
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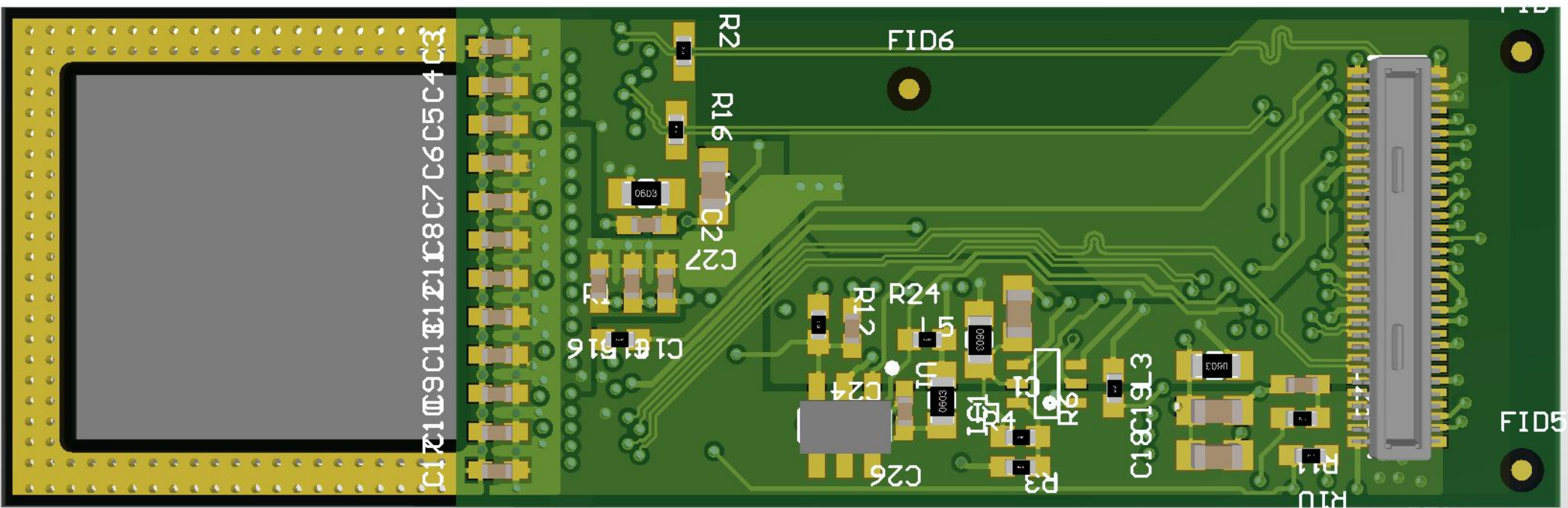
A

B

C

D





A

B

C

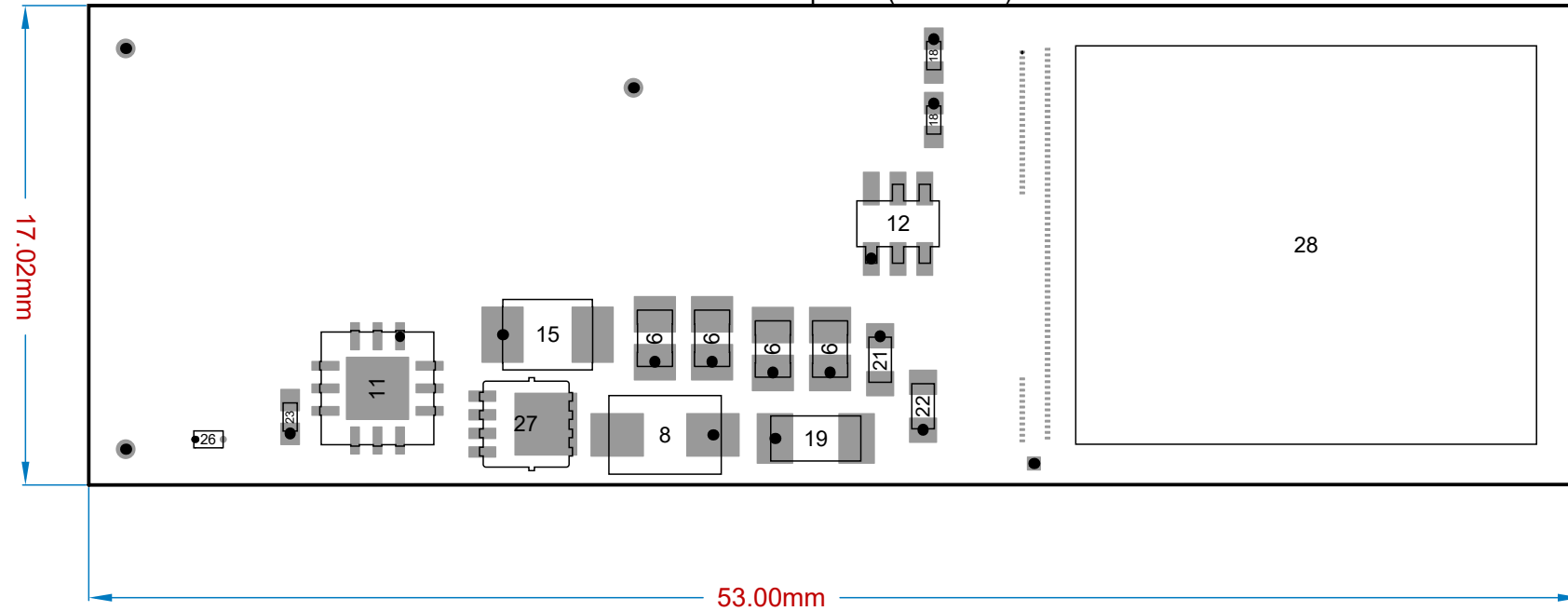
D

E

Bill Of Materials - TOP

Line #	Designator	Comment	Quantity
6	C20, C21, C22, C23	47nF/450V	4
8	D1	200V/1A	1
9	FID1, FID2, FID3	Fiducial	3
11	IC2	MAX1932ETC+	1
12	IC3	TMP100	1
15	L4	LQH32PZ101MN0L	1
18	R15, R17	100R	2
19	R5	10k	1
21	R7	Dummy	1
22	R8	2.2M	1
23	R9	33k	1
26	SB1	SB_ON	1
27	T1	SQS460EN-T1_GE3	1
28	TIMEPIX2	Timepix2_ASIC	1
29	TP1	TP	1

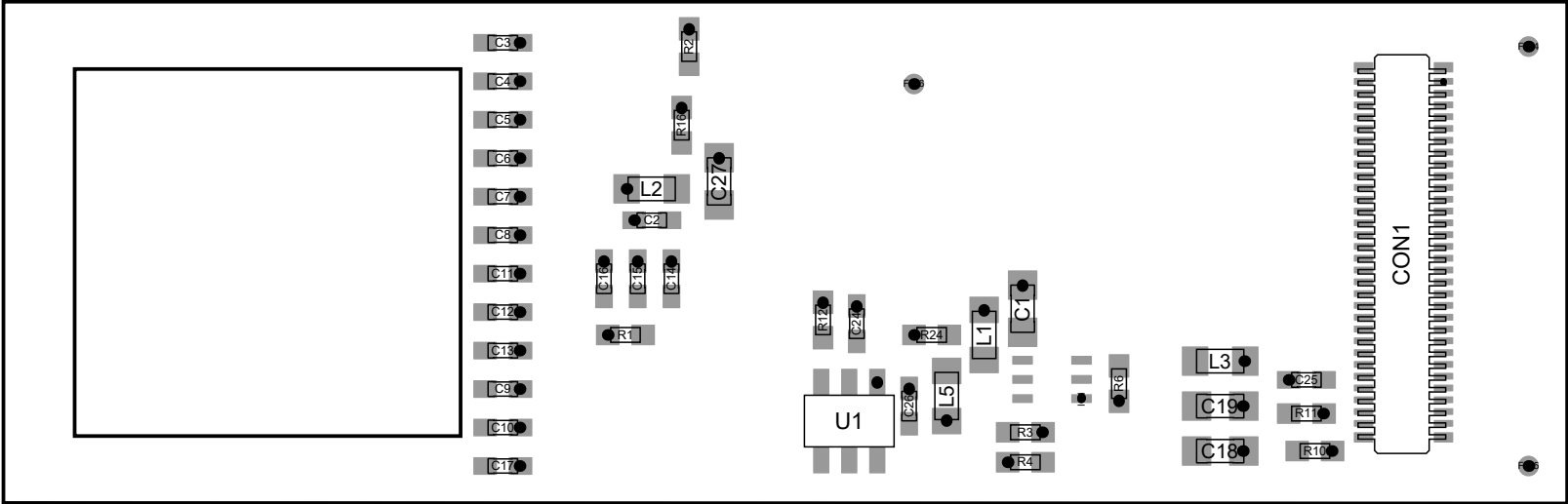
View from Top side (Scale 4:1)



Bill Of Materials - BOT

Line #	Designator	Comment	Quantity
1	C1, C4, C7, C10, C12, C15, C24, C26, C27	100nF	9
2	C2, C18	10uF	2
3	C3, C6, C9, C11, C14, C25	1uF	6
4	C5, C8, C13, C16, C17	10nF	5
5	C19	100nF/25V	1
7	CON1	DF40C-60DP-0.4V(51)	1
9	FID4, FID5, FID6	Fiducial	3
10	IC1	DG4157EDL-T1-GE3	1
13	L1, L3	BLM18PG331SN1	2
14	L2	BLM18KG	1
16	L5	BLM18	1
17	R1	0R	1
18	R2, R16	100R	2
19	R3, R6, R24	10k	3
20	R4	N/A	1
21	R12	Dummy	1
24	R10	47k	1
25	R11	1k	1
30	U1	LTC6252IS6#TRMPBF	1

View from Bottom side (Scale 4:1)





A

B

C

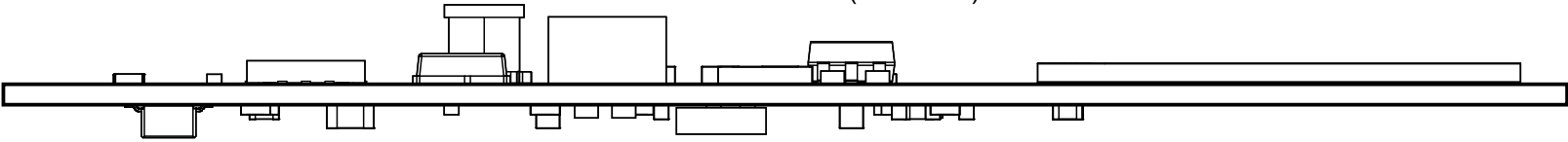
D

E

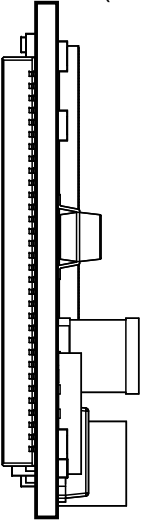
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1

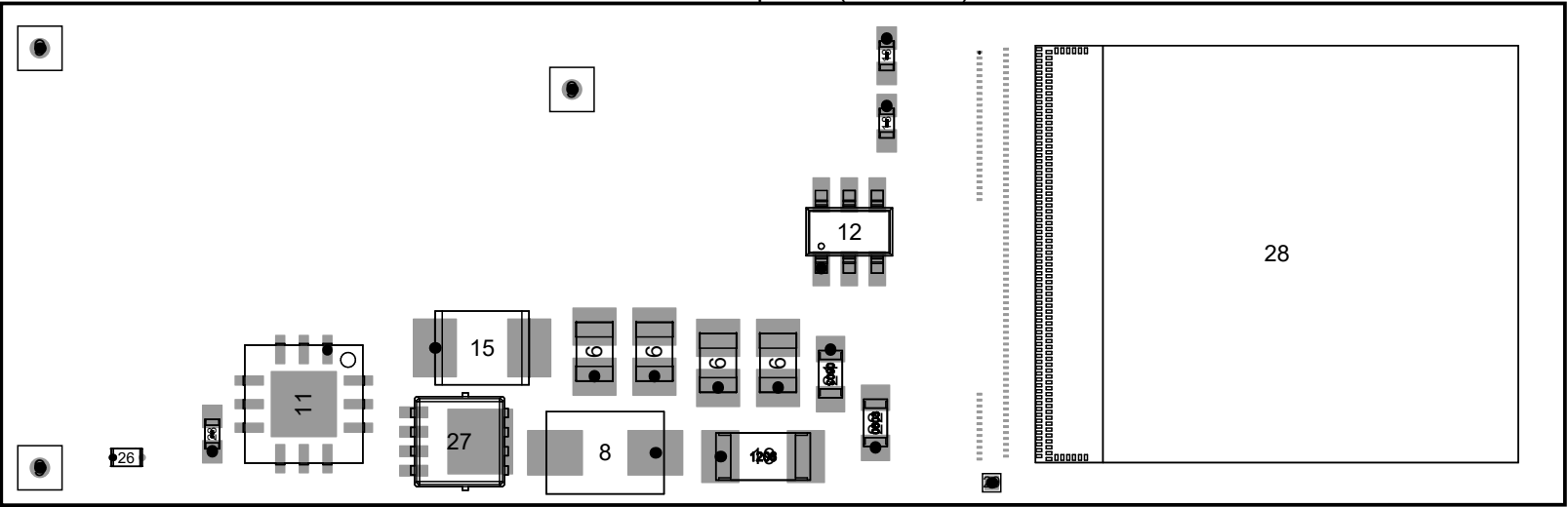
View from Front side (Scale 4:1)



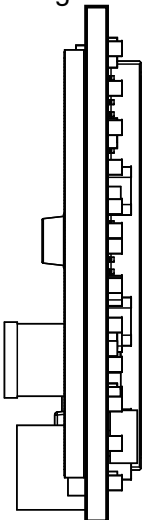
View from Left side (Scale 4:1)



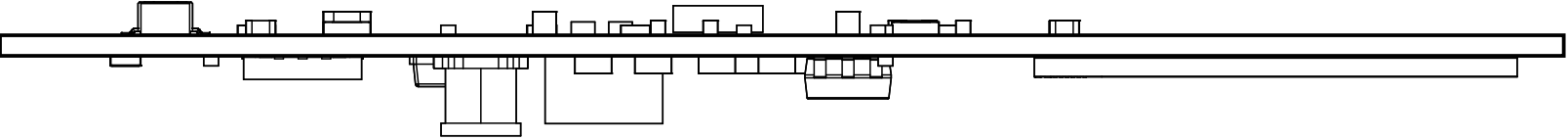
View from Top side (Scale 4:1)



View from Right side (Scale 4:1)



View from Back side (Scale 4:1)



A

B

C

D

E

4

4

