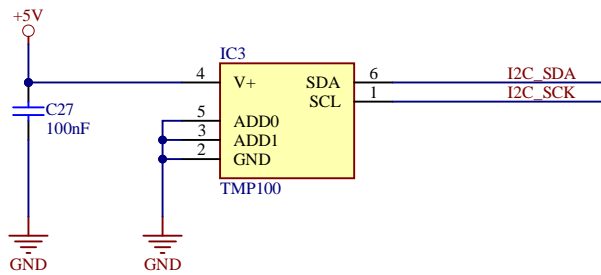


TERMINATION

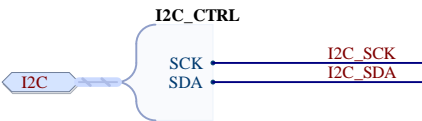
Design notes
=====

Is termin. necessary? (Is in the TPX2?)
-> no

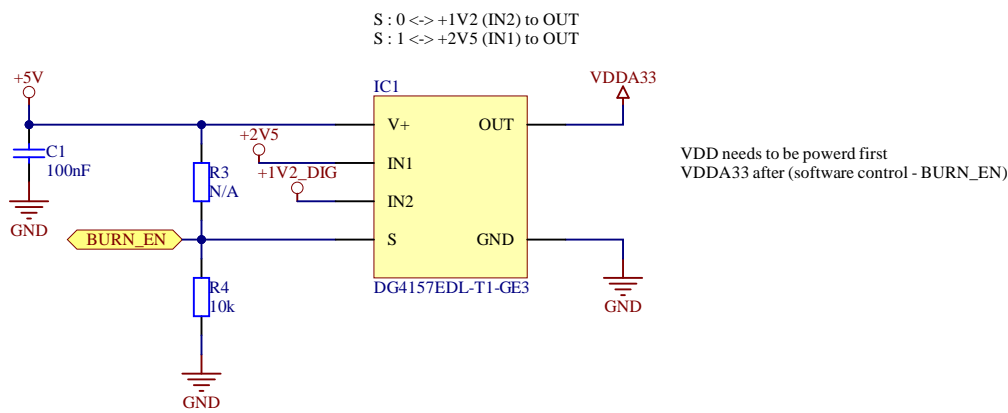
TEMP SENS



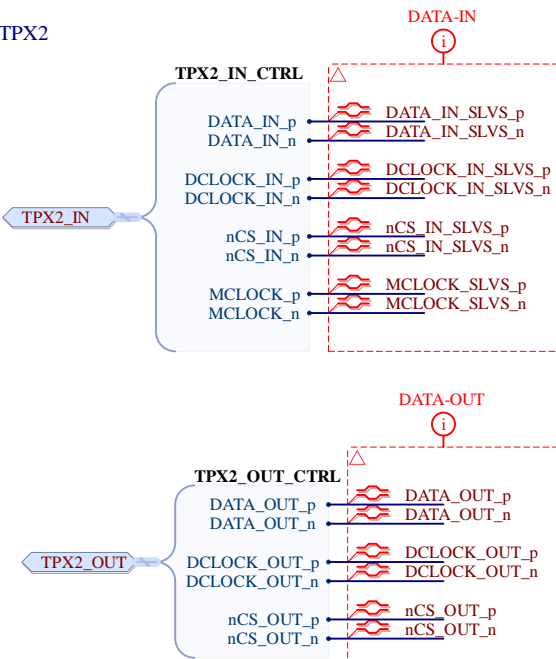
I2C



MUX VDDA33



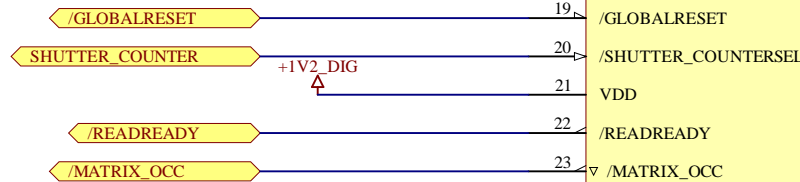
SPI TPX2



TPX2 CHIPBOARD

Design notes
=====

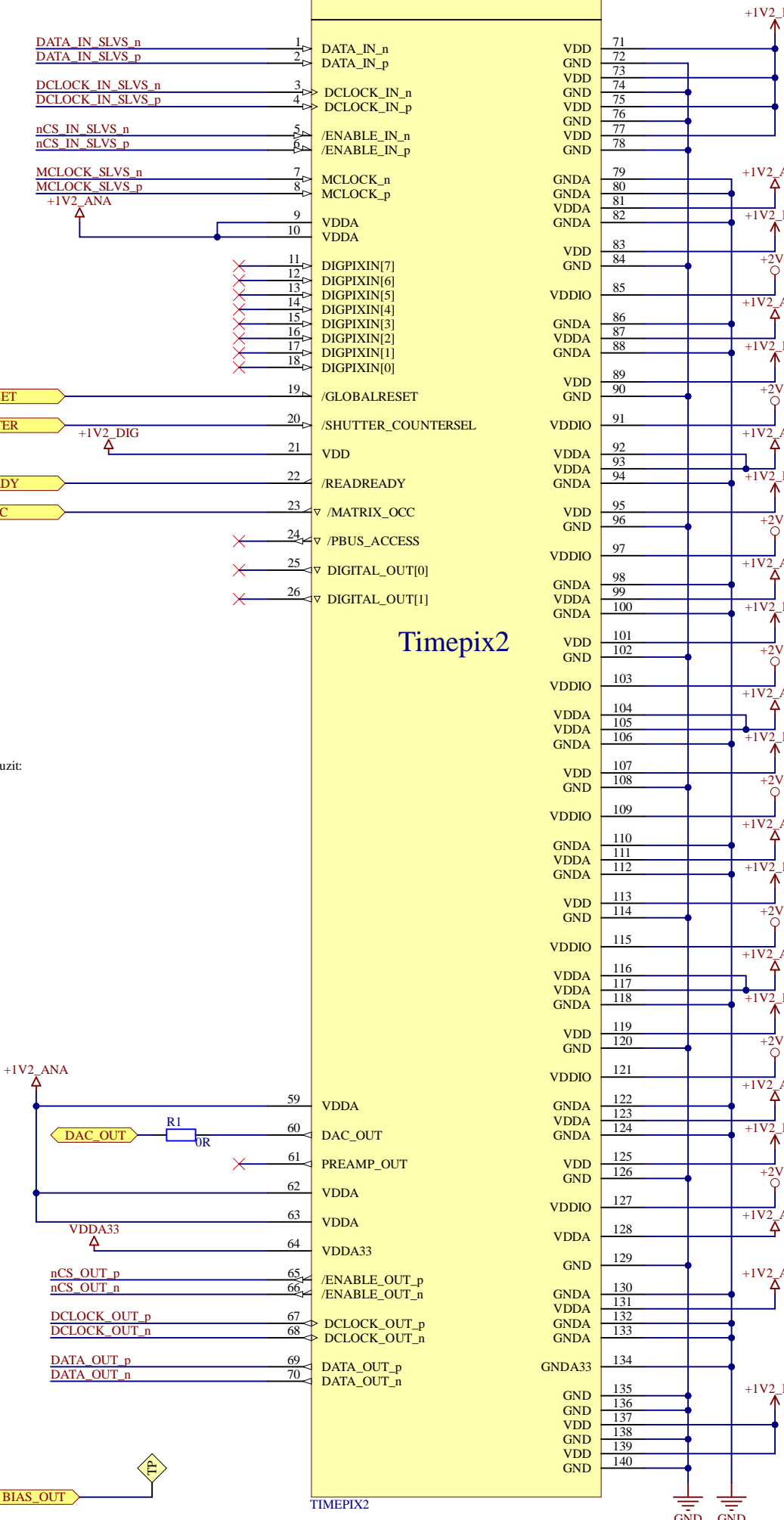
On GLOBALRESET should be in '0'
Is it necessary?



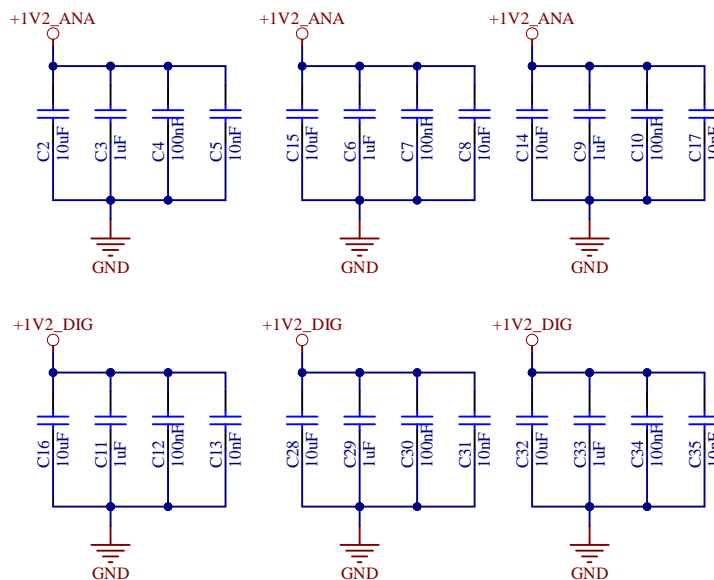
Design notes
=====

Pod chipboardem T2M_2 pouzit:
PGS EYG-A091201DM

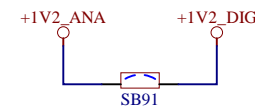
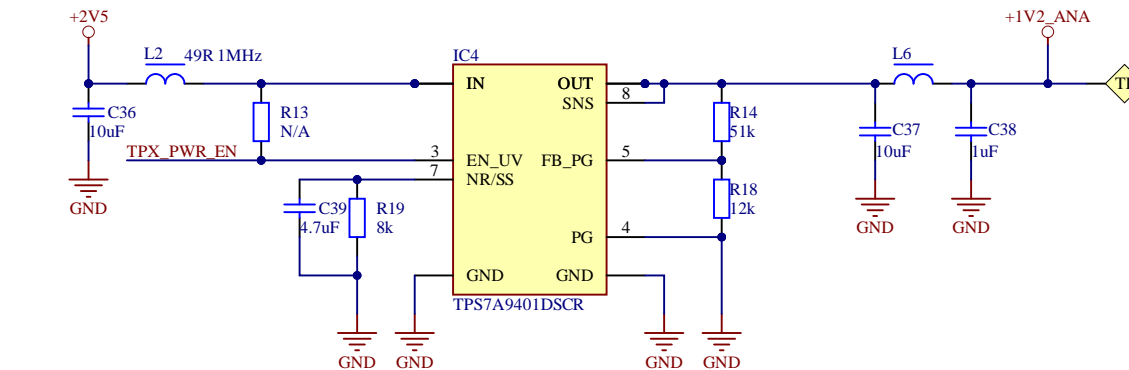
Timepix2



TPX PWR DECOUPLING

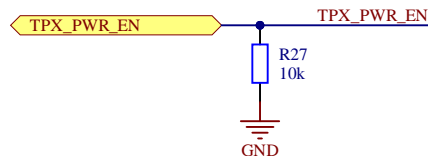
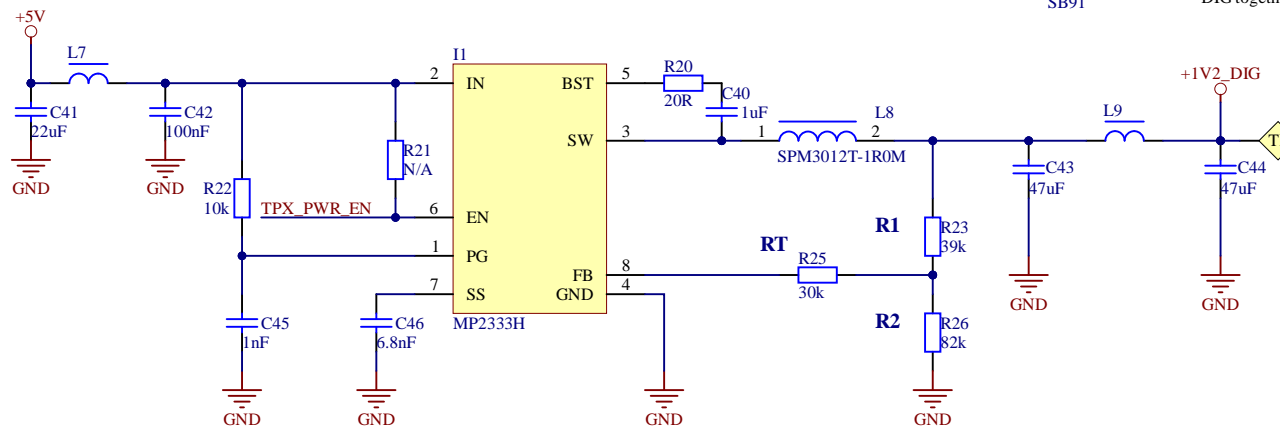



Title: TPX2.SchDoc	
Project: tpx2min_chipboard.PrjPcb	
	Author: Ondrej Pavlas
	Sheet: 2 of 5
Version: v2	

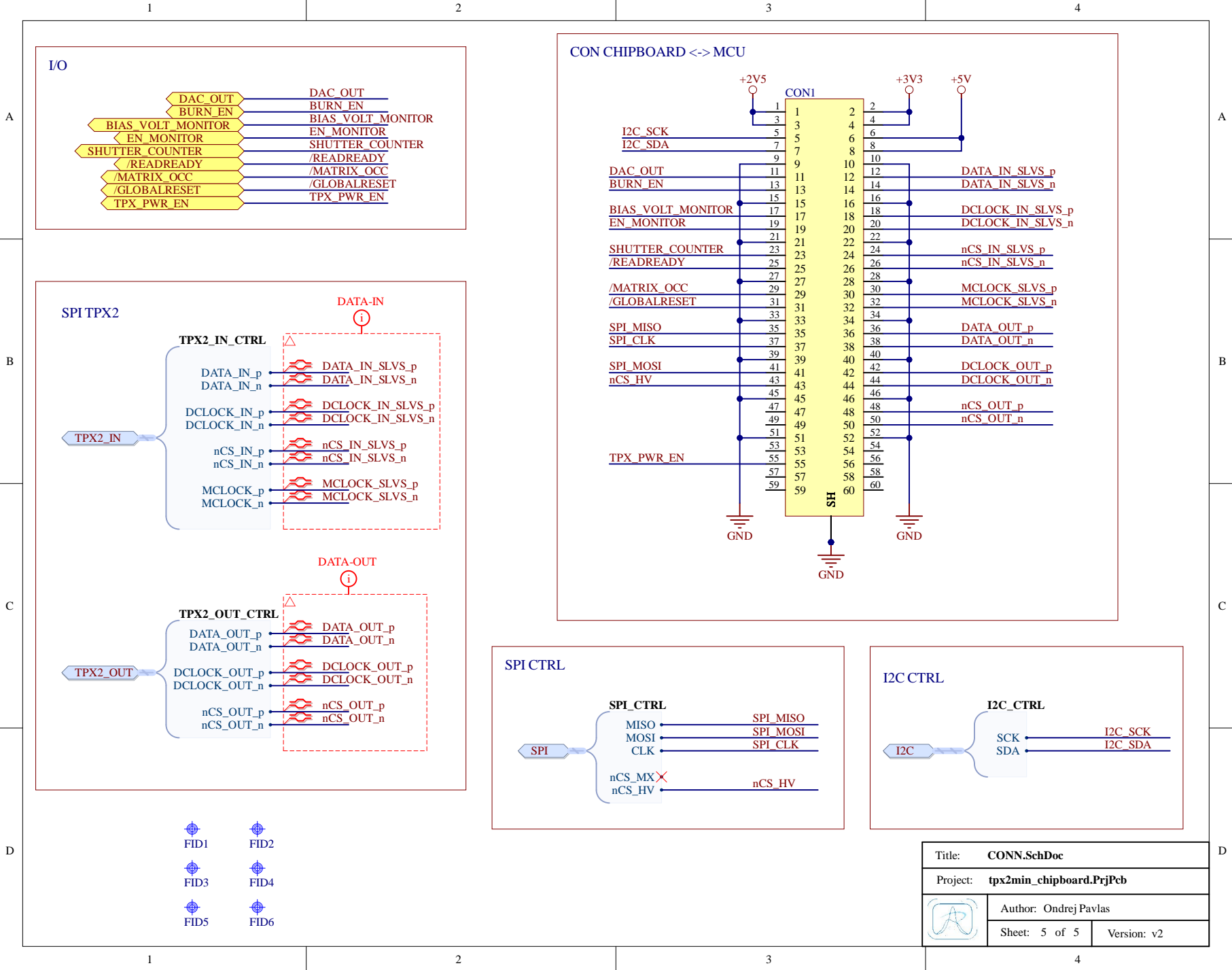


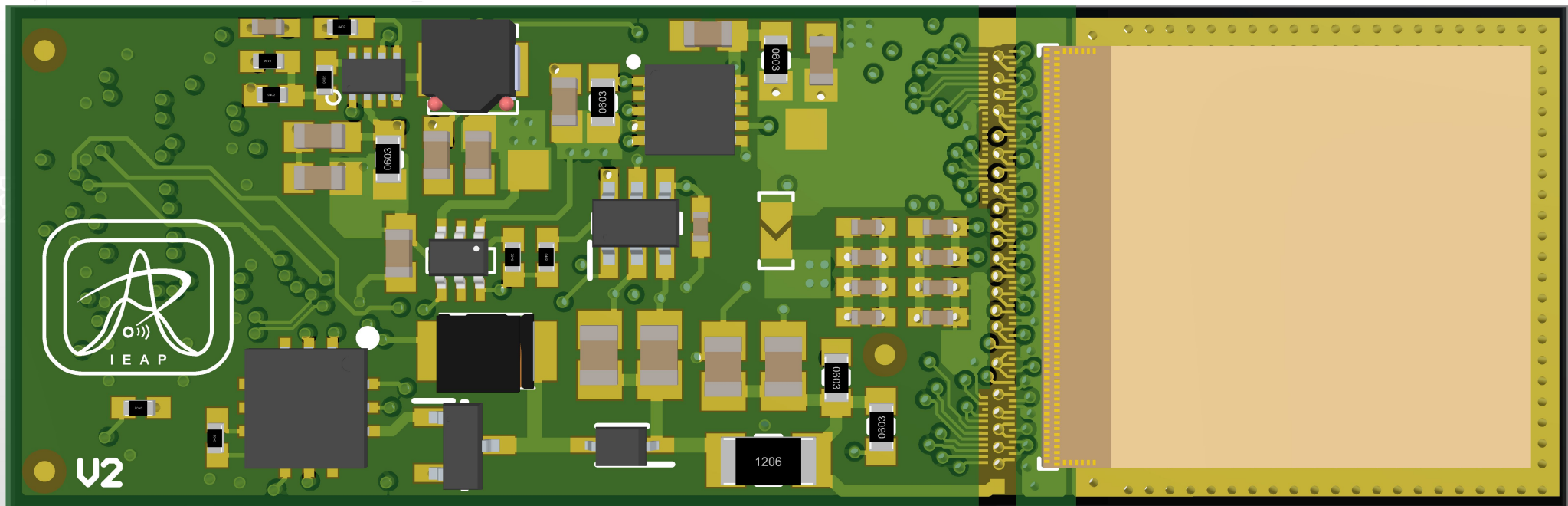
Design notes

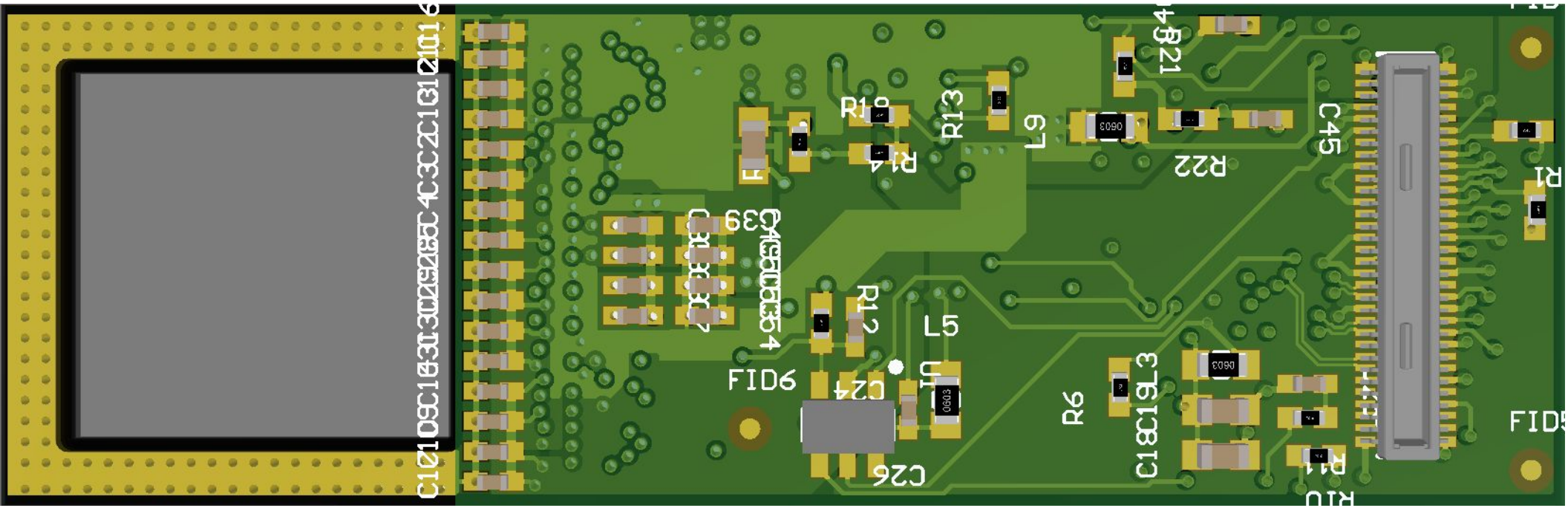
If possible to use only one regulator. Disconnect one regulator and connect ANA and DIG together



Title: PWR.SchDoc	
Project: tpx2min_chipboard.PrjPcb	
	Author: Ondrej Pavlas
	Sheet: 4 of 5 Version: v2



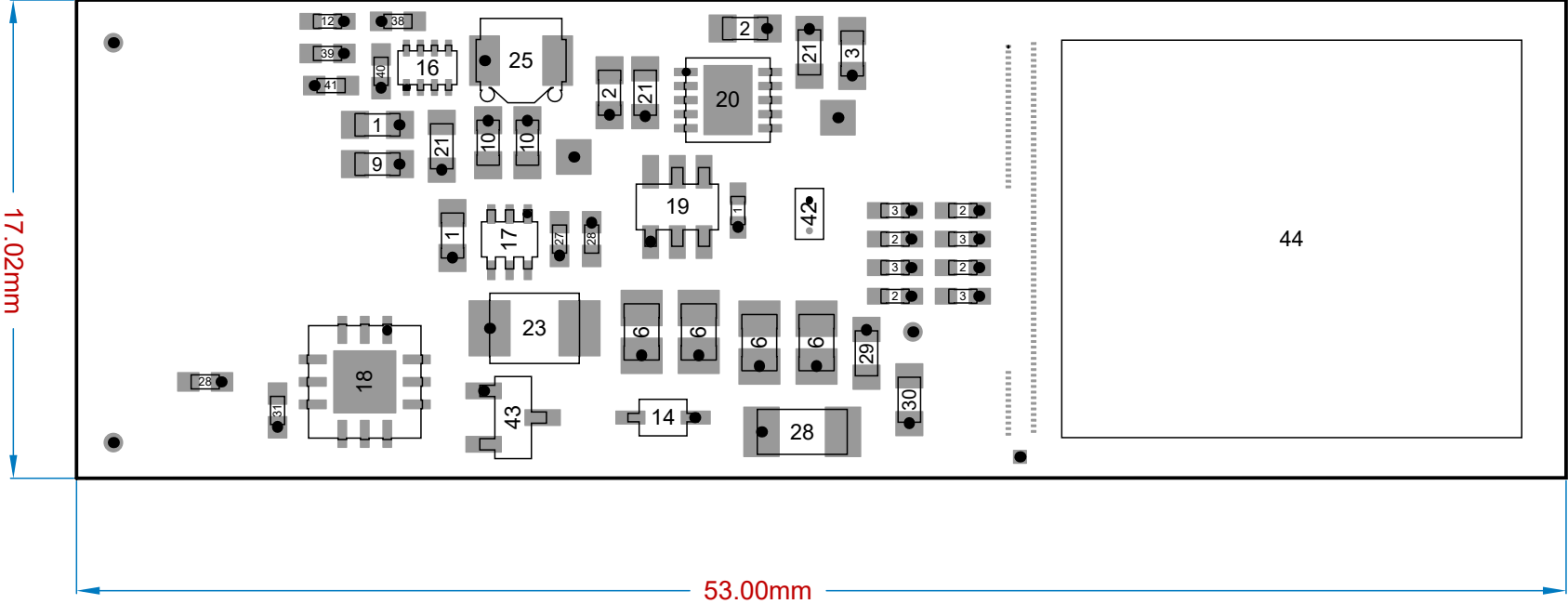




Bill Of Materials - TOP

Line #	Designator	Comment	Quantity
1	C1, C27, C42	100nF	3
2	C15, C32, C36, C37, C47, C51	10uF	6
3	C6, C33, C38, C48, C52	1uF	5
6	C20, C21, C22, C23	47nF/450V	4
9	C41	22uF	1
10	C43, C44	47uF	2
12	C46	6.8nF	1
14	D1	BAV3004WS-7	1
15	FID1, FID2, FID3	Fiducial	3
16	I1	MP2333H	1
17	IC1	DG4157EDL-T1-GE3	1
18	IC2	MAX1932ETC+	1
19	IC3	TMP100	1
20	IC4	TPS7A9401DSCR	1
21	L2, L6, L7	49R 1MHz	3
23	L4	744032101	1
25	L8	SPM3012T-1R0M	1
27	R3	N/A	1
28	R4, R5, R27	10k	3
29	R7	2M	1
30	R8	2.2M	1
31	R9	22k	1
38	R20	20R	1
39	R23	39k	1
40	R25	30k	1
41	R26	82k	1
42	SB91	SB_OFF	1
43	T1	TN2404K-T1-E3	1
44	TIMEPIX2	Timepix2_ASIC	1
45	TP1, TP2, TP3	TP	3

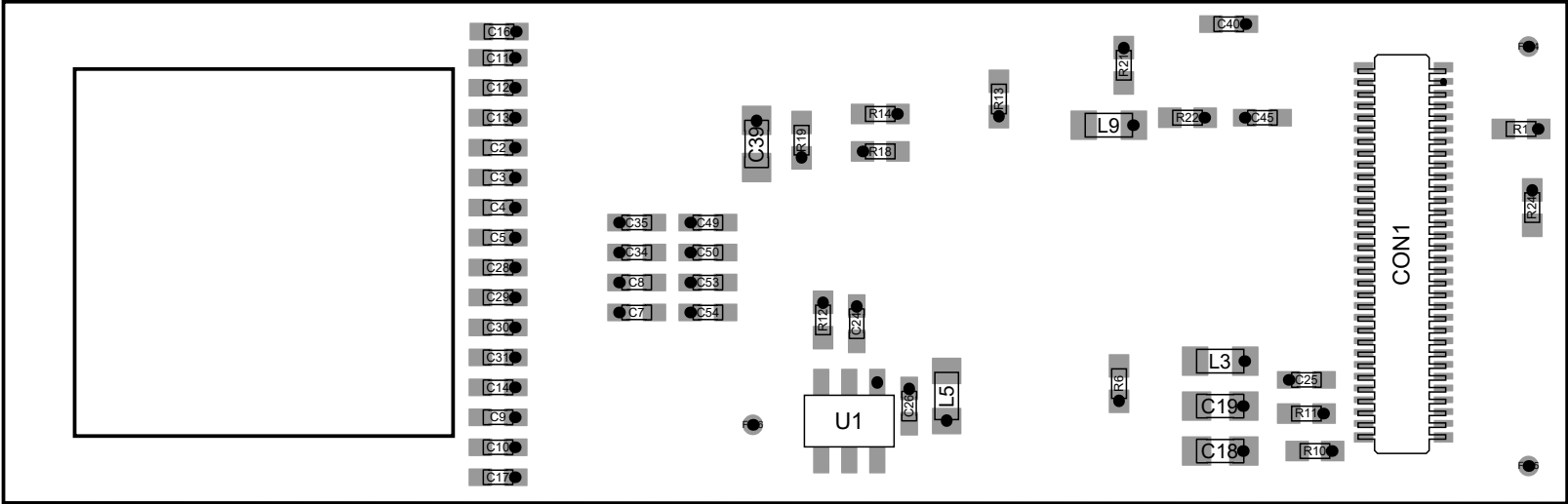
View from Top side (Scale 4:1)



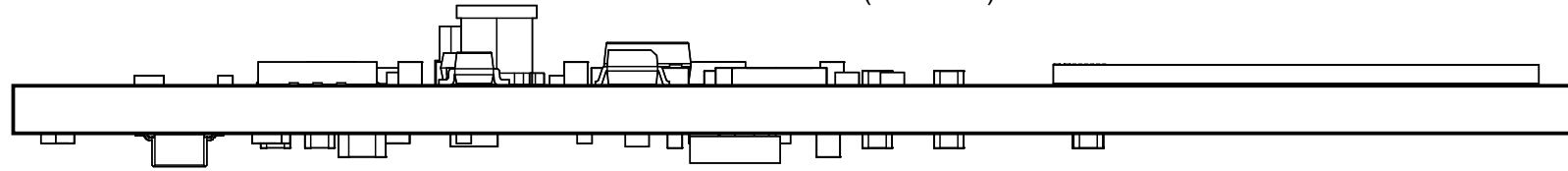
Bill Of Materials - BOT

Line #	Designator	Comment	Quantity
1	C4, C7, C10, C12, C24, C26, C30, C34, C49, C53	100nF	10
2	C2, C14, C16, C18, C28	10uF	5
3	C3, C9, C11, C29, C40	1uF	5
4	C5, C8, C13, C17, C31, C35, C50, C54	10nF	8
5	C19	100nF/25V	1
7	C25	220nF	1
8	C39	4.7uF	1
11	C45	1nF	1
13	CON1	DF40C-60DP-0.4V(51)	1
15	FID4, FID5, FID6	Fiducial	3
21	L9	49R 1MHz	1
22	L3	BLM18PG331SN1	1
24	L5	BLM18	1
26	R1	0R	1
27	R13, R21	N/A	2
28	R6, R22, R24	10k	3
32	R10	100k	1
33	R11	18k	1
34	R12	43k	1
35	R14	51k	1
36	R18	12k	1
37	R19	8k	1
46	U1	LTC6252IS6#TRMPBF	1

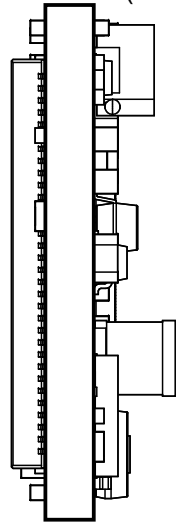
View from Bottom side (Scale 4:1)



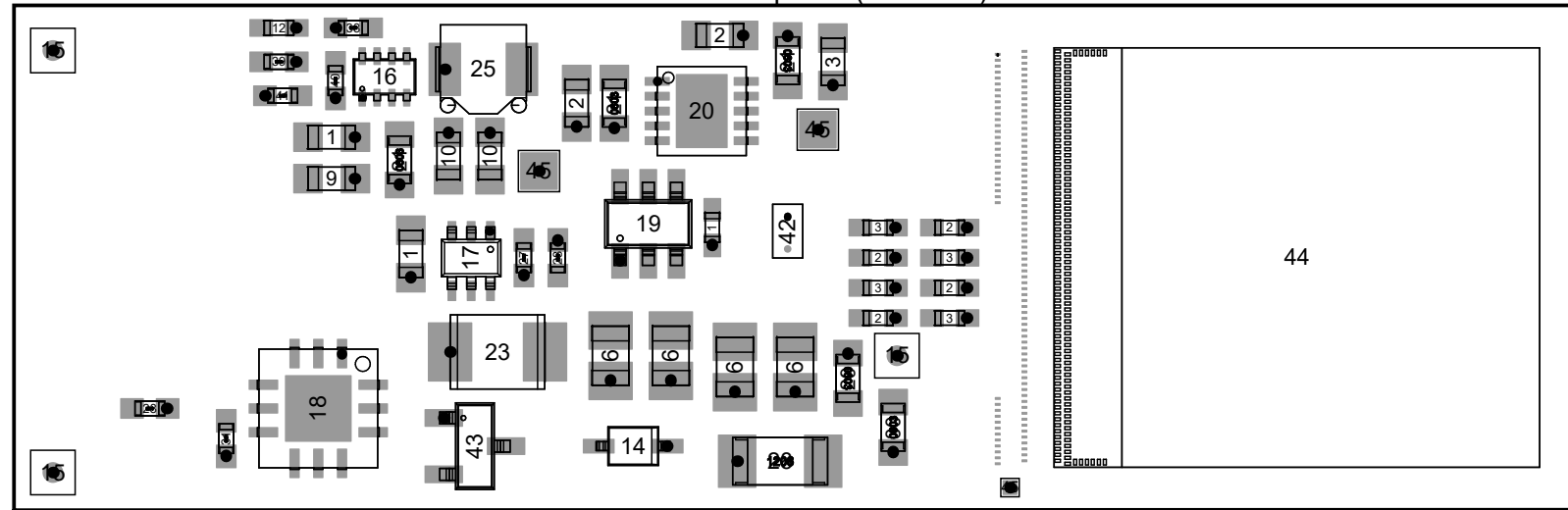
View from Front side (Scale 4:1)



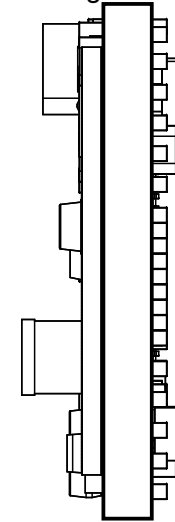
View from Left side (Scale 4:1)



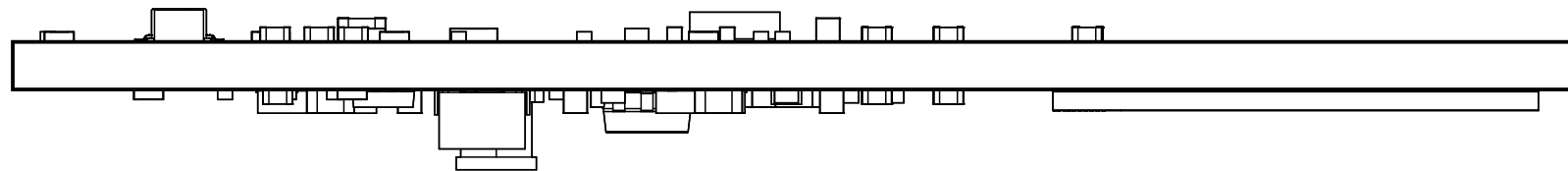
View from Top side (Scale 4:1)



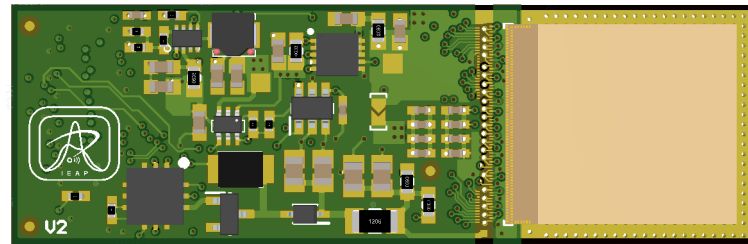
View from Right side (Scale 4:1)



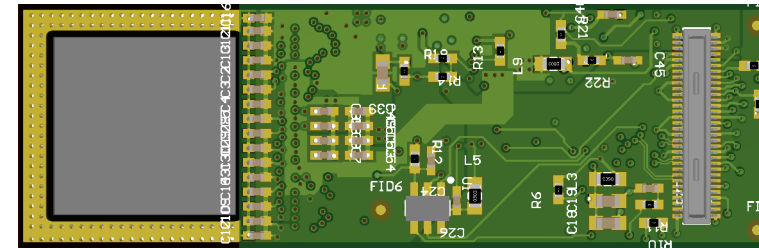
View from Back side (Scale 4:1)



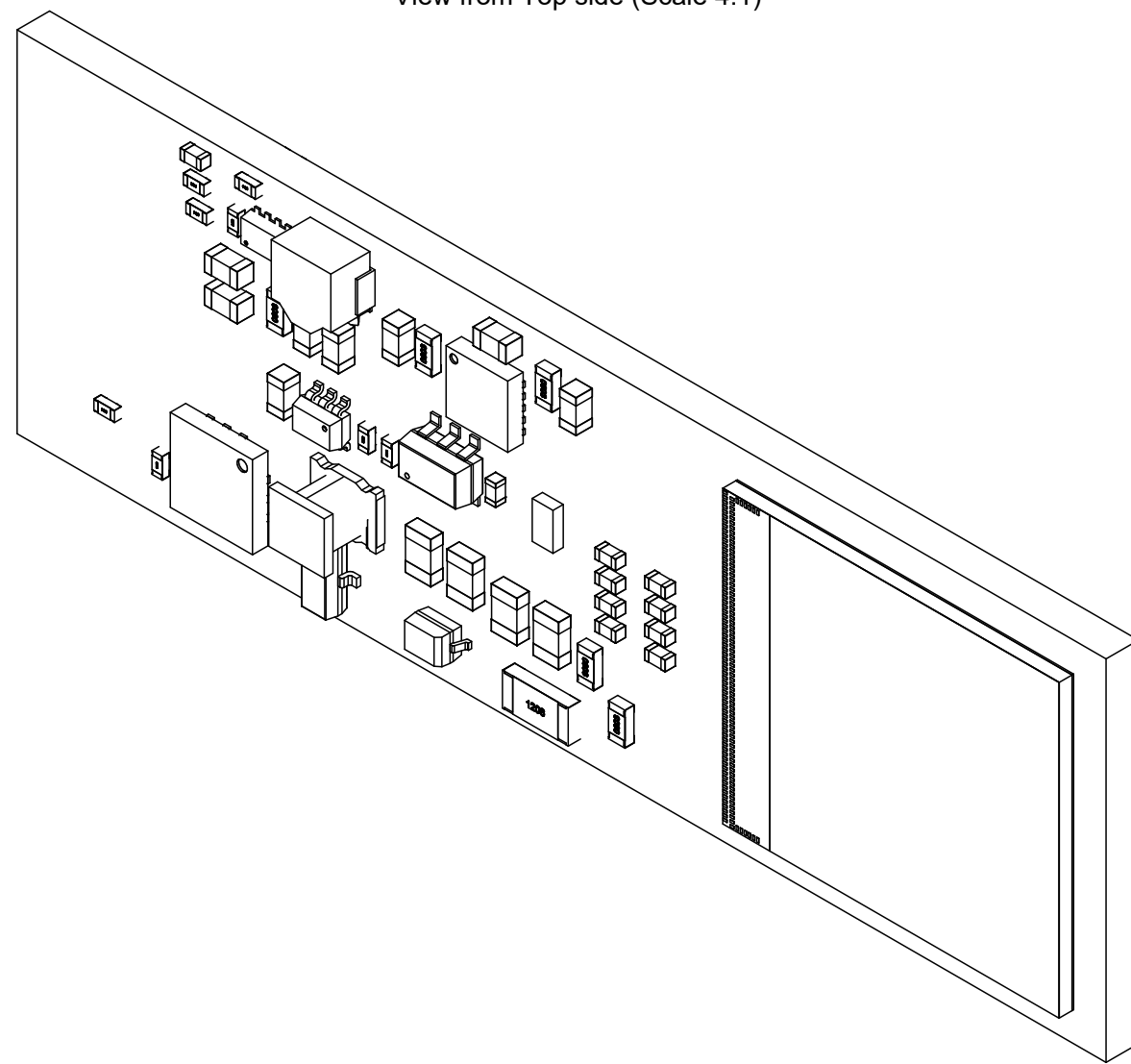
Realistic View - TOP



Realistic View - BOT



View from Top side (Scale 4:1)



View from Bottom side (Scale 4:1)

