

A

A

B

B

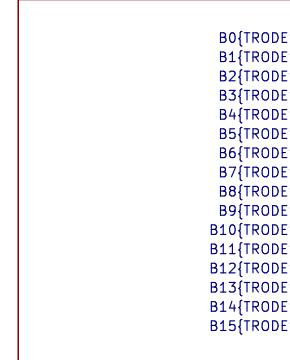
C

C

D

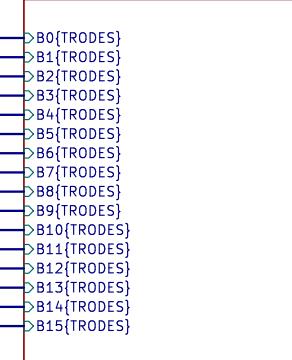
D

Test Signal Interface



File: test-signal-interface.kicad\_sch

Headstage Interface



File: headstage-interface.kicad\_sch

SN

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Sheet: Root

File: ephys-test-module-64.kicad\_sch

**Title: Ephys Test Module - 64 Channel (Hirose DF40C)**

Size: A4 Date:

KiCad E.D.A. 9.0.5

Rev: D  
Id: 1/3

1 2 3 4 5 6

A

A

B

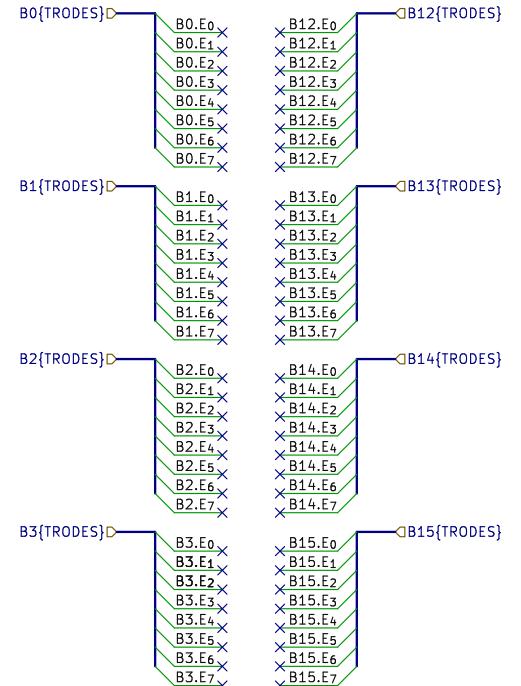
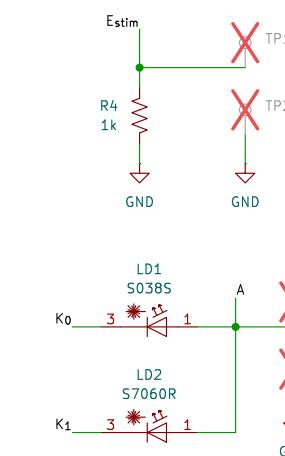
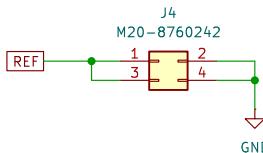
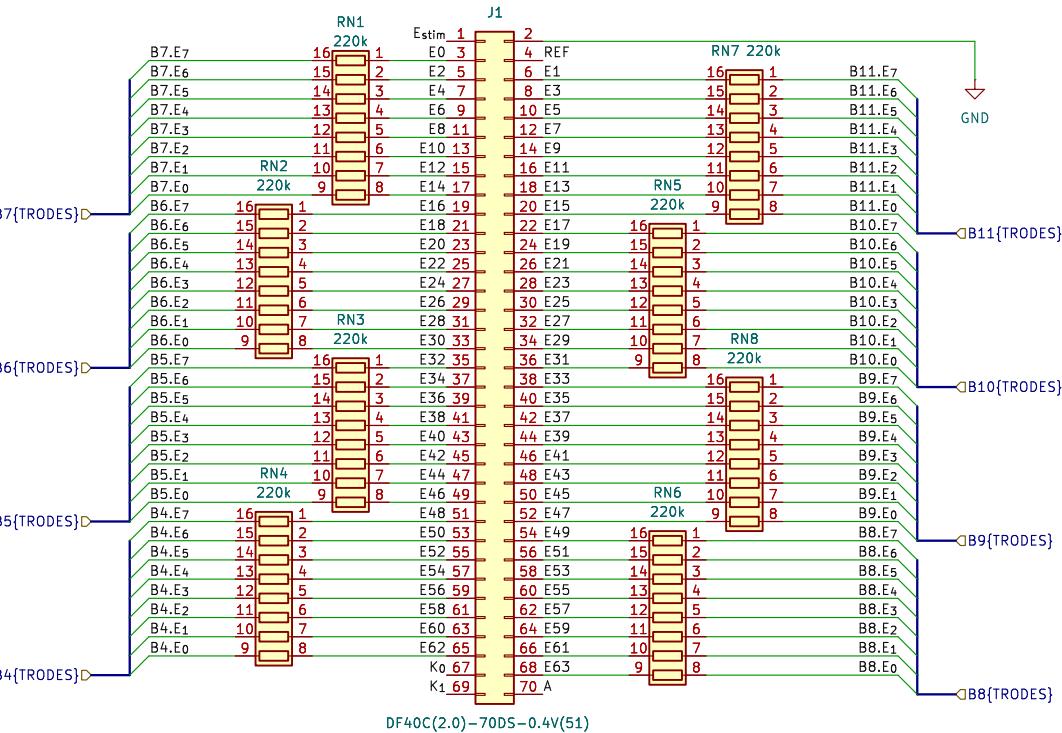
B

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C

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D



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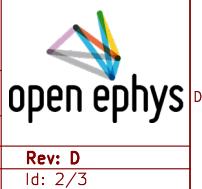
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**Title: Ephys Test Module – 64 Channel (Hirose DF40C)**

Size: A4 Date:

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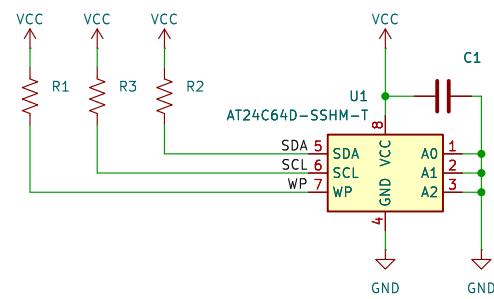
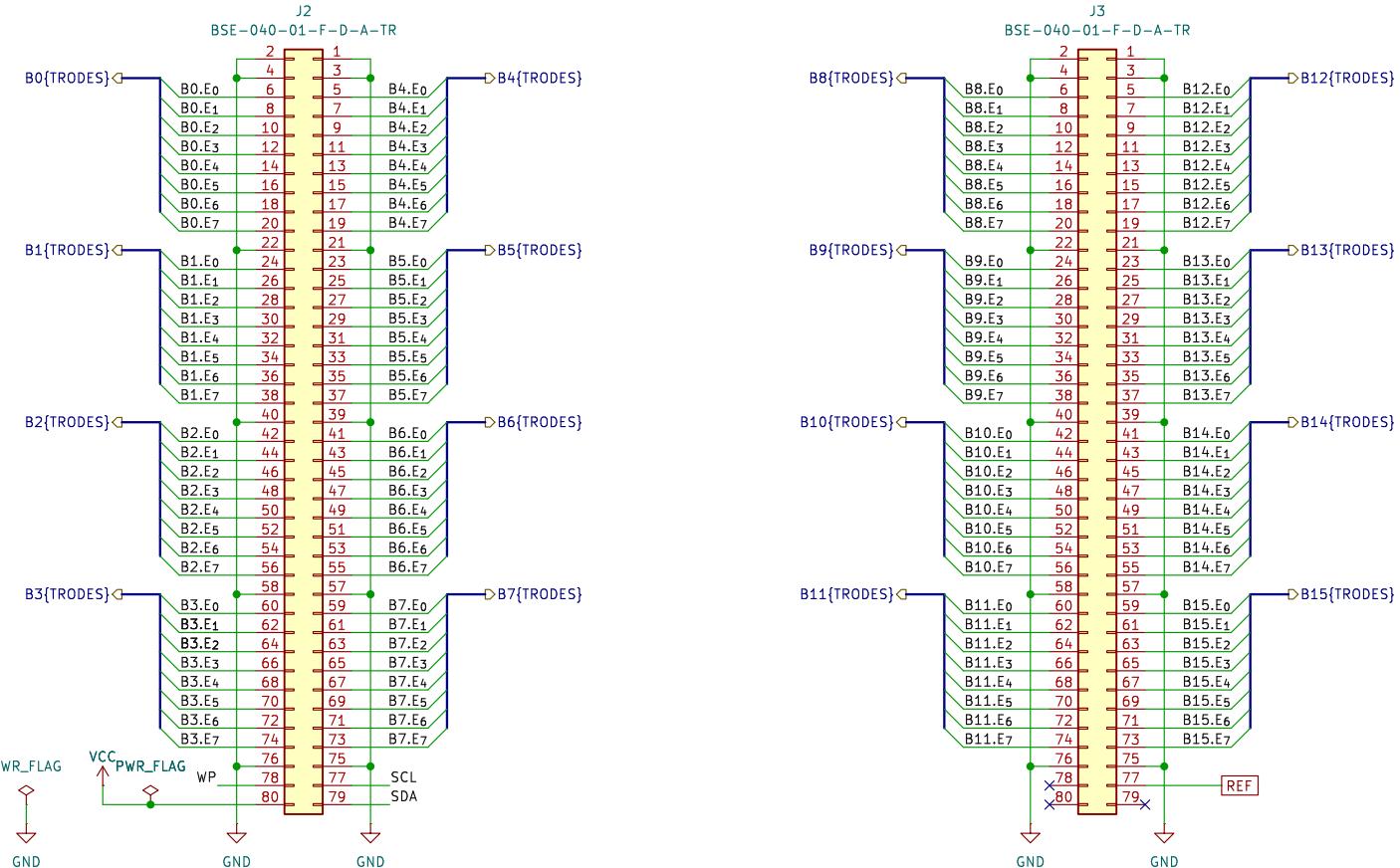


Rev: D

Id: 2/3

1 2 3 4 5 6

1 2 3 4 5 6



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Sheet: Test Signal Interface  
File: test-signal-interface.kicad\_sch

**Title: Ephys Test Module – 64 Channel (Hirose DF40C)**

Size: A4 Date:  
KiCad E.D.A. 9.0.5

  
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Rev: D  
Id: 3/3

1 2 3 4 5 6