

Jonathan P. Newman  
Open Ephys, Inc

Sheet:  
File: headstage-neuropix2e.kicad\_sch

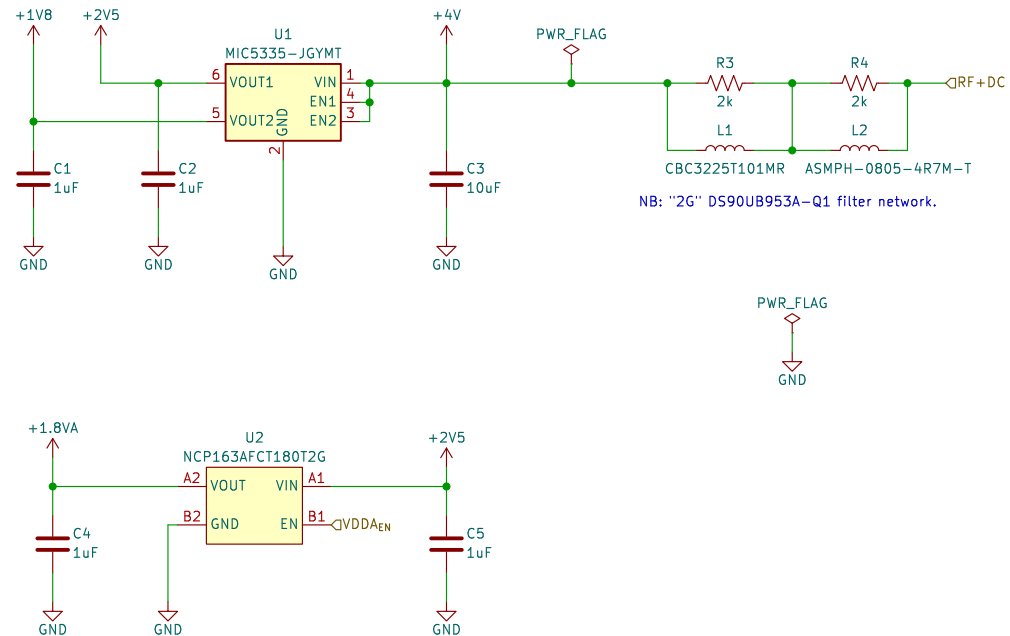
**Title: ONIX Neuropixels 2.0 Economical Headstage**

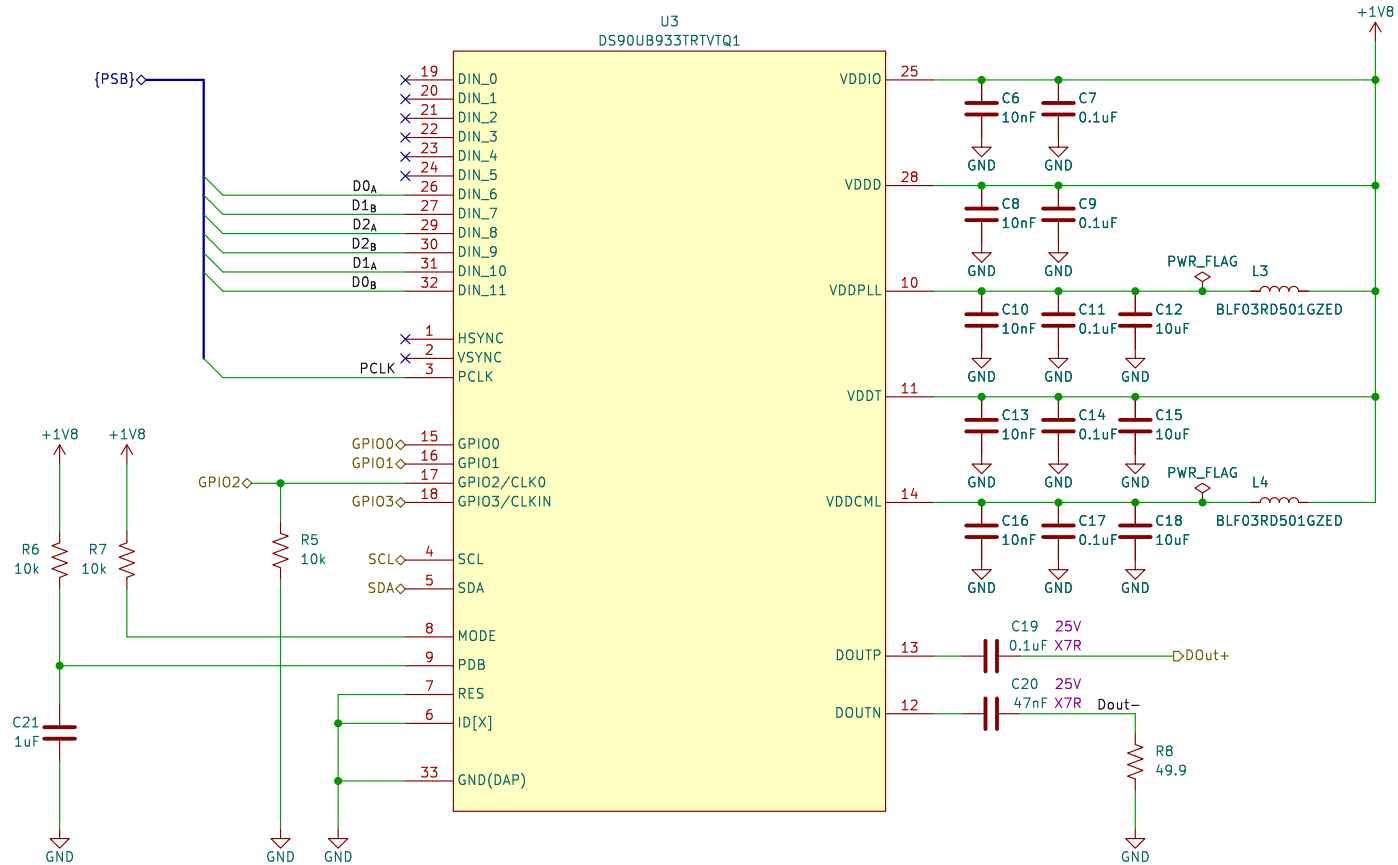
Size: A4  
KiCad E.D.A. kicad (7.0.0)

Date:



Rev: B  
Id: 1/5





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Sheet: Serializer

File: serializer.kicad\_sch

**Title: ONIX Neuropixels 2.0 Economical Headstage**

Size: A4 Date: 2021-12-16

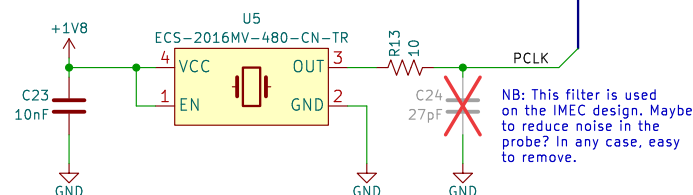
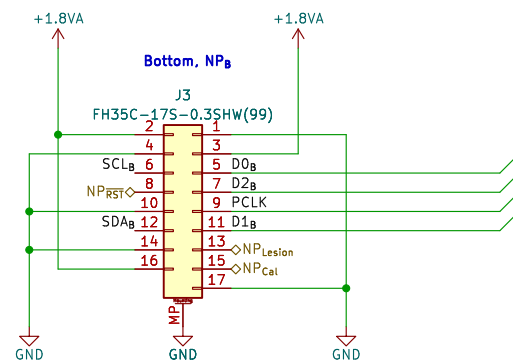
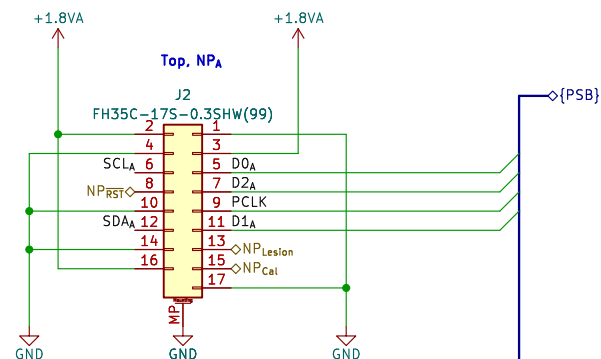
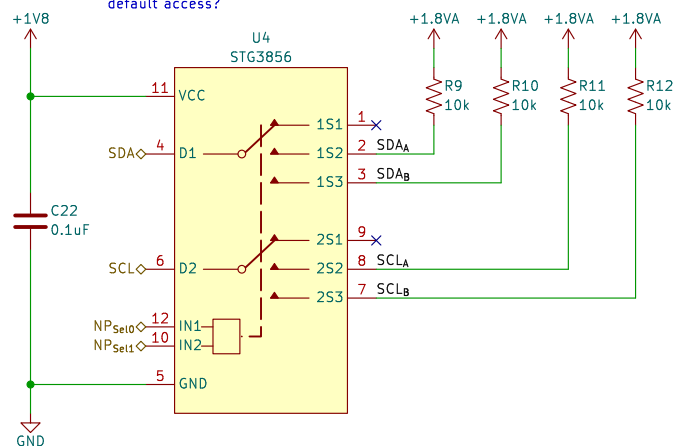
KiCad E.D.A. kicad (7.0.0)



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NB: This does not make sense to me but is copied from IMEC headstage since I dont know the idiosyncracies of the device, e.g. maybe i2c must be disconnected for noise reasons. Still does not explain double pullup (i.e why not put the nominal i2c bus as unused output of the switch). Maybe default access?



NB: This filter is used on the IMEC design. Maybe to reduce noise in the probe? In any case, easy to remove.

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Sheet: Neuropixels 2.0

File: neuropixels-2.0.kicad\_sch

**Title: ONIX Neuropixels 2.0 Economical Headstage**

Size: A4

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