

Jonathan P. Newman
Open Ephys, Inc

Sheet:
File: headstage-neuropix2e.kicad_sch

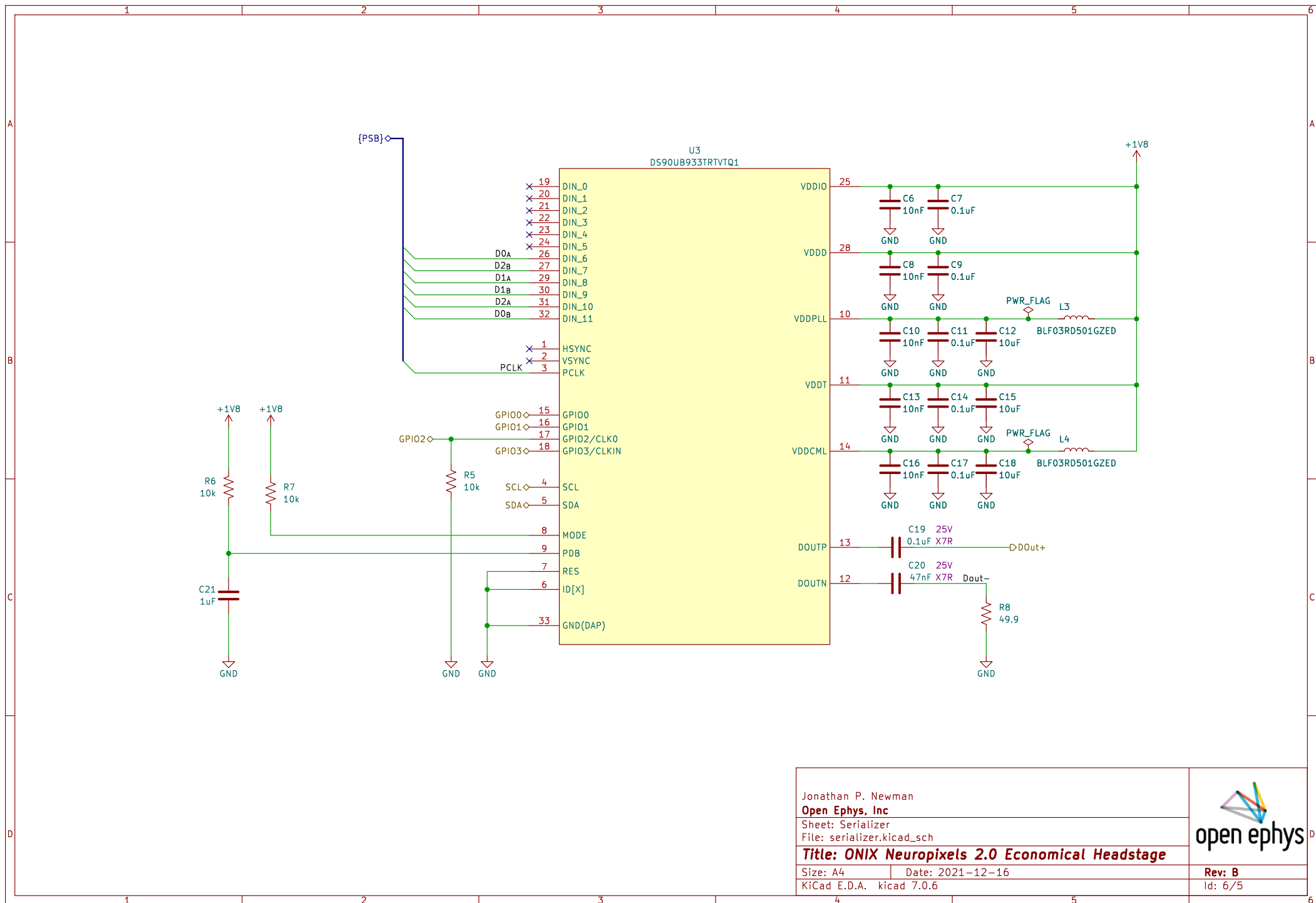
Title: ONIX Neuropixels 2.0 Economical Headstage


Size: A4
KiCad E.D.A. kicad 7.0.6

Date:

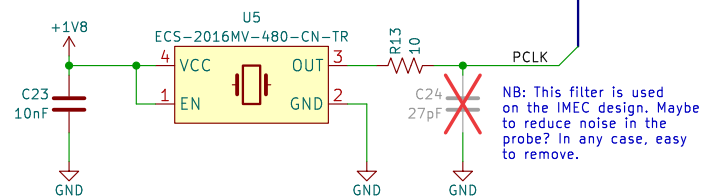
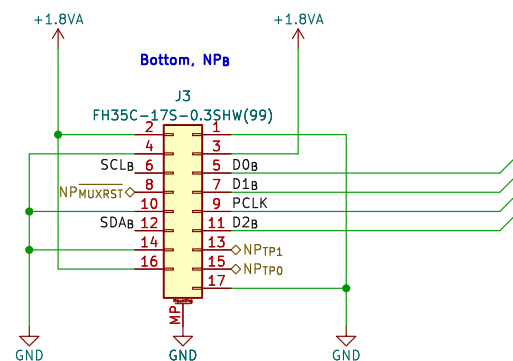
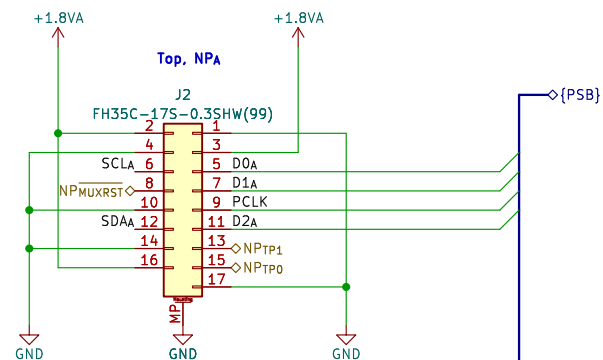
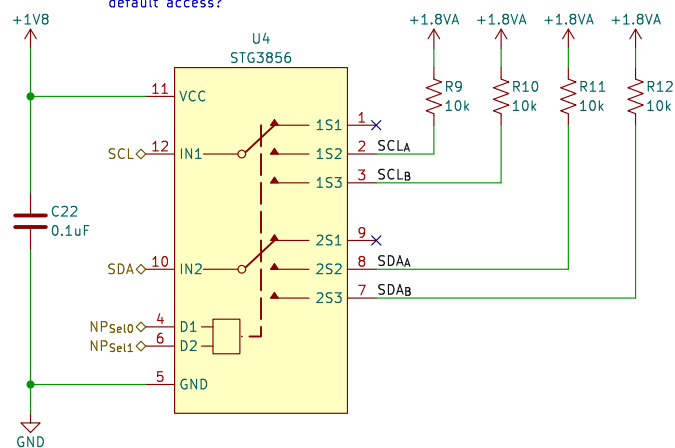


Rev: B
Id: 1/5



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Sheet: Serializer		
File: serializer.kicad_sch		
Title: ONIX Neuropixels 2.0 Economical Headstage		
Size: A4	Date: 2021-12-16	Rev: B
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NB: This does not make sense to me but is copied from IMEC headstage since I dont know the idiosyncracies of the device. e.g. maybe I2C must be disconnected for noise reasons. Still does not explain double pullup (i.e why not put the nominal I2C bus as unused output of the switch). Maybe default access?



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Sheet: Neuropixels 2.0
File: neuropixels-2.0.kicad_sch

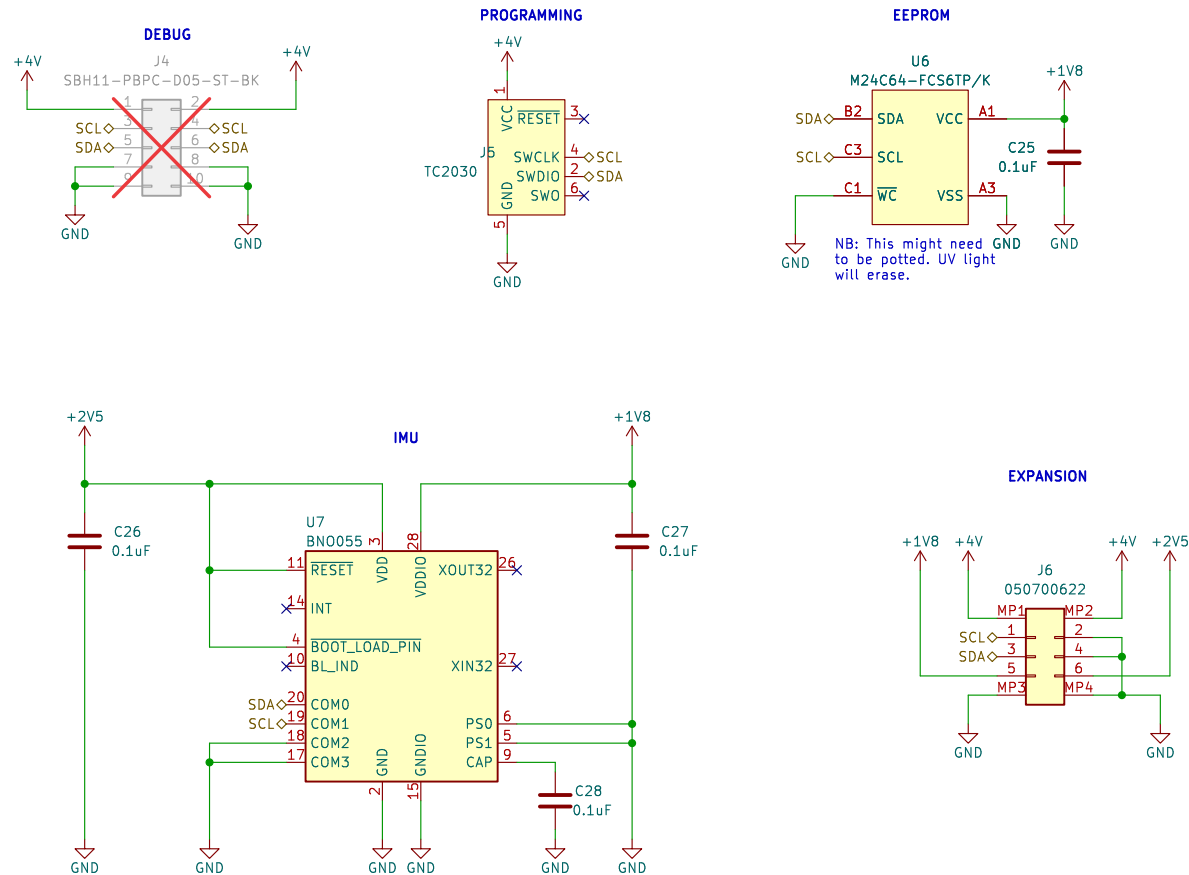
Title: ONIX Neuropixels 2.0 Economical Headstage


Size: A4
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Sheet: Peripherals		
File: peripherals.kicad_sch		
Title: ONIX Neuropixels 2.0 Economical Headstage		
Size: A4	Date:	Rev: B
KiCad E.D.A.	kicad 7.0.6	Id: 7/5