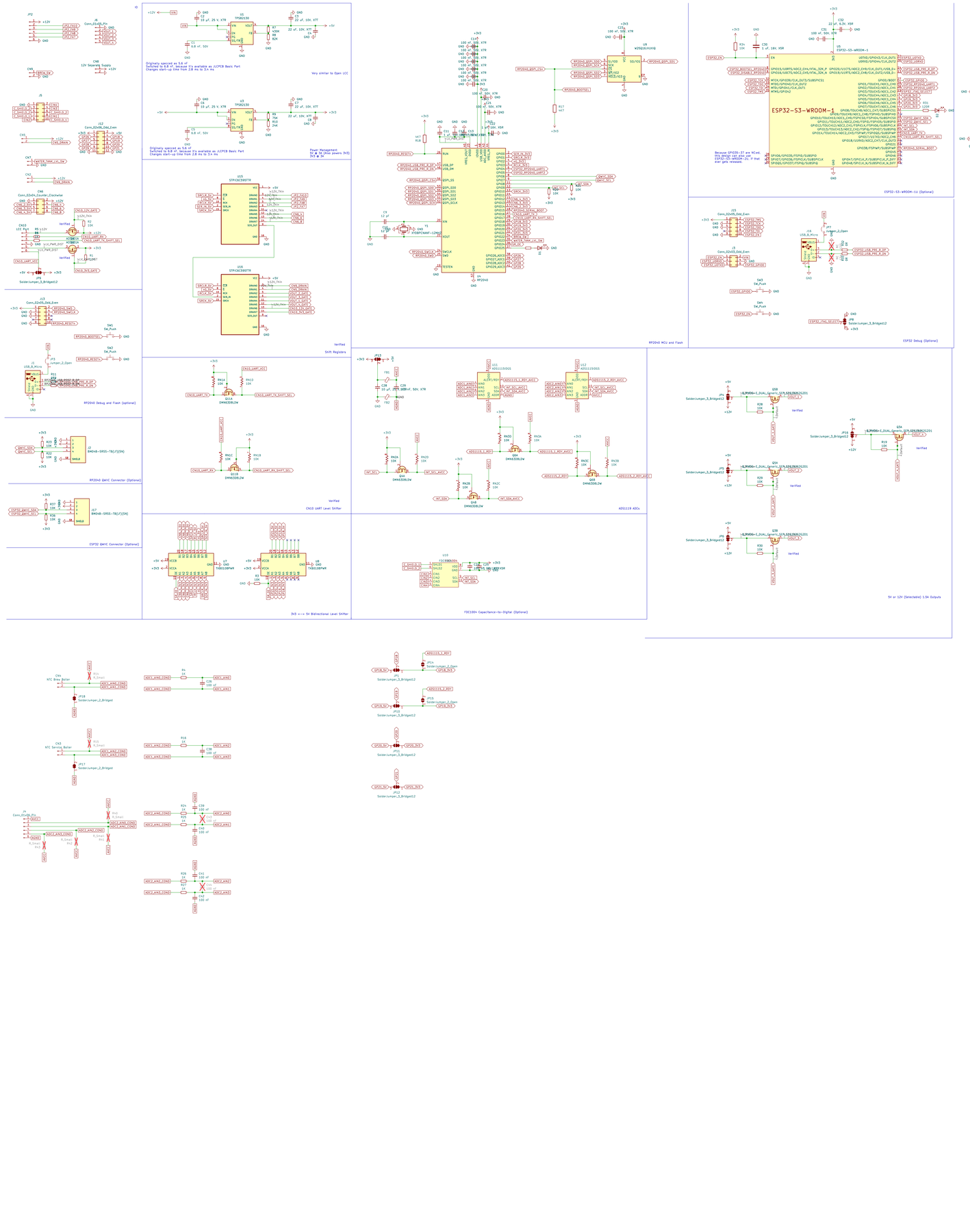
Originally specced as 5.6 nF
Changed to 8.2 nF because it's available as JLCPCB Basic Part
Changes start-up time from 28 ms to 34 ms.
Very similar to Open LCC
Originally specced as 5.6 nF
Changed to 8.2 nF because it's available as JLCPCB Basic Part
Changes start-up time from 28 ms to 34 ms.
Power Management
5V & 3V_Vbus powers 3V3
3V3 ⇐ 5V
ESP32-S3-WROOM-1
Because GPIO35-37 are NC-ed,
this design can also use
ESP32-S3-WROOM-2U, if that
ever gets released.
ESP32-S3-WROOM-1U (Optional)
RP2040 MCU and Flash
ESP32 Debug (Optional)
Verified
Shift Registers
RP2040 Debug and Flash (Optional)
RP2040 QWIC Connector (Optional)
ESP32 QWIC Connector (Optional)
CN10 UART Level Shifter
AD51119 ADCs
3V3 <-> 5V Bidirectional Level Shifter
FDC1004 Capacitance-to-Digital (Optional)
5V or 12V (Selectable) 1.5A Outputs