

X3-Class HiPerFET™ **Power MOSFET**

IXFK400N15X3 IXFX400N15X3

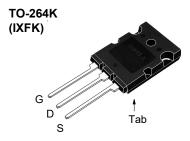
N-Channel Enhancement Mode Avalanche Rated

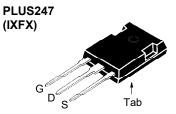


Symbol	Test Conditions	Maximum R	atings
V _{DSS}	T _J = 25°C to 150°C	150	V
V _{DGR}	$T_{_{\rm J}}$ = 25°C to 150°C, $R_{_{\rm GS}}$ = 1M Ω	150	V
V _{GSS}	Continuous	±20	V
V _{GSM}	Transient	±30	V
I _{D25}	T _c = 25°C (Chip Capability)	400	A
I _{L(RMS)}	External Lead Current Limit	160	Α
I _{DM}	$T_{_{\rm C}}$ = 25°C, Pulse Width Limited by $T_{_{\rm JM}}$	900	Α
I _A	T _c = 25°C	200	A
E _{AS}	T _c = 25°C	3.5	J
dv/dt	$I_{S} \leq I_{DM}, V_{DD} \leq V_{DSS}, T_{J} \leq 150^{\circ}C$	50	V/ns
P_{D}	T _C = 25°C	1250	W
T _J		-55 +150	°C
T _{JM}		150	°C
T _{stg}		-55 +150	°C
T _L	Maximum Lead Temperature for Soldering	300	°C
-	1.6 mm (0.062 in.) from Case for 10s		
M _d	Mounting Torque (TO-264K)	1.13/10	Nm/lb.in
F _c	Mounting Force (PLUS247)	20120 /4.527	N/lb
Weight	TO-264K	10	g
-	PLUS247	6	g

Symbol Test Conditions C		Char	aracteristic Values			
$(T_J = 25^{\circ}C)$	Unless Otherwise Specified)	Min.	Тур.	Max	ζ	
BV _{DSS}	$V_{GS} = 0V, I_{D} = 3mA$	150			V	
V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 8mA$	2.5		4.5	V	
GSS	$V_{GS} = \pm 20V, V_{DS} = 0V$			±200	nA	
I _{DSS}	$V_{DS} = V_{DSS}, V_{GS} = 0V$ $T_{.} = 125^{\circ}C$			25 1.5	μA mA	
R _{DS(on)}	$V_{GS} = 10V, I_{D} = 0.5 \cdot I_{D25}, Note 1$			3	mΩ	

150V 400A $3m\Omega$





G = Gate	D	=	Drain
S = Source	Tab	=	Drain

Features

- International Standard Packages
- Low R_{DS(ON)} and Q_G
 Avalanche Rated
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode **Power Supplies**
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

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Symbol Test Conditions Char		Chara	acteristic Values		
$(T_{J} = 25^{\circ}C, U)$	Inless Otherwise Specified)	Min.	Тур.	Max	
g _{fs}	$V_{DS} = 10V, I_{D} = 60A, Note 1$	85	145	S	
R_{Gi}	Gate Input Resistance		2.15	Ω	
C _{iss}			23.7	nF	
C _{oss}	$V_{GS} = 0V$, $V_{DS} = 25V$, $f = 1MHz$		3730	PF	
C _{rss}			140	pF	
	Effective Output Capacitance				
C _{o(er)}	Energy related $\bigvee_{GS} = 0V$		2200	pF	
$C_{o(tr)}$	Time related $\int_{DS} V_{DS} = 0.8 \cdot V_{DSS}$		5330	pF	
t _{d(on)}	Resistive Switching Times		36	ns	
t,	G		30	ns	
t _{d(off)}	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 0.5 \cdot I_{D25}$		210	ns	
\mathbf{t}_{f}	$R_{_{\rm G}} = 1\Omega$ (External)		19	ns	
$Q_{g(on)}$			365	nC	
Q _{gs}	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 0.5 \cdot I_{D25}$		103	nC	
\mathbf{Q}_{gd}^{gd}			87	nC	
R _{thJC}				0.10 °C/W	
R _{thCS}			0.15	°C/W	

Source-Drain Diode

			cteristic Values		
$(T_J = 25^{\circ}C, l)$	Jnless Otherwise Specified)	Min.	Тур.	Max	
I _s	$V_{GS} = 0V$			400	Α
I _{SM}	Repetitive, Pulse Width Limited by $\mathrm{T_{_{JM}}}$			1600	Α
V _{SD}	$I_F = 100A$, $V_{GS} = 0V$, Note 1			1.4	V
$\left. egin{array}{l} \mathbf{t}_{rr} & \ \mathbf{Q}_{RM} \ \mathbf{I}_{RM} & \end{array} ight. ight.$	$I_F = 150A$, -di/dt = 100A/ μ s $V_R = 100V$		132 580 8.8		ns nC A

Note 1. Pulse test, $t \le 300 \mu s$, duty cycle, $d \le 2\%$.



Fig. 1. Output Characteristics @ T_J = 25°C

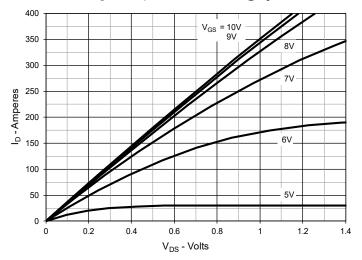


Fig. 2. Extended Output Characteristics @ T_J = 25°C

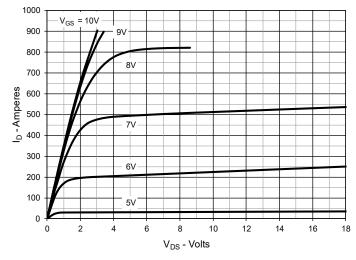


Fig. 3. Output Characteristics @ $T_J = 125$ °C

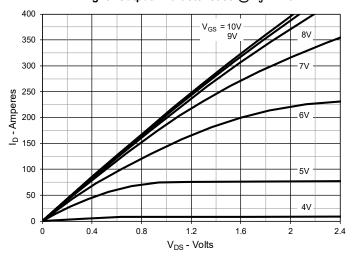


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 200A Value vs. Junction Temperature

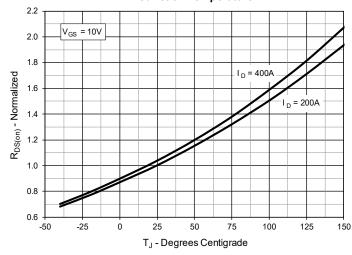


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 200A Value vs. Drain Current

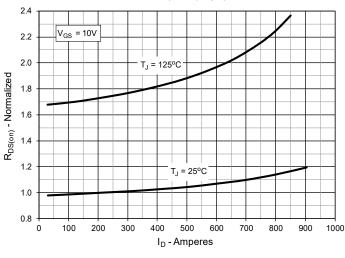
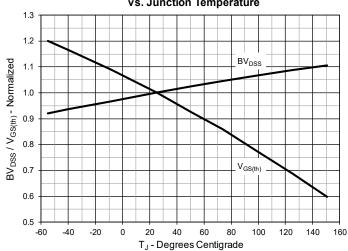


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature

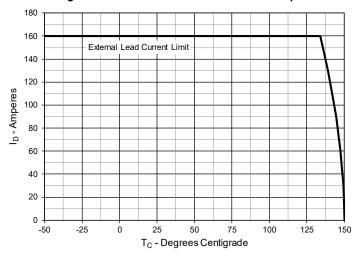


7.0

6.5



Fig. 7. Maximum Drain Current vs. Case Temperature



350 V_{DS} = 10V 300 250 40°C 150 150 50

0

3.0

3.5

4.0

4.5

Fig. 8. Input Admittance

Fig. 9. Transconductance

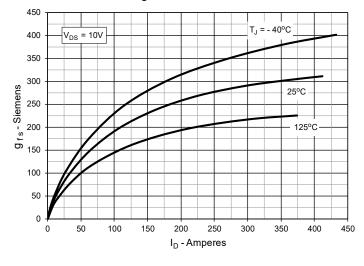


Fig. 10. Forward Voltage Drop of Intrinsic Diode

5.0

 V_{GS} - Volts

5.5

6.0

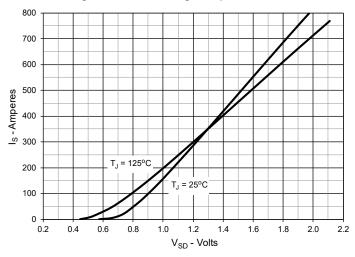


Fig. 11. Gate Charge

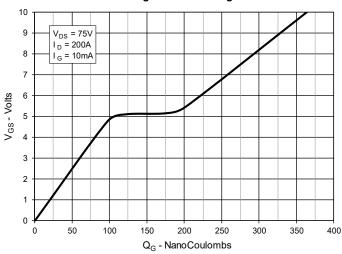
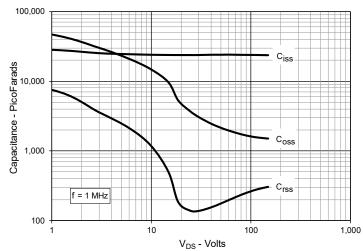


Fig. 12. Capacitance



Littelfuse reserves the right to change limits, test conditions, and dimensions.



20

24 20 20 89 16 12 - \$\frac{8}{3} \text{8}

60

80

V_{DS} - Volts

100

120

140

40

Fig. 13. Output Capacitance Stored Energy

Fig. 14. Forward-Bias Safe Operating Area

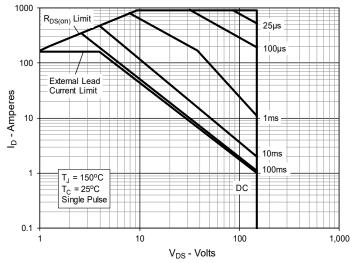
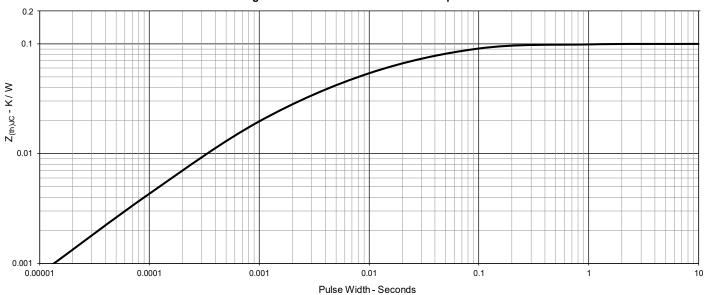


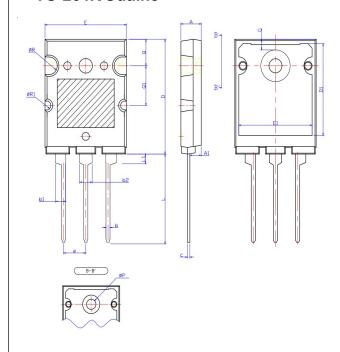
Fig. 15. Maximum Transient Thermal Impedance



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TO-264K Outline

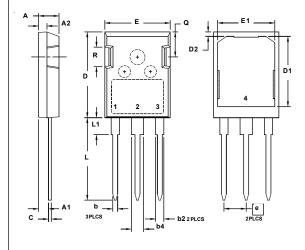


1 - Gate 2,4 - Drain

2,4 - Drain 3 - Source

SYM	INC	HES	MILLIMETERS	
STIVI	MIN	MAX	MIN	MAX
Α	0.189	0.205	4.80	5.20
A1	0.098	0.122	2.50	3.10
b	0.035	0.049	0.90	1.25
b1	0.091	0.106	2.30	2.70
b2	0.110	0.126	2.80	3.20
С	0.020	0.033	0.50	0.85
D	1.016	1.031	25.80	26.20
E	0.780	0.795	19.80	20.20
е	0.203	0.226	5.15	5.75
L.	0.768	0.807	19.50	20.50
L1	0.094	0.102	2.40	2.60
ØP	0.118	0.134	3.00	3.40
Q	0.228	0.244	5.80	6.20
Q1	0.346	0.362	8.80	9.20
E1	0.701	0.717	17.80	18.20
D1	0.811	0.827	20.60	21.00
G	0.087	0.102	2.20	2.60
ØR	0.0	79	2.	00
ØR1	0.039		1.00	

PLUS247™ Outline



1 - Gate

2,4 - Drain

3 - Source

CVM	INCHES		MILLIMETERS	
SYM	MIN	MAX	MIN	MAX
Α	. 190	205،	4,83	5,21
A1	.090	.100	2,29	2,54
A2	. 075	.085	1.91	2.16
b	. 045	055،	1.14	1.40
b2	. 075	.087	1.91	5,50
b4	.115	.126	2.92	3,20
С	.024	.031	0.61	0.80
D	. 819	840،	20 . 80	21,34
D1	. 650	.690	16,51	17,53
D2	. 035	.050	0.89	1.27
E	. 620	.635	15.75	16.13
E1	. 520	.560	13.08	14.22
е	<u>.</u> 215	BSC	5.45 BSC	
L	. 780	810،	19,81	20,57
L1	. 150	.170	3.81	4,32
Q	. 220	.244	5 . 59	6.20
R	.170	,190	4,32	4.83





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