

N-Channel Enhancement Mode MOSFET

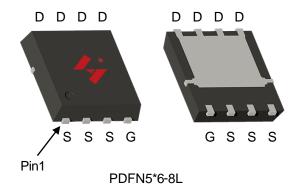
Feature

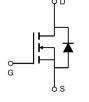
- 100V/105A $R_{DS(ON)=3.0} \, m\Omega(typ.) @V_{GS} = 10V$ $R_{DS(ON)=4.1} \, m\Omega(typ.) @V_{GS} = 4.5V$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen-Free Devices Available (RoHS Compliant)

Applications

- Switching application
- Battery Protection

Pin Description





N-Channel MOSFET

Ordering and Marking Information



Package Code

C2: PDFN5*6-8L

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		100	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		-55 to 175	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode) Tc=25°C		105	А
Mounted on	Large Heat Sink			•
Ірм	Pulsed Drain Current *	Tc=25°C	420	А
	Outing a Paris Out of	Tc=25°C	105	Α
lσ	Continuous Drain Current	Tc=100°C	74.2	А
			75	W
P _D Maximum Power Dissipation		Tc=100°C	37.5	W
R ₀ JC	Thermal Resistance, Junction-to-Case		2	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient **		45	°C/W
Eas	SinglePulsed-Avalanche Energy *** L=0.3mH		350	mJ

- Repetitive rating: pulse width limited by max.junction temperature. Surface mounted on 1in2 FR-4 board.
- Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 Ω , VGs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Compleal	Donomotor	Test Conditions		HYG037N10LS2		LS2	Unit
Symbol	bol Parameter Test Conditions		Min	Тур.	Max		
Static Cha	Static Characteristics						
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=2$	250μΑ	100	-	-	V
Inno	Drain-to-Source Leakage Current	Vps= 100V,Vgs=0V		-	-	1	μA
loss			TJ=125°C	-	-	50	μΑ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} = 250µA		1.2	1.6	2.5	V
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$		-	-	±100	nA
Dag(a)	RDS(ON) Drain-Source On-State Resistance		=20A	-	3.0	3.7	mΩ
KDS(ON)			_S =20A		4.1	5	mΩ
Diode Cha	Diode Characteristics						
VsD	Diode Forward Voltage	IsD=20A,VGS=0V		-	0.78	1.2	V
trr	Reverse Recovery Time	Isb=20A,dIsb/dt=100A/µs		-	60	-	ns
Qrr	Reverse Recovery Charge			-	91	-	nC

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Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Comple al	Dorometer Too	Took Conditions	HY	HYG037N10LS2		
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic (Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	2.4	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	4516	-	
Coss	Output Capacitance	V _{DS} = 25V,	-	2100	-	рF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	206	-	
td(ON)	Turn-on Delay Time		-	15	-	
Tr	Turn-on Rise Time	$V_{DD}=50V,R_{G}=4.0\Omega,$	-	37	-	
td(OFF)	Turn-off Delay Time	IDS= 20A,VGS= 10V	-	76	-	ns
Tf	Turn-off Fall Time		-	95	-	
Gate Char	Gate Charge Characteristics					
Qg(10V)	Total Gate Charge			94		
Qg(4.5V)	Total Gate Charge	$V_{DS} = 80V, V_{GS} = 10V,$	-	50	-	·- C
Qgs	Gate-Source Charge	I _{DS} = 20A	-	15	-	nC
Qgd	Gate-Drain Charge		-	25	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

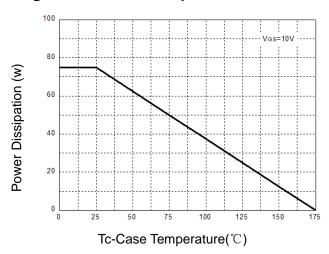


Figure 3: Safe Operation Area

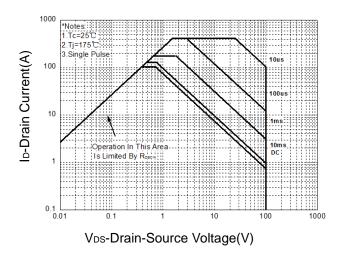


Figure 5: Output Characteristics

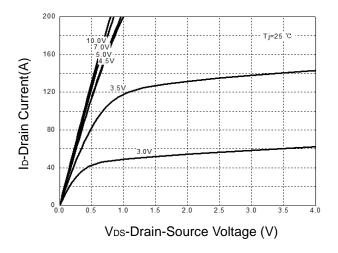


Figure 2: Drain Current

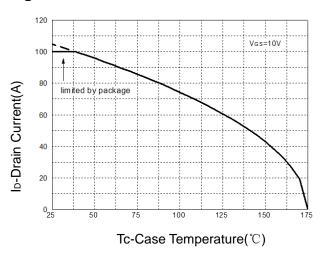
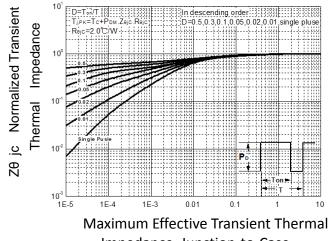
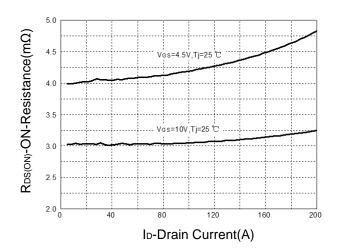


Figure 4: Thermal Transient Impedance



Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

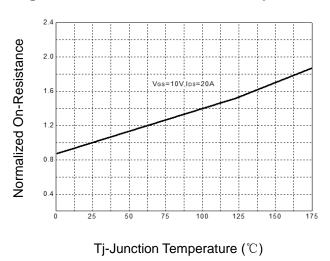


Figure 9: Capacitance Characteristics

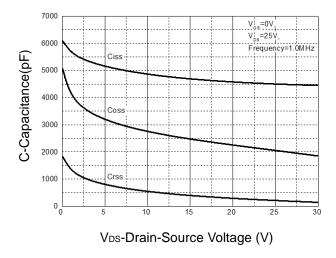


Figure 8: Source-Drain Diode Forward

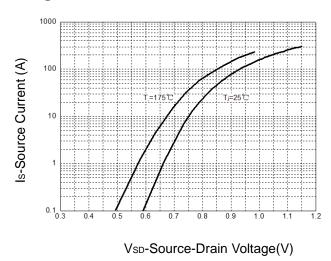
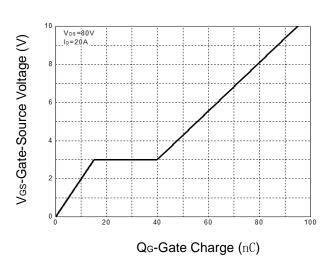
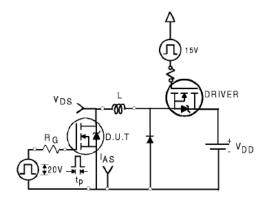


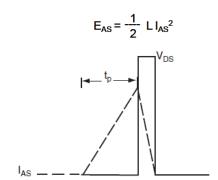
Figure 10: Gate Charge Characteristics



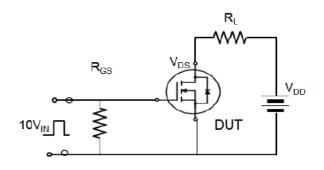


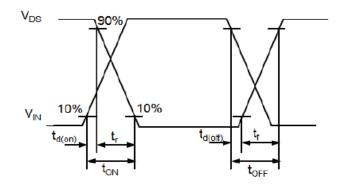
Avalanche Test Circuit



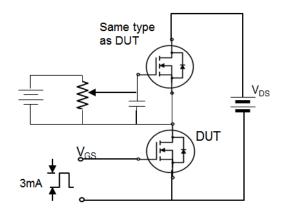


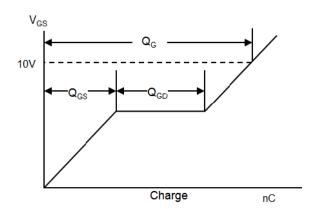
Switching Time Test Circuit





Gate Charge Test Circuit





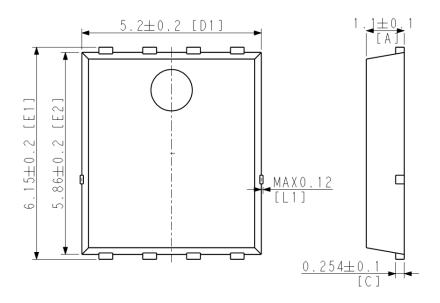


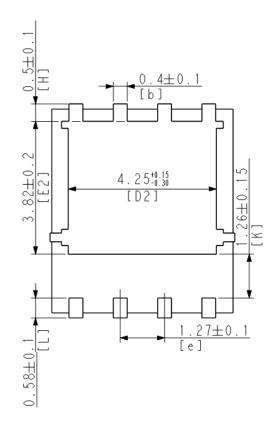
Device Per Unit

Package Type	Unit	Quantity
PDFN5*6-8L	Reel	5000

Package Information

PDFN5*6-8L







Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T _{smin}) Temperature max (T _{smax}) Time (Tsmin to Tsmax) (t _s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T _{smax} to T _P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T _L) Time at liquidous (t _L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T _P)*	See Classification Temp in table 1	SeeClassification Tempin table 2
Time (t _P)** within 5°C of the specified classification temperature (T _c)	20** seconds	30** seconds
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.

^{*}Tolerance for peak profile Temperature (T_P) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350	
<2.5 mm	235 °C	220 °C	
≥2.5 mm	220 °C	220 °C	

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm ³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
PRECON	JESD-22, A113	30°C/60%/192Hrs
HTRB	JESD-22, A108	168Hrs//500Hrs/1000Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 Hrs/500Hrs/1000Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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