

- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent Cdv/dt effect decline
- ★ Advanced high cell density Trench technology

### **Product Summary**



BVDSS	RDSON	ID
130V	165mΩ	2A

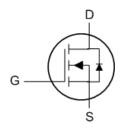
## **Description**

The XR2N13 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The XR2N13 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### **SOT23 Pin Configuration**





### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	130	V
V <sub>G</sub> S	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current	2	Α
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current		А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	8	А
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	1.5	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction-ambient <sup>1</sup>		83.3	°C/W
R <sub>θ</sub> JC	Thermal Resistance Junction-Case <sup>1</sup>			°C/W



# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	Breakdown Voltage V <sub>GS</sub> =0V , I <sub>D</sub> =250uA				V	
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient Reference to 25°C , I <sub>D</sub> =1mA					V/°C	
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =1.5A		165	200	m()	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance-	V <sub>GS</sub> =4.5V , I <sub>D</sub> =1A		185	220 mΩ		
V <sub>GS(th)</sub>	Gate Threshold Voltage	V =V 1 =250A	1.5	2	2.5	V	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$				mV/°C	
	Dunin Course Lockers Comment	V <sub>DS</sub> =130V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1		
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =130V, V <sub>GS</sub> =0V , T <sub>J</sub> =100°C				– uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =70V , I <sub>D</sub> =2A				S	
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz				Ω	
Qg	Total Gate Charge			2.66			
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =70V , $V_{GS}$ =10V , $I_{D}$ =2A		0.63		nC	
Q <sub>gd</sub>	Gate-Drain Charge			0.57			
T <sub>d(on)</sub>	Turn-On Delay Time			4			
Tr	Rise Time	VGS=10V, VDD=70V,		6			
T <sub>d(off)</sub>	Turn-Off Delay Time	RG=3Ω, ID=2A		11		ns	
T <sub>f</sub>	Fall Time			3		1	
C <sub>iss</sub>	Input Capacitance			126			
Coss	Output Capacitance	V <sub>DS</sub> =70V , V <sub>GS</sub> =0V , f=1MHz		17.8		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			26			

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			2	Α
Vsp	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25C			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=2A , di/dt=100A/μs ,		24		nS
Q <sub>rr</sub>	Reverse Recovery Charge	T <sub>J</sub> =25C		100		nC

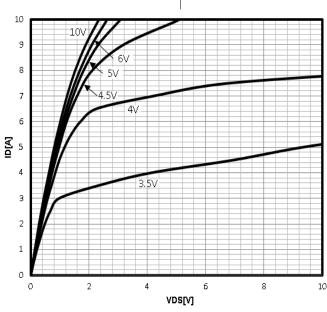
#### Notes:

- 1. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}$ =150°C.
- 2. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 3. Pulse Test: Pulse width≤300µs, duty cycle≤2%.
- 4. This value is guaranteed by design hence it is not included in the production test.

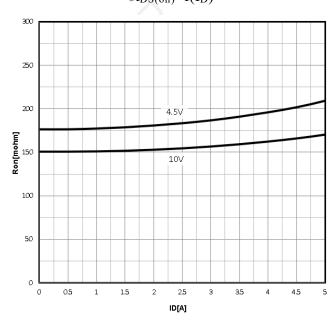


#### **Characteristics Curve:**

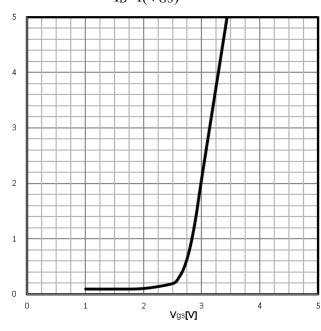
Typ. output characteristics  $I_D=f(V_{DS})$ 



Typ. drain-source on resistance  $R_{DS(on)}=f(I_D)$ 



Typ. transfer characteristics  $I_D=f(V_{\rm GS})$ 



Drain-source on-state resistance

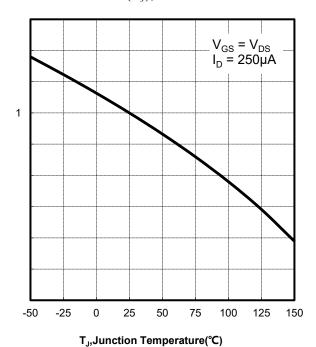
 $R_{DS(on)} = f(T_i); I_D = 2A; V_{GS} = 10V$ 500 450 400 350 Ron[mohm] 250 200 Max 150 100 50 -60 -40 -20 20 40 60 80 100 120 140 160  $Tj[^{\circ}C]$ 



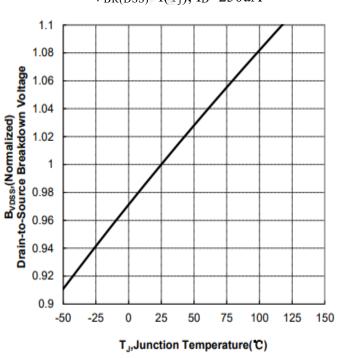
V<sub>GS(th)</sub>,(Normalized) Threshold Voltage

#### **N-Ch 130V Fast Switching MOSFETs**

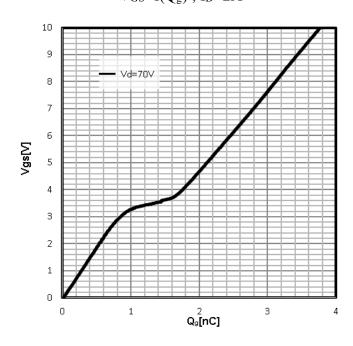
Gate Threshold Voltage V<sub>TH</sub>=f(T<sub>i</sub>); I<sub>D</sub>=250uA



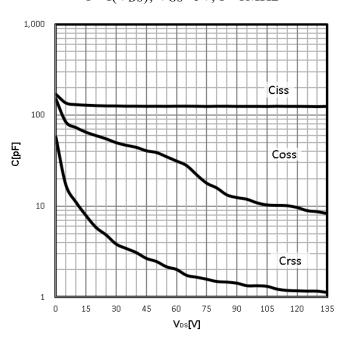
Drain-source breakdown voltage  $V_{BR(DSS)}$ = $f(T_i)$ ;  $I_D$ =250uA



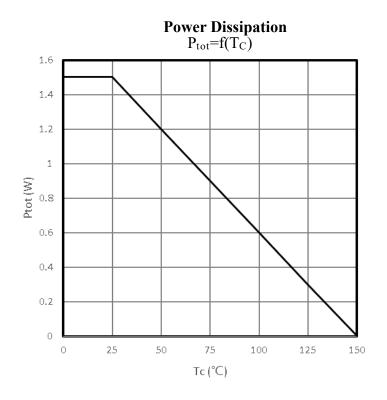
Typ. gate charge  $V_{GS}=f(Q_g)$ ;  $I_D=2A$ 

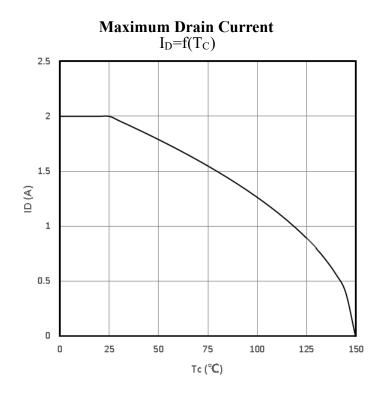


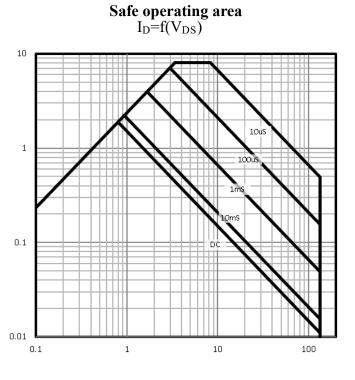
$$\label{eq:constraint} \begin{split} & \textbf{Typ. capacitances} \\ & C = & f(V_{DS}); \ V_{GS} = & 0V; \ f = & 1MHz \end{split}$$

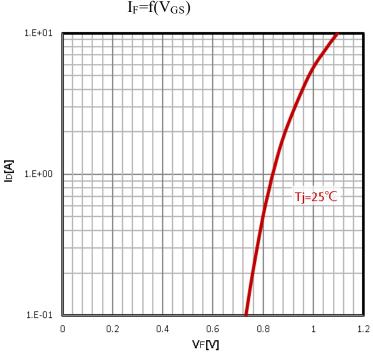








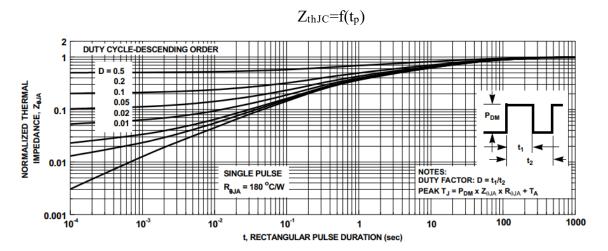




**Body Diode Forward Voltage Variation** 

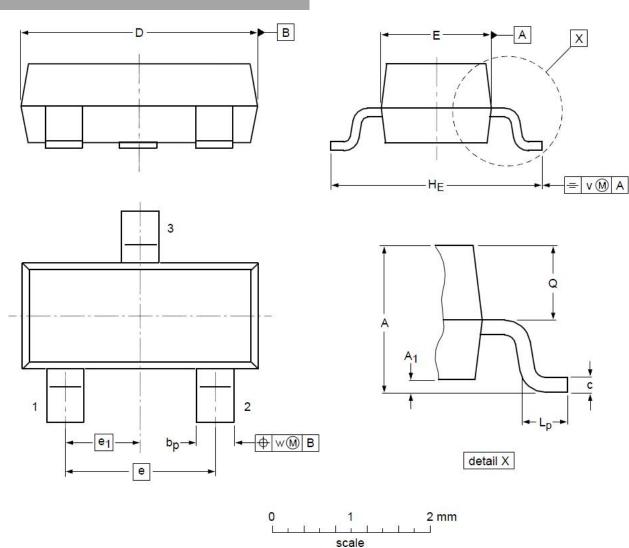


# Max. transient thermal impedance





# **SOT23 Mechanical Data**



#### **DIMENSIONS** ( unit : mm )

				1			
Symbol	Min	Тур	Max	Symbol	Min	Тур	Max
Α	0.90	1.01	1.15	<b>A</b> <sub>1</sub>	0.01	0.05	0.10
b <sub>p</sub>	0.30	0.42	0.50	С	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
е		1.90		<b>e</b> 1		0.95	
HE	2.25	2.40	2.55	Lp	0.30	0.42	0.50
Q	0.45	0.49	0.55	v		0.20	
w		0.10					