

N-Channel Enhancement Mode MOSFET

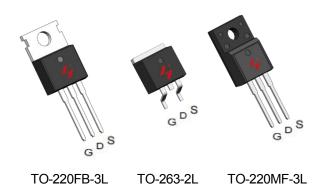
Feature

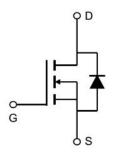
- 100V/180A $R_{DS(ON)}=4.2m\Omega$ (typ.) @ Vgs = 10V
- 100% Avalanche Tested
- Reliable and Rugged
- Lead-Free and Green Devices Available (RoHS Compliant)

Applications

- Power Switching application
- Uninterruptible Power Supply

Pin Description





N-Channel MOSFET

Ordering and Marking Information



Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plateTermi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr -oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Rat	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		100	V
Vgss	Gate-Source Voltage		±25	V
TJ	Maximum Junction Temperature		175	°C
Tstg	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)	Tc=25°C	180	Α
Mounted on	Large Heat Sink		'	
I DМ	Pulsed Drain Current *	Tc=25°C	620**	А
	Continuous Danis Compant	Tc=25°C	180	А
lσ	Continuous Drain Current	Tc=100°C	132	А
	Mariana Barra Birainatian	Tc=25°C	348	W
P _D	Maximum Power Dissipation	Maximum Power Dissipation Tc=100°C		W
R₀Jc	Thermal Resistance, Junction-to-Case		0.43	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		62.5	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.5mH	1128***	mJ

Note: * Repetitive rating; pulse width limited by max. junction temperature.

** Surface mounted on FR-4 board.

*** Limited by TJmax, starting TJ=25°C, L = 0.5mH, VDS=80V, VGS =10V.

Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Cumbal	Parameter	Test Conditions	HY3810		l lm!4	
Symbol	Parameter	rest Conditions	Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	100	-	-	V
leas	Drain-to-Source Leakage Current	V _{DS} =100V,V _{GS} =0V	-	-	1.0	μA
IDSS	Diam-to-Source Leakage Current	TJ=125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	2.0	3.0	4.0	V
Igss	Gate-Source Leakage Current	V _{GS} =±25V,V _{DS} =0V	-	-	±100	nA
RDS(ON)*	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =50A	-	4.2	5.5	mΩ
Diode Cha	Diode Characteristics					
VsD*	Diode Forward Voltage	IsD=50A,VGS=0V	-	0.83	1.3	٧
trr	Reverse Recovery Time	lon=50A dlon/dt=100A/us	_	65	_	ns
Qrr	Reverse Recovery Charge	Isp=50A,dIsp/dt=100A/µs	_	103	_	nC

HY3810P/B/MF



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Dovernator	Took Conditions	HY38	HY3810			I I m i 4
Symbol	Parameter	Test Conditions	Min	Тур.	Max	- Unit	
Dynamic (Characteristics						
Rg	Gate Resistance	V_{GS} =0V, V_{DS} =0V,F=1 MHz	-	1.3	-	Ω	
Ciss	Input Capacitance	V _{GS} =0V,	-	7430	-		
Coss	Output Capacitance	V _{DS} =50V,	-	615	-	pF	
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	459	-		
td(ON)	Turn-on Delay Time		-	30	-		
Tr	Turn-on Rise Time	V_{DD} =50 V , R_{G} =4 Ω ,	-	48	-		
td(OFF)	Turn-off Delay Time	los=50A,Vcs=10V	-	76	-	ns	
Tf	Turn-off Fall Time		-	50	-		
Gate Char	Gate Charge Characteristics						
Qg	Total Gate Charge	V _{DS} =50V, V _{GS} =10V,	_	174	-		
Qgs	Gate-Source Charge		-	38	-	nC	
Qgd	Gate-Drain Charge	10-304	-	66	-		

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

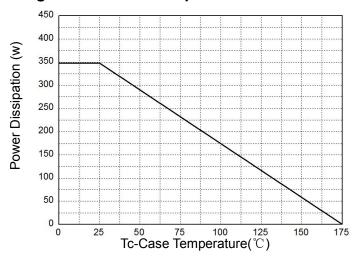


Figure 2: Drain Current

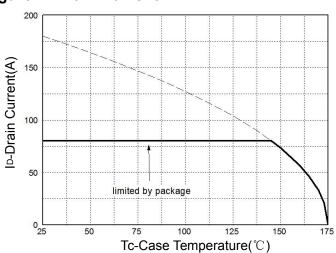


Figure 3: Safe Operation Area

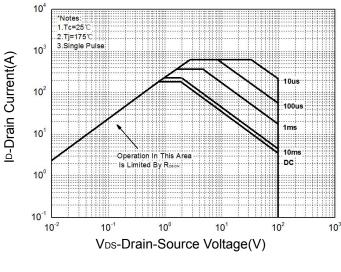


Figure 4: Thermal Transient Impedance

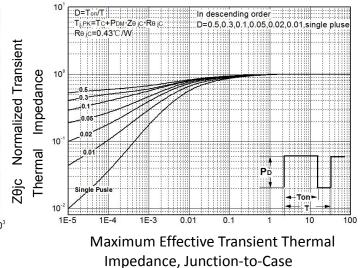


Figure 5: Output Characteristics

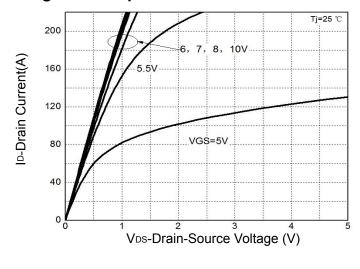
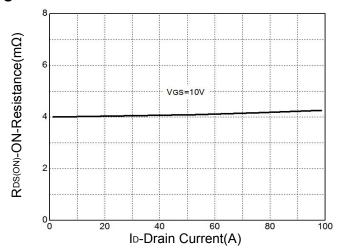


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

2.4 Rdson@Tj=25°C:4.2mΩ

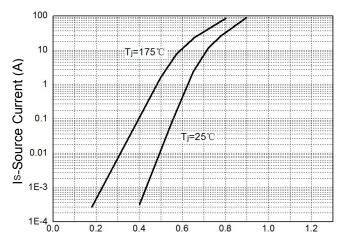
2.0 Rdson@Tj=25°C:4.2mΩ

1.6 VGS=10V,IDS=50A

1.2 0.8 0.4 0.4 0.4 0.4 0.4 0.5 150 175

Tj-Junction Temperature (°C)

Figure 8: Source-Drain Diode Forward



Vsp-Source-Drain Voltage(V)

Figure 9: Capacitance Characteristics

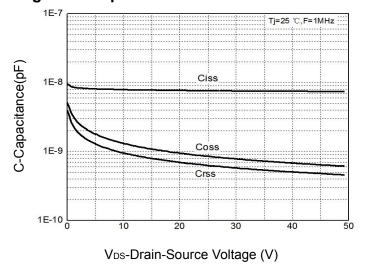
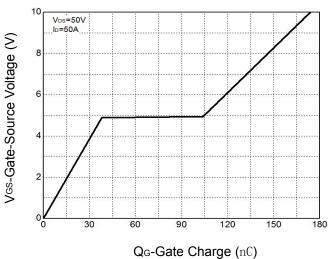


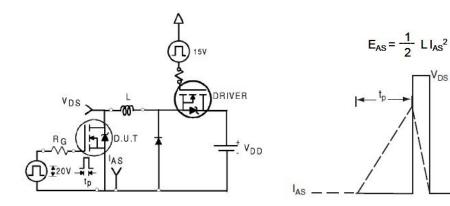
Figure 10: Gate Charge Characteristics



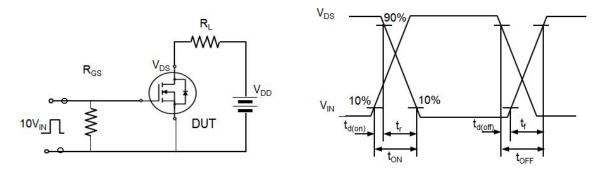
5



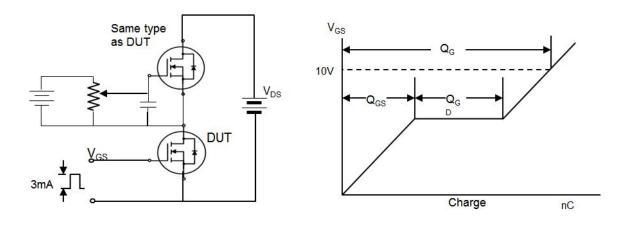
Avalanche Test Circuit



Switching Time Test Circuit



Gate Charge Test Circuit



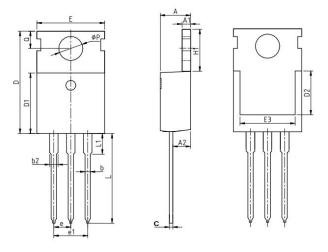


Device Per Unit

Package Type	Unit	Quantity
TO-220FB-3L	Tube	50
TO-263-2L	Tube	50
TO-263-2L	Reel	800
TO-220MF-3L	Tube	50

Package Information

TO-220FB-3L



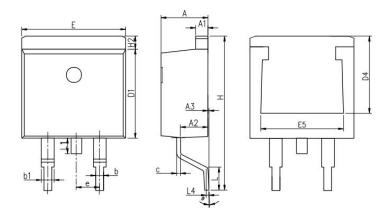
COMMON DIMENSIONS

SYMBOL		mm	
STIVIBUL	MIN	NOM	MAX
Α	4.37	4.57	4.77
A1	1.25	1.30	1.45
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b2	1.17	1.27	1.47
С	0.40	0.50	0.65
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.50	-	-
E	9.70	10.00	10.30
E3	7.00	-	-
е		2.54 BSC	
e1		5.08 BSC	
H1	6.25	6.50	6.85
L	12.75	13.50	13.80
L1	-	3.10	3.40
ФР	3.40	3.60	3.80
Q	2.60	2.80	3.00



Package Information

TO-263-2L



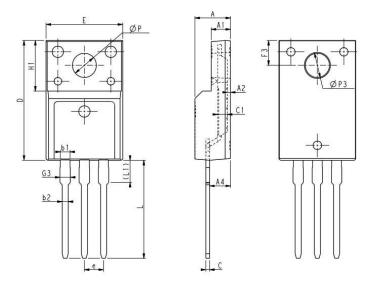
COMMON DIMENSIONS

SYMBOL		mm	
STIVIBUL	MIN	NOM	MAX
Α	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
A3	0	0.13	0.25
b	0.7	0.81	0.96
b1	1.17	1.27	1.47
С	0.3	0.38	0.53
D1	8.5	8.7	8.9
D4	6.6	-	-
Е	9.86	10.16	10.36
E5	7.06	-	-
е		2.54 BSC	
Н	14.7	15.1	15.5
H2	1.07	1.27	1.47
L	2	2.3	2.6
L1	1.4	1.55	1.7
L4	0.25 BSC		
θ	0°	5°	9°



Package Information

TO-220MF-3L

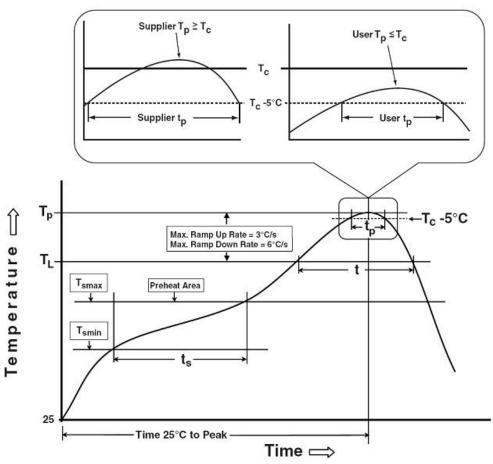


COMMON DIMENSIONS

	mm	
MIN	NOM	MAX
9.96	10.16	10.36
4.50	4.70	4.90
2.34	2.54	2.74
0.30	0.45	0.60
2.56	2.76	2.96
0.40	0.50	0.65
1.20	1.30	1.35
15.57	15.87	16.17
6.70REF		
	2.54BSC	
12.68	12.98	13.28
2.93	3.03	3.13
3.03	3.18	3.38
3.15	3.45	3.65
3.15	3.30	3.45
1.25	1.35	1.55
1.18	1.28	1.43
0.70	0.80	0.95
	9.96 4.50 2.34 0.30 2.56 0.40 1.20 15.57 12.68 2.93 3.03 3.15 3.15 1.25 1.18	MIN NOM 9.96 10.16 4.50 4.70 2.34 2.54 0.30 0.45 2.56 2.76 0.40 0.50 1.20 1.30 15.57 15.87 6.70REF 2.54BSC 12.68 12.98 2.93 3.03 3.03 3.18 3.15 3.45 3.15 3.30 1.25 1.35 1.18 1.28



Classification Profile



Classification Reflow Profiles

Sn-Pb Eutectic Assembly	Pb-Free Assembly
100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
3 °C/second max.	3°C/second max.
183 °C	217 °C
60-150 seconds	60-150 seconds
See Classification Temp in table 1	SeeClassification Tempin table 2
20** seconds	30** seconds
6 °C/second max.	6 °C/second max.
6 minutes max.	8 minutes max.
	100 °C 150 °C 60-120 seconds 3 °C/second max. 183 °C 60-150 seconds See Classification Temp in table 1 20** seconds 6 °C/second max.

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168/500/1000Hrs, V _{gs} 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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