

N-Channel Enhancement Mode MOSFET

Feature

• 60V/224A

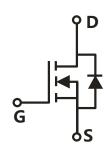
RDS(ON)= 1.4 m Ω (typ.) @VGS = 10V RDS(ON)= 2.2 m Ω (typ.) @VGS = 4.5V

- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Power Tool Application
- Networking DC-DC Power System

Pin Description



Single N-Channel MOSFET

PDFN8L(5x6)

Ordering and Marking Information



Package Code C2: PDFN8L(5x6)

Pin1

Date Code XYMXXXXXX

Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	ntings (Tc=25°C Unless Otherwise Noted)		,	•
VDSS	Drain-Source Voltage		60	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55. 475	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)	Source Current-Continuous(Body Diode) Tc=25°C		Α
Mounted on	Large Heat Sink	1	•	
Ідм	Pulsed Drain Current *	Tc=25°C	648	А
1	ID Continuous Drain Current	Tc=25°C	224	А
ID		Tc=100°C	160	А
P _D Maximum Power Dissipation		Tc=25°C	211	W
		Tc=100°C	106	W
R ₀ JC	Thermal Resistance, Junction-to-Case		0.71	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		80	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	702	mJ

- Note: * Repetitive rating; pulse width limited by max.junction temperature.
 - Surface mounted on 1in2 FR-4 board.
 - Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 Ω , VGs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions		HYG019N06LS1		SLS1	Unit
Symbol	Farameter			Min	Тур.	Max	Unit
Static Char	Static Characteristics						
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0V,I_{DS}=250\mu A$		60	-	-	V
Design to Course Legland Course		VDS=60V,VGS=0V		-	-	1	μΑ
IDSS	Drain-to-Source Leakage Current		T _J =125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	VDS=VGS, IDS=250µA		1	1.9	3	V
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$		-	-	±100	nA
Process	RDS(ON) Drain-Source On-State Resistance		=50A	-	1.4	2.1	mΩ
KDS(ON)			=50A	-	2.2	3.2	mΩ
Diode Characteristics							
VsD	Diode Forward Voltage	IsD=50A,Vgs=0V		-	0.83	1.30	V
trr	Reverse Recovery Time	IsD=50A,dIsD/dt=100A/μs		-	42	-	ns
Qrr	Reverse Recovery Charge			-	55	-	nC



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Dorometer	Tank Camalikiana	HY	HYG019N06LS1		
Symbol	Parameter Test Conditions		Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=500KHz	-	0.7	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	5044	-	
Coss	Output Capacitance	Vps=25V,	-	1150	-	pF
Crss	Reverse Transfer Capacitance	Frequency=500KHz	-	40	-	
td(ON)	Turn-on Delay Time		-	18	-	
Tr	Turn-on Rise Time	V _{DD} =30V,R _G =2.5Ω,	-	60	-	
td(OFF)	Turn-off Delay Time	Ips=50A,Vgs=10V	-	41	-	ns
Tf	Turn-off Fall Time		-	44	-	
Gate Cha	Gate Charge Characteristics					
	Total Gate Charge(V _{GS} =10V)		-	79	-	
Q_g	Total Gate Charge(V _{GS} =4.5V)		-	39	-	C
Qgs	Gate-Source Charge	V _{DS} =48V, I _{DS} =50A	-	24	-	nC
Qgd	Gate-Drain Charge		-	14	-	
V _{plateau}	Gate plateau voltage		-	4.4	-	V

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

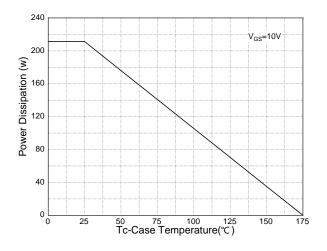


Figure 3: Safe Operation Area

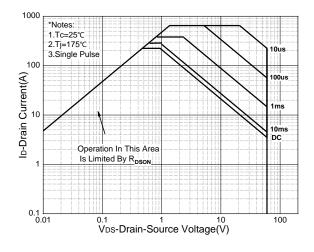


Figure 5: Output Characteristics

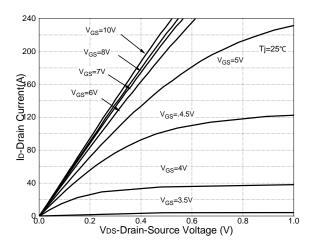


Figure 2: Drain Current

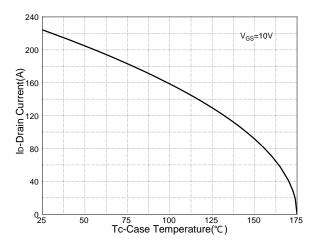


Figure 4: Thermal Transient Impedance

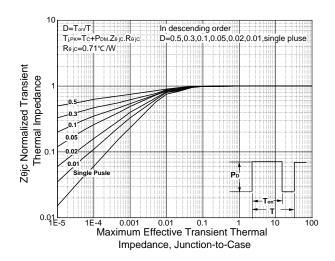
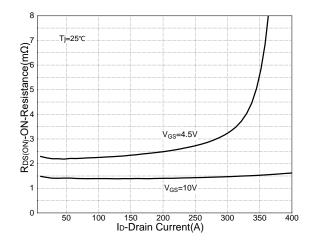


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

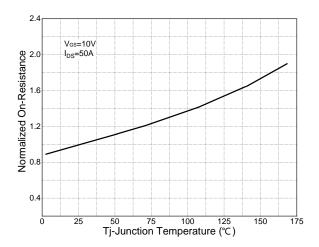


Figure 8: Source-Drain Diode Forward

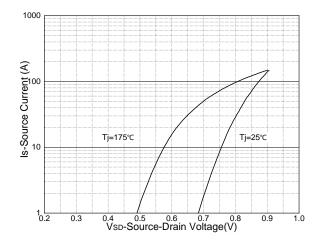


Figure 9: Capacitance Characteristics

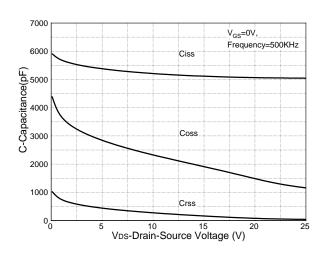
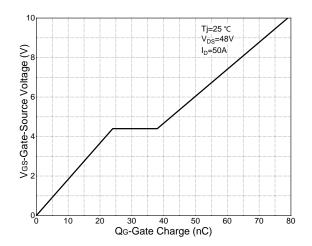
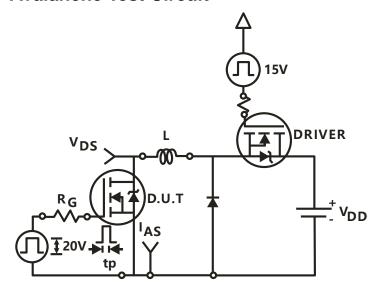


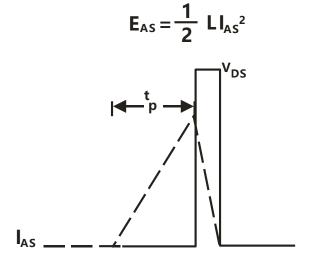
Figure 10: Gate Charge Characteristics



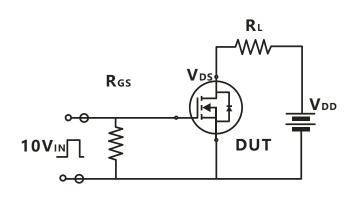


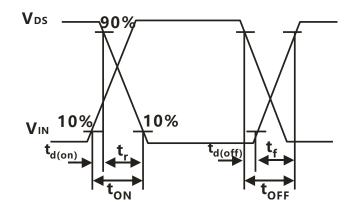
Avalanche Test Circuit



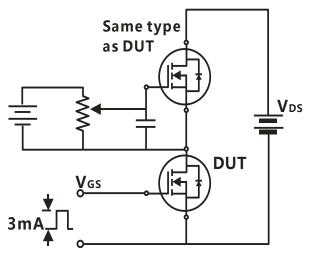


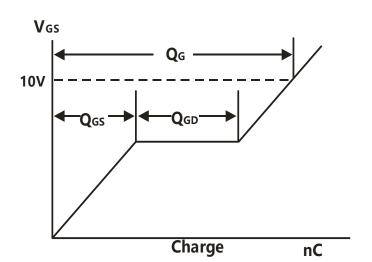
Switching Time Test Circuit





Gate Charge Test Circuit







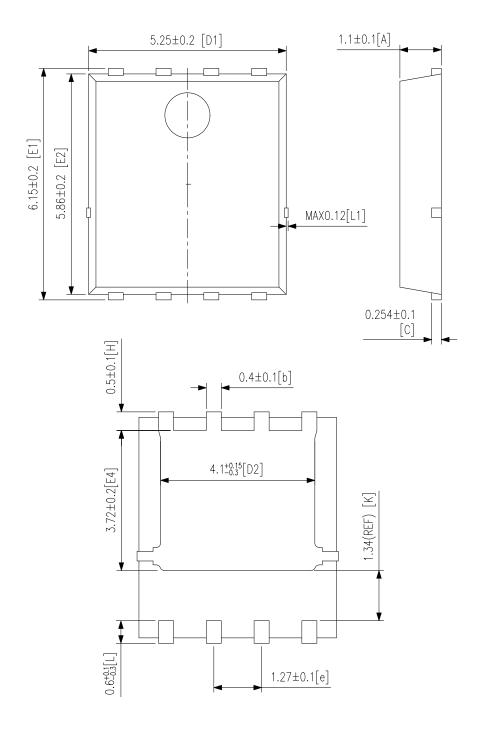
Device Per Unit

Package Type	Unit	Quantity
PDFN8L(5x6)	Reel	5000

Package Information

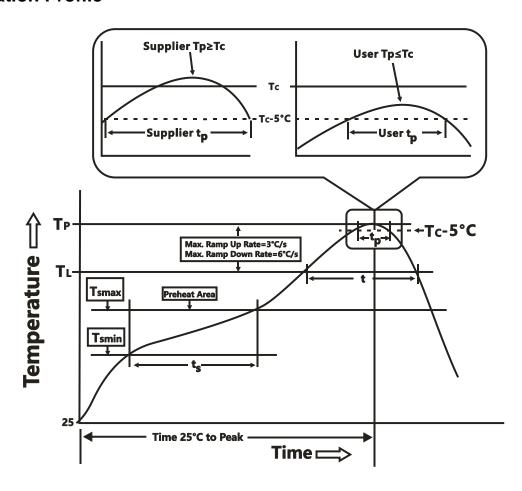
PDFN8L(5x6)

(unit:mm)





Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly			
Preheat & Soak					
Temperature min (T _{smin})	100 °C	150 °C			
Temperature max (T _{smax})	150 °C	200 °C			
Time (Tsmin to Tsmax) (ts)	60-120 seconds	60-120 seconds			
Average ramp-up rate	3 °C/second max.	3°C/second max.			
(T _{smax} to T _P)	5 C/second max.				
Liquidous temperature (TL)	183 °C	217 °C			
Time at liquidous (t _L)	60-150 seconds	60-150 seconds			
Peak package body Temperature	See Classification Temp in table 1	SacClassification Tampin table 2			
(T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2			
Time (t _P)** within 5°C of the specified	20**	30** seconds			
classification temperature (T _c)	20** seconds				
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.			
Time 25°C to peak temperature	6 minutes max.	8 minutes max.			

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com Technical Support:Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: http://www.hymexa.com/