

#### N-Channel Enhancement Mode MOSFET

#### **Feature**

- 100V/55A  $R_{DS(ON)}$ = 12.5 m $\Omega$ (typ.) @VGS = 10V  $R_{DS(ON)}$ = 16 m $\Omega$ (typ.) @VGS = 4.5V
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

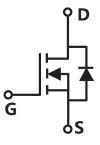
#### **Pin Description**



TO-220FB-3L TO-263-2L

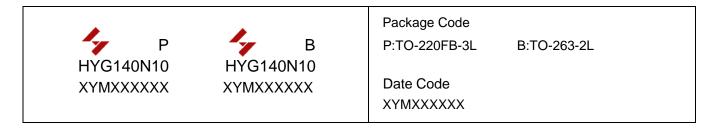
### **Applications**

- Power Switching application
- Uninterruptible Power Supply



Single N-Channel MOSFET

### **Ordering and Marking Information**



Note: HUAYI halogen free products contain molding compounds and 100% matter tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)		•	
VDSS	Drain-Source Voltage		100	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55 1. 475	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
Is	Source Current-Continuous(Body Diode)	Tc=25°C	55	А
Mounted on	Large Heat Sink			
Ірм	Pulsed Drain Current *	Tc=25°C	198	А
1	Continuous Paris Correct	Tc=25°C	55	А
lσ	Continuous Drain Current	Tc=100°C	39	А
	Mariana Baran Biratantina	Tc=25°C	90	W
Pb	Maximum Power Dissipation Tc=100°C		45	W
R₀JC	Thermal Resistance, Junction-to-Case		1.65	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		62.5	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		55	mJ

- Note: \* Repetitive rating; pulse width limited by max.junction temperature.
  - Surface mounted on 1in2 FR-4 board.
  - Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 $\Omega$ , VGs =10V.

### **Electrical Characteristics**(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions		HYG140N10LS1		Unit	
Symbol	Farameter			Min	Тур.	Max	Unit
Static Char	acteristics						
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0V,I_{DS}=2$	50μΑ	100	-	-	V
Inno	Drain to Source Leakage Current	VDS=100V,VG	s=0V	-	-	1	μΑ
IDSS	Ibss Drain-to-Source Leakage Current		TJ=125°C	-	-	50	μΑ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA		1.2	1.7	2.2	<b>V</b>
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$		-	-	±100	nA
Process	Drain-Source On-State Resistance	V <sub>GS</sub> =10V,I <sub>DS</sub> =	=20A	-	12.5	15	mΩ
Rds(on)	Dialii-Source Oil-State Resistance	V <sub>GS</sub> =4.5V,I <sub>DS</sub>	=20A	-	16	21	mΩ
Diode Char	racteristics						
VsD	Diode Forward Voltage	IsD=20A,VGS=0V		-	0.9	1.2	V
trr	Reverse Recovery Time	- Isb=20A,dIsb/dt=100A/μs		-	35	-	ns
Qrr	Reverse Recovery Charge			-	35	-	nC

# HYG140N10LS1P/B



# Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Coursels al	Devementer	Tool Conditions	HY	HYG140N10LS1			
Symbol	Parameter	Parameter Test Conditions		Тур.	Max	Unit	
Dynamic	Characteristics						
Rg	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	1.4	-	Ω	
Ciss	Input Capacitance	Vgs=0V,	-	1226	-		
Coss	Output Capacitance	VDS=25V,	-	477	-	pF	
Crss	Reverse Transfer Capacitance	Frequency=1MHz	-	35	-		
td(ON)	Turn-on Delay Time		-	9.4	-		
Tr	Turn-on Rise Time	$V_{DD}=50V,R_{G}=4\Omega,$	-	32	-		
td(OFF)	Turn-off Delay Time	IDS=20A,VGS=10V	-	23	-	ns	
Tf	Turn-off Fall Time		-	73	-		
Gate Cha	rge Characteristics						
	Total Gate Charge(V <sub>GS</sub> =10V)		-	21	-		
$Q_g$	Total Gate Charge(V <sub>GS</sub> =4.5V)		-	11	-	<b>~</b> C	
Qgs	Gate-Source Charge	V <sub>DS</sub> =80V, I <sub>DS</sub> =20A	-	4.8	-	nC	
Qgd	Gate-Drain Charge		-	3.7	-		
V <sub>plateau</sub>	Gate plateau voltage		-	3.4	-	V	

Note: \*Pulse test, pulse width  $\leq 300$ us, duty cycle  $\leq 2\%$ 



## **Typical Operating Characteristics**

**Figure 1: Power Dissipation** 

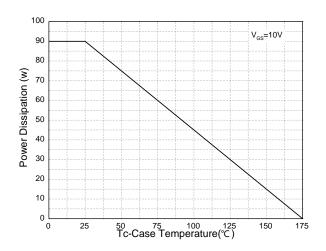


Figure 3: Safe Operation Area

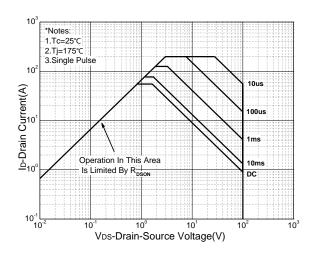
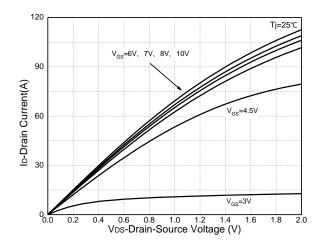
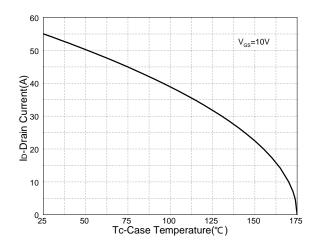


Figure 5: Output Characteristics



**Figure 2: Drain Current** 



**Figure 4: Thermal Transient Impedance** 

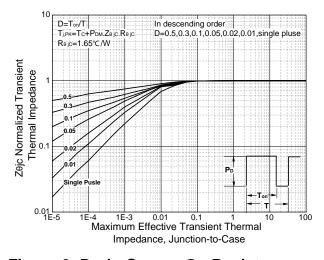
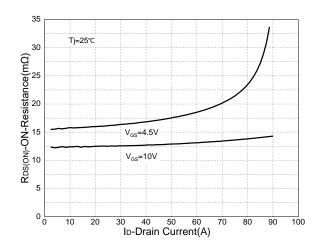


Figure 6: Drain-Source On Resistance





### **Typical Operating Characteristics(Cont.)**

Figure 7: On-Resistance vs. Temperature

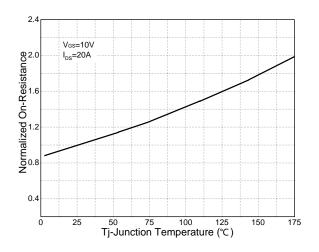


Figure 8: Source-Drain Diode Forward

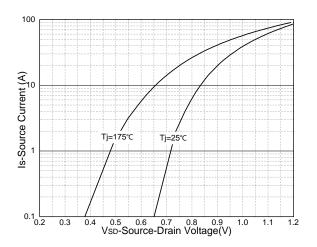
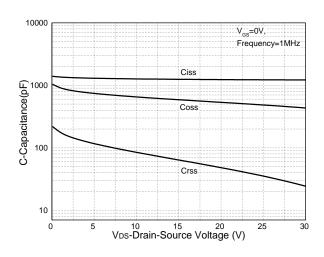
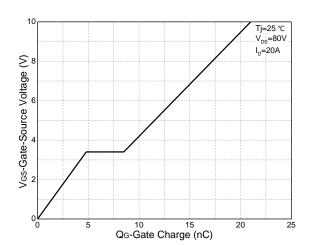


Figure 9: Capacitance Characteristics

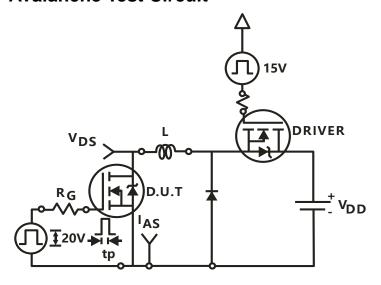


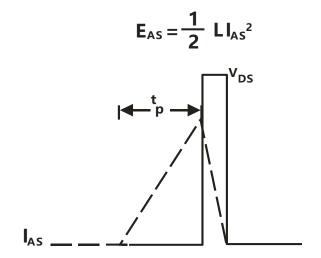
**Figure 10: Gate Charge Characteristics** 



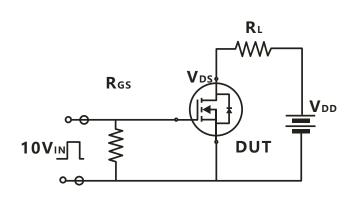


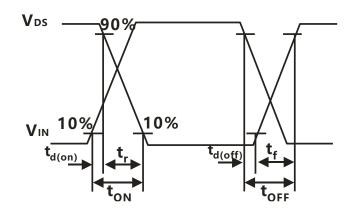
#### **Avalanche Test Circuit**



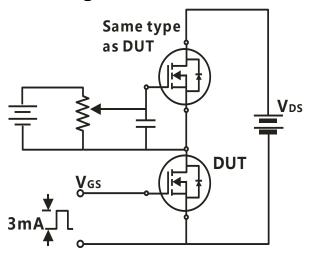


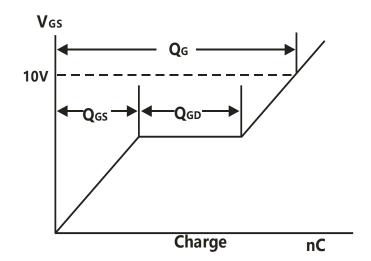
## **Switching Time Test Circuit**





### **Gate Charge Test Circuit**





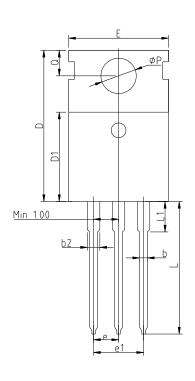


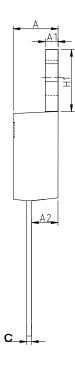
# **Device Per Unit**

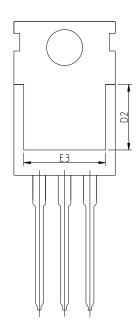
Package Type	Unit	Quantity
TO-220FB-3L	Tube	50
TO-263-2L	Reel	800

# **Package Information**

TO-220FB-3L



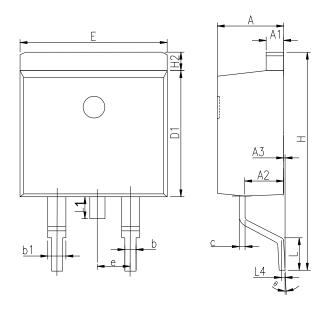


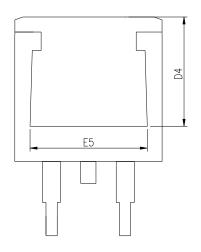


COMMON DIMENSIONS				
CVMDOL	mm			
SYMBOL	MIN	NOM	MAX	
Α	4.37	4.57	4.77	
A1	1.25	1.30	1.45	
A2	2.20	2.40	2.60	
b	0.70	0.80	0.95	
b2	1.17	1.27	1.47	
С	0.40	0.50	0.65	
D	15.10	15.60	16.10	
D1	8.80	9.10	9.40	
D2	5.50	-	-	
E	9.70	10.00	10.30	
E3	7.00	-	-	
е		2.54 BSC		
e1		5.08 BSC		
H1	6.25	6.50	6.85	
L	12.75	13.50	13.80	
L1	-	3.10	3.40	
ФР	3.40	3.60	3.80	
Q	2.60	2.80	3.00	



#### TO-263-2L

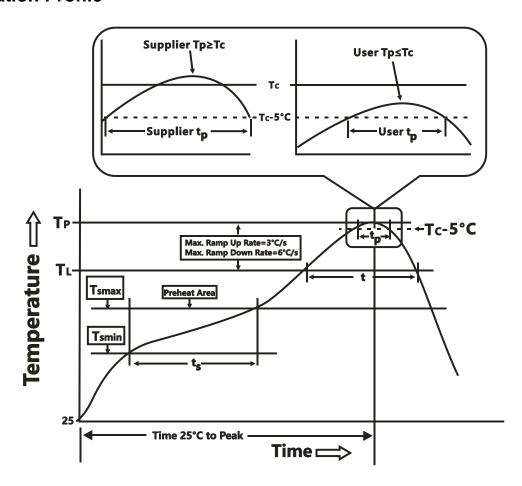




COMMON DIMENSIONS				
SYMBOL	mm			
STIVIBUL	MIN	NOM	MIN	
А	4.37	4.57	4.77	
A1	1.22	1.27	1.42	
A2	2.49	2.69	2.89	
А3	0	0.13	0.25	
b	0.7	0.81	0.96	
b1	1.17	1.27	1.47	
С	0.3	0.38	0.53	
D1	8.5	8.7	8.9	
D4	6.6	-	-	
Е	9.86	10.16	10.36	
E5	7.06	-	-	
е		2.54 BSC		
Н	14.7	15.1	15.5	
H2	1.07	1.27	1.47	
L	2	2.3	2.6	
L1	1.4	1.55	1.7	
L4	0.25 BSC			
θ	0°	5°	9°	



#### **Classification Profile**



## **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
	Preheat & Soak		
Temperature min (T <sub>smin</sub> )	100 °C	150 °C	
Temperature max (T <sub>smax</sub> )	150 °C	200 °C	
Time (Tsmin to Tsmax) (t <sub>s</sub> )	60-120 seconds	60-120 seconds	
Average ramp-up rate	3 °C/second max.	3°C/second max.	
(T <sub>smax</sub> to T <sub>P</sub> )	5 C/second max.		
Liquidous temperature (T <sub>L</sub> )	183 °C	217 °C	
Time at liquidous (t∟)	60-150 seconds	60-150 seconds	
Peak package body Temperature	See Classification Temp in table 1	SeeClassification Tempin table 2	
(T <sub>p</sub> )*	See Classification Temp in table 1	SeeClassification Tempin table 2	
Time (t <sub>P</sub> )** within 5°C of the specified	20** seconds	20**	
classification temperature (T <sub>c</sub> )	20 seconds	30** seconds	
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	

<sup>\*</sup>Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup> Tolerance for time at peak profile temperature (t<sub>p</sub>) is defined as a supplier minimum and a user maximum.

## HYG140N10LS1P/B



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

#### Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm <sup>3</sup>
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

### **Reliability Test Program**

Test item	Method	Description	
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C	
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C	
HTGB	JESD-22, A108	168/500 Hrs, V <sub>gs</sub> 100% @ 150°C	
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C	
тст	JESD-22, A104	JESD-22, A104 250/500 Cycles, -55°C~150°C	

#### **Customer Service**

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