

N-Channel Super Junction Power MOSFET

Feature

- 650V/60A
 R_{DS(ON)}= 44 mΩ(typ.) @VGS = 10V
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

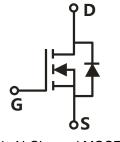
Pin Description



TO-247A-3L

Applications

- Solar/Renewable/UPS-Micro Inverter System
- Charger
- Power Supply



Single N-Channel MOSFET

Ordering and Marking Information



Package Code W:TO-247A-3L

Date Code XYMXXXXXX

Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)		<u>, </u>	
VDSS	Drain-Source Voltage		650	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55. 475	°C
Tstg	Storage Temperature Range		-55 to 175	°C
Is	Source Current-Continuous(Body Diode) Tc=25°C		60	А
Mounted on	Large Heat Sink	•	,	1
Ідм	Pulsed Drain Current *	Tc=25°C	180	А
1	ID Continuous Drain Current	Tc=25°C	60	А
ID		Tc=100°C	42.4	А
	Mariana Baran Biratantina	Tc=25°C	500	W
PD	P _D Maximum Power Dissipation		250	W
R _θ JC	Thermal Resistance, Junction-to-Case		0.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		40	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=80mH		4032	mJ

- Note: * Repetitive rating; pulse width limited by max.junction temperature.
 - Surface mounted on 1in2 FR-4 board.
 - Limited by TJmax , starting TJ=25°C, L = 80mH, Rg= 25 Ω , Vgs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Cumbal	Devemeter	Test Conditions		HY65R420		Unit
Symbol Parameter		rest Conditions	Min	Тур.	Max	
Static Char	Static Characteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	650	-	-	V
L Design to Course Leelings Course		VDS=650V,VGS=0V	-	-	1	μΑ
ldss	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μΑ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	3	4	5	V
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =30A	-	44	52	mΩ
Diode Char	Diode Characteristics					
VsD	Diode Forward Voltage	IsD=30A,Vgs=0V	-	0.88	1.3	V
trr	Reverse Recovery Time	Isp=30A,dIsp/dt=100A/µs	-	146	-	ns
Qrr	Reverse Recovery Charge	150-30A, 4150/41=100A/µS	-	1.08	-	μC



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Comple al	Parameter	Tank Camalikiana	I	HY65R420		
Symbol		Test Conditions	Min	Тур.	Max	Unit
Dynamic (Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=100KHz	-	1.6	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	6252	-	
Coss	Output Capacitance	V _{DS} =200V,	-	163	-	pF
Crss	Reverse Transfer Capacitance	Frequency=100KHz	-	8.2	-	
td(ON)	Turn-on Delay Time		-	42	-	
Tr	Turn-on Rise Time	$V_{DD}=350V,R_{G}=5\Omega,$	-	104	-	
td(OFF)	Turn-off Delay Time	IDS=30A,VGS=10V	-	101	-	ns
Tf	Turn-off Fall Time		-	61	-	
Gate Char	ge Characteristics					
Qg	Total Gate Charge(V _{GS} =10V)		-	144	-	
Qgs	Gate-Source Charge	\/ _520\/ _20\	-	50	-	nC
Qgd	Gate-Drain Charge	V_{DS} =520V, I_{DS} =30A	-	60	-	
V _{plateau}	Gate plateau voltage		-	7.3	-	V

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

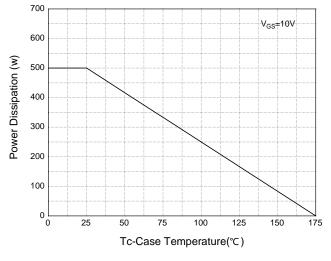


Figure 2: Drain Current

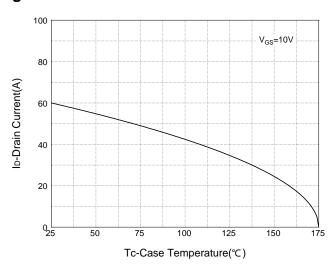


Figure 3: Safe Operation Area

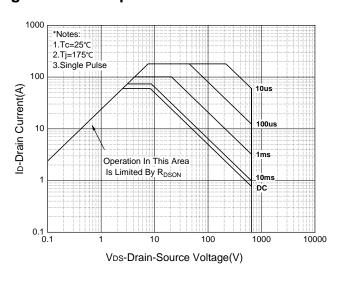


Figure 4: Thermal Transient Impedance

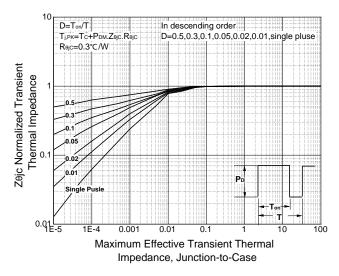


Figure 5: Output Characteristics

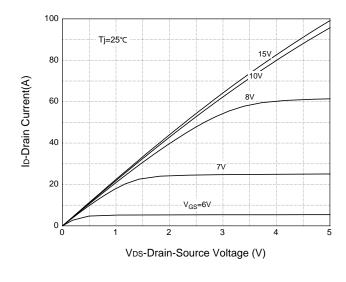
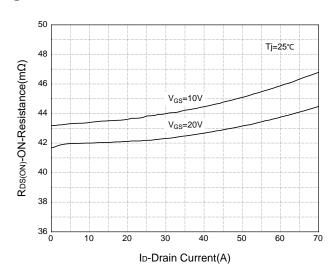


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

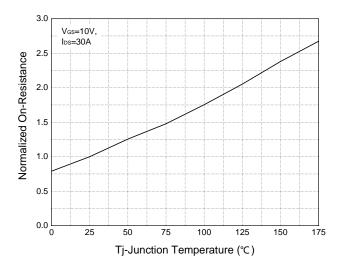


Figure 8: Source-Drain Diode Forward

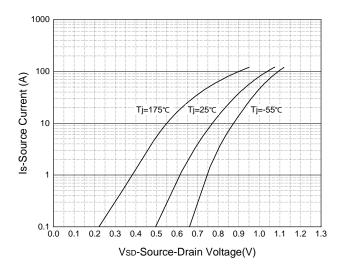


Figure 9: Capacitance Characteristics

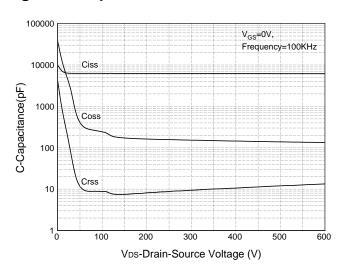
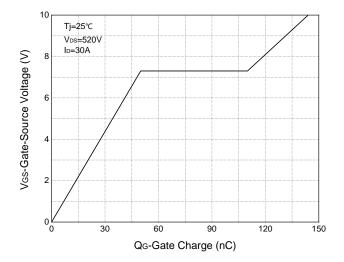
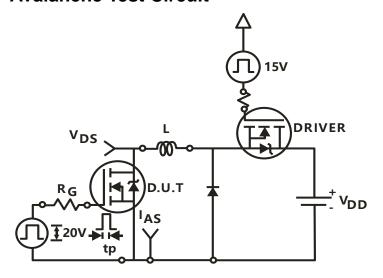


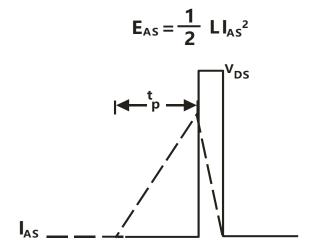
Figure 10: Gate Charge Characteristics



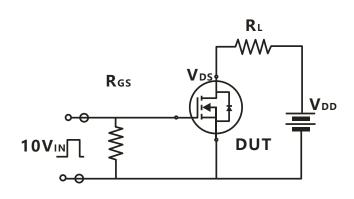


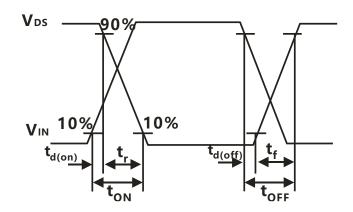
Avalanche Test Circuit



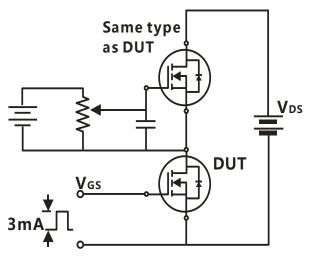


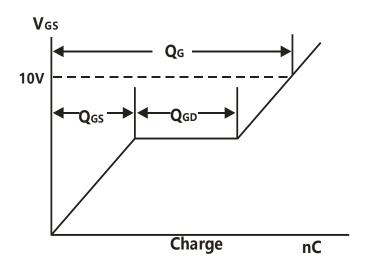
Switching Time Test Circuit





Gate Charge Test Circuit





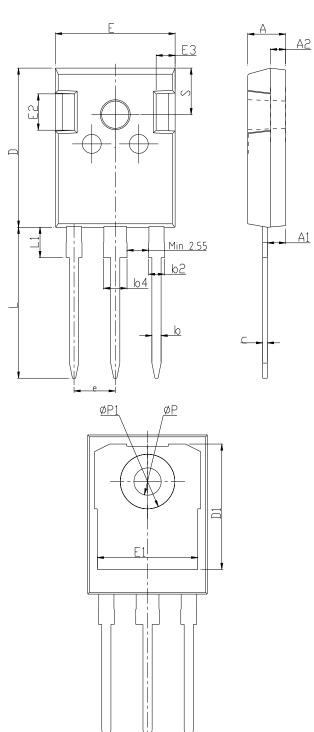


Device Per Unit

Package Type	Unit	Quantity
TO-247A-3L	Tube	30

Package Information

TO-247A-3L

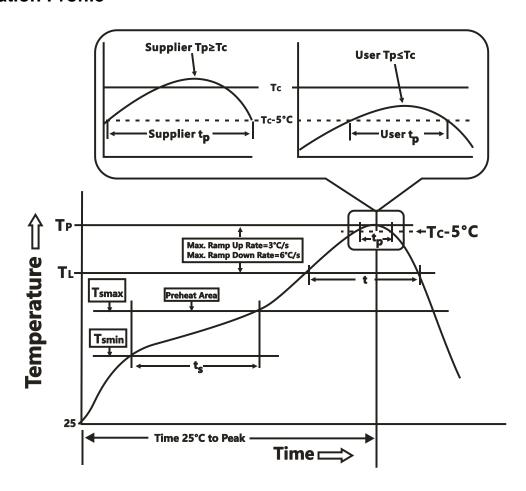


COMMON DIMENSIONS

SYMBOL	mm			
STIVIBUL	MIN	NOM	MAX	
Α	4.80	5.00	5.20	
A1	2.21	2.41	2.59	
A2	1.85	2.00	2.15	
b	1.11	1.21	1.36	
b2	1.91	2.01	2.21	
b4	2.91	3.01	3.21	
С	0.51	0.61	0.75	
D	20.70	21.00	21.30	
D1	16.25	16.55	16.85	
Е	15.50	15.80	16.10	
E1	13.00	13.30	13.60	
E2	4.80	5.00	5.20	
E3	2.30	2.50	2.70	
е	5.44BSC			
L	19.62	19.92	20.22	
L1	-	-	4.30	
ФР	3.40	3.60	3.80	
ФР1	-	-	7.30	
S	6.15BSC			



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly		
Preheat & Soak				
Temperature min (T _{smin})	100 °C	150 °C		
Temperature max (T _{smax})	150 °C	200 °C		
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds		
Average ramp-up rate	3 °C/second max.	3°C/second max.		
(T _{smax} to T _P)	5 C/Second max.			
Liquidous temperature (T _L)	183 °C	217 °C		
Time at liquidous (t _L)	60-150 seconds	60-150 seconds		
Peak package body Temperature	See Classification Temp in table 1	SacClassification Tampin table 2		
(T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2		
Time (t _P)** within 5°C of the specified	20**	30** seconds		
classification temperature (T _c)	20** seconds			
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.		
Time 25°C to peak temperature	6 minutes max.	8 minutes max.		

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500 Cycles, -55°C~150°C

Customer Service

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