

N-Channel Enhancement Mode MOSFET

Feature

- 100V/135A RDS(ON)= 4.7 m Ω (typ.) @VGS = 10V
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

Pin Description

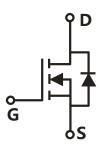


TO-220FB-3L

TO-263-2L

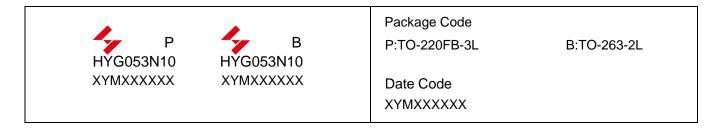
Applications

- Switching application
- Battery management
- Motor control



Single N-Channel MOSFET

Ordering and Marking Information



Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Rat	tings (Tc=25°C Unless Otherwise Noted)		•	
VDSS	Drain-Source Voltage		100	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55	°C
Tstg	Storage Temperature Range		-55 to 175	°C
Is	Source Current-Continuous(Body Diode)	Tc=25°C	135	А
Mounted on	Large Heat Sink		•	1
Ідм	Pulsed Drain Current *	Tc=25°C	400	А
1			135	А
lo	Continuous Drain Current	Tc=100°C	95.4	А
	M	Tc=25°C	208.3	W
Po	Maximum Power Dissipation Tc=		104.2	W
R₀c	Thermal Resistance, Junction-to-Case		0.72	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		62.5	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		416	mJ

- Note: * Repetitive rating; pulse width limited by max.junction temperature.
 - Surface mounted on 1in2 FR-4 board.
 - Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 Ω , VGs =10V.

Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY	HYG053N10NS2		
Symbol	Faranietei	rest Conditions	Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	100	-	-	V
During to Committee Committee		VDS=100V,VGS=0V	-	-	1	μΑ
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μΑ
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	2.2	3	3.8	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	1	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =50A	-	4.7	5.5	$m\Omega$
Diode Cha	Diode Characteristics					
VsD	Diode Forward Voltage	IsD=50A,Vgs=0V	-	0.92	1.3	V
trr	Reverse Recovery Time	lon_F0A_dlon/dt_100A/ug	-	57	-	ns
Qrr	Reverse Recovery Charge	IsD=50A,dIsD/dt=100A/µs	-	102	-	nC

HYG053N10NS2P/B



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Ob. a.l	Barrantan	T	HY	HYG053N10NS2		
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=500kHz	-	0.83	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	6457	-	
Coss	Output Capacitance	V _{DS} =25V,	-	1416	-	pF
Crss	Reverse Transfer Capacitance	Frequency=500kHz	-	104	-]
td(ON)	Turn-on Delay Time		-	22	-	
Tr	Turn-on Rise Time	V _{DD} =50V,R _G =2.5Ω,	-	70	-	
t d(OFF)	Turn-off Delay Time	IDS=50A,VGS=10V	-	38	-	ns
Tf	Turn-off Fall Time		-	51	-]
Gate Cha	Gate Charge Characteristics					
Qg	Total Gate Charge(V _{GS} =10V)		-	94	-	
Qgs	Gate-Source Charge	\/ 90\/ I 50A	-	37	-	nC
Qgd	Gate-Drain Charge	V_{DS} =80V, I_{DS} =50A	-	15	-	
V _{plateau}	Gate plateau voltage		-	5.3	-	V

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

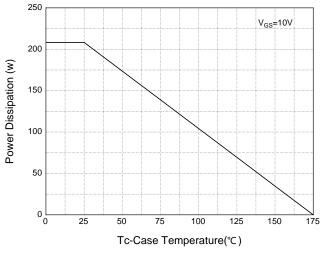


Figure 3: Safe Operation Area

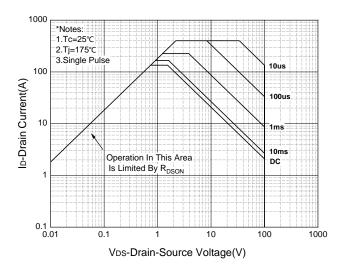


Figure 5: Output Characteristics

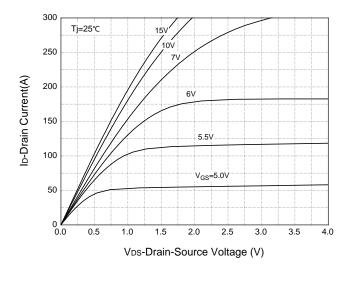


Figure 2: Drain Current

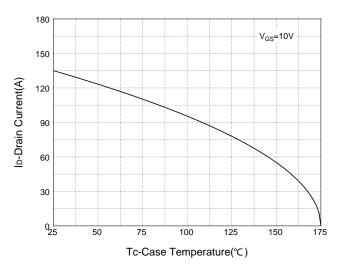


Figure 4: Thermal Transient Impedance

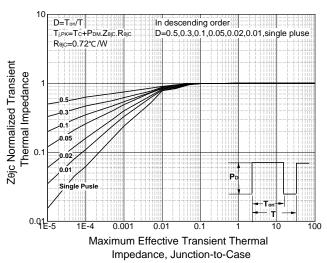
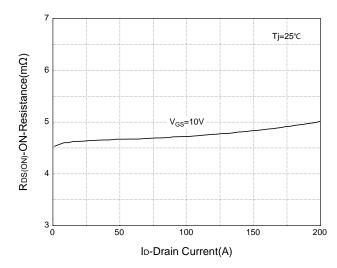


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

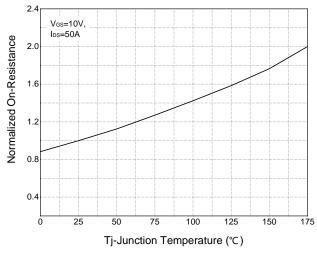


Figure 9: Capacitance Characteristics

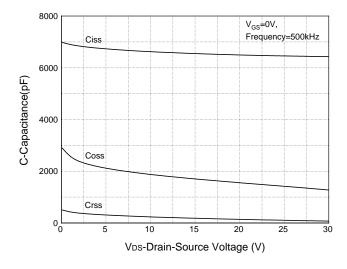


Figure 8: Source-Drain Diode Forward

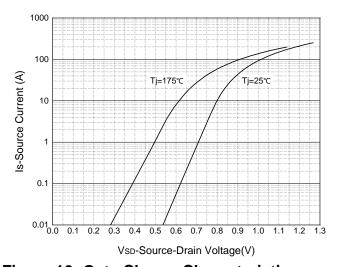
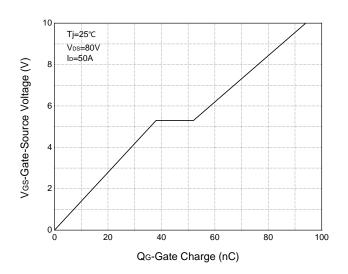
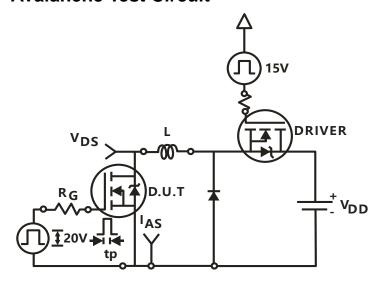


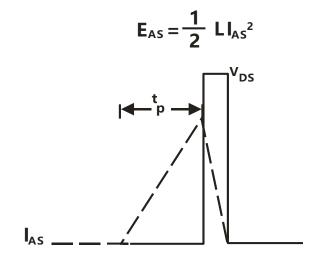
Figure 10: Gate Charge Characteristics



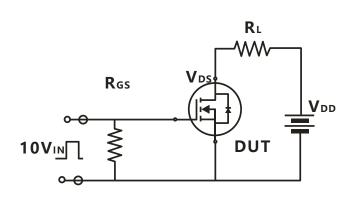


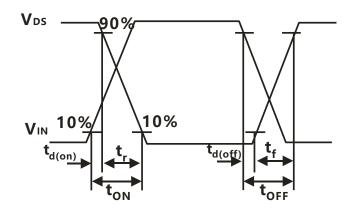
Avalanche Test Circuit



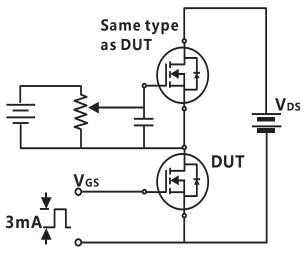


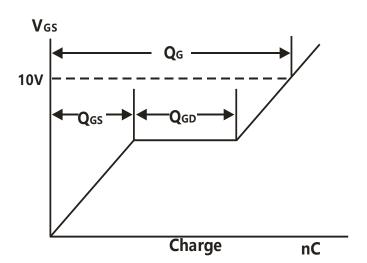
Switching Time Test Circuit





Gate Charge Test Circuit





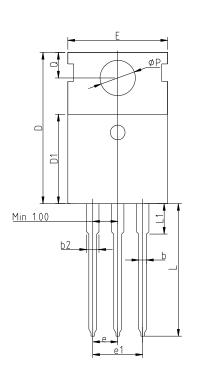


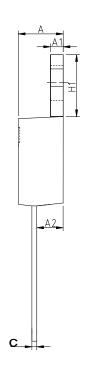
Device Per Unit

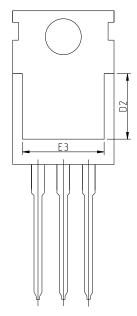
Package Type	Unit	Quantity
TO-220FB-3L	Tube	50
TO-263-2L	Reel	800

Package Information

TO-220FB-3L



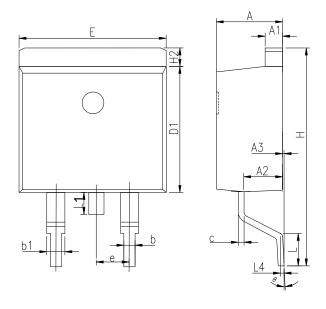


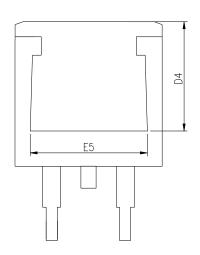


COMMON DIMENSIONS				
SYMBOL	mm			
STIVIBUL	MIN	NOM	MAX	
Α	4.37	4.57	4.77	
A1	1.25	1.30	1.40	
A2	2.20	2.40	2.60	
р	0.70	0.80	0.95	
b2	1.17	1.27	1.47	
С	0.45	0.50	0.60	
D	15.10	15.60	16.10	
D1	8.80	9.10	9.40	
D2	5.50	6.30	7.10	
Е	9.70	10.00	10.30	
E3	7.00	7.80	8.60	
е		2.54 BSC		
e1		5.08 BSC		
H1	6.25	6.50	6.85	
L	12.75	13.50	13.80	
L1	-	3.10	3.40	
ФР	3.40	3.60	3.80	
Q	2.60	2.80	3.00	



TO-263-2L

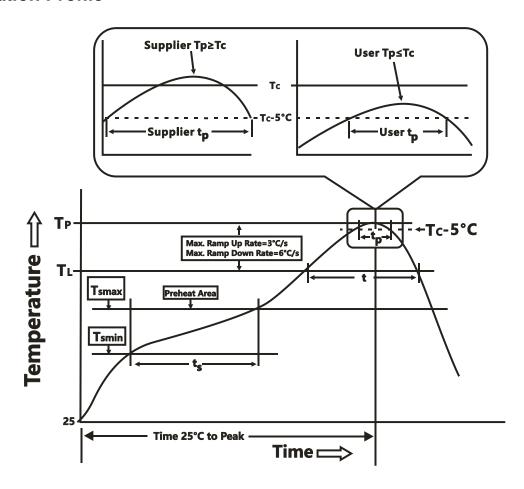




COMMON DIMENSIONS				
SYMBOL		mm		
STIVIBOL	MIN	NOM	MIN	
Α	4.37	4.57	4.77	
A1	1.22	1.27	1.42	
A2	2.49	2.69	2.89	
A3	0	0.13	0.25	
b	0.70	0.81	0.96	
b1	1.17	1.27	1.47	
С	0.30	0.38	0.53	
D1	8.50	8.70	8.90	
D4	6.60	-	-	
Е	9.86	10.16	10.36	
E5	7.06	-	-	
е		2.54 BSC		
Н	14.70	15.10	15.50	
H2	1.07	1.27	1.47	
L	2.00	2.30	2.60	
L1	1.40	1.55	1.70	
L4		0.25 BSC		
θ	0° 5° 9°			



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
	Preheat & Soak		
Temperature min (T _{smin})	100 °C	150 °C	
Temperature max (T _{smax})	150 °C	200 °C	
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds	
Average ramp-up rate	3 °C/second max.	3°C/second max.	
(T _{smax} to T _P)	5 C/second max.	3 C/Second max.	
Liquidous temperature (T _L)	183 °C	217 °C	
Time at liquidous (t _L)	60-150 seconds	60-150 seconds	
Peak package body Temperature	See Classification Temp in table 1	See Classification Tempin table 2	
(T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2	
Time (t _P)** within 5°C of the specified	20** accords	20** 000000	
classification temperature (T _o)	20** seconds	30** seconds	
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	

^{*}Tolerance for peak profile Temperature (T_P) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	250/500 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com Technical Support:Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: http://www.hymexa.com/