

• General Description

The AGM20T09C combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

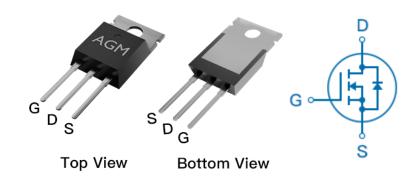
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
200V	9.3mΩ	110A

TO-220 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM20T09C	AGM20T09C	TO-220			1000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	200	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	110	А
_	Drain Current-Continuous(T⊂=100 ℃)	75	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	440	А
PD	Maximum Power Dissipation(Tc=25℃)	278	w
	Maximum Power Dissipation(Tc=100℃)	111	w
EAS	Avalanche energy (Note 3)	1352	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹		62	°C/W
RøJC	Thermal Resistance Junction-Case ¹		0.45	°C/W



Table 3. Electrical Characteristics (TJ=25℃unless otherwise noted)

Гable 3. Electrical Characteristics (ТЈ=25°С unless otherwise noted)						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	200			V
IDSS	Zero Gate Voltage Drain Current	VDS=200V,VGS=0V			1.0	μΑ
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250µA	2.0		4.0	V
gFS	Forward Transconductance	VDS=5V,ID=10A		10	-	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=35A		9.3	10.5	mΩ
Dynamic	Characteristics					
Ciss	Input Capacitance	VDS=100V,VGS=0V,		10656		pF
Coss	Output Capacitance	F=1MHZ		16		pF
Crss	Reverse Transfer Capacitance			389		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz				Ω
Switching	j Times					
td(on)	Turn-on Delay Time			46	-	nS
tr	Turn-on Rise Time	VGS=10V,VDS=100V,		24		nS
td(off)	Turn-Off Delay Time	ID=55A,RGEN=4.7Ω		88	-	nS
tf	Turn-Off Fall Time			18	-	nS
Qg	Total Gate Charge			145	-	nC
Qgs	Gate-Source Charge	VGS=10V, VDS=100V, ID=55A		49		nC
Qgd	Gate-Drain Charge	- 1001, 15 00/1		27		nC
Source-D	rain Diode Characteristics					
ISD	Source-Drain Current(Body Diode)				110	А
VSD	Forward on Voltage	VGS=0V,IS=50A			1.2	V
trr	Reverse Recovery Time	IF=50A , dI/dt=100A/μs		185		ns
Qrr	Reverse Recovery Charge			469		nc

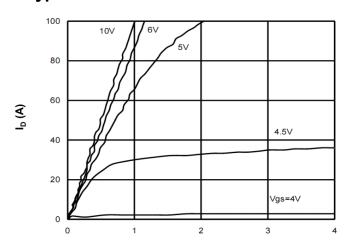
Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

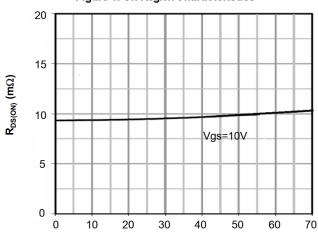
Notes 3.EAS condition: TJ=25 $^{\circ}$ C,VDD=50V,Vgs=10V,ID=52A,L=1mH,RG=25ohm



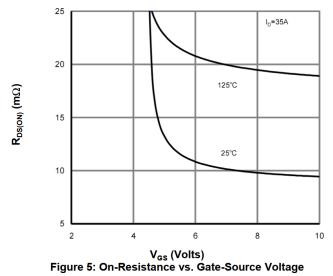
Typical Characteristics

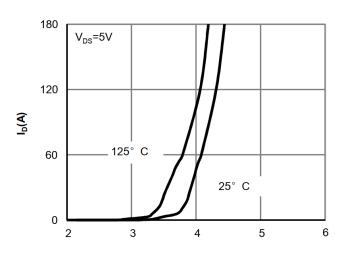


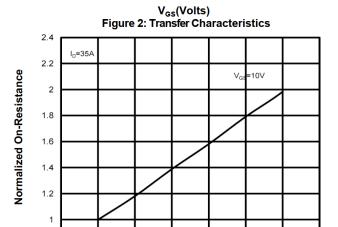




 $I_{\text{D}}\left(\text{A}\right)$ Figure 3: On-Resistance vs. Drain Current and Gate Voltage

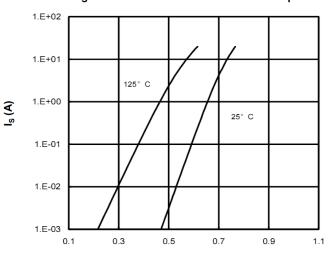






Temperature (°C) Figure 4: On-Resistance vs. Junction Temperature

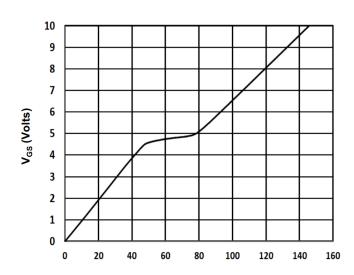
175



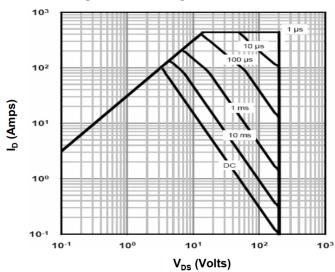
 V_{SD} (Volts) Figure 6: Body-Diode Characteristics

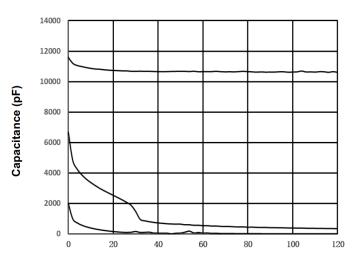
8.0





 \mathbf{Q}_{g} (nC) Figure 7: Gate-Charge Characteristics





V_{DS} (Volts) Figure 8: Capacitance Characteristics



Test Circuits and Waveforms

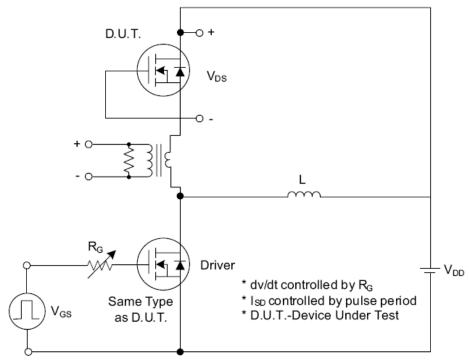


Fig. 1.1 Peak Diode Recovery dv/dt Test Circuit

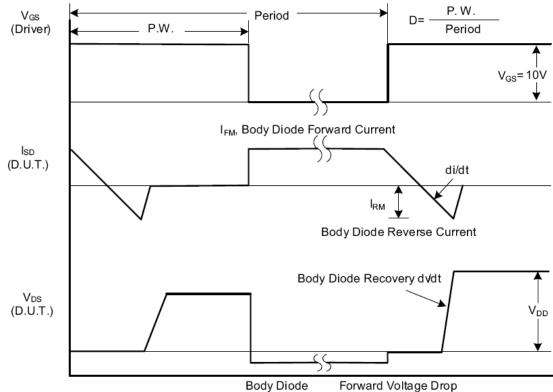


Fig. 1.2 Peak Diode Recovery dv/dt Waveforms



Test Circuits and Waveforms (Cont.)

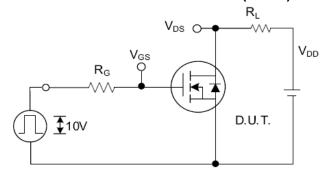


Fig. 2.1 Switching Test Circuit

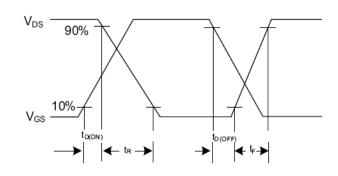


Fig. 2.2 Switching Waveforms

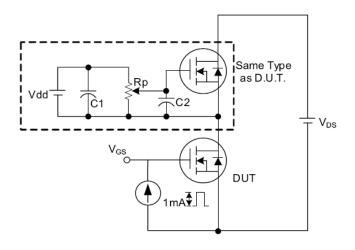


Fig. 3 . 1 Gate Charge Test Circuit

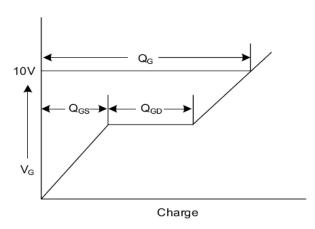


Fig. 3.2 Gate Charge Waveform

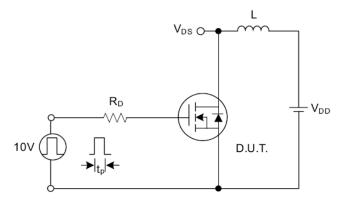


Fig. 4.1 Unclamped Inductive Switching Test Circuit

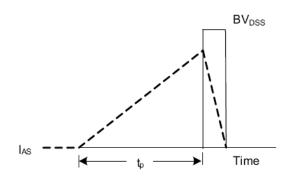
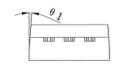
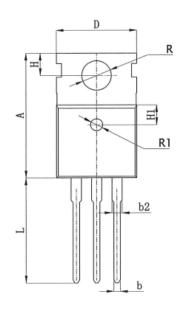


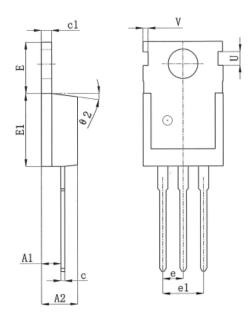
Fig. 4.2 Unclamped Inductive Switching Waveforms



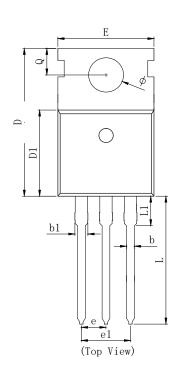
TO-220 PACKAGE INFORMATION

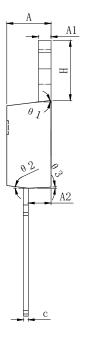


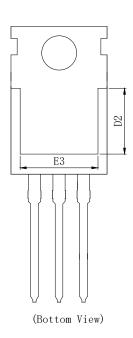




MILLIMETER		
MIN	NOM	MAX
15. 400	15. 600	15. 800
2. 350	2. 400	2. 500
4. 400	4. 500	4. 700
0.700	0.800	0. 900
1. 180	1. 310	1. 440
0.480	0.500	0.560
1. 290	1. 300	1. 320
9.800	10.000	10. 200
6. 400	6. 500	6. 600
9. 000	9. 100	9. 200
2. 420	2. 540	2. 660
4. 840	5. 080	5. 320
2, 730	2. 800	2. 870
2. 400	2. 500	2. 600
13. 020	13. 370	13. 720
3. 500	3. 600	3. 730
1.400	1.500	1.600
1.650	1.750	1.850
0.580	0.680	0.780
2°	2.5°	3°
6.5°	7°	7.5°
	15. 400 2. 350 4. 400 0. 700 1. 180 0. 480 1. 290 9. 800 6. 400 9. 000 2. 420 4. 840 2. 730 2. 400 13. 020 3. 500 1. 400 1. 650 0. 580 2°	MIN NOM 15. 400 15. 600 2. 350 2. 400 4. 400 4. 500 0. 700 0. 800 1. 180 1. 310 0. 480 0. 500 1. 290 1. 300 9. 800 10. 000 6. 400 6. 500 9. 000 9. 100 2. 420 2. 540 4. 840 5. 080 2. 730 2. 800 2. 400 2. 500 13. 020 13. 370 3. 500 3. 600 1. 400 1. 500 1. 650 1. 750 0. 580 0. 680 2° 2. 5°







SYMBOL	MILLIMETER			
SIMDOL	MIN	Тур.	MAX	
A	4. 370	4. 570	4.700	
A1	1. 250	1.300	1.400	
A2	2. 150	2.350	2.550	
b	0.700	0.800	0.950	
b1	1. 170	1.270	1.470	
С	0.450	0.500	0.600	
D	15. 100	15.600	16. 100	
D1	8.800	9.100	9.400	
D2	5. 500	6.300 REF		
Е	9. 700	10.000	10. 300	
E3	7. 000	7.600 REF		
е		2.540 BSC		
e1		5.080 BSC		
L	13. 200	13.500	13.800	
L1		3.100	3. 400	
Н	6. 250	6. 500	6.750	
ф	3. 400	3. 600	3.800	
Q	2.600	2. 800	3.000	
θ 1	7° TYP			
θ2	7° TYP			
θ3	3° TYP			



Disclaimer:

The information provided in this document is believed to be accurate and reliable. however, Shenzhen Core Control Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the third version issued on Dec. 20th, 2023. This document replaces all previously provided information.

It is a registered trademark of Shenzhen Core Control Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Electronics Technology Co., Ltd. all rights reserved.