International Rectifier

IRF8734PbF

HEXFET® Power MOSFET

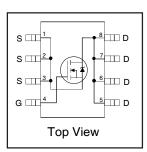
| V _{DSS} | R _{DS(on)} max | Qg (typ.) |
|------------------|-----------------------------------|-----------|
| 30V | $3.5 \text{m}\Omega@V_{GS} = 10V$ | 20nC |

Applications

- Synchronous MOSFET for Notebook Processor Power
- Synchronous Rectifier MOSFET for Isolated DC-DC Converters in Networking Systems

Benefits

- $\bullet~$ Very Low R_{DS(on)} at 4.5V V_{GS}
- Low Gate Charge
- Fully Characterized Avalanche Voltage and Current
- 100% Tested for R_G
- Lead-Free





Absolute Maximum Ratings

| Absolute Maximum Hatings | | | | | |
|--|---|--------------|-------|--|--|
| | Parameter | Max. | Units | | |
| V_{DS} | Drain-to-Source Voltage | 30 | V | | |
| V_{GS} | Gate-to-Source Voltage | ± 20 | V | | |
| I _D @ T _A = 25°C | Continuous Drain Current, V _{GS} @ 10V | 21 | | | |
| I _D @ T _A = 70°C | Continuous Drain Current, V _{GS} @ 10V | 17 | Α | | |
| I _{DM} | Pulsed Drain Current ① | 168 | 1 | | |
| P _D @T _A = 25°C | Power Dissipation ④ | 2.5 | W | | |
| P _D @T _A = 70°C | Power Dissipation ④ | 1.6 | T VV | | |
| | Linear Derating Factor | 0.02 | W/°C | | |
| TJ | Operating Junction and | -55 to + 150 | °C | | |
| T _{STG} | Storage Temperature Range | | | | |

Thermal Resistance

| | Parameter | Тур. | Max. | Units |
|-----------------|--------------------------|------|------|-------|
| $R_{\theta JL}$ | Junction-to-Drain Lead ® | | 20 | °C/W |
| $R_{\theta JA}$ | Junction-to-Ambient ® | | 50 | C/VV |

Notes ① through ⑤ are on page 10

ORDERING INFORMATION:

See detailed ordering and shipping information on the last page of this data sheet.

International
Rectifier

Static @ T_J = 25°C (unless otherwise specified)

| | Parameter | Min. | Тур. | Max. | Units | Conditions |
|--------------------------------|---|------|-------|------|-------|---|
| BV _{DSS} | Drain-to-Source Breakdown Voltage | 30 | | | V | $V_{GS} = 0V, I_D = 250\mu A$ |
| $\Delta BV_{DSS}/\Delta T_{J}$ | Breakdown Voltage Temp. Coefficient | | 0.023 | | V/°C | Reference to 25°C, I _D = 1mA |
| R _{DS(on)} | Static Drain-to-Source On-Resistance | | 2.9 | 3.5 | 0 | V _{GS} = 10V, I _D = 21A ③ |
| | Static Drain-to-Source Off-nesistance | | 4.2 | 5.1 | mΩ | $V_{GS} = 4.5V, I_D = 17A$ ③ |
| V _{GS(th)} | Gate Threshold Voltage | 1.35 | 1.80 | 2.35 | ٧ | $V_{DS} = V_{GS}, I_{D} = 50 \mu A$ |
| $\Delta V_{GS(th)}$ | Gate Threshold Voltage Coefficient | | -6.5 | | mV/°C | $V_{DS} = V_{GS}$, $I_D = 30\mu$ A |
| I _{DSS} | Drain-to-Source Leakage Current | | | 1.0 | uА | $V_{DS} = 24V, V_{GS} = 0V$ |
| | | | | 150 | μΑ | $V_{DS} = 24V, V_{GS} = 0V, T_{J} = 125^{\circ}C$ |
| I _{GSS} | Gate-to-Source Forward Leakage | | | 100 | nA | $V_{GS} = 20V$ |
| | Gate-to-Source Reverse Leakage | | | -100 | TIA | V _{GS} = -20V |
| gfs | Forward Transconductance | 85 | | | S | $V_{DS} = 15V, I_{D} = 17A$ |
| Q_g | Total Gate Charge | | 20 | 30 | | |
| Q_{gs1} | Pre-Vth Gate-to-Source Charge | | 5.2 | | | $V_{DS} = 15V$ |
| Q _{gs2} | Post-Vth Gate-to-Source Charge | | 2.3 | | nC | $V_{GS} = 4.5V$ |
| Q_{gd} | Gate-to-Drain Charge | | 6.9 | | | I _D = 17A |
| Q_{godr} | Gate Charge Overdrive | | 5.4 | | | See Figs. 16a &16b |
| Q _{sw} | Switch Charge (Q _{gs2} + Q _{gd}) | | 9.2 | | | |
| Q _{oss} | Output Charge | | 15 | | nC | $V_{DS} = 16V, V_{GS} = 0V$ |
| R _G | Gate Resistance | | 1.7 | 3.1 | Ω | |
| t _{d(on)} | Turn-On Delay Time | | 13 | | | $V_{DD} = 15V, V_{GS} = 4.5V$ ③ |
| t _r | Rise Time | | 16 | | | I _D = 17A |
| t _{d(off)} | Turn-Off Delay Time | | 15 | | ns | $R_G = 1.8\Omega$ |
| t _f | Fall Time | | 8.0 | | | See Figs. 15a &15b |
| C _{iss} | Input Capacitance | | 3175 | | | $V_{GS} = 0V$ |
| Coss | Output Capacitance | | 627 | | pF | $V_{DS} = 15V$ |
| C _{rss} | Reverse Transfer Capacitance | | 241 | |] | f = 1.0MHz |

Avalanche Characteristics

| | Parameter | Тур. | Max. | Units |
|-----------------|---------------------------------|------|------|-------|
| E _{AS} | Single Pulse Avalanche Energy ② | | 216 | mJ |
| I _{AB} | Avalanche Current ① | | 17 | Α |

Diode Characteristics

| | Parameter | Min. | Тур. | Max. | Units | Conditions |
|-----------------|---------------------------|------|------|------|-------|--|
| Is | Continuous Source Current | | | 3.1 | | MOSFET symbol |
| | (Body Diode) | | | 3.1 | Α | showing the |
| I _{SM} | Pulsed Source Current | | | 168 | ^ | integral reverse |
| | (Body Diode) ① | | | 100 | | p-n junction diode. |
| V_{SD} | Diode Forward Voltage | | | 1.0 | V | $T_J = 25$ °C, $I_S = 17A$, $V_{GS} = 0V$ ③ |
| t _{rr} | Reverse Recovery Time | | 20 | 30 | ns | $T_J = 25$ °C, $I_F = 17A$, $V_{DD} = 15V$ |
| Q _{rr} | Reverse Recovery Charge | | 25 | 38 | nC | di/dt = 345A/µs ③ |

International TOR Rectifier

IRF8734PbF

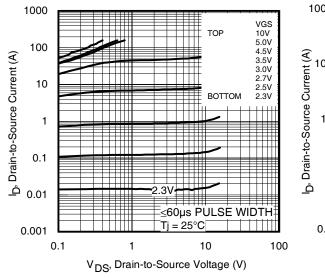
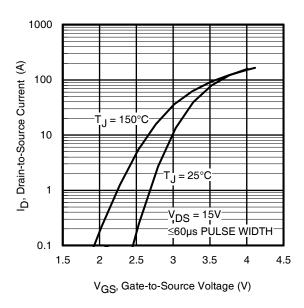


Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics





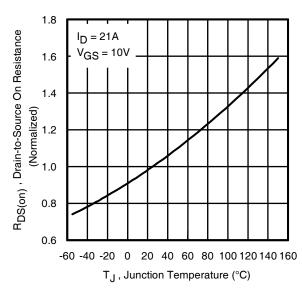


Fig 4. Normalized On-Resistance Vs. Temperature

International

TOR Rectifier

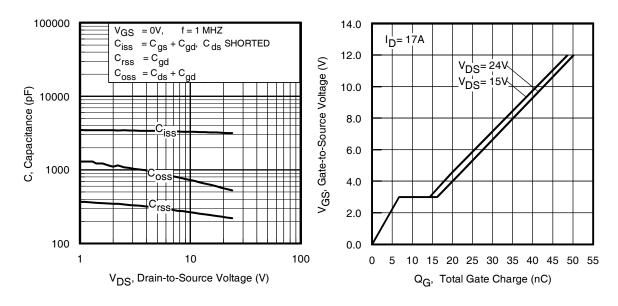


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

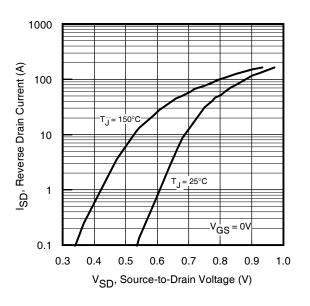


Fig 7. Typical Source-Drain Diode Forward Voltage

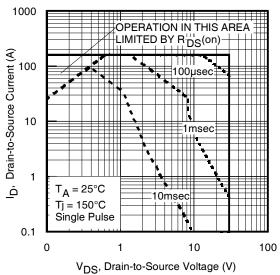
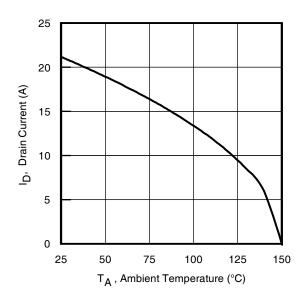


Fig 8. Maximum Safe Operating Area





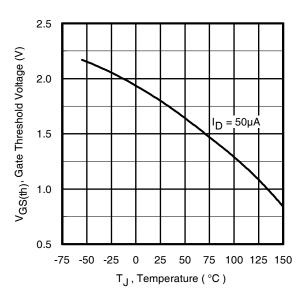


Fig 9. Maximum Drain Current Vs. Ambient Temperature

Fig 10. Threshold Voltage Vs. Temperature

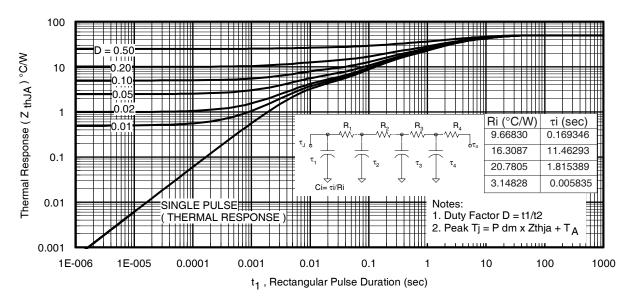


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

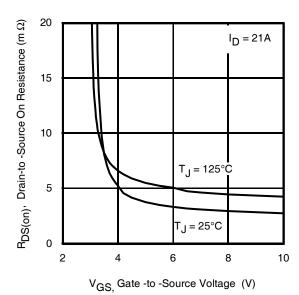


Fig 12. On-Resistance Vs. Gate Voltage

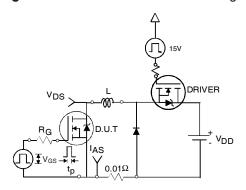


Fig 14a. Unclamped Inductive Test Circuit

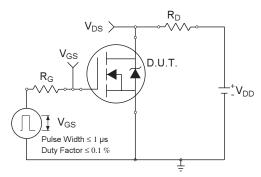


Fig 15a. Switching Time Test Circuit

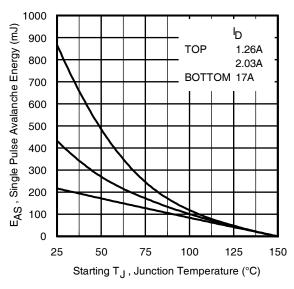


Fig 13c. Maximum Avalanche Energy Vs. Drain Current

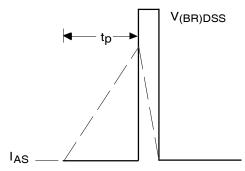


Fig 14b. Unclamped Inductive Waveforms

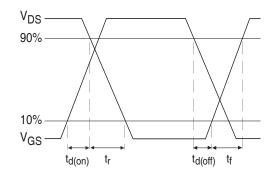


Fig 15b. Switching Time Waveforms www.irf.com

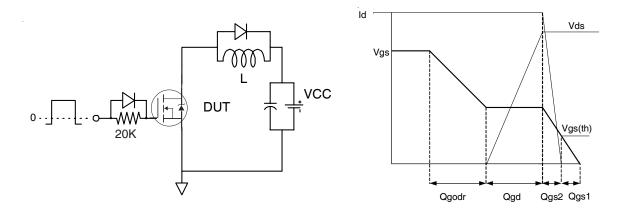


Fig 16a. Gate Charge Test Circuit

Fig 16b. Gate Charge Waveform

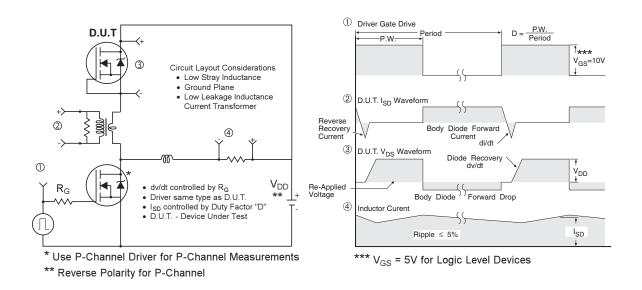
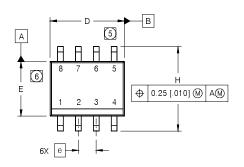


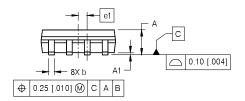
Fig 17. Diode Reverse Recovery Test Circuit for HEXFET® Power MOSFETs

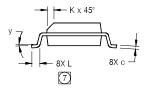
International IOR Rectifier

SO-8 Package Outline(Mosfet & Fetky) Dimensions are shown in milimeters (inches)



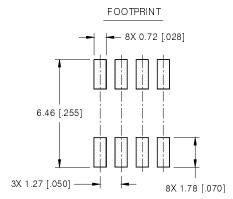
| DIM | INCHES | | MILLIM | ETERS |
|-----|---------|------------|------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | .0532 | .0688 | 1.35 | 1.75 |
| A1 | .0040 | .0098 | 0.10 | 0.25 |
| b | .013 | .020 | 0.33 | 0.51 |
| С | .0075 | .0098 | 0.19 | 0.25 |
| D | .189 | .1968 | 4.80 | 5.00 |
| Е | .1497 | .1574 | 3.80 | 4.00 |
| е | .050 B/ | ASIC | 1.27 BASIC | |
| e 1 | .025 B/ | .025 BASIC | | BASIC |
| Н | .2284 | .2440 | 5.80 | 6.20 |
| K | .0099 | .0196 | 0.25 | 0.50 |
| L | .016 | .050 | 0.40 | 1.27 |
| У | 0° | 8° | 0° | 8° |



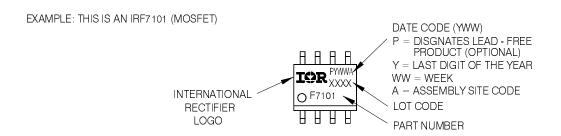


NOTES:

- 1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994
- 2. CONTROLLING DIMENSION: MILLIMETER
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- 5 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 [.006].
- 6 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS MOLD PROTRUSIONS NOT TO EXCEED 0.25 [.010].
- 7 DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE

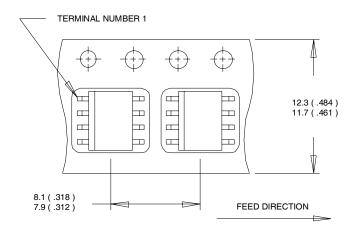


SO-8 Part Marking Information



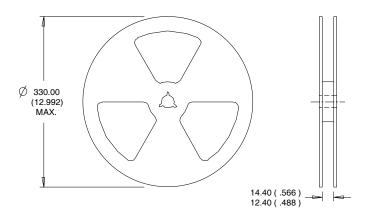
Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

SO-8 Tape and Reel (Dimensions are shown in milimeters (inches))



NOTES:

- CONTROLLING DIMENSION : MILLIMETER.
 ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
- 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

International

TOR Rectifier

| Orderable part number | Package Type | Standard Pack | | Note |
|-----------------------|--------------|---------------|----------|------|
| | | Form | Quantity | |
| IRF8734PbF | SO-8 | Tube/Bulk | 95 | |
| IRF8734TRPbF | SO-8 | Tape and Reel | 4000 | |

Qualification Information[†]

| Qualification level | Consumer ^{††} | | | | |
|----------------------------|---|--|--|--|--|
| Qualification level | (per JEDEC JESD47F ^{†††} guidelines) | | | | |
| Maiatura Canaitivity Laval | SO-8 | MSL1 | | | |
| Moisture Sensitivity Level | 30-6 | (per JEDEC J-STD-020D ^{†††}) | | | |
| RoHS Compliant | Yes | | | | |

- † Qualification standards can be found at International Rectifier's web site http://www.irf.com/product-info/reliability
- † † Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information: http://www.irf.com/whoto-call/salesrep/
- ††† Applicable version of JEDEC standard at the time of product release.

Note: For the most current drawing please refer to IR website at: http://www.irf.com/package/

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^{\circ}C$, L = 1.69mH $R_G = 25\Omega$, $I_{AS} = 16A$.
- ③ Pulse width \leq 400 μ s; duty cycle \leq 2%.
- 4 When mounted on 1 inch square copper board
- ⑤ R_{θ} is measured at T_J of approximately 90°C.

Data and specifications subject to change without notice



IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105

TAC Fax: (310) 252-7903

Visit us at www.irf.com for sales contact information.02/2009

IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (www.infineon.com).

WARNINGS

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.