

• General Description

The AGM150P10S combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

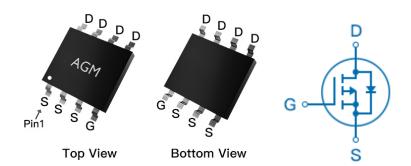
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
-100V	112mΩ	-12A

SOP8 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM150P10S	AGM150P10S	SOP8	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	-12	А
	Drain Current-Continuous(Tc=100℃)	-8.0	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	-48	Α
PD	Maximum Power Dissipation(Tc=25℃)	2.5	w
	Maximum Power Dissipation(Tc=100℃)	1.0	w
EAS	Avalanche energy (Note 3)	64	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹		50	°C/W
RøJC	Thermal Resistance Junction-Case ¹			°C/W



Table 3. Electrical Characteristics (TJ=25℃ unless otherwise noted)

Symbol	Electrical Characteristics (TJ=25℃unio Parameter	Conditions	Min	Тур	Max	Unit	
On/Off Sta	On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	-100			V	
IDSS	Zero Gate Voltage Drain Current	VDS=-100V,VGS=0V			-1.0	μΑ	
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA	
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.2		-2.2	V	
gFS	Forward Transconductance	VDS=-5V,ID=-3A		9.0		S	
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-5A		112	150	mΩ	
		VGS=-4.5V, ID=-3A		128	165	mΩ	
Dynamic C	Characteristics						
Ciss	Input Capacitance			700		pF	
Coss	Output Capacitance	VDS=-50V,VGS=0V		56		pF	
Crss	Reverse Transfer Capacitance	F=1MHZ		8.6		pF	
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz		18		Ω	
Switching	Times						
td(on)	Turn-on Delay Time			5.9		nS	
tr	Turn-on Rise Time	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		3.7		nS	
td(off)	Turn-Off Delay Time	VGS=-10V,VDS=-50V, ID=-5A,RGEN=5Ω		39.5	-	nS	
tf	Turn-Off Fall Time			25		nS	
Qg	Total Gate Charge			13		nC	
Qgs	Gate-Source Charge	VGS=-10V, VDS=-50V, ID=-5A		2.0		nC	
Qgd	Gate-Drain Charge	_ VBG 00V, IB 0/V		2.3		nC	
Source-Drain Diode Characteristics							
ISD	Source-Drain Current(Body Diode)				-12	А	
VSD	Forward on Voltage	VGS=0V,IS=-5A			-1.2	V	
trr	Reverse Recovery Time	IF=-5A , dl/dt=100A/μs ,		21		ns	
Qrr	Reverse Recovery Charge	TJ=25℃		23		nc	

Notes 1. The maximum current rating is package limited.

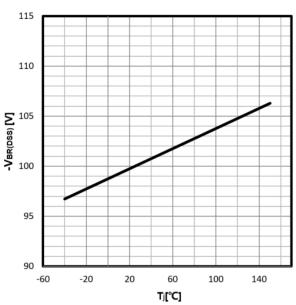
Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

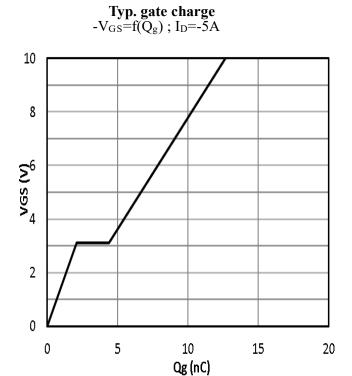
Notes 3.EAS condition: TJ=25 $^{\circ}$ C,VDD=-50V,Vgs=-10V, ID=-16A,L=0.5mH,RG=25ohm



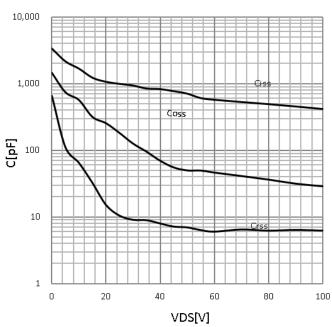
 $\begin{array}{l} \textbf{Gate Threshold Voltage} \\ \textbf{-}V_{TH} \text{=-} f(T_j); \ I_D \text{=-} 250 uA \end{array}$ 1.3 I_D=250uA 1.2 1.1 Vgs(th)_Normalized 1.0 0.9 0.8 0.7 0.6 0.5 0.4 -50 -25 0 25 50 75 100 125 150 Tj - Junction Temperature (°C)

 $\begin{array}{c} \textbf{Drain-source breakdown voltage} \\ \textbf{-}V_{BR(DSS)} \!\!=\!\! f(T_j); \ I_D \!\!=\!\! -250 uA \end{array}$

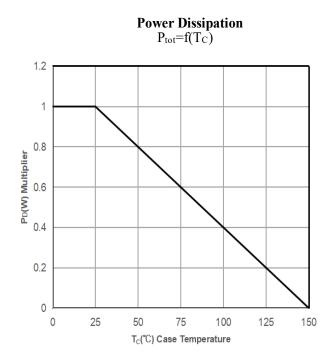


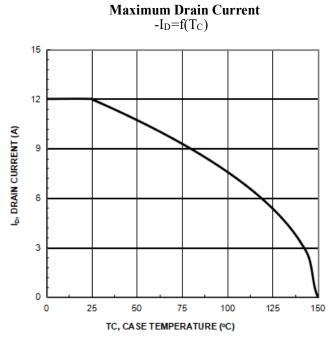


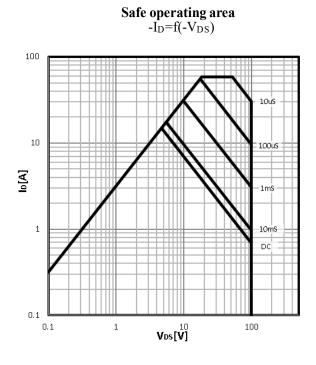
$$\label{eq:capacitances} \begin{split} & \textbf{Typ. capacitances} \\ & C = & f(\text{-}V_{DS}); \ V_{GS} = & 0V; \ f = & 1MHz \end{split}$$

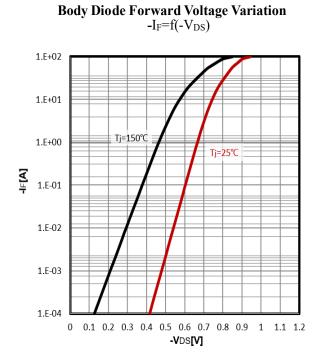




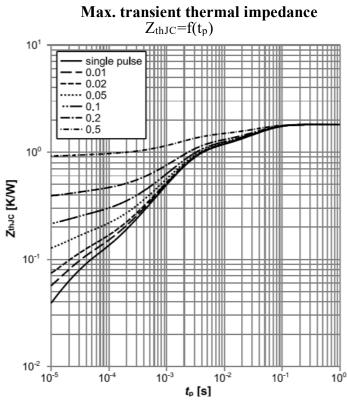






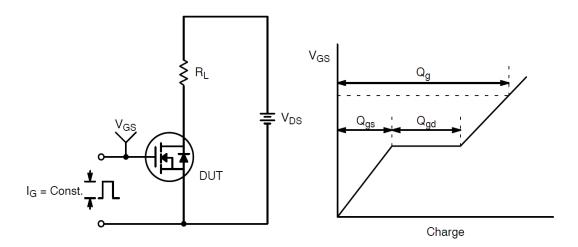




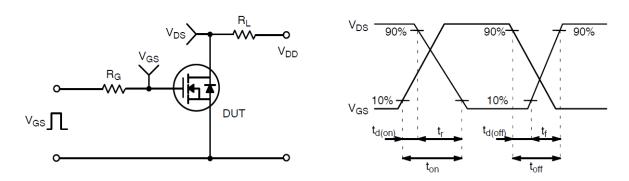




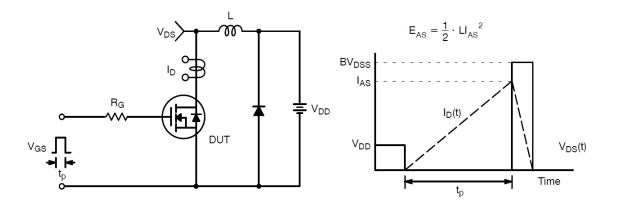
Test Circuit and Waveform:



Gate Charge Test Circuit & Waveform



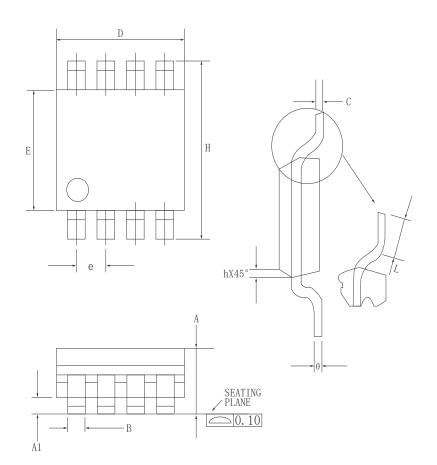
Resistive Switching Test Circuit & Waveforms



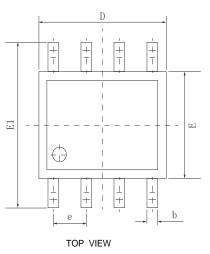
Unclamped Inductive Switching Test Circuit & Waveforms

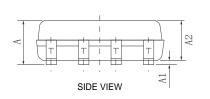


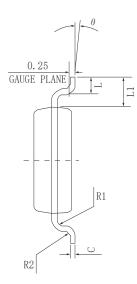
•Dimensions (SOP8)



	MILLIMETERRS		
DIM	MIN	MAX	
A	1. 35	1.75	
A1	0.02	0.15	
В	0. 33	0.5	
С	0.1	0.25	
D	4.8	5	
Е	3.8	4	
е	1. 27 (BSC)		
Н	5.8	6.2	
h	0. 25	0.5	
I	0.4	1.25	
θ	0°	7°	





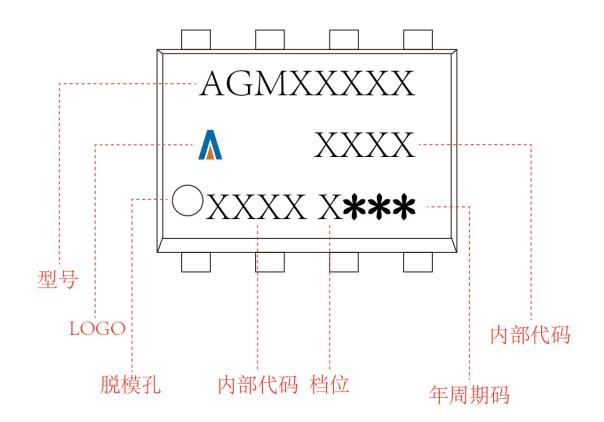


SIDE	VIEW

SYMBOL	MIN	NOM	MAX
A	1.40	1.60	1.80
A1	0.05	0.15	0.25
A2	1.35	1.45	1.55
b	0.30	0.40	0.50
С	0.153	0. 203	0. 253
D	4.80	4.90	5.00
Е	3. 80	3, 90	4.00
E1	5. 80	6.00	6. 20
L	0.45	0.70	1.00
θ	2°	4°	6°
L1	1.04 REF		
е	1.27 BSC		
R1	0.07 TYP		
R2	0.07 TYP		



SOP8
Marking Instructions:





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