

N-Ch 120V Fast Switching MOSFETs

Features

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

Product Summary

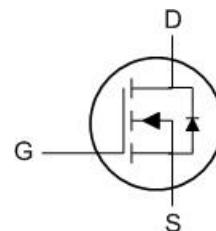
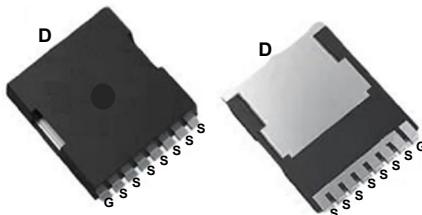


BVDSS	RDS(on)	ID
120V	2.1mΩ	250A

Applications

- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

TOLL-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	120	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1.6}$	250	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1.6}$	158	A
I_{DM}	Pulsed Drain Current ²	1000	A
EAS	Single Pulse Avalanche Energy ³	1468	mJ
I_{AS}	Avalanche Current	---	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ⁴	278	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.45	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	120	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	---	---	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{ V}$, $I_{\text{D}}=98\text{A}$	---	2.1	2.7	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=98\text{A}$	---	---	---	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	2.0	3.0	4.0	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	---	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=120\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=120\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=98\text{A}$	---	222.6	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.6	---	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=98\text{A}$	---	148.9	---	nC
Q_{gs}	Gate-Source Charge		---	61	---	
Q_{gd}	Gate-Drain Charge		---	28.9	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=60\text{V}$, $R_{\text{G_ext}}=2.7\Omega$, $V_{\text{GS}}=10\text{V}$	---	45.3	---	ns
T_r	Rise Time		---	84.7	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	96.8	---	
T_f	Fall Time		---	47.4	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	9847	---	pF
C_{oss}	Output Capacitance		---	1216	---	
C_{rss}	Reverse Transfer Capacitance		---	36	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,4}	$V_G=V_D=0\text{V}$, Force Current	---	---	250	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=98\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time	$I_F=98\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	90.2	---	nS
			---	276	---	nC

Typical Performance Characteristics

Fig 1: Output Characteristics

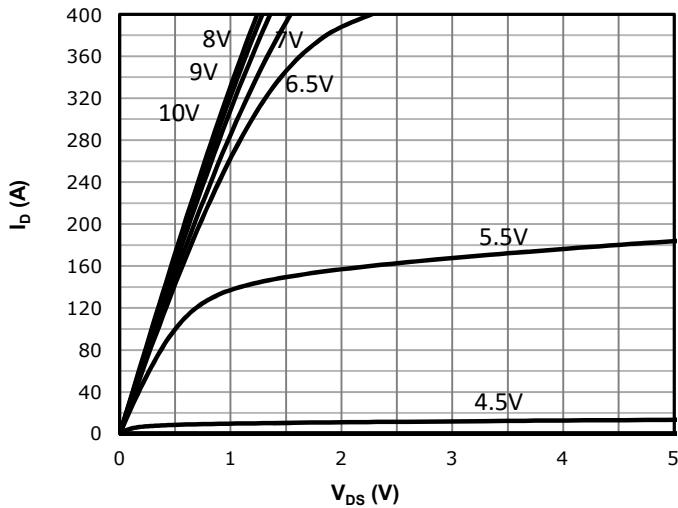


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

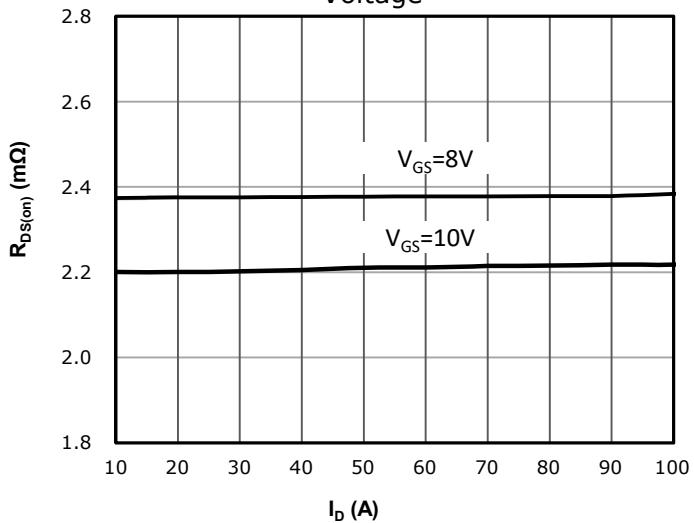


Fig 5: $R_{DS(on)}$ vs. Temperature

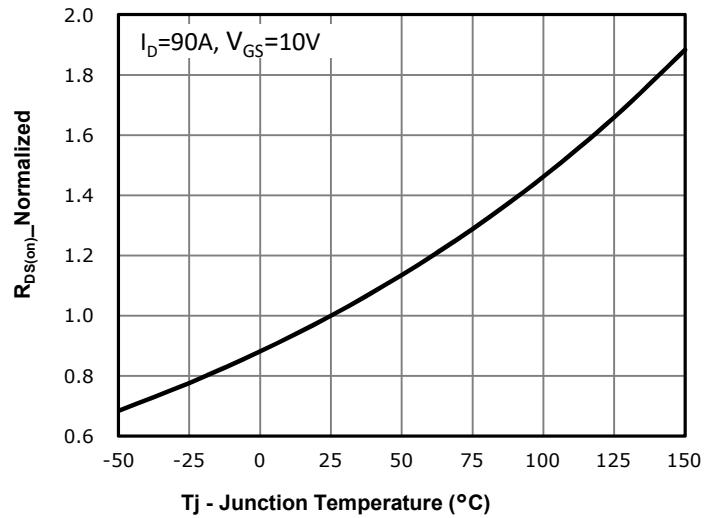


Fig 2: Transfer Characteristics

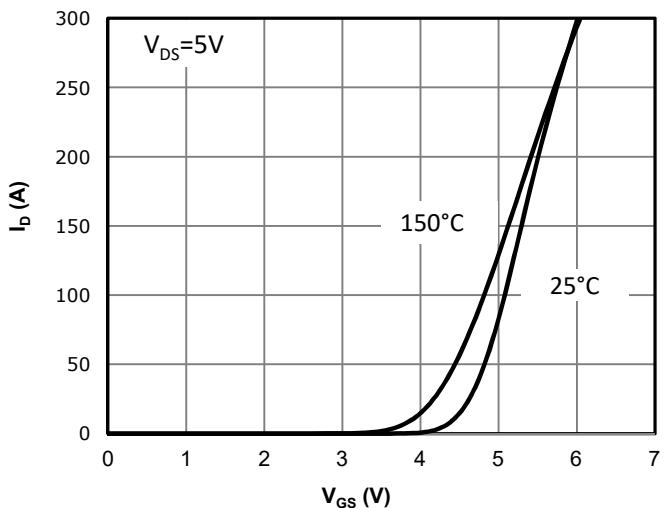


Fig 4: $R_{DS(on)}$ vs Gate Voltage

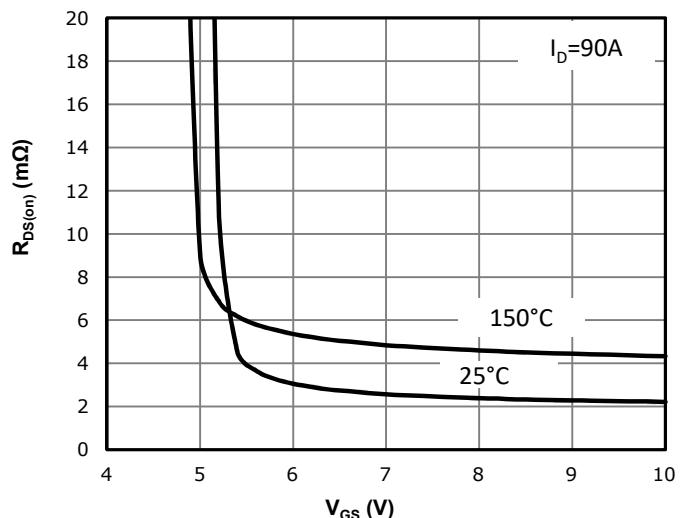
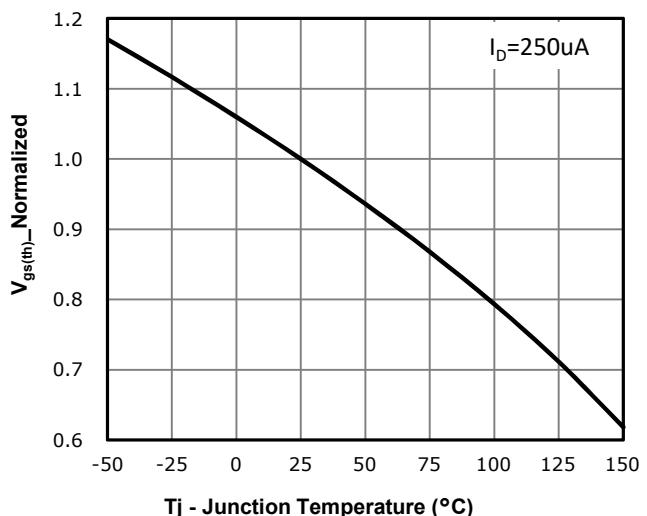


Fig 6: $V_{GS(\text{th})}$ vs. Temperature



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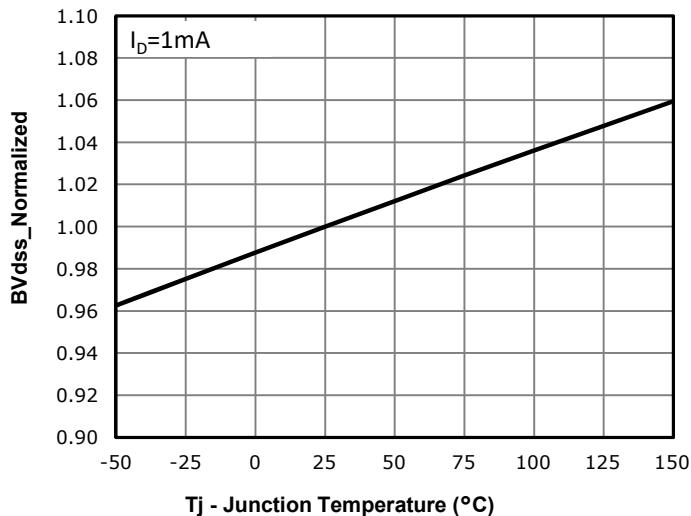
Fig 7: BV_{dss} vs. Temperature

Fig 8: Capacitance Characteristics

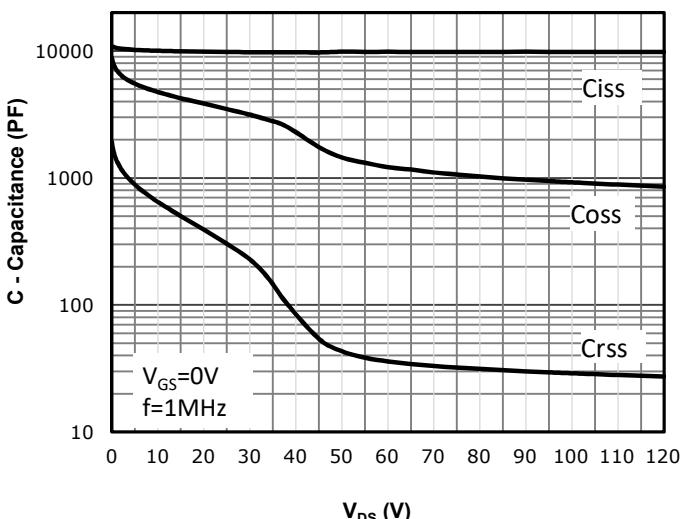


Fig 9: Gate Charge Characteristics

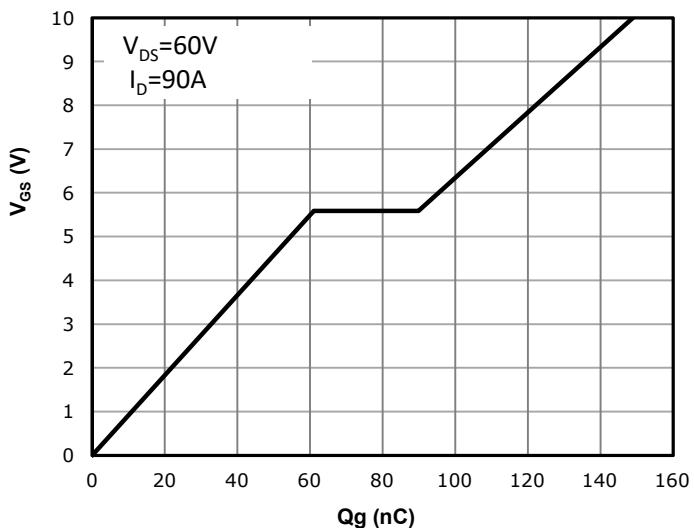


Fig 10: Body-diode Forward Characteristics

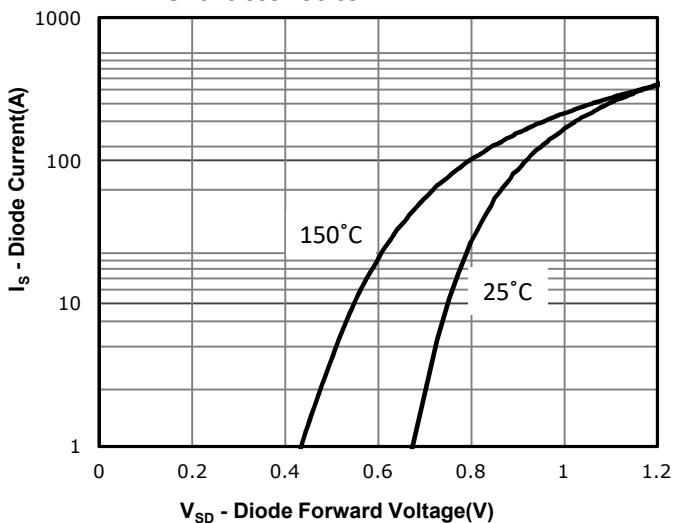


Fig 11: Power Dissipation

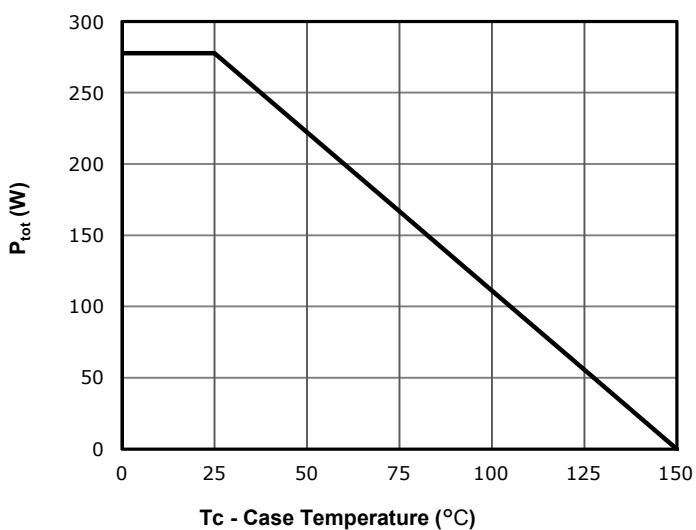
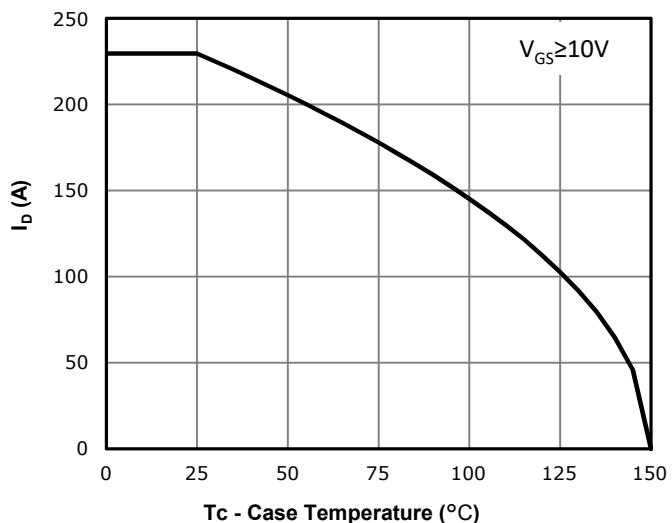


Fig 12: Drain Current Derating



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Fig 13: Safe Operating Area

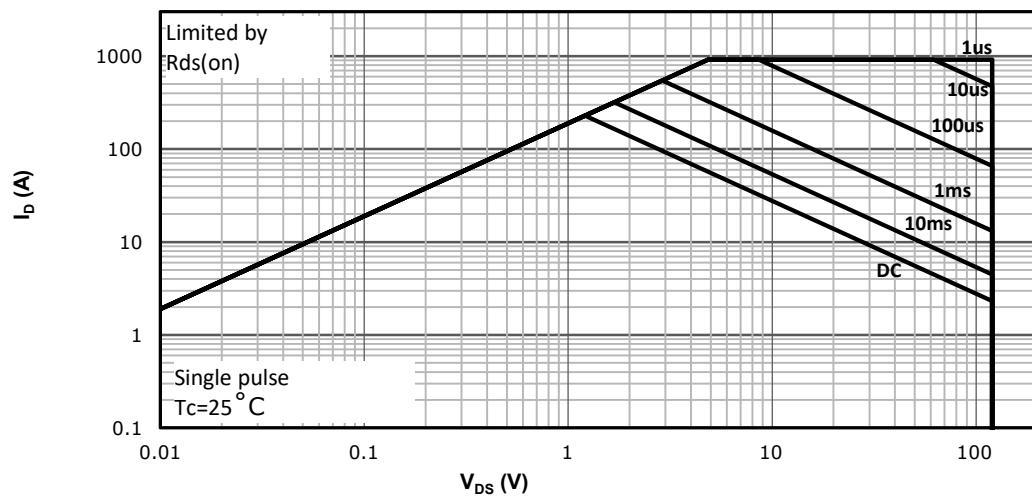
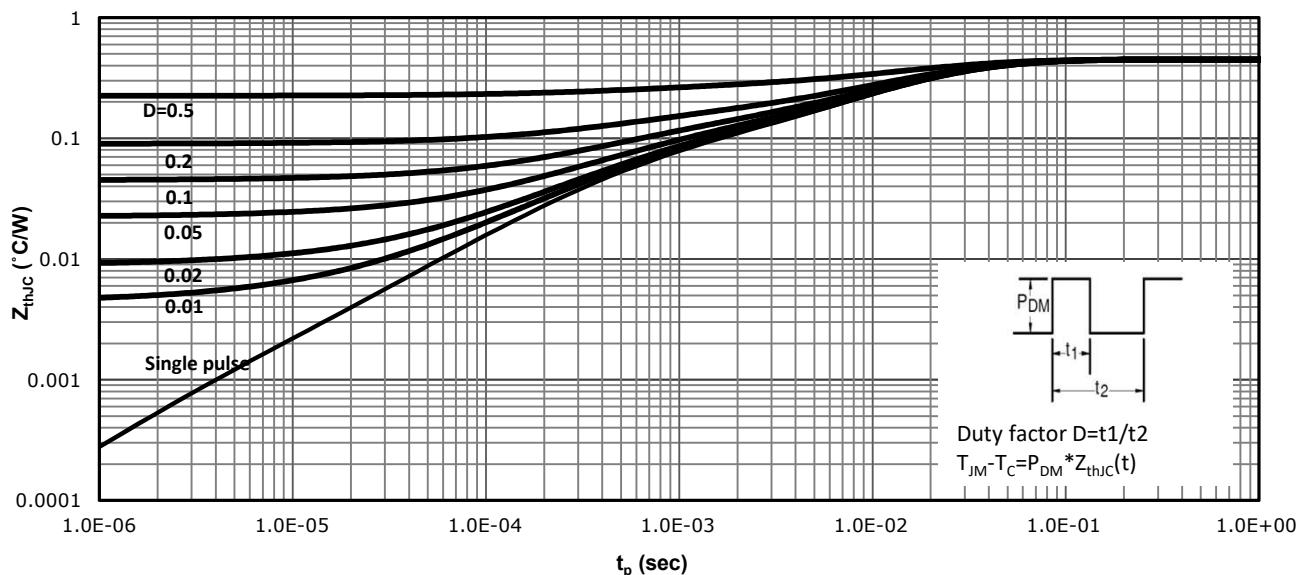
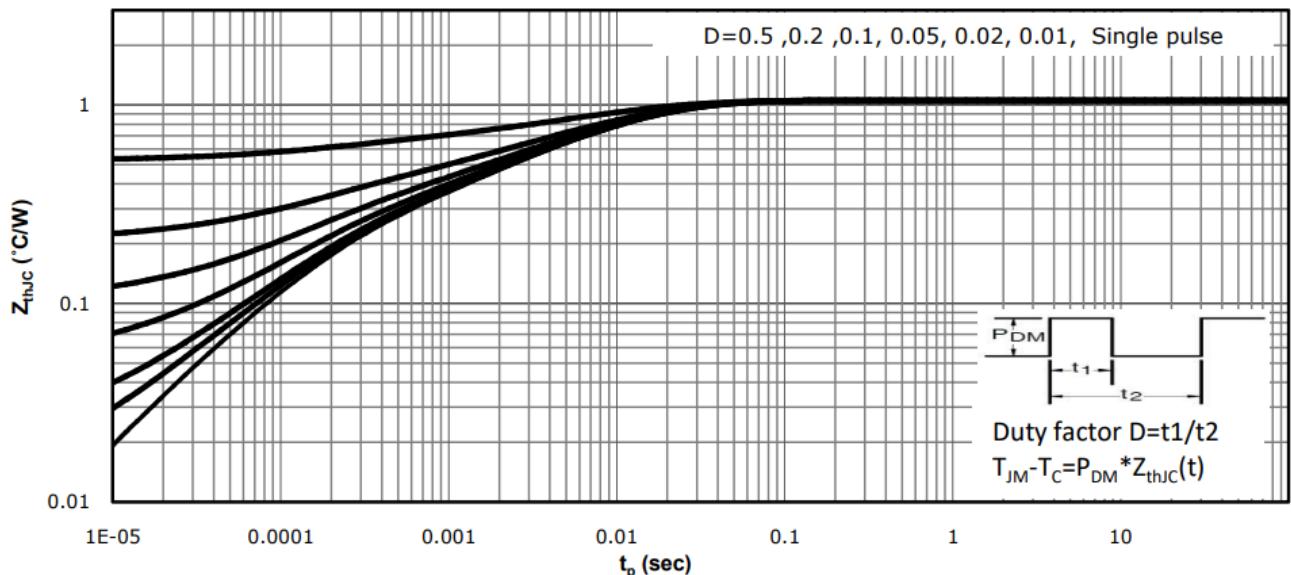


Fig 14: Max. Transient Thermal Impedance



Max. transient thermal impedance

$$Z_{thJC} = f(t_p)$$



Test Circuit

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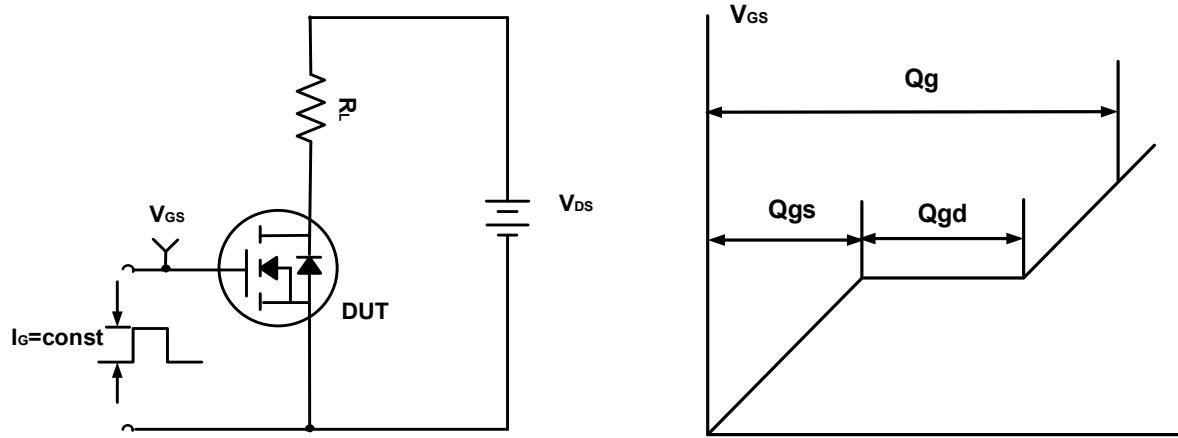


Figure A. Gate Charge Test Circuit & Waveforms

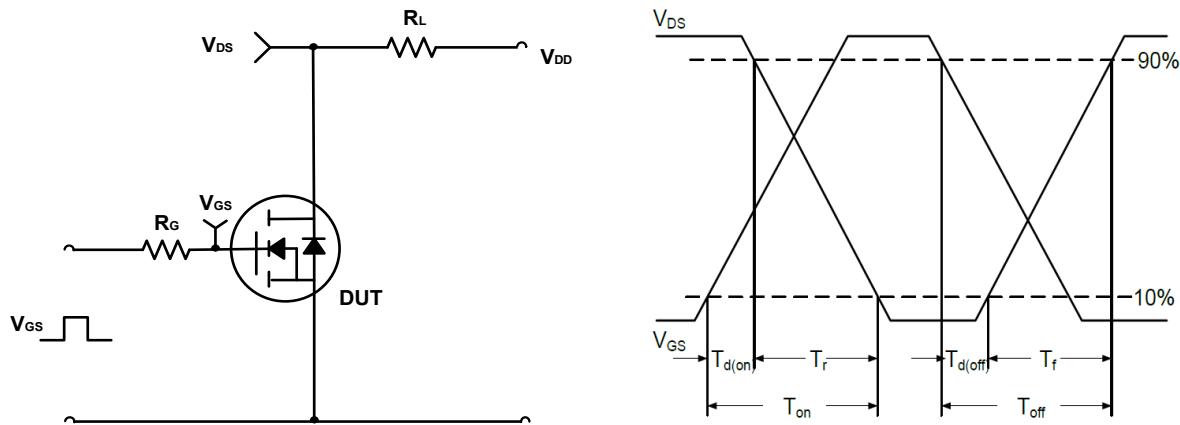


Figure B. Switching Test Circuit & Waveforms

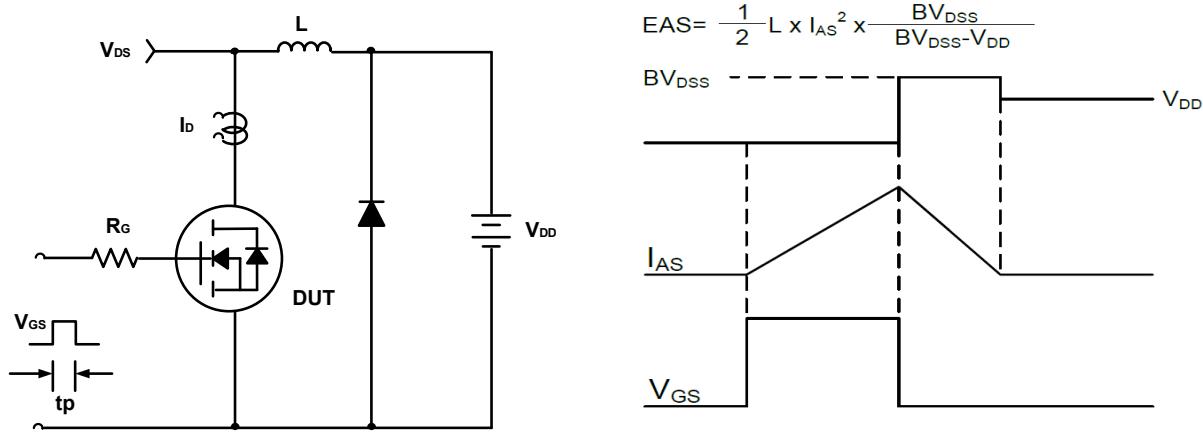
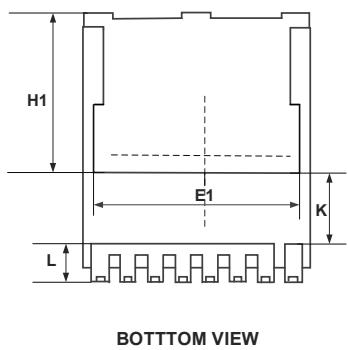
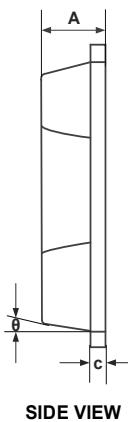
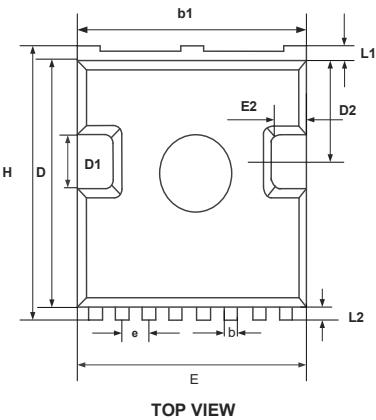


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for TOLL-8L



COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	2.20	2.40
b	0.60	0.90
b1	9.70	9.90
c	0.40	0.60
D	10.20	10.60
D1	3.10	3.50
D2	4.45	4.75
E	9.70	10.10
E1	7.80BSC	
E2	0.50	0.70
e	1.200 BSC	
H	11.45	11.90
H1	6.75 BSC	
K	3.10 REF	
L	1.70	2.10
L1	0.60	0.80
L2	0.50	0.70
θ	10° REF	