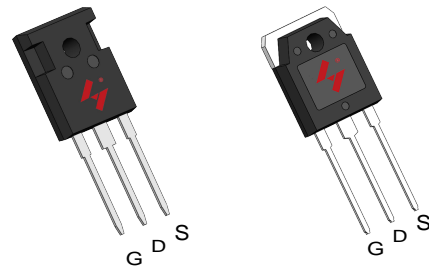


N-Channel Enhancement Mode MOSFET

Features

- 80V/170A
 $R_{DS(ON)} = 3.6 \text{ m}\Omega$ (typ.) @ $V_{GS} = 10\text{V}$
- 100% avalanche tested
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

Pin Description

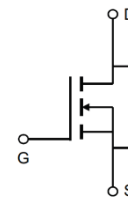


TO-247A-3L

TO-3P-3L

Applications

- Power Management for Inverter Systems.



N Channel MOSFET

Ordering and Marking Information

 W HY3708 YYXXXJWW G	 A HY3708 YYXXXJWW G
	Package Code W : TO-247A-3L A : TO-3P-3L Date Code Assembly Material YYXXX WW G : Lead Free Device

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termination finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this product and/or to this document at any time without notice.

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _C =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		80	V
V _{GSS}	Gate-Source Voltage		±25	
T _J	Maximum Junction Temperature		175	°C
T _{STG}	Storage Temperature Range		-55 to 175	°C
I _S	Diode Continuous Forward Current	T _C =25°C	170	A
Mounted on Large Heat Sink				
I _{DM}	Pulsed Drain Current *	T _C =25°C	680**	A
I _D	Continuous Drain Current	T _C =25°C	170	A
		T _C =100°C	126	
P _D	Maximum Power Dissipation	T _C =25°C	348	W
		T _C =100°C	174	
R _{θJC}	Thermal Resistance-Junction to Case		0.43	°C/W
R _{θJA}	Thermal Resistance-Junction to Ambient		40	
Avalanche Ratings				
E _{AS}	Avalanche Energy, Single Pulsed	L=0.5mH	1320***	mJ

Note : * Repetitive rating ; pulse width limited by junction temperature

** Drain current is limited by junction temperature

*** $V_D=64\text{V}$

Electrical Characteristics ($T_C = 25^{\circ}\text{C}$ Unless Otherwise Noted)

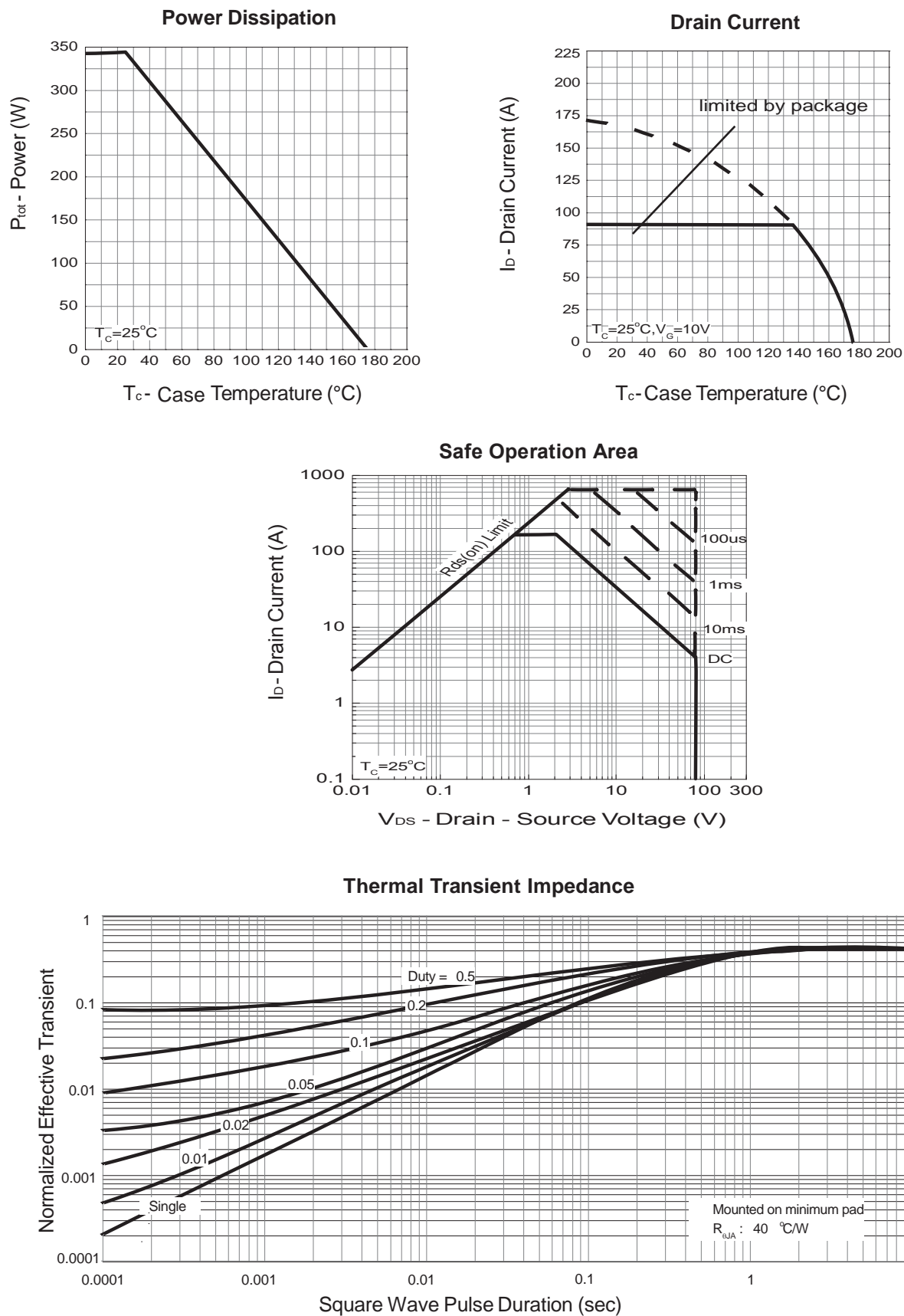
Symbol	Parameter	Test Conditions	HY3708			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	80	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	10	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2.0	3.0	4.0	V
I _{GSS}	Gate Leakage Current	V _{GS} =±25V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} *	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =85A	-	3.6	4.5	mΩ
Diode Characteristics						
V _{SD} *	Diode Forward Voltage	I _{SD} =85 A, V _{GS} =0V	-	0.8	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =85 A, dI _{SD} /dt=100A/μs	-	30	-	ns
Q _{rr}	Reverse Recovery Charge		-	52	-	nC

Electrical Characteristics (Cont.) ($T_C = 25^\circ\text{C}$ Unless Otherwise Noted)

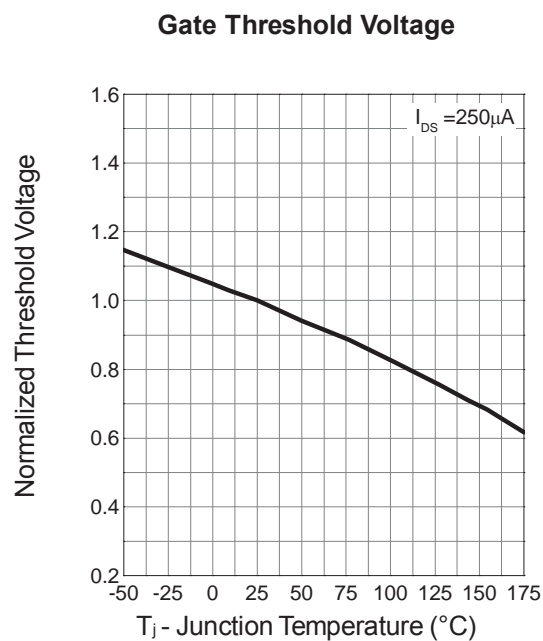
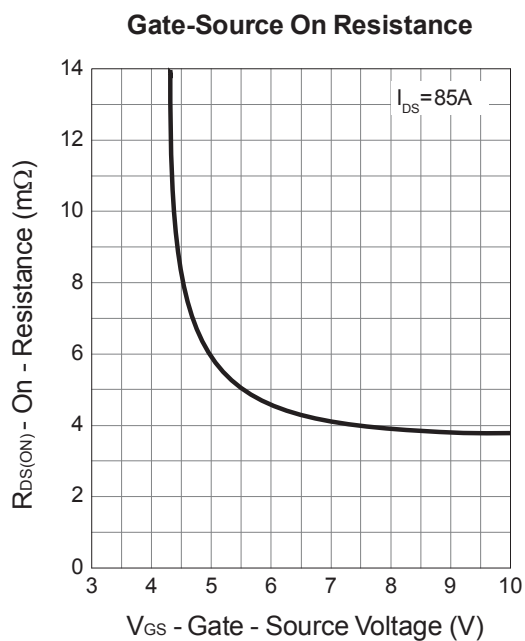
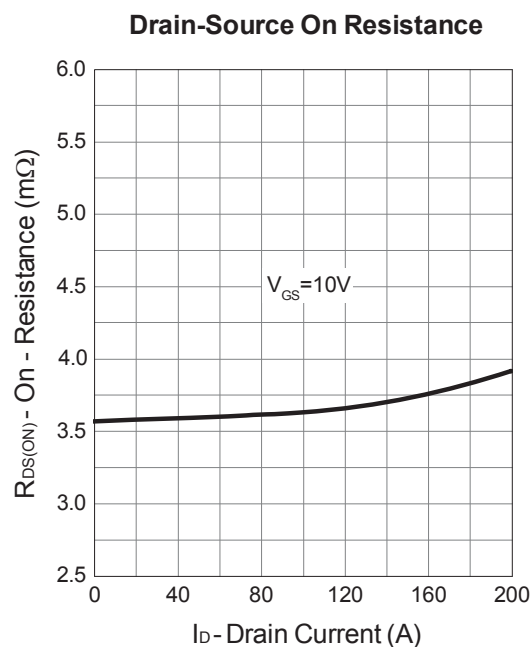
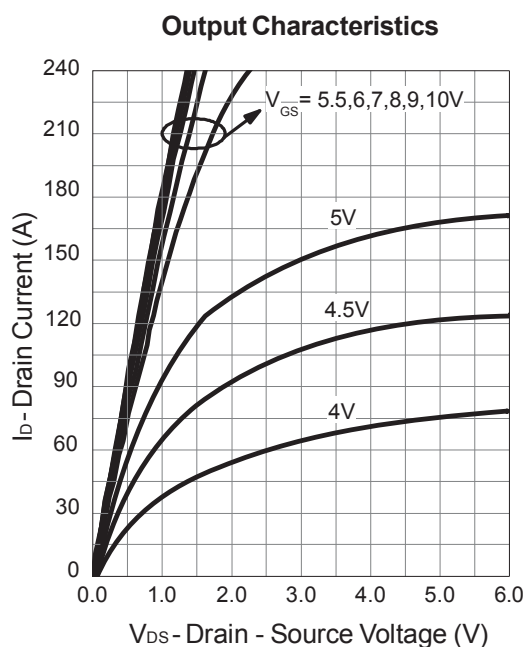
Symbol	Parameter	Test Conditions	HY3708			Unit
			Min.	Typ.	Max.	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.8	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, Frequency=1.0MHz	-	6410	-	pF
C _{oss}	Output Capacitance		-	995	-	
C _{rss}	Reverse Transfer Capacitance		-	530	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =40V, R _G =6 Ω, I _{DS} =85A, V _{GS} =10V,	-	28	-	ns
T _r	Turn-on Rise Time		-	18	-	
t _{d(OFF)}	Turn-off Delay Time		-	42	-	
T _f	Turn-off Fall Time		-	54	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =64V, V _{GS} =10V, I _{DS} =85A	-	154	-	nC
Q _{gs}	Gate-Source Charge		-	25	-	
Q _{gd}	Gate-Drain Charge		-	53	-	

Note * : Pulse test ; pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Typical Operating Characteristics

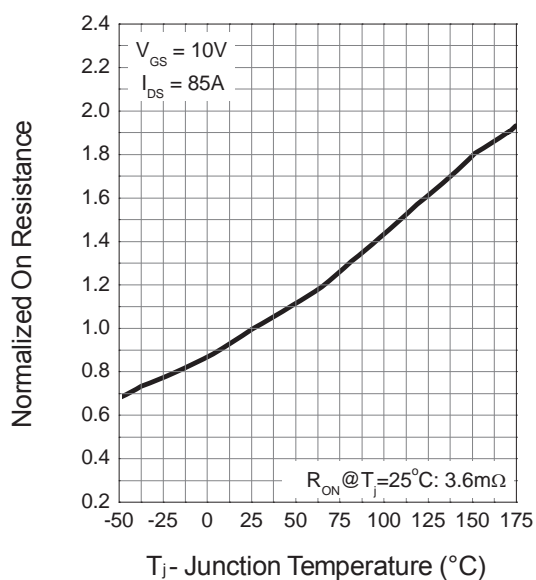


Typical Operating Characteristics (Cont.)

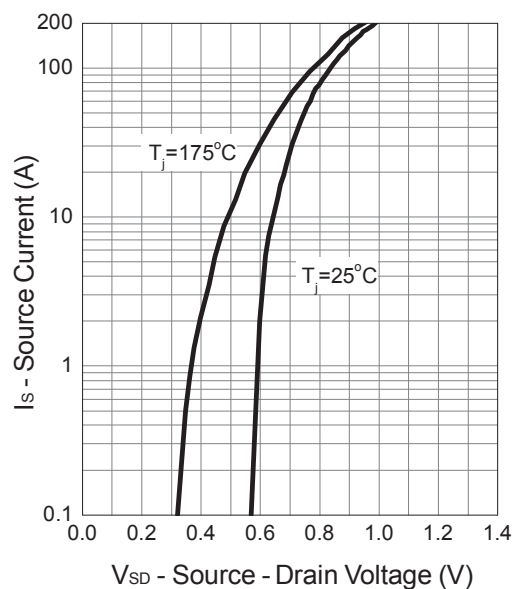


Typical Operating Characteristics (Cont.)

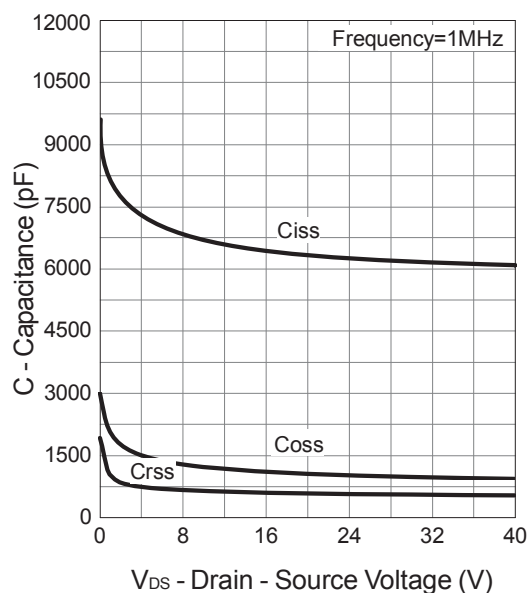
Drain-Source On Resistance



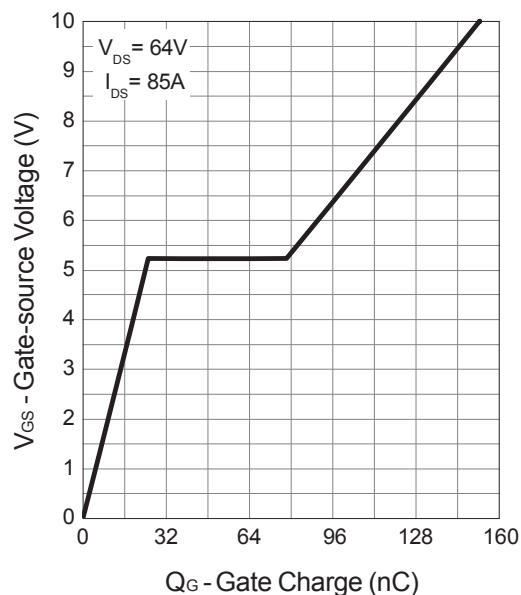
Source-Drain Diode Forward



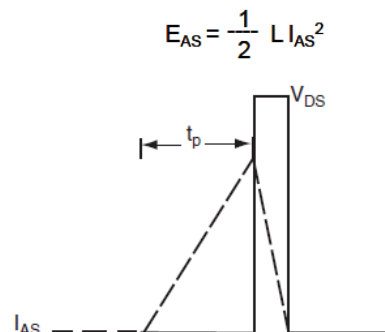
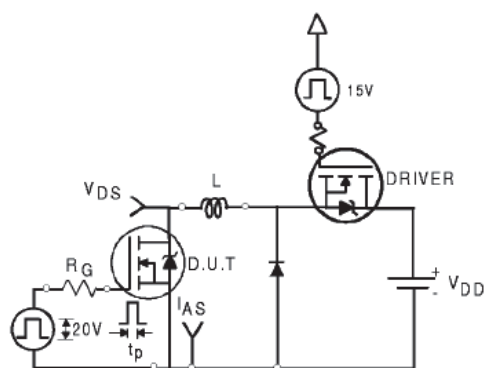
Capacitance



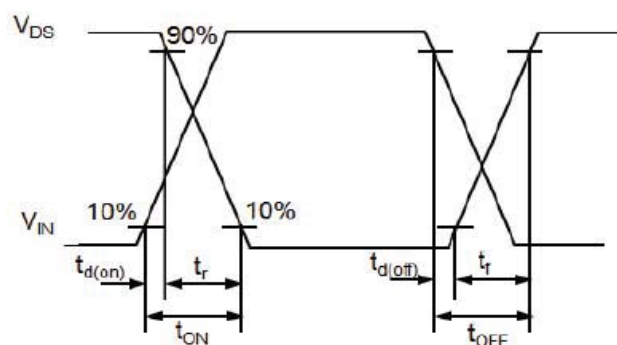
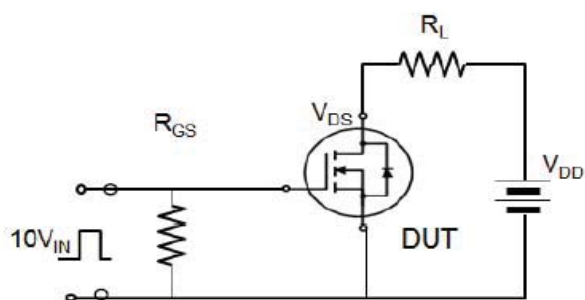
Gate Charge



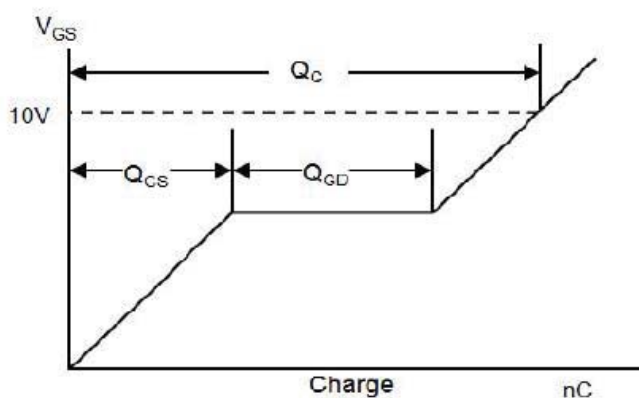
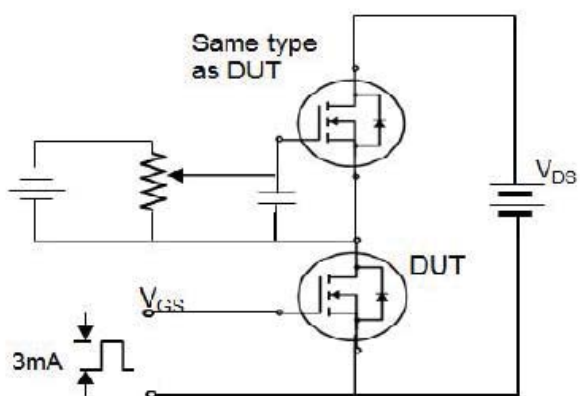
Avalanche Test Circuit



Switching Time Test Circuit



Gate Charge Test Circuit

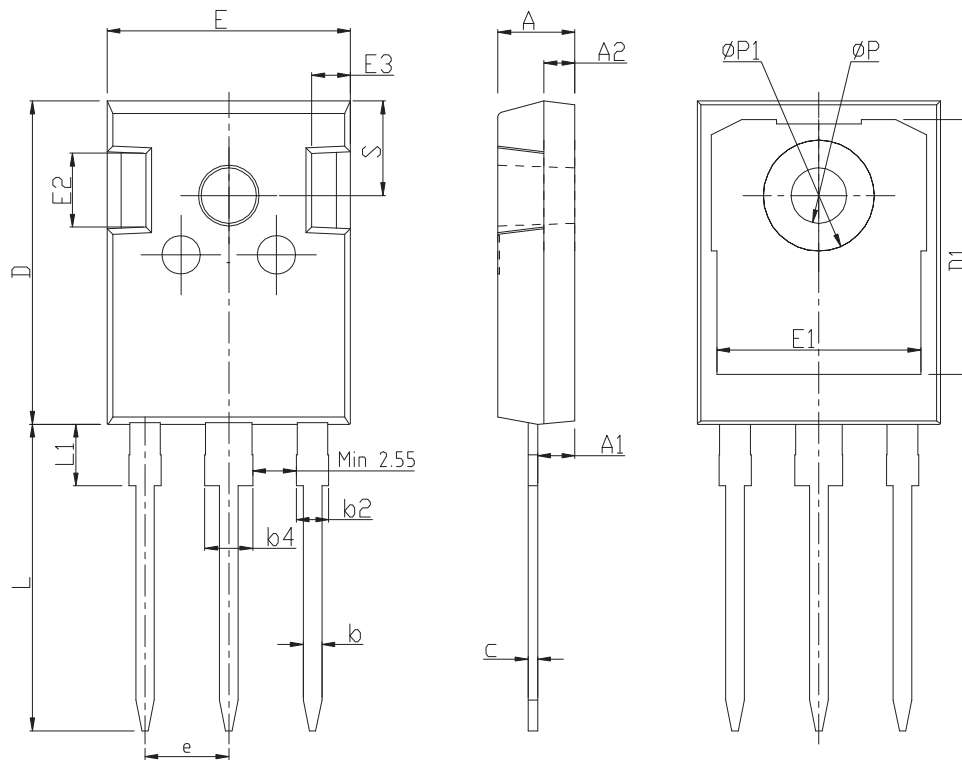


Device Per Unit

Package Type	Unit	Quantity
TO-247A-3L	Tube	30

Package Information

TO-247A-3L



COMMON DIMENSIONS

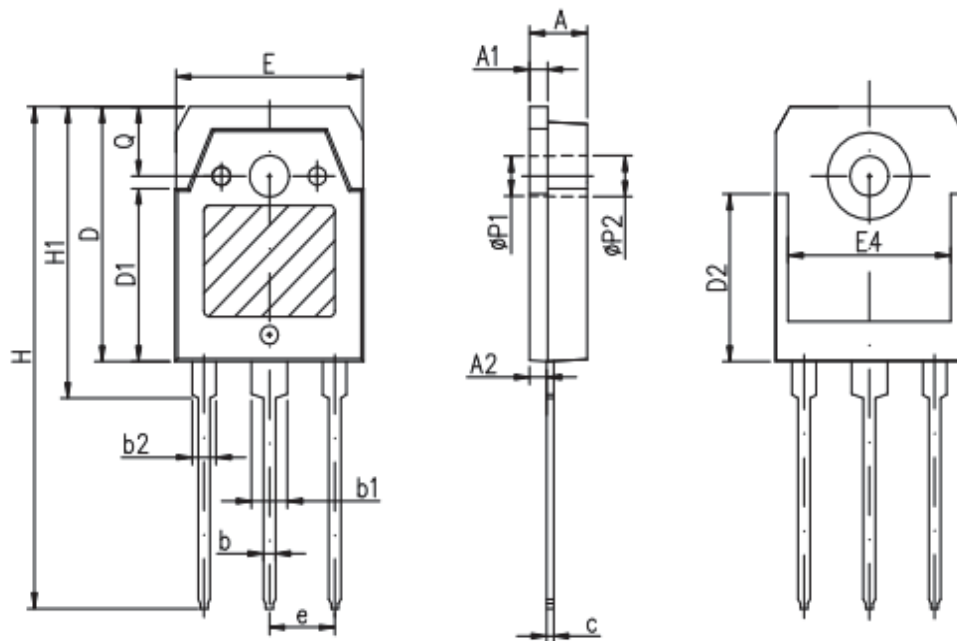
SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	—	—	4.30
P	3.40	3.60	3.80
P1	—	—	7.30
S	6.15BSC		

Device Per Unit

Package Type	Unit	Quantity
TO-3P-3L	Tube	30

Package Information

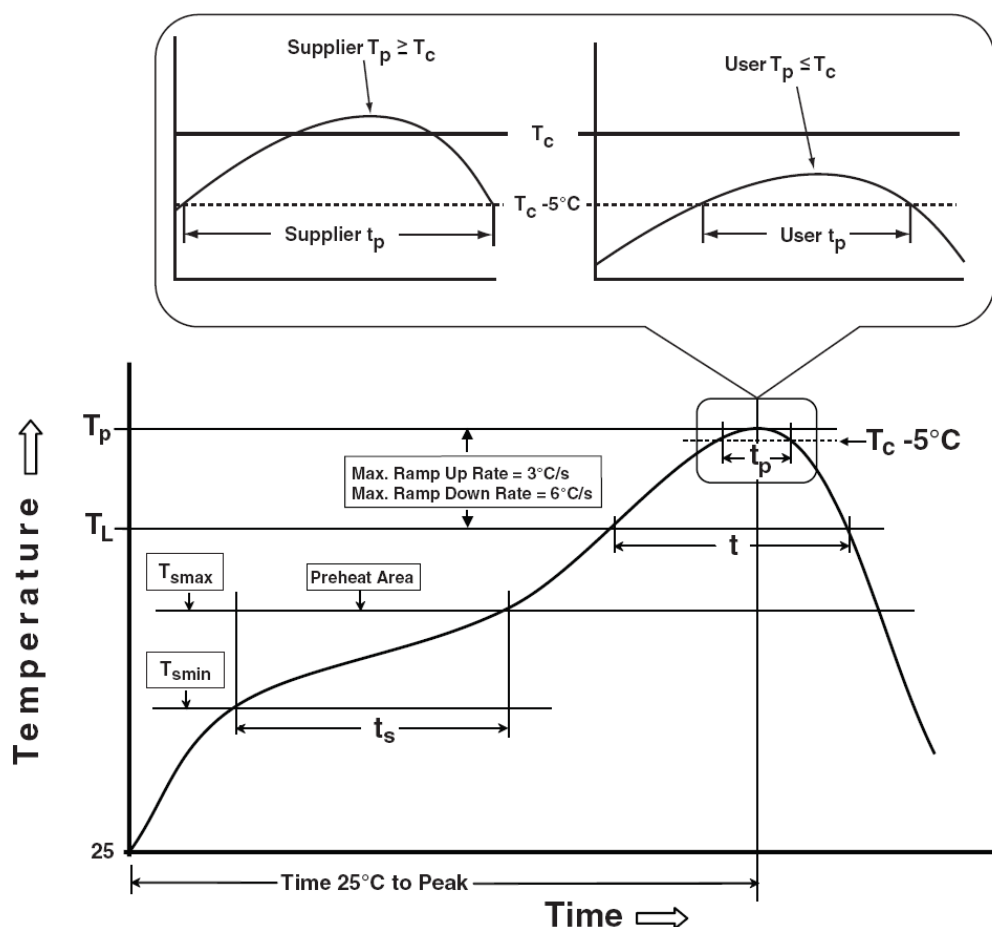
TO-3P-3L



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.60	4.80	5.00
A1	1.40	1.50	1.65
A2	1.18	1.38	1.58
b	0.80	1.00	1.20
b1	2.80	3.00	3.20
b2	1.80	2.00	2.20
c	0.50	0.60	0.75
D	19.60	19.90	20.20
D1	13.55	13.90	14.25
D2	12.90		REF
E	15.35	15.60	15.85
E4	12.60	-	-
e	5.45		TYP
H	40.10	40.50	40.90
H1	23.15	23.40	23.65
ΦP1	3.20		REF
ΦP2	3.50		REF

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168 Hrs /500 Hrs /1000 Hrs, Bias @ 150°C
PCT	JESD-22, A102	96Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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