

N-Channel Enhancement Mode MOSFET

Feature

- 40V/260A
 R_{DS(ON)}=1.3 mΩ (typ.) @VGS = 10V
- 100% Avalanche Tested
- Reliable and Rugged
- Lead-Free and Green Devices Available (RoHS Compliant)

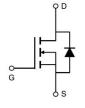
Pin Description



TO-220FB-3L

Applications

- Switching application
- Li-battery protection



N-Channel MOSFET

Ordering and Marking Information



Package Code P:TO-220FB-3L

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)		'	
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		-55 to 175	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)	Tc=25°C	260	Α
Mounted on	Large Heat Sink			
І рм	Pulsed Drain Current *	Tc=25°C	1000**	А
	Outliness Paris Outline	Tc=25°C	260	Α
lσ	Continuous Drain Current	Tc=100°C	184	Α
	M · B S · · ·	Tc=25°C	230	W
Po	Maximum Power Dissipation Tc=100°C		115	W
R₀JC	Thermal Resistance, Junction-to-Case		0.65	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		62.5	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	1984**	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
** Surface mounted on 1in2 FR-4 board.

Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Cumbal	Dovementer	Took Conditions	HYG013N04NA1		11:4	
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} = 250μA	40	-	-	V
l	Drain to Source Leekage Current	V _{DS} =40V,V _{GS} =0V	-	-	1	μA
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	_	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} = 250µA	2	2.8	4	V
Igss	Gate-Source Leakage Current	V_{GS} = $\pm20V$, V_{DS} = $0V$	-	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V _{GS} = 10V,I _{DS} = 60A	-	1.3	1.6	mΩ
Diode Cha	Diode Characteristics					
VsD	Diode Forward Voltage	Isp=60A,Vgs=0V	-	0.85	1.2	V
trr	Reverse Recovery Time	Isp=60A,dIsp/dt=100A/µs		41	-	ns
Qrr	Reverse Recovery Charge	15D-00A, 015D/01-100A/µS	-	39	-	nC

Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 Ω , Vgs =10V.



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Doromotor	Test Conditions	HY	HYG013N04NA1		
Symbol Parameter		rest conditions	Min	Тур.	Max	Unit
Dynamic (Characteristics					
RG	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	2.3	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	11620	-	
Coss	Output Capacitance	V _{DS} = 25V,	-	1502	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	1050	-	
td(ON)	Turn-on Delay Time		-	38	-	
Tr	Turn-on Rise Time	V_{DD} = 20 V , R_{G} =4 Ω ,	-	225	-	no
td(OFF)	Turn-off Delay Time	IDS=60A,VGS= 10V	-	168	-	ns
Tf	Turn-off Fall Time		-	147	-	
Gate Chai	Gate Charge Characteristics					
Qg	Total Gate Charge	\/ - 22\/ \/ - 10\/	-	265	-	
Qgs	Gate-Source Charge	$V_{DS} = 32V, V_{GS} = 10V,$ $V_{DS} = 60A$	-	55	-	nC
Qgd	Gate-Drain Charge	IDS- OOA	-	94	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

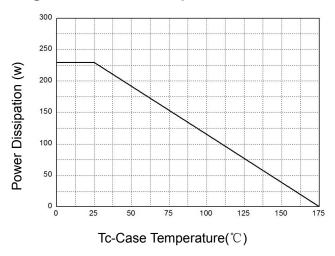
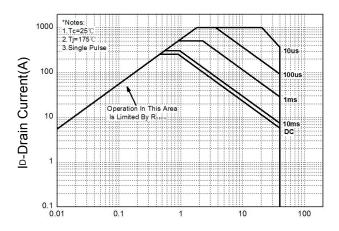


Figure 3: Safe Operation Area



VDS-Drain-Source Voltage(V)

Figure 5: Output Characteristics

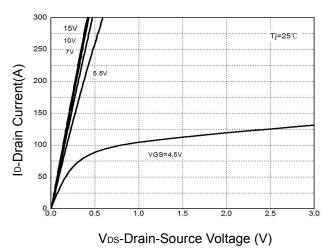


Figure 2: Drain Current

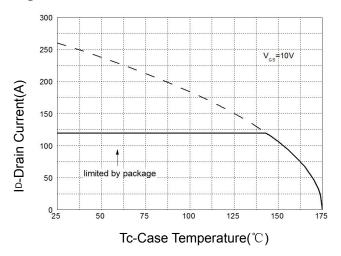
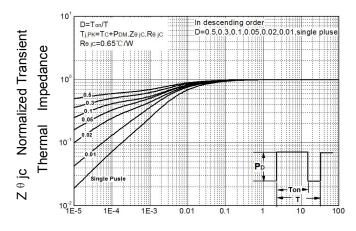
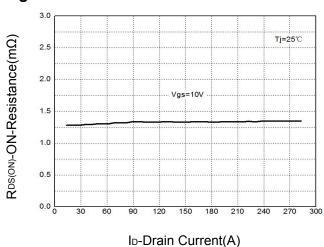


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

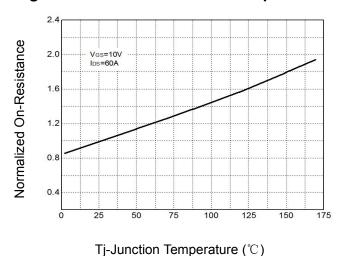
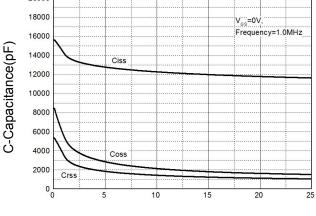


Figure 9: Capacitance Characteristics



V_{DS}-Drain-Source Voltage (V)

Figure 8: Source-Drain Diode Forward

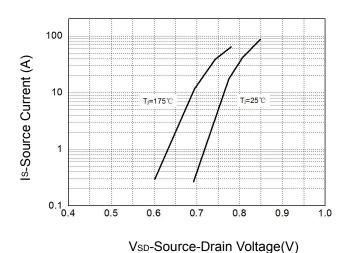
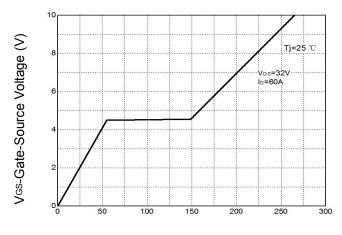


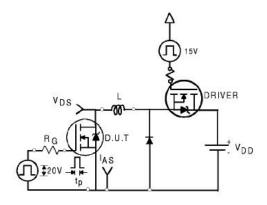
Figure 10: Gate Charge Characteristics

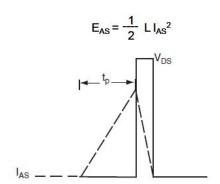


QG-Gate Charge (nC)

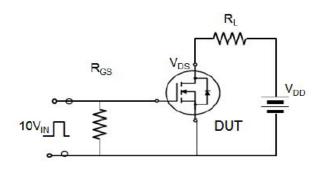


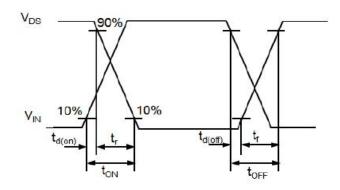
Avalanche Test Circuit



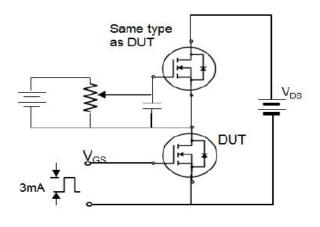


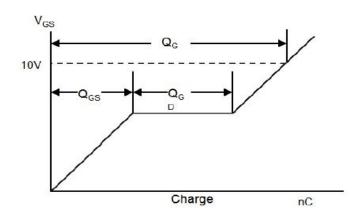
Switching Time Test Circuit





Gate Charge Test Circuit





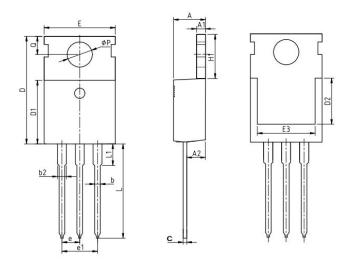


Device Per Unit

Package Type	Unit	Quantity
TO-220FB-3L	Tube	50

Package Information

TO-220FB-3L

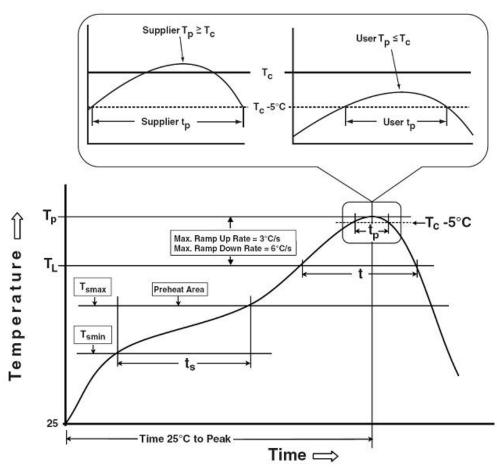


COMMON DIMENSIONS

SYMBOL	mm			
STIVIBUL	MIN	NOM	MAX	
Α	4.37	4.57	4.77	
A1	1.25	1.30	1.45	
A2	2.20	2.40	2.60	
b	0.70	0.80	0.95	
b2	1.17	1.27	1.47	
С	0.40	0.50	0.65	
D	15.10	15.60	16.10	
D1	8.80	9.10	9.40	
D2	5.50	-	-	
E	9.70	10.00	10.30	
E3	7.00	-	ı	
е		2.54 BSC		
e1	5.08 BSC			
H1	6.25	6.50	6.85	
L	12.75	13.50	13.80	
L1	-	3.10	3.40	
ФР	3.40	3.60	3.80	
Q	2.60	2.80	3.00	



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak	400 °C	450 °C	
Temperature min (T _{smin})	100 °C	150 °C	
Temperature max (T _{smax})	150 °C	200 °C	
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds	
Average ramp-up rate	2.90/2.2.2.4	3°C/second max.	
(T _{smax} to T _P)	3 °C/second max.		
Liquidous temperature (T _L)	183 °C	217 °C	
Time at liquidous (t∟)	60-150 seconds	60-150 seconds	
Peak package body Temperature	Coo Classification Town in table 1	SeeClassification Tempin table 2	
(T _p)*	See Classification Temp in table 1		
Time (t _P)** within 5°C of the specified	20**	30** seconds	
classification temperature (T _c)	20** seconds		
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	
*Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.			

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

HYG013N04NA1P



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, V _{gs} 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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