

#### N-Channel Enhancement Mode MOSFET

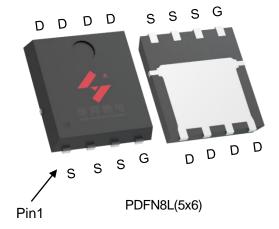
#### **Feature**

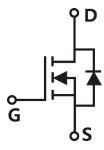
- 100V/50A
- RDS(ON)=  $12 \text{ m}\Omega(\text{typ.}) \text{ @VGS} = 10\text{V}$
- RDS(ON)=  $16m\Omega(typ.)$  @VGS = 4.5V
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

## **Applications**

- Power switching application
- High Frequency Circuits
- DC-DC Converters

## **Pin Description**





Single N-Channel MOSFET

## **Ordering and Marking Information**



Package Code C2: PDFN8L(5x6)

Date Code XYMXXXXXX

Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)		•	
VDSS	Drain-Source Voltage		100	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55 1. 475	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)	Tc=25°C	50	Α
Mounted on	Large Heat Sink	•	,	1
Ідм	Pulsed Drain Current *	Tc=25°C	150	А
1			50	А
lp	Continuous Drain Current	Tc=100°C	36	А
			77	W
P <sub>D</sub> Maximum Power Dissipation		Tc=100°C	36.5	W
R₀JC	Thermal Resistance, Junction-to-Case		1.94	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		80	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		50	mJ

- Note: \* Repetitive rating; pulse width limited by max.junction temperature.
  - Surface mounted on 1in2 FR-4 board.
  - Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 $\Omega$ , VGs =10V.

## **Electrical Characteristics**(Tc =25°C Unless Otherwise Noted)

Cymphal	Parameter	Test Conditions		HYG140N10LS1			l les id
Symbol	Parameter			Min	Тур.	Max	Unit
Static Cha	Static Characteristics						
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>DS</sub> =250μA	$V_{GS}=0V,I_{DS}=250\mu A$		-	-	V
Ipss	Drain to Source Leekage Current	Vps=100V,Vgs=0V		-	-	1	μA
IDSS	Drain-to-Source Leakage Current	TJ=	=125°C	-	-	50	μΑ
VGS(th)	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA		1	1.8	2.2	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$		-	-	±100	nA
Process	Drain-Source On-State Resistance	V <sub>GS</sub> =10V,I <sub>DS</sub> =20A			12	14.5	mΩ
Rds(on)	Dialii-Source Oii-State Resistance	V <sub>GS</sub> =4.5V,I <sub>DS</sub> =20A			16	18.5	mΩ
Diode Cha	Diode Characteristics						
VsD	Diode Forward Voltage	IsD=20A,VGS=0V		-	0.9	1.3	V
trr	Reverse Recovery Time	Isp=20A,dIsp/dt=100A/µs		-	33	-	ns
Qrr	Reverse Recovery Charge			-	28.4	-	nC



# Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Comple ed		T 10 1111	HY	HYG140N10LS1		
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	1.4	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	1232	-	
Coss	Output Capacitance	VDS=25V,	-	463	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1MHz	-	33.4	-	
td(ON)	Turn-on Delay Time		-	10.3	-	
Tr	Turn-on Rise Time	$V_{DD}=50V,R_{G}=5\Omega,$	-	28.6	-	
td(OFF)	Turn-off Delay Time	lps=20A,Vgs=10V	-	36.3	-	ns
Tf	Turn-off Fall Time		-	65	-	
Gate Cha	rge Characteristics					
0	Total Gate Charge(V <sub>GS</sub> =10V)		-	21	-	
$Q_g$	Total Gate Charge(V <sub>GS</sub> =4.5V)			10.6		0
Qgs	Gate-Source Charge	V <sub>DS</sub> =80V, I <sub>DS</sub> =20A	-	5.1	-	nC
Qgd	Gate-Drain Charge		-	3.8	-	
V <sub>plateau</sub>	Gate plateau voltage		-	3.6	-	V

Note: \*Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%



## **Typical Operating Characteristics**

**Figure 1: Power Dissipation** 

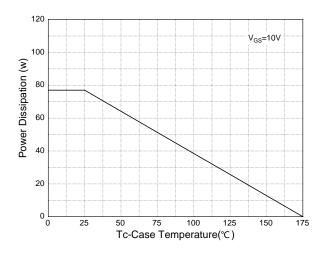


Figure 3: Safe Operation Area

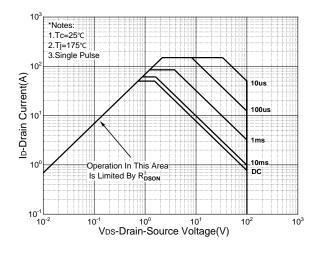
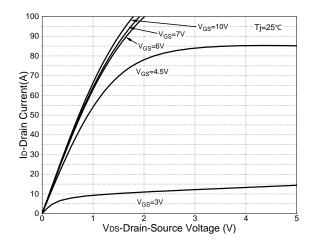


Figure 5: Output Characteristics



**Figure 2: Drain Current** 

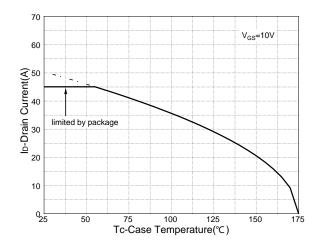


Figure 4: Thermal Transient Impedance

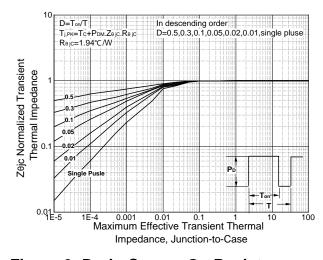
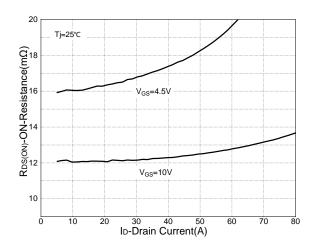


Figure 6: Drain-Source On Resistance





# **Typical Operating Characteristics(Cont.)**

Figure 7: On-Resistance vs. Temperature

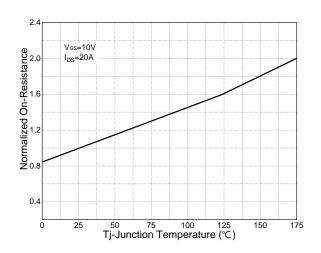


Figure 8: Source-Drain Diode Forward

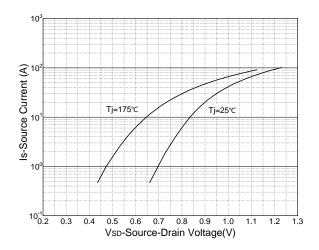
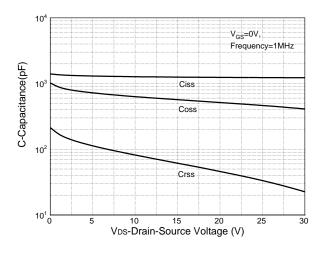
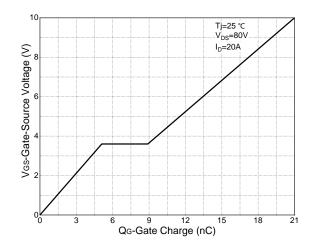


Figure 9: Capacitance Characteristics

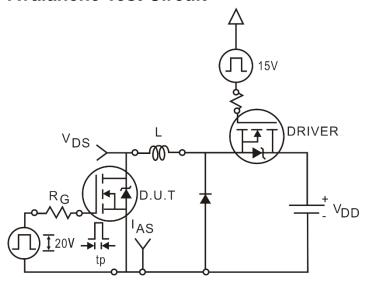


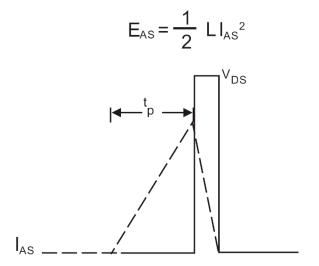
**Figure 10: Gate Charge Characteristics** 



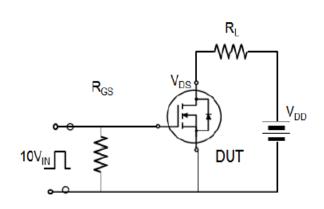


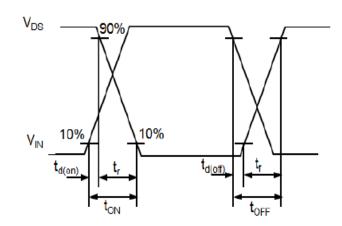
## **Avalanche Test Circuit**



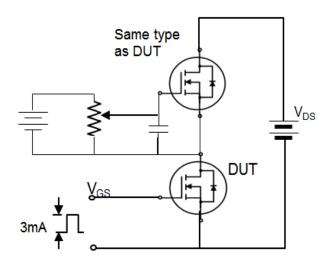


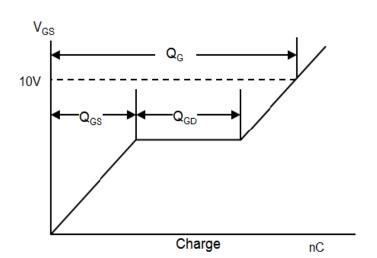
## **Switching Time Test Circuit**





# **Gate Charge Test Circuit**





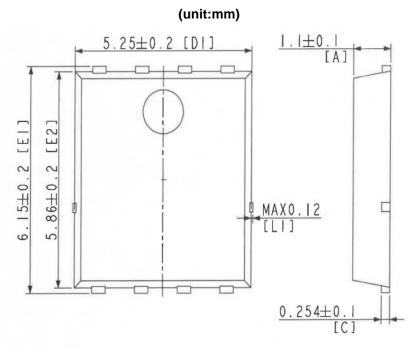


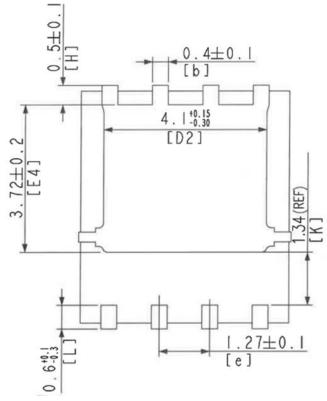
## **Device Per Unit**

Package Type	Unit	Quantity
PDFN8L(5x6)	Reel	5000

# **Package Information**

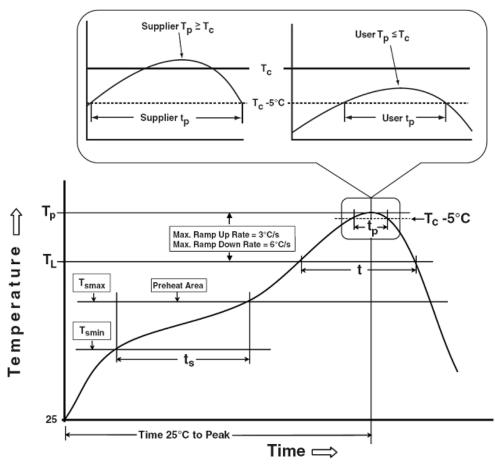
## PDFN8L(5x6)







#### **Classification Profile**



### **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly			
Preheat & Soak					
Temperature min (T <sub>smin</sub> )	100 °C	150 °C			
Temperature max (T <sub>smax</sub> )	150 °C	200 °C			
Time (Tsmin to Tsmax) (t <sub>s</sub> )	60-120 seconds	60-120 seconds			
Average ramp-up rate	2 °C/sseed may	200/20204			
(T <sub>smax</sub> to T <sub>P</sub> )	3 °C/second max.	3°C/second max.			
Liquidous temperature (T <sub>L</sub> )	183 °C	217 °C			
Time at liquidous (t <sub>L</sub> )	60-150 seconds	60-150 seconds			
Peak package body Temperature	See Classification Temp in table 1	SeeClassification Tempin table 2			
(T <sub>p</sub> )*	Coo Clacomount Fortip in table				
Time (t <sub>P</sub> )** within 5°C of the specified	20** seconds	30** seconds			
classification temperature (T <sub>c</sub> )	20 Seconds	30 seconds			
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.			
Time 25°C to peak temperature	6 minutes max.	8 minutes max.			

<sup>\*</sup>Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup> Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

# **HYG140N10LS1C2**



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

### Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm <sup>3</sup>	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## **Reliability Test Program**

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	250/500 Cycles, -55°C~150°C

### **Customer Service**

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