

### Single N-Channel Enhancement Mode MOSFET

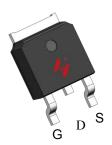
#### **Feature**

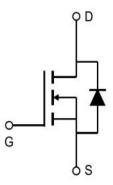
- 40V/125A
   R<sub>DS(ON)</sub>= 2.5mΩ (typ.) @VGS = 10V
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free Devices Available

## **Applications**

- Load Switch
- Lithium battery protect board

### **Pin Description**





Single N-Channel MOSFET

# **Ordering and Marking Information**



Package Code

D: TO-252-2L

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		-55 to 175	$^{\circ}$ C
Тѕтс	Storage Temperature Range		-55 to 175	$^{\circ}$
ls	Source Current-Continuous(Body Diode)	Tc=25°C	125	А
Mounted on	Large Heat Sink		'	
<b>I</b> DM	Pulsed Drain Current *	Tc=25℃	490	А
ī	Out to the Darie Out of	Tc=25℃	125	Α
lь	Continuous Drain Current	Tc=100°C	88	Α
_		Tc=25°C	93	W
Po	Maximum Power Dissipation Tc=100℃		46	W
R <sub>θ</sub> JC	Thermal Resistance, Junction-to-Case		1.60	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		60.0	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	664***	mJ

Note: \* Repetitive rating; pulse width limited by max.junction temperature.
\*\* Surface mounted on FR-4 board.

# **Electrical Characteristics**(Tc =25°C Unless Otherwise Noted)

Cumbal	Parameter	Toot Conditions	HYG025N04NA1			11:4
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>DS</sub> =250μA	40	-	_	V
Ipss	Drain to Source Leakage Current	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μA
IDSS	Drain-to-Source Leakage Current	TJ=125℃	-	-	50	μΑ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{DS}=250\mu A$	2	2.7	4	V
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V,I <sub>DS</sub> =40A	-	2.5	3.0	mΩ
Diode Cha	Diode Characteristics					
V <sub>SD</sub> *	Diode Forward Voltage	IsD=40A,Vgs=0V	-	0.9	1.2	V
trr	Reverse Recovery Time	1 -00 A dl /dt-400 A/	-	27	-	ns
Qrr	Reverse Recovery Charge	- Isp=20A,dIsp/dt=100A/μs		22		nC

Limited by TJmax , starting TJ=25  $^{\circ}$ C, L = 0.3mH, Rg =25 $\Omega$ ., Vgs =10V.

# HYG025N04NA1D



# Electrical Characteristics (Cont.) (Tc =25℃ Unless Otherwise Noted)

Cumbal	Doromotor	Toot Conditions	HY	HYG025N04NA1		
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic (	Characteristics					
Rg	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	4.7	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	5625	-	
Coss	Output Capacitance	V <sub>DS</sub> =25V,	-	774	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	569	-	
td(ON)	Turn-on Delay Time		-	14.6	-	
Tr	Turn-on Rise Time	V <sub>DD</sub> =20V,R <sub>G</sub> =4Ω,	-	55.6	-	
td(OFF)	Turn-off Delay Time	IDS=20A,VGS=10V	-	121.0	-	ns
Tf	Turn-off Fall Time		-	74.8	-	
Gate Cha	rge Characteristics				•	
Qg	Total Gate Charge (V <sub>GS</sub> =10V)	\/ -22\/ \/ -10\/	-	122.6	-	
Qgs	Gate-Source Charge	$V_{DS} = 32V, V_{GS} = 10V,$	-	27.2	-	nC
Qgd	Gate-Drain Charge	I <sub>D</sub> =40A	-	40.8	-	

Note: \*Pulse test, pulse width  $\leq 300$ us, duty cycle  $\leq 2\%$ 



### **Typical Operating Characteristics**

**Figure 1: Power Dissipation** 

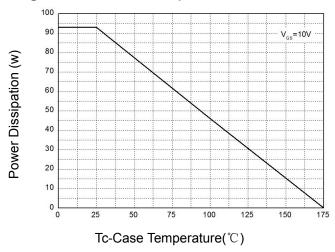
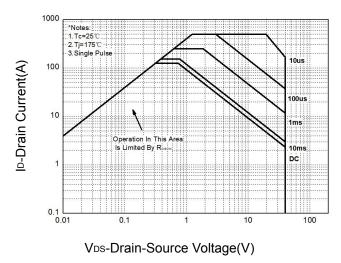


Figure 3: Safe Operation Area



**Figure 5: Output Characteristics** 

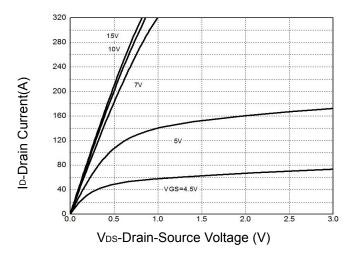


Figure 2: Drain Current

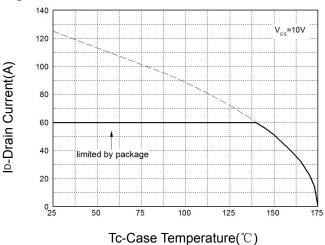
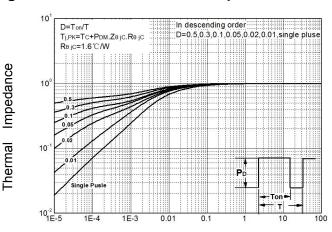
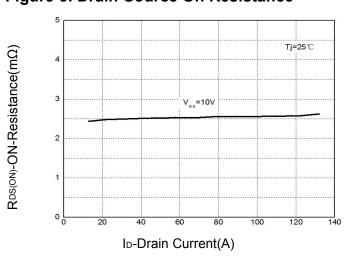


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance

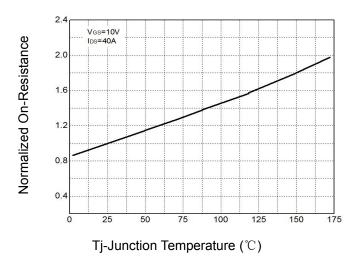


 $Z^{\theta}$  jc Normalized Transient



# **Typical Operating Characteristics(Cont.)**

Figure 7: On-Resistance vs. Temperature



**Figure 9: Capacitance Characteristics** 

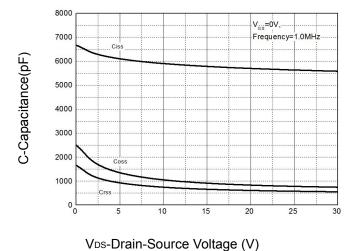
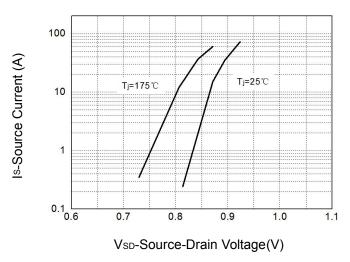
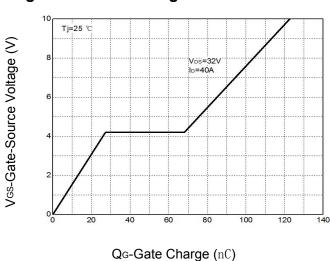


Figure 8: Source-Drain Diode Forward

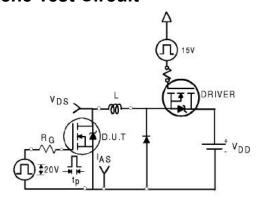


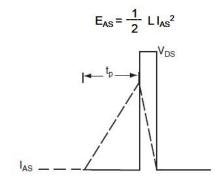
**Figure 10: Gate Charge Characteristics** 



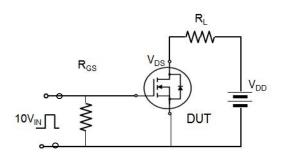


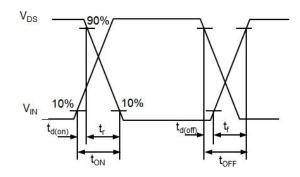
# **Avalanche Test Circuit**



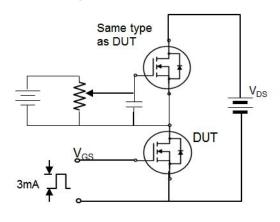


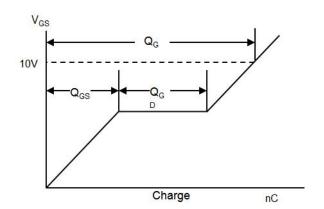
# **Switching Time Test Circuit**





# **Gate Charge Test Circuit**





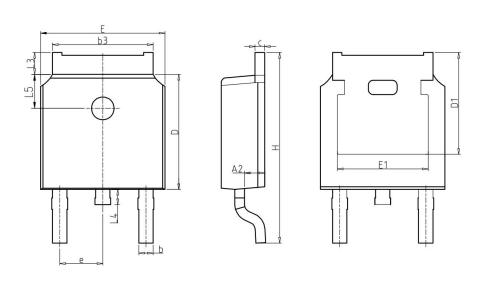


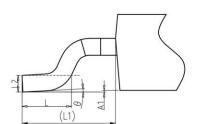
## **Device Per Unit**

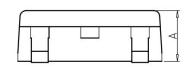
Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-252-2L	Reel	2500

# **Package Information**

### TO-252-2L





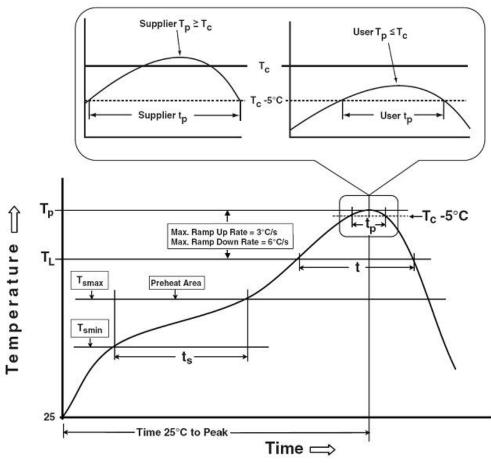


#### **COMMON DIMENSIONS**

	mm		
SYMBOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
Е	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BS0	2
Н	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	-	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°



### **Classification Profile**



### **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T <sub>smin</sub> )	100 ℃ 150 ℃	150 ℃ 200 ℃
Temperature max (T <sub>smax</sub> ) Time (Tsmin to Tsmax) (t <sub>s</sub> )	60-120 seconds	60-120 seconds
Average ramp-up rate (T <sub>smax</sub> to T <sub>P</sub> )	3 °C/second max.	3℃/second max.
Liquidous temperature (T <sub>L</sub> ) Time at liquidous (t <sub>L</sub> )	183 ℃ 60-150 seconds	217 ℃ 60-150 seconds
Peak package body Temperature (Tp)*	See Classification Temp in table 1	SeeClassification Tempin table 2
Time (t <sub>P</sub> )** within 5°C of the specified classification temperature (T₀)	20** seconds	30** seconds
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.
Time 25℃ to peak temperature	6 minutes max.	8 minutes max.

<sup>\*</sup>Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup> Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

## HYG025N04NA1D



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 ℃	<b>220</b> ℃
≥2.5 mm	220 ℃	220 ℃

#### Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 ℃	260 ℃	260 ℃
1.6 mm – 2.5 mm	260 ℃	250 ℃	245 ℃
≥2.5 mm	250 ℃	245 ℃	<b>245</b> ℃

# **Reliability Test Program**

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, V <sub>gs</sub> 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

#### **Customer Service**

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