Gate(G)

Source(S) Drain(D)



### • General Description

The AGMH022N10LL combines advanced trench MOSFET technology with a low resistance package to provide extremely low R<sub>DS(ON)</sub> .

This device is ideal for load switch and battery protection applications.

### Features

- Advance high cell density Trench technology
- Low R<sub>DS(ON)</sub> to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

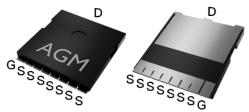
## Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

### **Product Summary**

BVDSS	RDSON	ID
100V	1.8mΩ	295A

**TOLL Pin Configuration** 





_		
lop	View	

**Bottom View** 

# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGMH022N10LL	AGMH022N10LL	TOLL	330mm	25mm	2000

#### Absolute Maximum Ratings (TA=25℃) Table 1.

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	295	А
	Drain Current-Continuous(Tc=100℃)	177	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	1180	А
PD	Maximum Power Dissipation(Tc=25℃)	520	W
	Maximum Power Dissipation(Tc=100℃)	259	W
EAS	Avalanche energy (Note 3)	1024	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 175	$^{\circ}$

#### Table 2. **Thermal Characteristic**

Symbol	Parameter	Тур	Max	Unit
R0JC	Thermal Resistance Junction-Case <sup>1</sup>		0.29	°C/W



Table 3. Electrical Characteristics (TJ=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off St	ates					
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	100			V
IDSS	Zero Gate Voltage Drain Current	VDS=100V,VGS=0V			1	μΑ
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	2	3.4	4	V
gFS	Forward Transconductance	VDS=5V,ID=10A		35		S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=50A		1.8	2.3	mΩ
Dynamic	Characteristics					
Ciss	Input Capacitance	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		6220		pF
Coss	Output Capacitance	VDS=40V,VGS=0V ,F=1MHZ		2100		pF
Crss	Reverse Transfer Capacitance			120		pF
Switching	Times					
td(on)	Turn-on Delay Time			82		nS
tr	Turn-on Rise Time	VGS=10V,VDS=50V,		99		nS
td(off)	Turn-Off Delay Time	ID=80A,RGEN=1.6Ω		146		nS
tf	Turn-Off Fall Time			42	-	nS
Qg	Total Gate Charge			163		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=50V, ID=20A		51		nC
Qgd	Gate-Drain Charge			43		nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)				295	А
VSD	Forward on Voltage	VGS=0V,IS=20A			1.2	V
trr	Reverse Recovery Time	IF=20A , dI/dt=100A/μs ,		134		ns
Qrr	Reverse Recovery Charge	TJ=25℃		689		nc

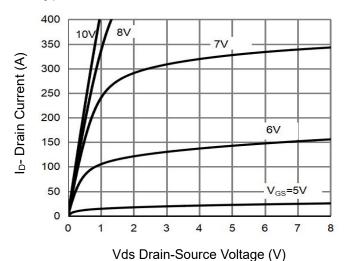
Notes 1. The maximum current rating is package limited.

Notes 2. Repetitive Rating: Pulse width limited by maximum junction temperature

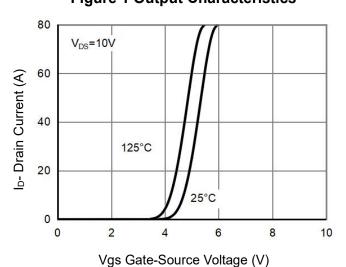
Notes 3.EAS condition: TJ=25  $^{\circ}\text{C}\,\text{,VDD}=50\text{V},\text{Vgs}=10\text{V}$  , ID=64A,L=0.5mH,RG=25ohm



## **Typical Electrical and Thermal Characteristics**



**Figure 1 Output Characteristics** 



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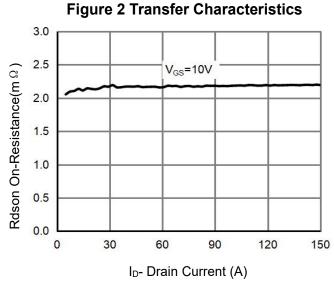


Figure 3 Rdson- Drain Current

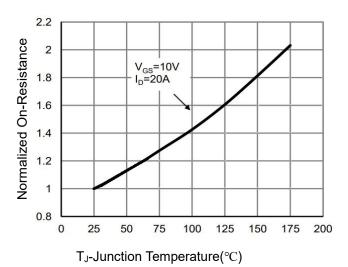


Figure 4 Rdson-Junction Temperature

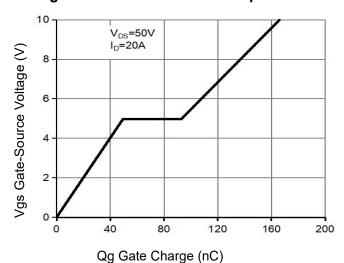


Figure 5 Gate Charge

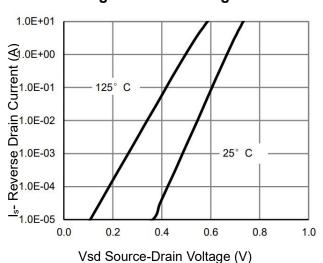
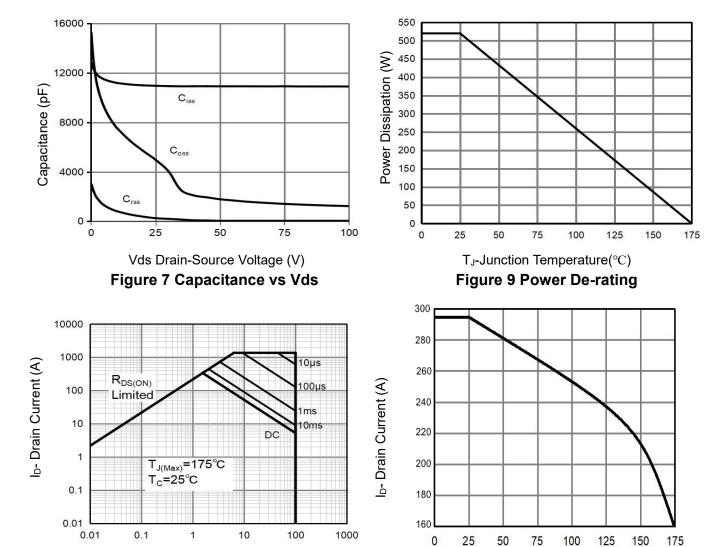


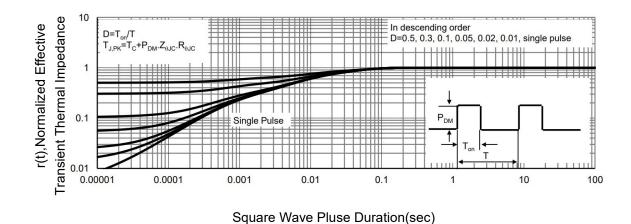
Figure 6 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area (Note3)



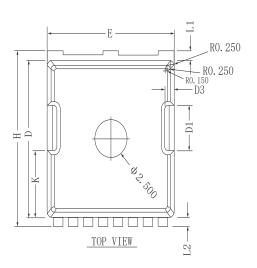
T<sub>J</sub>-Junction Temperature (°C)

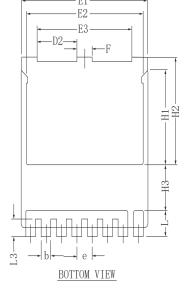
Figure 10 Current De-rating

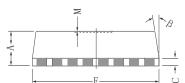
Figure 11 Maximum Transient Thermal Impedance



## •Dimensions (TOLL)







Symbols	Millimeters				
Symbols	MIN.	NOM.	MAX.		
A	2.20	2.30	2.40		
b	0.65	0.65 0.75 0			
С		0.508 REF			
D	10. 25	10.40	10.55		
D1	2.85	3.00	3. 15		
D2	2.95	3.10	3. 25		
D3		0.75 REF			
Е	9.75	9.90	10.05		
E1	9.65	9.80	9. 95		
E2	8.95	8.95 9.10			
E3	7. 25 7. 40		7. 55		
е	1.20 BSC				
F	1.05	1.20	1.35		
Н	11.55	11.70	11.85		
H1	6.03	6.18	6.33		
H2	6. 85	7.00	7. 15		
Н3		3.00 BSC			
L	1.55	1.70	1.85		
L1	0.55	0.70	0.85		
L2	0.45	0.60	0.75		
L3	1.00	1. 15	1.30		
M	0.08 REF				
β	8°	10°	12°		
K	4. 25	4.40	4. 55		

MILLIMETER

NOM.

2.300

1.800

0.700

9.800

0.750

2.400

1.900

0.800

9.900

0.850

0.600

3.200

SYMBOL

A1

b

b1

b2

MIN.

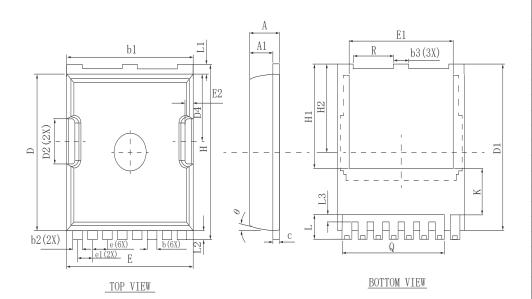
2.200

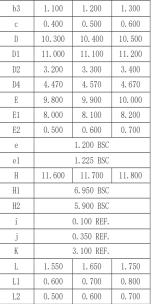
1.700

0.600

9.700

0.650





0.400

3.000

0.500

7.950 REF.

3.100

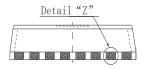
10° REF.

L3

Q

R

θ

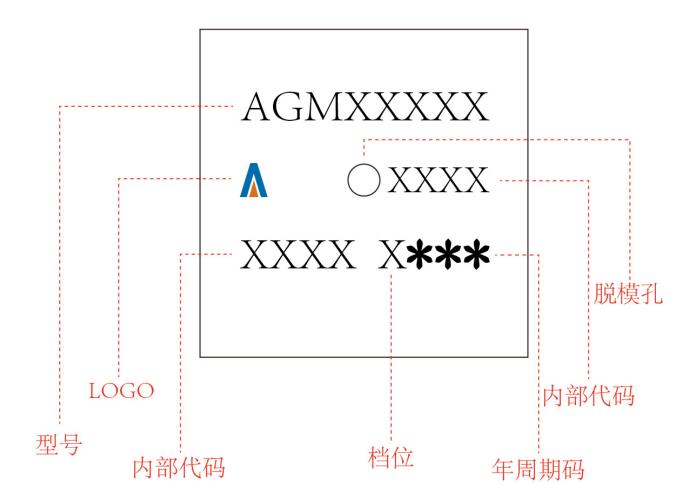




Detail "Z"



TOLL
Marking Instructions:





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