Product Summary

V _{(BR)DSS}	R _{DS(on)TYP}	I _D
100V	6.7mΩ@10V	904
	8.7mΩ@4.5V	80A



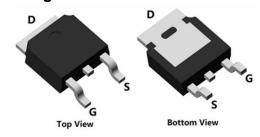
Feature

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

Applications

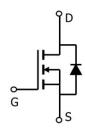
- Power switching application
- Battery management
- Uninterruptible power supply

Package

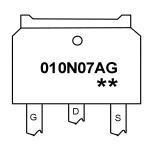


TO-252(1:G 2:D 3:S)

Circuit diagram



Marking



010N07AG : Product code ** : Week code

Order Information

Device	Package	Unit/Tube		
SP010N07AGTH	TO-252	2500		



Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current (Tc=25°C)	I _D	80	Α
Continuous Drain Current (Tc=100°C)	I _D	55	Α
Pulsed Drain Current	I _{DM}	320	Α
Single Pulse Avalanche Energy ¹	Eas	272	mJ
Power Dissipation (Tc=25°C)	P _D	100	W
Thermal Resistance Junction-to-Case	R _{0JC}	1.25	°C/W
Storage Temperature Range	T _{STG}	55 to 150	$^{\circ}$
Operating Junction Temperature Range	TJ	55 to 150	$^{\circ}$ C

Electrical characteristics (Ta=25°C, unless otherwise noted)

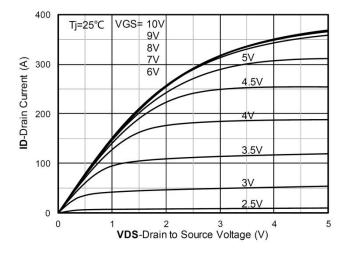
Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V	-	-	1	uA	
Gate Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±0.1	nA	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.7	2.5	V	
		V _{GS} = 10V, I _D = 30A	-	6.7	8.5	10	
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 25A	-	8.7	12	mΩ	
Dynamic Characteristics					•		
Input Capacitance	Ciss		-	1942	-	pF	
Output Capacitance	Coss	V _{DS} =50V, V _{GS} = 0V, f = 1.0MHz	-	388	-		
Reverse Transfer Capacitance	Crss		-	12	-		
Total Gate Charge	Qg		-	67	-	nC	
Gate-Source Charge	Q _{gs}	V _{DS} =50V , VGS=10V , ID=50A	-	12	-		
Gate-Drain Charge	Q _{gd}		-	21	-		
Switching Characteristics			<u>.</u>	•			
Turn-On Delay Time	t _{d(on)}		-	12	-		
Rise Time	t _r	$V_{GS} = 50V, V_{DS} = 50V, ID = 50A$	-	11	-	nS	
Turn-Off Delay Time	t _{d(off)}	$R_G = 4.7\Omega$	-	42	-		
Fall Time	t _f		-	6	-		
Drain-Source Body Diode Characteris	stics		<u>.</u>	•			
Source-Drain Diode Forward Voltage	V _{SD}	V _{GS} =0V , I _S =1A , T _J =25℃	-	-	1.2	V	
Maximum Body-Diode Continuous Current	Is		-	-	80	А	
Reverse Recovery Time	Trr	L=20A di/dt=100A/\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	59	-	nS	
Reverse Recovery Charge	Qrr	I _S =20A, di/dt=100A/us, T _J =25℃	-	88	-	nC	

Note:

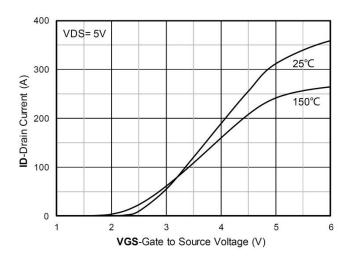
1. The EAS test condition is VDD=50V,VGS=10V,L=0.5mH,RG=25 Ω



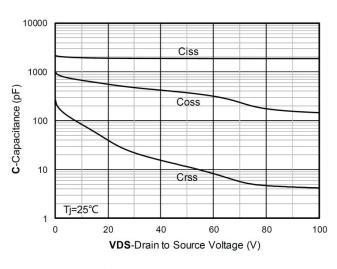
Typical Characteristics



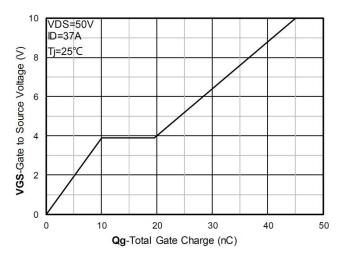
Output Characteristics



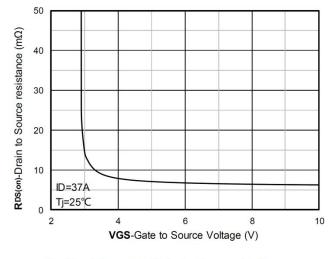
Transfer Characteristics



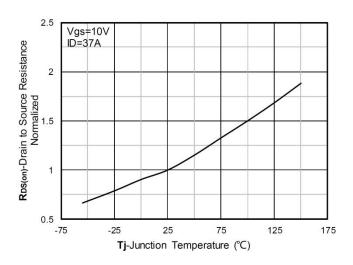
Capacitance Characteristics



Gate Charge

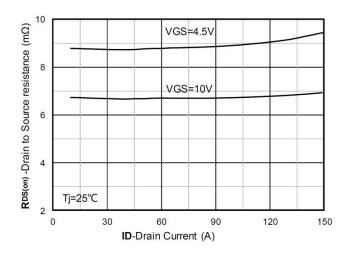


On-Resistance vs Gate to Source Voltage

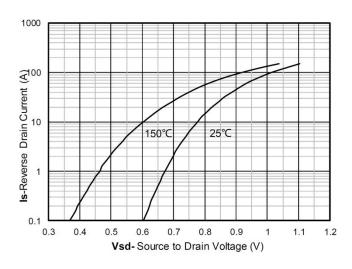


Normalized On-Resistance

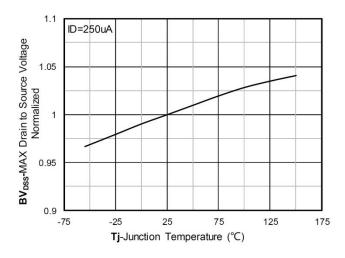




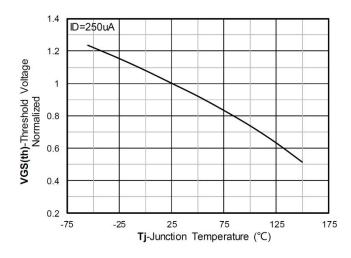
RDS(on) VS Drain Current



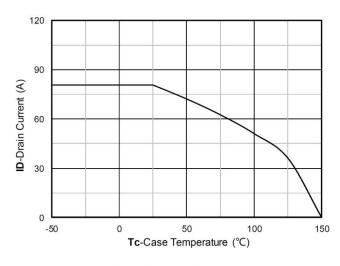
Forward characteristics of reverse diode



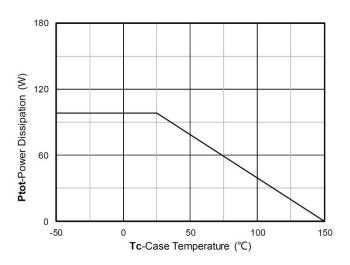
Normalized breakdown voltage



Normalized Threshold voltage

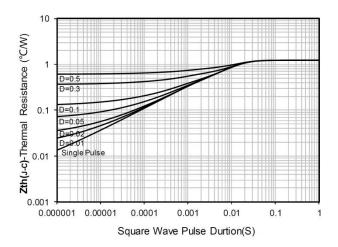


Current dissipation

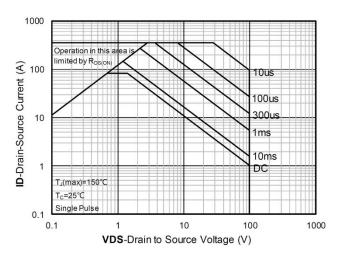


Power dissipation

Siliup Semiconductor

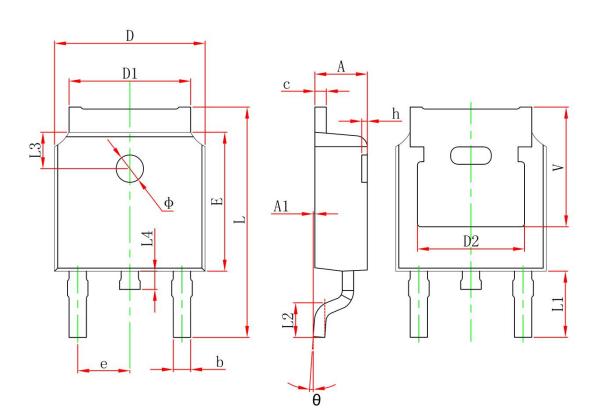


Maximum Transient Thermal Impedance



Safe Operation Area

TO-252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	4.830 REF.		REF.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 REF.		0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 F	REF.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 REF.		0.211 F	REF.	