

30V N-Channel Power MOSFET

TO-252 (DPAK)

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Pin Definition:

- 1. Gate
- 2. Drain
- 3. Source

Key Parameter Performance

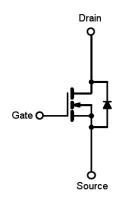
Parameter		Value	Unit	
$V_{ t DS}$		30	V	
R _{DS(on)} (max)	$V_{GS} = 10V$	9	mΩ	
	$V_{GS} = 4.5V$	13		
Q_{g}		7.5	nC	

Ordering Information

Ordering code	Package	Packing		
TSM090N03CP ROG	TO-252	2.5kpcs / 13" Reel		
Note: Halogon from according to IEC 61240 2 21 definition				

Note: Halogen-free according to IEC 61249-2-21 definition

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	Tc=25°C		55	А
Continuous Drain Current	Tc=100°C	I _D	35	Α
Pulsed Drain Current (Note 1)		I _{DM}	220	А
Single Pulse Avalanche Energy (Note 2)		E _{AS}	45	mJ
Single Pulse Avalanche Current (Note 2)		I _{AS}	30	Α
Total Power Dissipation	@ T _C =25°C		40	W
	Derate above T _C =25°C	- P _D	0.32	W/ºC
Operating Junction Temperature		T _J	150	°C
Storage Temperature Range		T _{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit	
Thermal Resistance - Junction to Case	$R_{\Theta JC}$	3.1	°C/W	
Thermal Resistance - Junction to Ambient	$R_{\Theta JA}$	62	°C/W	

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Electrical Specifications (T_C=25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	30			V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 16A$			7.5	9	mΩ
	$V_{GS} = 4.5V, I_D = 8A$	$R_{DS(ON)}$		10	13	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	V _{GS(TH)}	1	1.6	2.5	V
Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$				1	μΑ
	V _{DS} = 24V, T _J = 125°C	I _{DSS}			10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I _{GSS}			±100	nA
Forward Transconductance	$V_{DS} = 10V, I_{D} = 8A$	g fs		14		S
Dynamic						
Total Gate Charge ^(Note 3,4)		Qg		7.5		nC
Gate-Source Charge ^(Note 3,4)	$V_{DS} = 15V, I_{D} = 20A,$	Q_gs		1.3		
Gate-Drain Charge ^(Note 3,4)	$V_{GS} = 4.5V$	Q_gd		4.5		
Input Capacitance		C _{iss}		750		
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$	C _{oss}		150		pF
Reverse Transfer Capacitance	f = 1MHz	C _{rss}		110		
Gate Resistance	f = 1MHz	R_g		2.7		Ω
Switching						
Turn-On Delay Time ^(Note 3,4)		t _{d(on)}		4.8		
Turn-On Rise Time ^(Note 3,4)	V _{DD} =15V , V _{GS} =10V ,	t _r		12.5		
Turn-Off Delay Time ^(Note 3,4)	$R_G=3.3\Omega$, $I_D=-15A$	t _{d(off)}		27.6		ns
Turn-Off Fall Time ^(Note 3,4)	1	t _f		8.2		
Source-Drain Diode Ratings and C	haracteristic	•				
Continuous Drain-Source Diode		Is			55	Α
Pulse Drain-Source Diode		I _{SM}			220	Α
Diode-Source Forward Voltage	$V_{GS} = 0V, I_{S} = 1A$	V_{SD}			1	V

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Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =30A., R_{G} =25 Ω ,Starting T_{J} =25 $^{\circ}$ C
- 3. The data tested by pulsed , pulse width \leq 300µs, duty cycle \leq 2%
- 4. Essentially independent of operating temperature.

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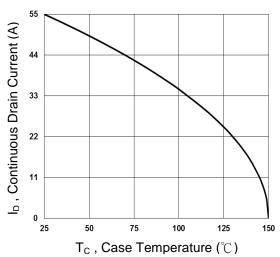


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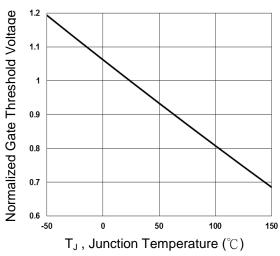
Pb ROHS COMPLIANT

Electrical Characteristics Curve

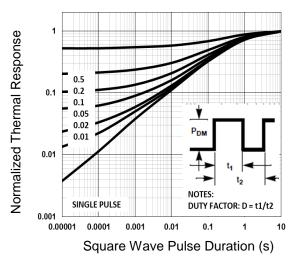




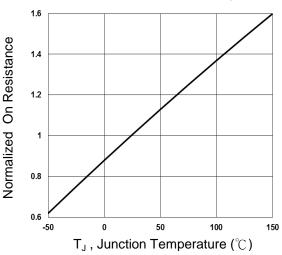
Normalized V_{th} vs. T_J



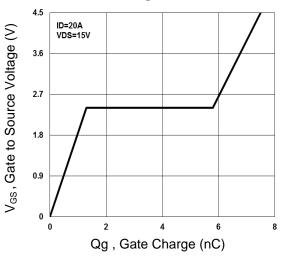
Normalized Transient Impedance



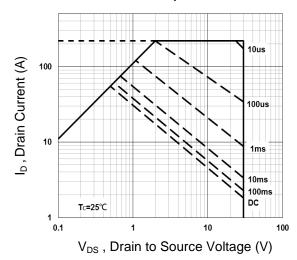
Normalized RDSON vs. T_J



Gate Charge Waveform



Maximum Safe Operation Area

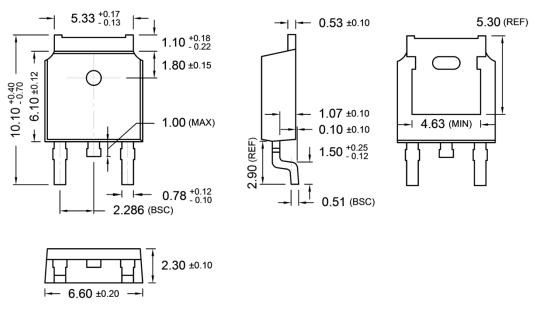


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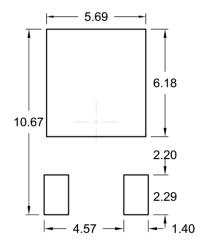
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TO-252 Mechanical Drawing



Unit: Millimeters

SUGGESTED PAD LAYOUT (Unit: Millimeters)



Marking Diagram



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TSM090N03CP 30V N-Channel Power MOSFET

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