

N-Channel Enhancement Mode MOSFET

Feature Pin Description

• 60V/171A

RDS(ON)= $2.6 \text{ m}\Omega(\text{typ.})$ @VGS = 10V RDS(ON)= $3.8\text{m}\Omega(\text{typ.})$ @VGS = 4.5V

- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)



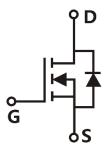
TO-252-2L

TO-251-3L

TO-251-3S

Applications

High Frequency Point-of-Load Synchronous Buck Converter



Single N-Channel MOSFET

Ordering and Marking Information



Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit		
Common Rat	Common Ratings (Tc=25°C Unless Otherwise Noted)				
VDSS	Drain-Source Voltage		60	V	
Vgss	Gate-Source Voltage		±20	V	
TJ	Junction Temperature Range		55 1. 475	°C	
Tstg	Storage Temperature Range		-55 to 175	°C	
Is	Source Current-Continuous(Body Diode)	Tc=25°C	171	А	
Mounted on I	Large Heat Sink				
Ідм	Pulsed Drain Current *	Tc=25°C	616	А	
	Cantinua de Brain Coment	Tc=25°C	171	А	
lσ	Continuous Drain Current	Tc=100°C	121	А	
1	N	Tc=25°C	187.5	W	
Po	Maximum Power Dissipation	Maximum Power Dissipation Tc=100°C		W	
R₀uc	Thermal Resistance, Junction-to-Case		0.8	°C/W	
R _{eJA}	Thermal Resistance, Junction-to-Ambient **		75	°C/W	
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	305	mJ	

Note:

- * Repetitive rating; pulse width limited by max.junction temperature.
- ** Surface mounted on 1in2 FR-4 board.
- *** Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25Ω , Vgs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Cumbal	Dougmaton	Took Conditions	HY	G025N06	6LS2	1114:4
Symbol	Parameter Test Conditions		Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	60	-	-	V
I Built to October 1 and 1 and 1		VDS=60V,VGS=0V	-	-	1	μΑ
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μΑ
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	1	2.1	3	V
lgss	Gate-Source Leakage Current	Vgs=±20V,Vps=0V	-	-	±100	nA
Dragovii)	Drain Source On State Registeres	V _{GS} =10V,I _{DS} =40A		2.6	3.2	mΩ
Rds(on)	Drain-Source On-State Resistance V _{GS} =4.5V,I _{DS} =40A			3.8	4.7	mΩ
Diode Cha	Diode Characteristics					
VsD	Diode Forward Voltage	IsD=40A,Vgs=0V	-	0.84	1.3	V
trr	Reverse Recovery Time	los 404 dlos/dt 4004/ug	-	37	-	ns
Qrr	Reverse Recovery Charge	- Isb=40A,dIsb/dt=100A/μs	-	37	-	nC

HYG025N06LS2D/U/V



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cymahal	Banamatan	Tank Camalikiana	HYG025N06L		Took Conditions HYO	HYG025N06LS2		11
Symbol Parameter		Test Conditions	Min	Тур.	Max	Unit		
Dynamic	Characteristics							
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=500KHz	-	0.5	-	Ω		
Ciss	Input Capacitance	V _G S=0V,	-	4008	-			
Coss	Output Capacitance	V _{DS} = 25V,	-	1134	-	pF		
Crss	Reverse Transfer Capacitance	Frequency=500KHz	-	50	-			
td(ON)	Turn-on Delay Time		-	15	-			
Tr	Turn-on Rise Time	V _{DD} =30V,R _G =2.5Ω,	-	54	-			
t d(OFF)	Turn-off Delay Time	IDS=40A,VGS=10V	-	35	-	ns		
Tf	Turn-off Fall Time		-	68	-			
Gate Cha	rge Characteristics					•		
0	Total Gate Charge(V _{GS} =10V)		-	62.5	-			
\mathbf{Q}_{g}	Total Gate Charge(V _{GS} =4.5V)			30		0		
Qgs	Gate-Source Charge	V _{DS} =48V, I _{DS} =40A	-	16.5	-	nC		
Qgd	Gate-Drain Charge		-	10	-			
V _{plateau}	Gate plateau voltage		-	3.8	-	V		

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

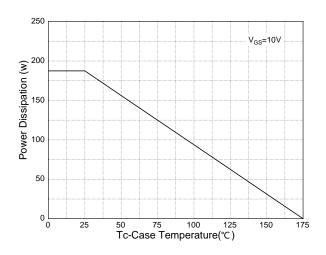


Figure 3: Safe Operation Area

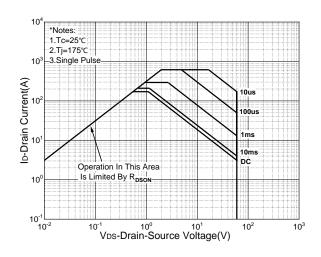


Figure 5: Output Characteristics

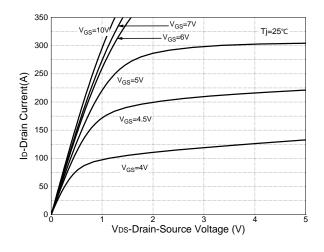


Figure 2: Drain Current

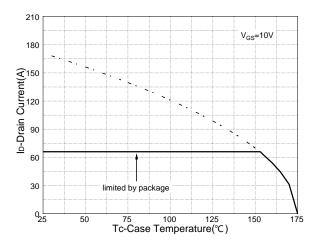


Figure 4: Thermal Transient Impedance

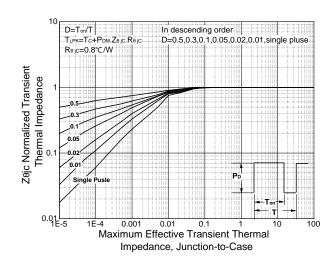
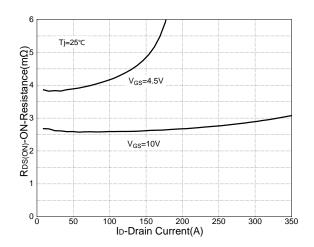


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

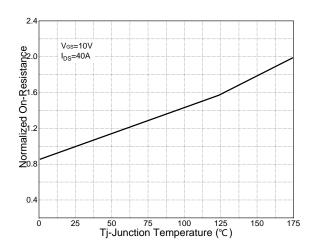


Figure 8: Source-Drain Diode Forward

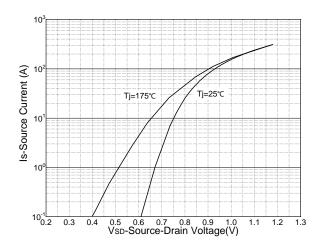


Figure 9: Capacitance Characteristics

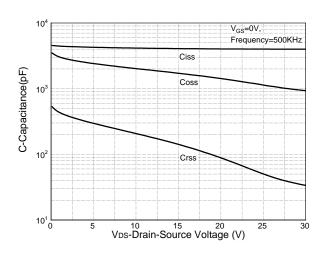
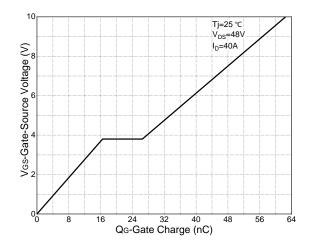
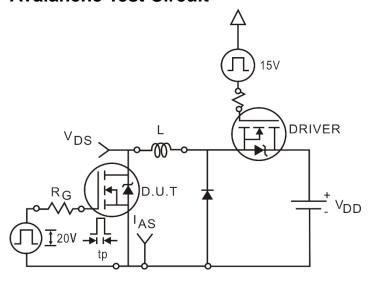


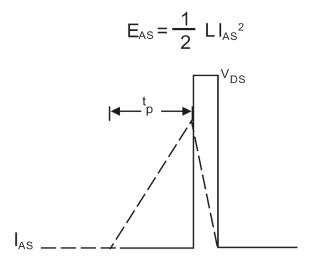
Figure 10: Gate Charge Characteristics



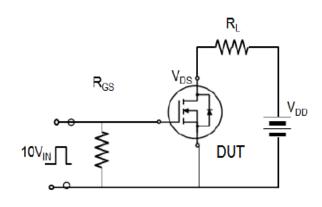


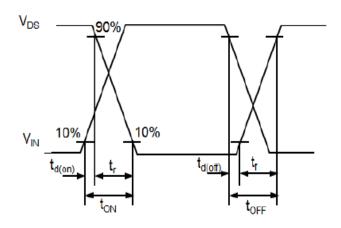
Avalanche Test Circuit



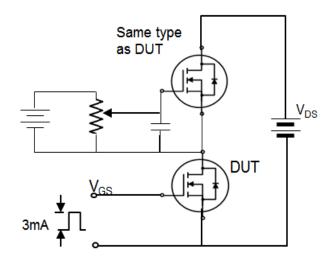


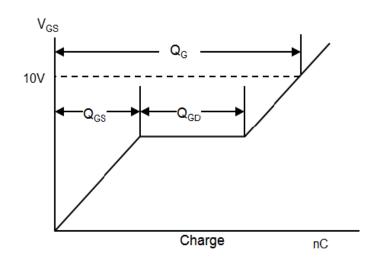
Switching Time Test Circuit





Gate Charge Test Circuit





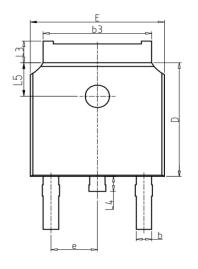


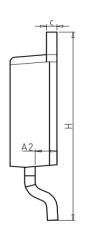
Device Per Unit

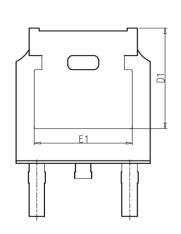
Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-252-2L	Reel	2500
TO-251-3L	Tube	75
TO-251-3S	Tube	75

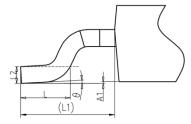
Package Information

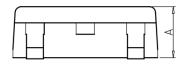
TO-252-2L







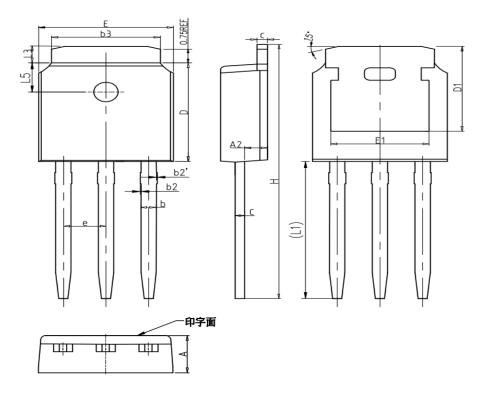




COMMON DIMENSIONS			
SYMBOL		mm	
STIVIDOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A1	0.00	ı	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	•
Е	6.40	6.60	6.80
E1	4.63	ı	ı
е		2.286BS0	
Н	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	-	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°



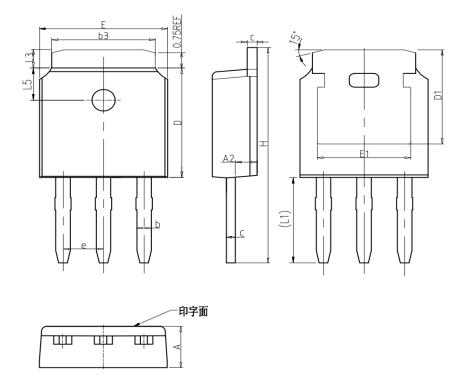
TO-251-3L



COMMON DIMENSIONS			
CVMPOL	mm		
SYMBOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
E	6.40	6.60	6.80
E1	4.63	-	-
е	2.286BSC		
Н	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



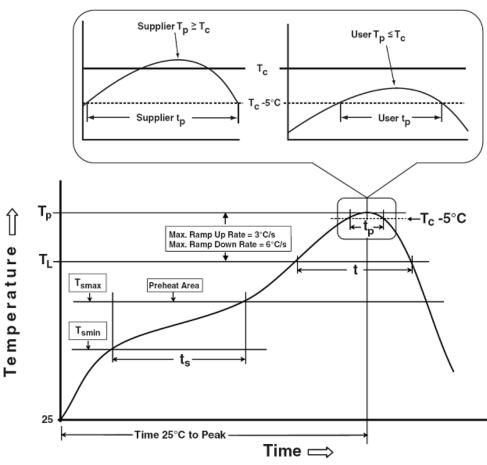
TO-251-3S



COMMON DIMENSIONS			
SYMBOL	mm		
STIVIBUL	MIN	NOM	MAX
А	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
E	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BSC	
Н	10.00	11.22	11.44
L1	3.90	4.10	4.30
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly			
	Preheat & Soak				
Temperature min (T _{smin})	100 °C	150 °C			
Temperature max (T _{smax})	150 °C	200 °C			
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds			
Average ramp-up rate	2 °C/cocond mov	29C/22221d may			
(T _{smax} to T _P)	3 °C/second max.	3°C/second max.			
Liquidous temperature (T _L)	183 °C	217 °C			
Time at liquidous (t _L)	60-150 seconds	60-150 seconds			
Peak package body Temperature (T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2			
Time (t _P)** within 5°C of the specified classification temperature (T _c)	20** seconds	30** seconds			
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.			
Time 25°C to peak temperature	6 minutes max.	8 minutes max.			

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

HYG025N06LS2D/U/V



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm ³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	250/500 Cycles, -55°C~150°C

Customer Service

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