

# PolarHT<sup>™</sup> Power MOSFET

IXTK 100N25P IXTQ 100N25P IXTT 100N25P  $V_{DSS} = 250 V \ I_{D25} = 100 A \ R_{DS(on)} \le 27 m\Omega$ 

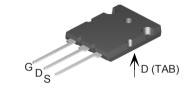
N-Channel Enhancement Mode Avalanche Rated

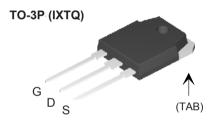


Symbol	Test Conditions	Maximum	Ratings
V <sub>DSS</sub> V <sub>DGR</sub>	$T_{_{\rm J}} = 25^{\circ} \text{C to } 150^{\circ} \text{C}$ $T_{_{\rm J}} = 25^{\circ} \text{C to } 150^{\circ} \text{C};  R_{_{\rm GS}} = 1 \text{M}\Omega$	250 250	V V
V <sub>GSS</sub> V <sub>GSM</sub>	Continuous Transient	±20 ±30	V
I <sub>D25</sub>	T <sub>C</sub> =25°C	100	А
I <sub>D(RMS)</sub>	External lead current limit	75	Α
I <sub>DM</sub>	$T_{\rm C}$ = 25° C, pulse width limited by $T_{\rm JM}$	250	Α
I <sub>AR</sub>	T <sub>C</sub> =25°C	60	А
E <sub>AR</sub>	T <sub>c</sub> = 25° C	60	mJ
E <sub>as</sub>	T <sub>C</sub> = 25° C	2.0	J
dv/dt	$I_{S} \leq I_{DM}$ , di/dt $\leq 100$ A/ $\mu$ s, $V_{DD} \leq V_{DSS}$ , $T_{J} \leq 150^{\circ}$ C, $R_{G} = 4 \Omega$	10	V/ns
$\overline{\mathbf{P}_{\scriptscriptstyle \mathrm{D}}}$	T <sub>C</sub> =25°C	600	W
T <sub>J</sub>		-55 <b>+</b> 150	°C
T <sub>JM</sub> T <sub>stg</sub>		150 -55 +150	°C
T <sub>L</sub> T <sub>SOLD</sub>	1.6 mm (0.062 in.) from case for 10 s Plastic body for 10 s	300 260	°C °C
M <sub>d</sub>	Mounting torque	1.13/10	Nm/lb.in.
Weight	TO-3P TO-264 TO-268	5.5 10 5.0	g g g

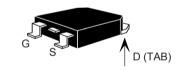
Symbol $(T_J = 25^{\circ} C,$	Test Conditions unless otherwise specified)		Ch Min.	_	istic Va Max	
BV <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		250			V
V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2.5		5.0	V
I <sub>GSS</sub>	$V_{GS} = \pm 20 V_{DC}, V_{DS} = 0$				±100	nA
I <sub>DSS</sub>	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$	T <sub>J</sub> = 125° C			25 250	μ <b>Α</b> μ <b>Α</b>
R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_{D} = 0.5 I_{D25}$ Pulse test, t $\leq$ 300 $\mu$ s, duty	cycle d ≤ 2 %			27	mΩ

## TO-264 (IXTK)





# TO-268 (IXTT)



G = Gate D = Drain S = Source TAB = Drain

#### **Features**

- <sup>1</sup> International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

## **Advantages**

- Easy to mount
- Space savings
- High power density



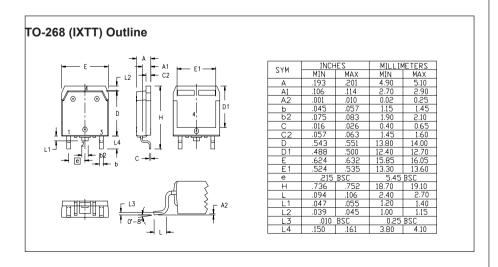
#### **Symbol Test Conditions Characteristic Values** (T<sub>1</sub> = 25° C, unless otherwise specified) Min. Typ. Max. $V_{DS}$ = 10 V; $I_{D}$ = 0.5 $I_{D25}$ , pulse test 56 S $g_{fs}$ Ciss 6300 рF $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$ 1150 pF 240 pF 25 $\boldsymbol{t}_{\text{d(on)}}$ ns t, $V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \text{ V}_{DSS}, I_{D} = I_{D25}$ 26 ns $R_c = 3.3 \Omega$ (External) 100 $\mathbf{t}_{d(off)}$ 28 ns $\boldsymbol{\mathsf{Q}_{\mathsf{g(on)}}}$ 185 nC $\mathbf{Q}_{\mathrm{gs}}$ $V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_{D} = 0.5 I_{D25}$ 43 nC $\mathbf{Q}_{\underline{\mathsf{gd}}}$ 91 nC $\mathbf{R}_{\mathrm{thJC}}$ 0.21° C/W TO-3P ° C/W $R_{thCS}$ 0.21 TO-264 ° C/W 0.15 R<sub>thCS</sub>

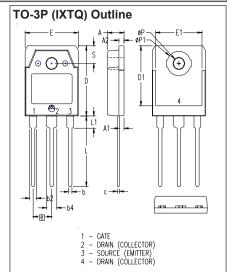
#### Source-Drain Diode

Characteristic Values

(T<sub>J</sub> = 25° C, unless otherwise specified)

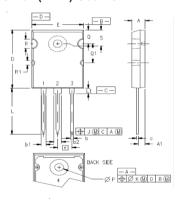
Symbol	Test Conditions	Min.	Тур.	Max.	
I <sub>s</sub>	$V_{GS} = 0 V$			100	Α
I <sub>sm</sub>	Repetitive			250	Α
V <sub>SD</sub>	$I_F = I_S$ , $V_{GS} = 0$ V, Pulse test, t ≤300 µs, duty cycle d≤ 2 %			1.5	V
$\left\{ egin{array}{c} \mathbf{t}_{rr} \\ \mathbf{Q}_{RM} \end{array} \right\}$	$I_F = 25 \text{ A}, -\text{di/dt} = 100 \text{ A/}\mu\text{s}$ $V_R = 100 \text{ V}, V_{GS} = 0 \text{ V}$		200 3.0		ns μC





SYM	INCHES		MILLIMETERS			
2 I M	MIN	MAX	MIN	MAX		
Α	.185	.193	4.70	4.90		
A1	.051	.059	1.30	1.50		
A2	.057	.065	1.45	1.65		
Ь	.035	.045	0.90	1.15		
b2	.075	.087	1.90	2.20		
b4	.114	.126	2.90	3.20		
С	.022	.031	0.55	0.80		
D	.780	.799	19.80	20.30		
D1	.665	.677	16.90	17.20		
E	.610	.622	15.50	15.80		
E1	.531	.539	13.50	13.70		
е	.215	.215 BSC		BSC		
L	.779	.795	19.80	20.20		
L1	.134	.142	3.40	3.60		
ØΡ	.126	.134	3.20	3.40		
øP1	.272	.280	6.90	7.10		
S	.193	.201	4.90	5.10		

## TO-264 (IXTK) Outline



1 - GATE 2, 4 - DRAIN (COLLECTOR) 3 - SOURCE (EMITTER)

	INCHES		MILLIMETERS			
SYM	MIN	MAX	MIN	MAX		
А	.185	.209	4.70	5.31		
A1	.102	.118	2.59	3.00		
ь	.037	.055	0.94	1.40		
b1	.087	.102	2.21	2.59		
b2	.110	.126	2.79	3.20		
С	.017	.029	0.43	0.74		
D	1.007	1.047	25.58	26.59		
E	.760	.799	19.30	20.29		
е	.215BSC		5.46	BSC		
J	.000	.010	0.00	0.25		
K	.000	.010	0.00	0.25		
L	.779	.842	19.79	21.39		
L1	.087	.102	2.21	2.59		
ØΡ	.122	.138	3.10	3.51		
Q	.240	.256	6.10	6.50		
Q1	.330	.346	8.38	8.79		
ØR	.155	.187	3.94	4.75		
ØR1	.085	.093	2.16	2.36		
S	.243	.253	6.17	6.43		

IXYS reserves the right to change limits, test conditions, and dimensions.



Fig. 1. Output Characteristics

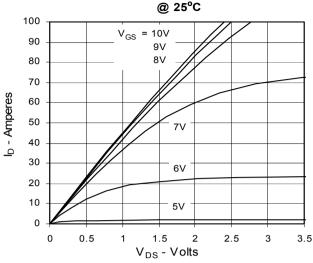


Fig. 3. Output Characteristics @ 125°C

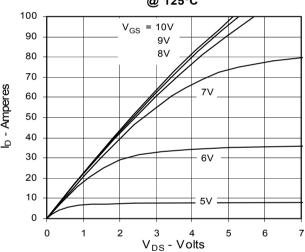


Fig. 5. R<sub>DS(on)</sub> Normalized to

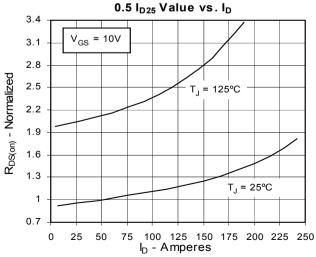


Fig. 2. Extended Output Characteristics

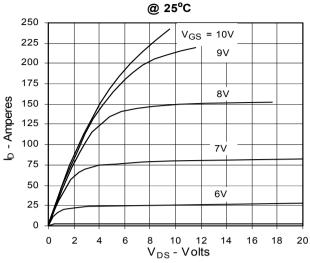


Fig. 4.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Junction Temperature

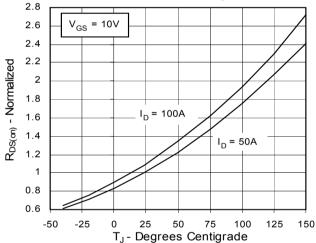


Fig. 6. Drain Current vs. Case Temperature

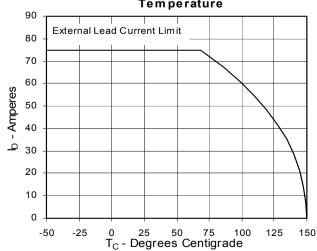




Fig. 7. Input Admittance

150
125
100
75
50
25°C
25°C
40°C
4 4.5 5 5.5 6 6.5 7 7.5 8

Fig. 9. Source Current vs. Source-To-Drain Voltage

V<sub>GS</sub> - Volts

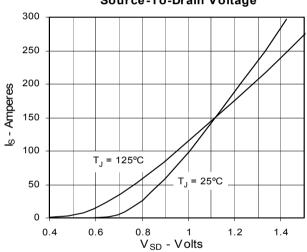


Fig. 11. Capacitance

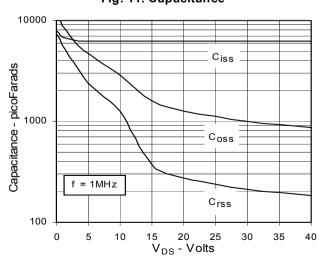


Fig. 8. Transconductance

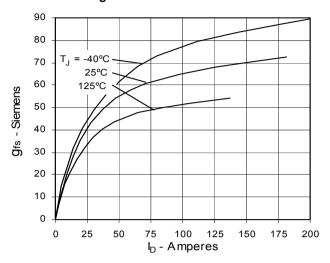


Fig. 10. Gate Charge

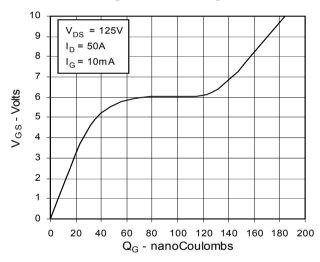


Fig. 12. Forward-Bias Safe Operating Area

