

Advance Technical Information

GigaMOS™ HiperFET™ Power MOSFET

IXFZ140N25T

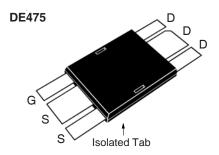
(Electrically Isolated Tab)

N-Channel Enhancement Mode Avalanche Rated Fast Intrinsic Diode

Symbol	Test Conditions	Maximum Ratings 250 V		
V _{DSS}	T _J = 25°C to 150°C			
V _{DGR}	$T_J = 25$ °C to 150°C, $R_{GS} = 1M\Omega$		250	V
V _{GSS}	Continuous		±20	V
V _{GSM}	Transient		±30	V
I _{D25}	$T_{c} = 25^{\circ}C$		100	Α
I _{DM}	$T_{\rm C} = 25^{\circ}$ C, Pulse Width Limited by $T_{\rm JM}$		400	Α
I _A	$T_{\rm C} = 25^{\circ} C$		40	Α
E _{as}	$T_{c} = 25^{\circ}C$		3	J
P _D	$T_{\rm C} = 25^{\circ} C$		445	W
dv/dt	$I_{\text{S}} \leq I_{\text{DM}}, V_{\text{DD}} \leq V_{\text{DS}}$	$I_{S} \le I_{DM}, V_{DD} \le V_{DSS}, T_{J} \le 150^{\circ}C$		V/ns
T _J			-55 +150	°C
T _{JM}			150	°C
T _{stg}			-55 +150	°C
V _{ISOL}	50/60 Hz, RMS	t = 1 minute	2500	٧~
	$I_{ISOL} \le 1mA$	t = 1 second	3000	V~
T,	1.6mm (0.062 in.) from Case for 10s		300	°C
T _{SOLD}	Plastic Body for 10s		260	°C
F _c	Mounting Force		20120 / 4.527	N/lb.
Weight			3	g

Symbol			cteristic Values		
$(1_{J} = 25^{\circ}C, C)$	Jnless Otherwise Specified)	Min.	Тур.	Max	
BV _{DSS}	$V_{GS} = 0V, I_{D} = 3mA$	250			V
V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 4mA$	2.5		5.0	V
I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±200	nA
I _{DSS}	$V_{DS} = V_{DSS}, V_{GS} = 0V$			50	μΑ
	$T_{_{\mathrm{J}}} = 12$	25°C		3	mΑ
R _{DS(on)}	$V_{GS} = 10V, I_{D} = 60A, Note 1$			17	mΩ

 $V_{_{DSS}} = 250V$ $I_{_{D25}} = 100A$ $R_{_{DS(on)}} \le 17m\Omega$ $t_{_{rr}} \le 200ns$



G = Gate D = Drain S = Source

Features

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Isolated Substrate
- Excellent Thermal Transfer
- Increased Temperature and Power Cycling Capability
- High Isolation Voltage (2500V~)
- Very High Current Handling Capability
- Fast Intrinsic Diode
- Avalanche Rated

Advantages

- Easy to Mount
- Space Savings
- High Power Density

Applications

- DC-DC Converters
- Battery Chargers
- Switch-Mode and Resonant-Mode Power Supplies
- DC Choppers
- AC Motor Drives
- Uninterruptible Power Supplies
- High Speed Power Switching Applications



Symbol	Symbol Test Conditions Chara			cteristic Values		
$(T_J = 25^{\circ}C, I)$	Unless Otherwise Specified)	Min.	Тур.	Max.		
g _{fs}	$V_{DS} = 10V, I_{D} = 60A, \text{ Note 1}$	80	135	S		
C _{iss}			19	nF		
C _{oss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		1500	pF		
C _{rss}			185	pF		
t _{d(on)}			33	ns		
t _r	Resistive Switching Times		29	ns		
t _{d(off)}	$V_{GS} = 15V$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 70A$		92	ns		
t _f	$R_{G} = 1\Omega$ (External)		22	ns		
$Q_{g(on)}$			255	nC		
Q _{gs}	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 70A$		90	nC		
\mathbf{Q}_{gd}			62	nC		
R _{thJC}				0.28 °C/W		
R _{thCS}			0.15	°C/W		

Source-Drain Diode

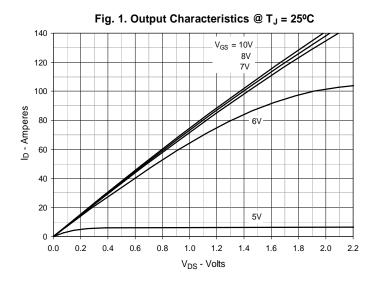
SymbolTest ConditionsChara $(T_J = 25^{\circ}C, Unless Otherwise Specified)$ Min.			cteristic Values Typ. Max.		
I _s	$V_{GS} = 0V$		140	Α	
I _{SM}	Repetitive, Pulse Width Limited by T_{JM}		560	Α	
V _{SD}	$I_F = 60A$, $V_{GS} = 0V$, Note 1		1.3	٧	
t _{rr}	$I_F = 70A$, $V_{GS} = 0V$ -di/dt = 100A/ μ s $V_R = 75V$	9.3 600	200	ns A nC	

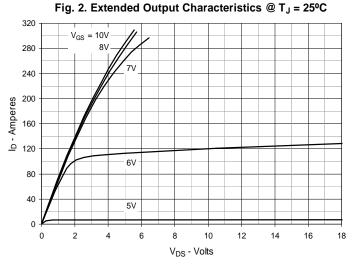
Note 1. Pulse test, $t \le 300\mu s$, duty cycle, $d \le 2\%$.

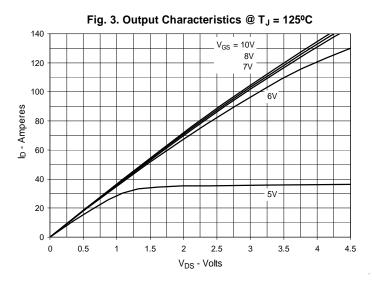
ADVANCE TECHNICAL INFORMATION

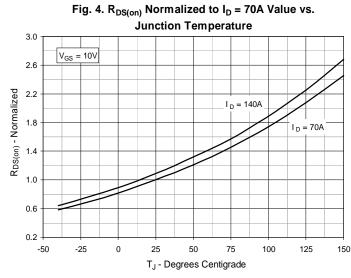
The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

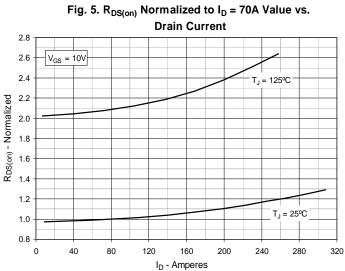


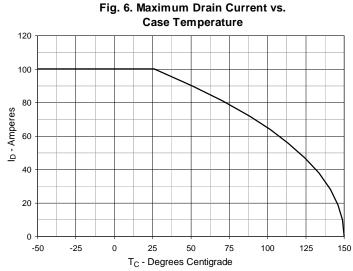




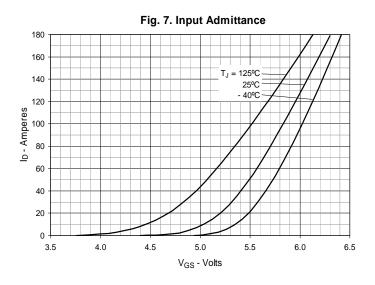


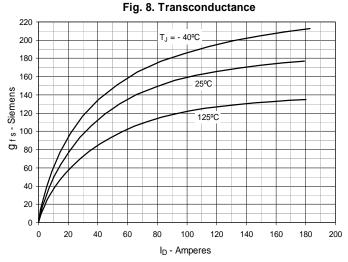


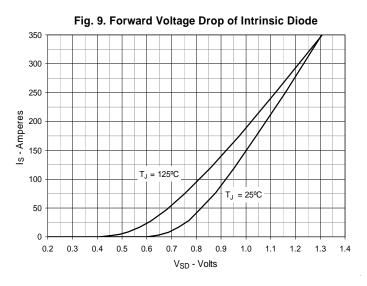


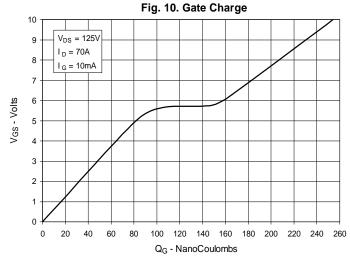


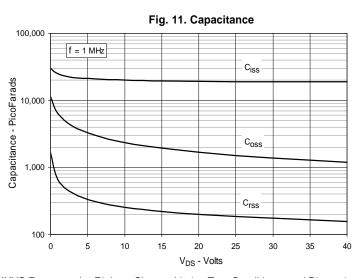


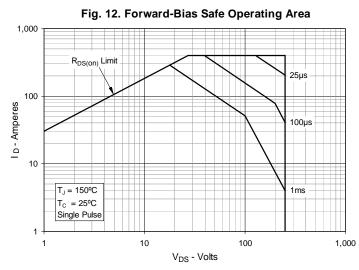












 $\ensuremath{\mathsf{IXYS}}$ Reserves the Right to Change Limits, Test Conditions, and Dimensions.

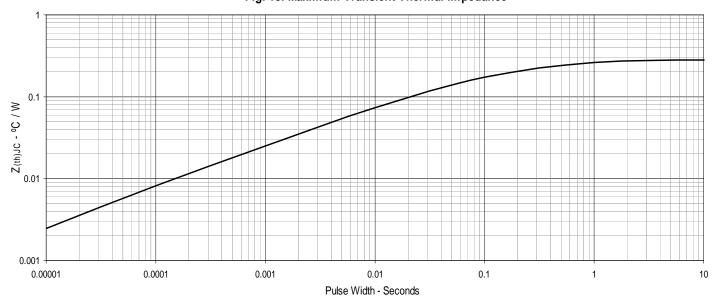


Fig. 13. Maximum Transient Thermal Impedance

