

General Description

The BSC027N06LS5ATMA1 use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness.



DFN5X6-8L

General Features

V_{DS} =60V I_D =125A

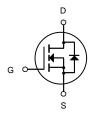
 $R_{DS(ON)}$ < 2.9m Ω @ V_{GS} =10V

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
BSC027N06LS5ATMA1	DFN5X6-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_C =25°C unless otherwise noted)

Symbol	Parameter	Rating	Units	
Vos	Drain-Source Voltage	60	V	
Vgs	Gate-Source Voltage	±20	V	
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V	125	Α	
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V	101	А	
Ірм	Pulsed Drain Current ²	641	Α	
EAS	Single Pulse Avalanche Energy ³	189	mJ	
P _D @T _C =25°C	Total Power Dissipation ⁴	113	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
R _θ JC	Thermal Resistance from Junction-to-Ambient ³ 1.11		°C/W	
Reja	Thermal Resistance Junction-Ambient ¹	39.4	°C/W	



Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V,	-	-	1.0	μΑ
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	1.2	1.6	2.2	V
R _{DS(on)}	Static Drain-Source on-Resistance	V _{GS} =10V, I _D =20A	-	2.4	2.9	mΩ
C _{iss}	Input Capacitance	\/ -20\/\/ -0\/	-	4610	6915	pF
Coss	Output Capacitance	V _{DS} =30V,V _{GS} =0V, f=1.0MHz	-	2188	3282	pF
C _{rss}	Reverse Transfer Capacitance	1-1.0101112	-	66	132	pF
Qg	Total Gate Charge	\/ -20\/ -404	-	74.37	111.56	nC
Q _{gs}	Gate-Source Charge	V_{DS} =30V, I_{D} =40A, V_{GS} =10V	-	17.26	-	nC
Q _{gd}	Gate-Drain("Miller") Charge	VGS-10V	-	9.44	18.88	nC
t _{d(on)}	Turn-on Delay Time		-	14.13	-	ns
t _r	Turn-on Rise Time	V _{DD} =30V, I _D =40A,	-	63.73	-	ns
t _{d(off)}	Turn-off Delay Time	$R_G=2.7\Omega, V_{GS}=10V$	-	46.8	-	ns
t _f	Turn-off Fall Time		-	105.07	-	ns
ls	Maximum Continuous Drain to Source Diode Forward Current		-	-	125	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	641	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =40A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	T 05°0	-	52.78	105.56	ns
Qrr	Body Diode Reverse Recovery Charge	T _J =25℃, I _F =40A,dI/dt=100A/μs	-	56.31	112.62	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: $T_J = 25\,^{\circ}\mathrm{C}$, $V_{DD} = 30V$, $V_G = 10V$, $R_G = 25\Omega$, L = 0.5mH , $I_{AS} = 12A$
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%

N-SGT Enhancement Mode MOSFET

Typical Characteristics

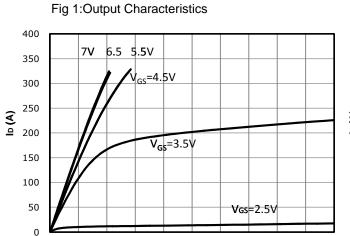


Fig 2:Transfer Characteristics

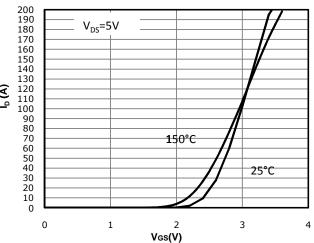


Fig 3: Rds(on) vs Drain Current and Gate Voltage

² V_{DS} (V)

1

0

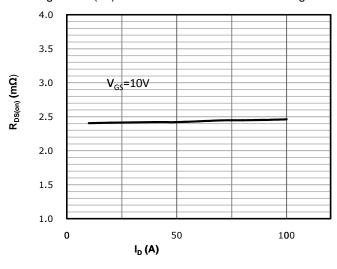


Fig 4: Rds(on) vs Gate Voltage

5

4

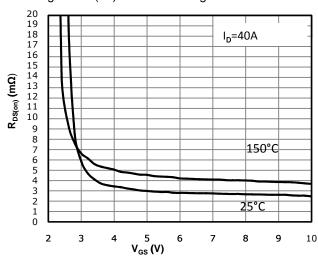


Fig 5: Rds(on) vs. Temperature

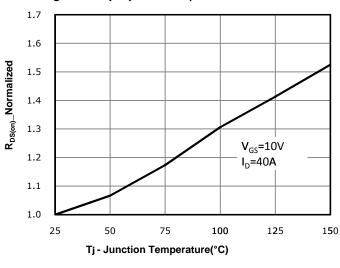
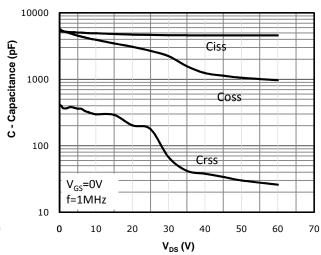
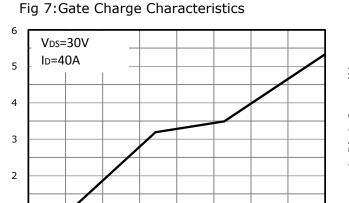


Fig 6: Capacitance Characteristics



BSC027N06LS5ATMA1

N-SGT Enhancement Mode MOSFET



20

Qg (nC)

25

30

35

40

Fig 8: Body-diode Forward
Characteristics

100
150°C
25°C

0.5
0.6
0.7
0.8
0.9
V_{SD} -Diode Forward Voltage(V)

Fig 9: Power Dissipation

10

15

5

1

0

0

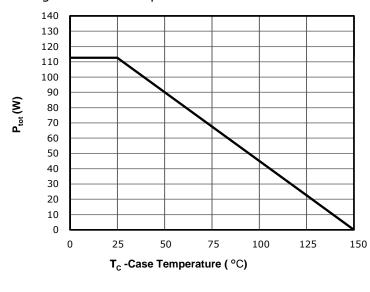


Fig 10: Drain Current Derating

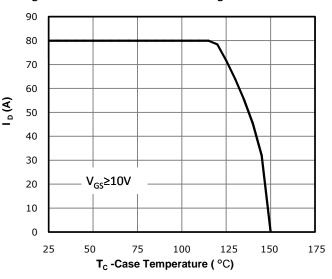
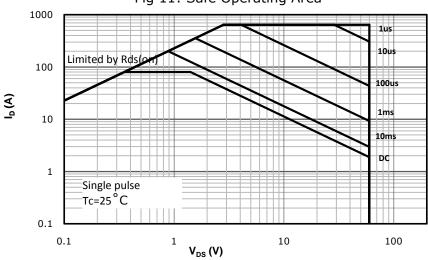
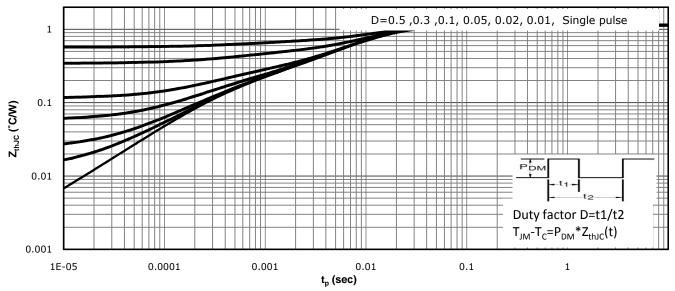


Fig 11: Safe Operating Area

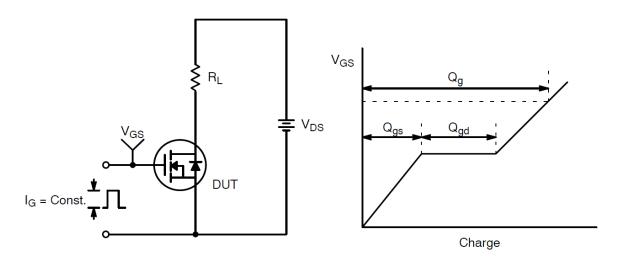


N-SGT Enhancement Mode MOSFET

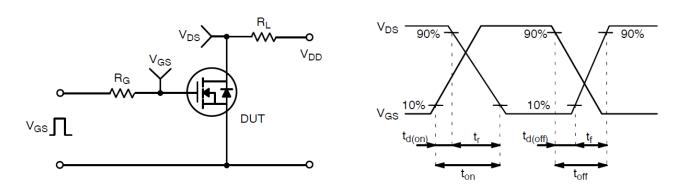
Fig 12: Max. Transient Thermal Impedance



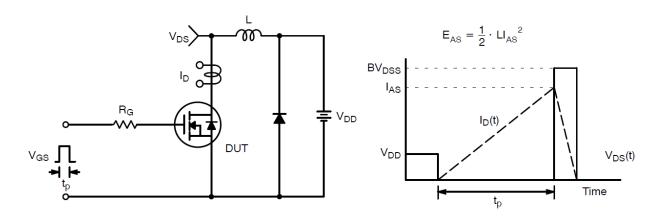
Test Circuit and Waveform:



Gate Charge Test Circuit & Waveform

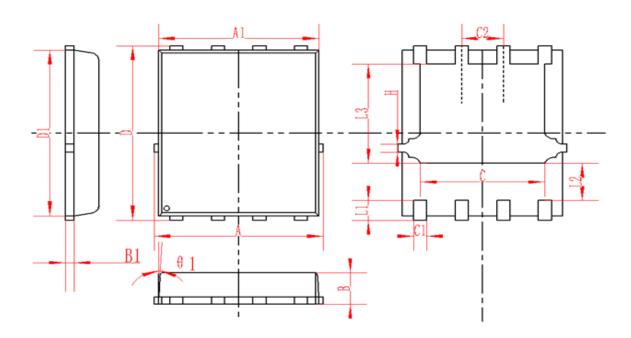


Resistive Switching Test Circuit & Waveforms

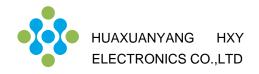


Unclamped Inductive Switching Test Circuit & Waveforms

DFN5X6-8L Package Information



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2		1.27TYP			0.5TYP	
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.

 HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.