

• General Description

The AGM55N15A combines advanced trenchMOSFET technology with a low resistance package to provide extremely low $R_{\text{DS}(\text{ON})}$. This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

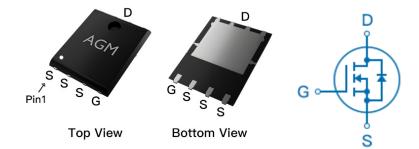
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
150V	48mΩ	23A

PDFN5*6 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM55N15A	AGM55N15A	PDFN5*6	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	150	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	23	А
-	Drain Current-Continuous(Tc=100℃)	12	Α
IDM (pluse)	Drain Current-Pulsed (Note 2)	92	А
PD	Maximum Power Dissipation(Tc=25℃)	52	W
	Maximum Power Dissipation(Tc=100℃)	20	w
EAS	Avalanche energy (Note 3)	20	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}\!\mathbb{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹		20	°C/W
RøJC	Thermal Resistance Junction-Case ¹	2.0	2.5	°C/W



Table 3. Electrical Characteristics (TJ=25 ℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit			
On/Off States									
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	150			V			
IDSS	Zero Gate Voltage Drain Current	VDS=150V,VGS=0V			1	μΑ			
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA			
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250µA	2.5	3.0	4.2	V			
gFS	Forward Transconductance	VDS=5V,ID=10A		10		S			
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A		48	63	mΩ			
Dynamic	Characteristics								
Ciss	Input Capacitance	VDS=75V,VGS=0V,		300		pF			
Coss	Output Capacitance	F=1MHZ		71		pF			
Crss	Reverse Transfer Capacitance	-		3.6		pF			
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz		1.6		Ω			
Switching	Switching Times								
td(on)	Turn-on Delay Time			4.3		nS			
tr	Turn-on Rise Time	VGS=10V,VDS=75V,		3.5		nS			
td(off)	Turn-Off Delay Time	RL=8 Ω ,RGEN=6 Ω		7.6		nS			
tf	Turn-Off Fall Time			3.5		nS			
Qg	Total Gate Charge			3.6		nC			
Qgs	Gate-Source Charge	VGS=0V, VDS=75V, ID=9A		1.6		nC			
Qgd	Gate-Drain Charge			1.9		nC			
Source-Drain Diode Characteristics									
ISD	Source-Drain Current(Body Diode)	TC=25℃			23	А			
VSD	Forward on Voltage	VGS=0V,IS=20A		0.73	1.2	V			
trr	Reverse Recovery Time	IF=20A , dI/dt=100A/μs		75		ns			
Qrr	Reverse Recovery Charge	,TJ=25℃		98		nc			

Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25℃



Typical Electrical & Thermal Characteristics

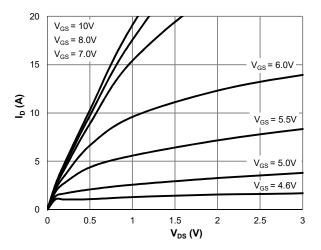


Figure 1: Saturation Characteristics

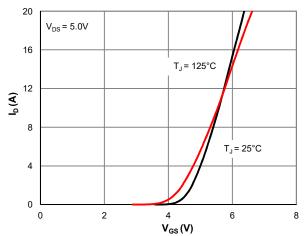


Figure 2: Transfer Characteristics

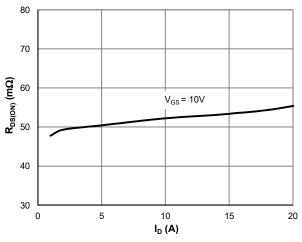


Figure 3: R_{DS(ON)} vs. Drain Current

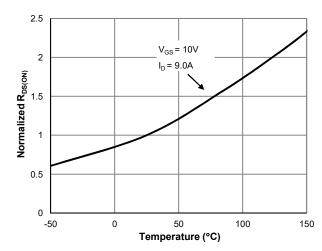


Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

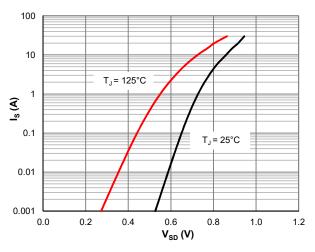


Figure 5: Body-Diode Characteristics

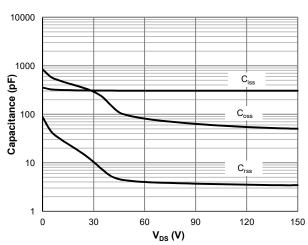
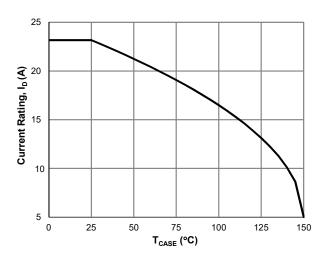


Figure 6: Capacitance Characteristics



Typical Electrical & Thermal Characteristics





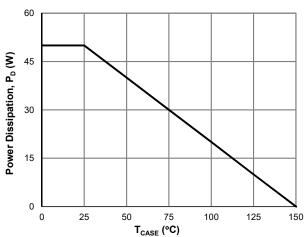


Figure 8: Power De-rating

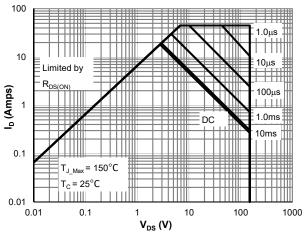


Figure 9: Maximum Safe Operating Area

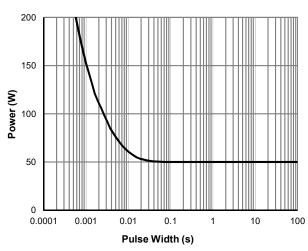


Figure 10: Single Pulse Power Rating, Junction-to-Case

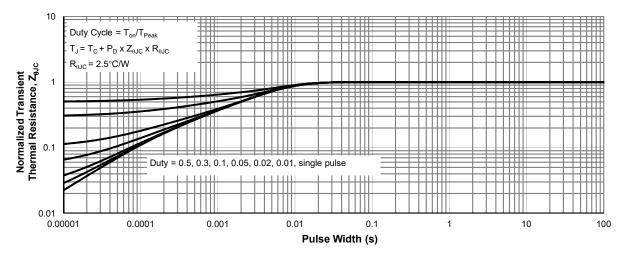
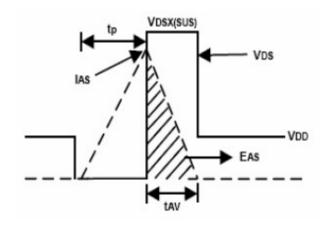


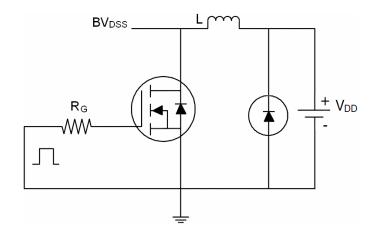
Figure 11: Normalized Maximum Transient Thermal Impedance



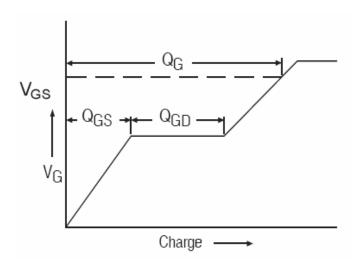
Test Circuit

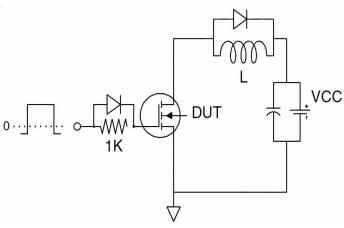
1) E_{AS} Test Circuits



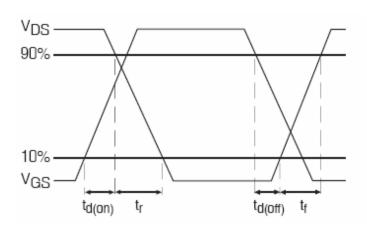


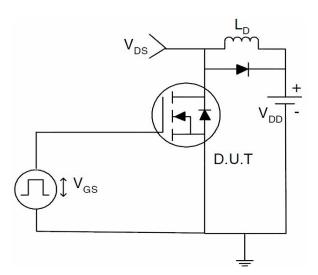
2) Gate Charge Test Circuit:





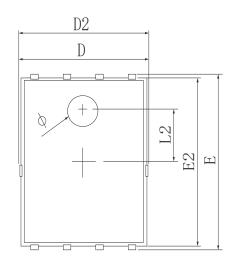
3) Switch Time Test Circuit:

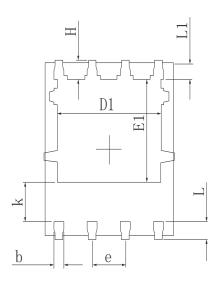


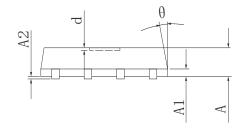




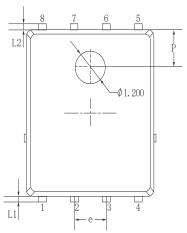
•Dimensions (PDFN5*6)

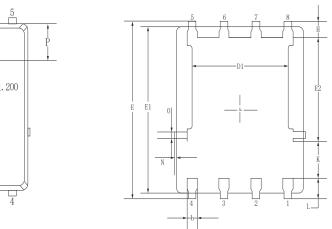






avnen av	MILLIMETER					
SYMBOL	MIN Typ.		MAX			
A	0.900	1.000	1.100			
A1		0.254 REF.				
A2		0~0.05				
D	4. 824	4.900	4.976			
D1	3.910	4.010	4.110			
D2	4. 924	5.000	5. 076			
Е	5. 924	6.000	6.076			
E1	3. 375	3. 475	3. 575			
E2	5. 674	5. 674 5. 750				
b	0.350	0.450				
е		1.270 TYP.				
L	0. 534	0.610	0.686			
L1	0.424	0.500	0.576			
L2	1.800 REF.					
k	1.190	1.290	1.390			
Н	0.549	0.625	0.701			
θ	8°	10°	12°			
Ф	1.100	1.200	1.300			
d			0.100			





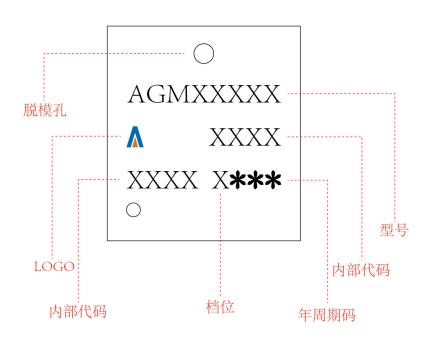
M					-	Ψθ
				_		Ā
С						
*		—-[)——			

Millimeters					
MIN.	NOM.	MAX.			
0.90	1.05	1. 20			
0.35	0.40	0.50			
0.20	0. 25	0.35			
4.90	5. 05	5. 20			
3. 72	3. 82	3. 92			
6.00	6. 00 6. 15				
5. 60	5. 60 5. 75				
3. 47 3. 57		3. 67			
1.27 BSC.					
0.48	0.58	0.68			
1. 17	1. 27	1. 37			
0.64	0.74	0.84			
	0.20 REF.				
8° 10°		12°			
0.08 REF.					
0	-	0. 15			
0.25 REF.					
1.28 REF.					
	MIN. 0. 90 0. 35 0. 20 4. 90 3. 72 6. 00 5. 60 3. 47 0. 48 1. 17 0. 64	MIN. NOM. 0. 90 1. 05 0. 35 0. 40 0. 20 0. 25 4. 90 5. 05 3. 72 3. 82 6. 00 6. 15 5. 60 5. 75 3. 47 3. 57 1. 27 BSC. 0. 48 0. 58 1. 17 1. 27 0. 64 0. 74 0. 20 REF. 8° 10° 0. 08 REF. 0 - 0. 25 REF.			

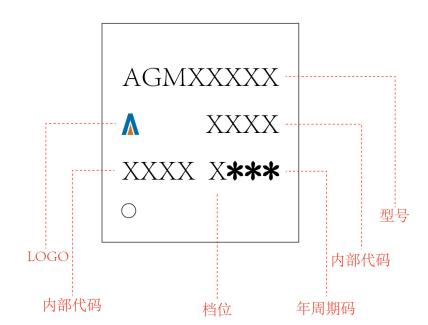


PDFN5*6 Marking Instructions:

Model1:



Model2:





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