

## • General Description

The AGM20T09LL combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{\text{DS(ON)}}$ .

This device is ideal for load switch and battery protection applications.

#### Features

- Advance high cell density Trench technology
- Low R<sub>DS(ON)</sub> to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

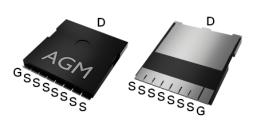
#### Application

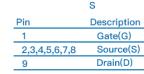
- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

## **Product Summary**

BVDSS	RDSON	ID
200V	8.3mΩ	110A

## **TOLL Pin Configuration**





Top View Bo

Bottom View

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM20T09LL	AGM20T09LL	TOLL	330mm	25mm	2000

## Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	200	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	110	А
	Drain Current-Continuous(Tc=100℃)	75	Α
IDM (pluse)	Drain Current-Pulsed (Note 2)	440	А
PD	Maximum Power Dissipation(Tc=25℃)	278	W
	Maximum Power Dissipation(Tc=100℃)	111	w
EAS	Avalanche energy (Note 3)	1682	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$

#### Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>		62	°C/W
RθJC	Thermal Resistance Junction-Case <sup>1</sup>		0.45	°C/W



Table 3. Electrical Characteristics (TJ=25°Cunless otherwise noted)

Table 3. Electrical Characteristics (TJ=25 ℃ unless otherwise noted)						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	200			V
IDSS	Zero Gate Voltage Drain Current	VDS=200V,VGS=0V			1.0	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250µA	2.0		4.0	V
gFS	Forward Transconductance	VDS=5V,ID=10A		5		S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=35A		8.3	10.5	mΩ
Dynamic (	Characteristics					
Ciss	Input Capacitance	VDS=100V,VGS=0V,		10656		pF
Coss	Output Capacitance	F=1MHZ		16		pF
Crss	Reverse Transfer Capacitance			389		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz				Ω
Switching	Times					
td(on)	Turn-on Delay Time			46		nS
tr	Turn-on Rise Time	VGS=10V,VDS=100V,		24		nS
td(off)	Turn-Off Delay Time	ID=55A,RGEN=4.7Ω		88		nS
tf	Turn-Off Fall Time			18	-	nS
Qg	Total Gate Charge			145		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=100V, ID=55A		49		nC
Qgd	Gate-Drain Charge	- VBG-100V, IB-00A		27		nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)				110	Α
VSD	Forward on Voltage	VGS=0V,IS=50A			1.2	V
trr	Reverse Recovery Time	IF=50A , dI/dt=100A/μs		185		ns
Qrr	Reverse Recovery Charge			469		nc

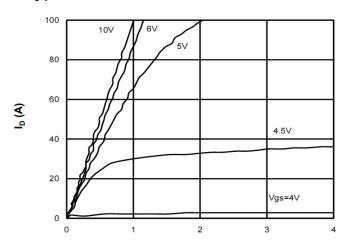
Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

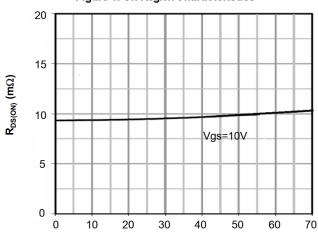
Notes 3.EAS condition: TJ=25  $^{\circ}$ C,VDD=50V,Vgs=10V,ID=58A,L=1mH,RG=25ohm



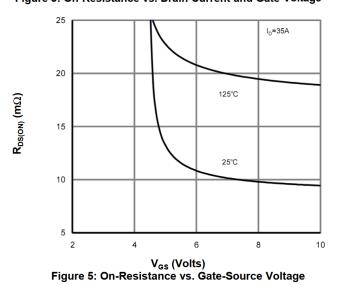
# **Typical Characteristics**

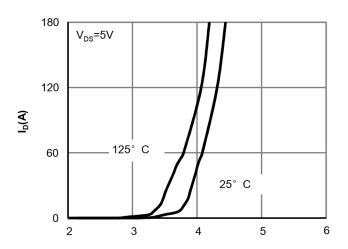




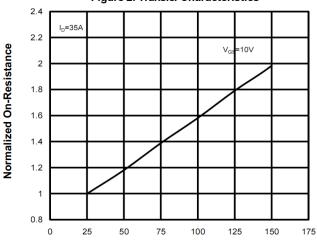


 $I_{\text{D}}\left(\text{A}\right)$  Figure 3: On-Resistance vs. Drain Current and Gate Voltage

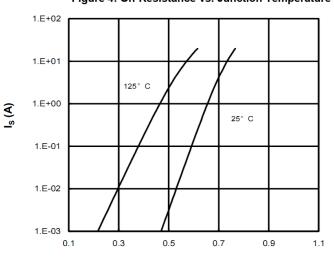




V<sub>GS</sub>(Volts) Figure 2: Transfer Characteristics

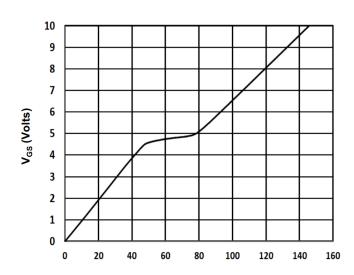


Temperature (°C) Figure 4: On-Resistance vs. Junction Temperature

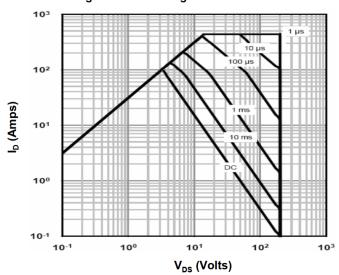


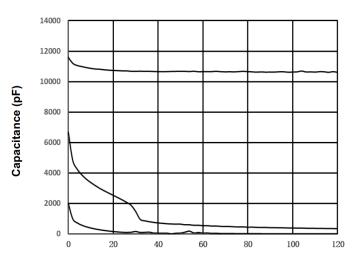
 $V_{\text{SD}}$  (Volts) Figure 6: Body-Diode Characteristics





 $\mathbf{Q}_{\mathrm{g}}$  (nC) Figure 7: Gate-Charge Characteristics





V<sub>DS</sub> (Volts) Figure 8: Capacitance Characteristics



# Test Circuits and Waveforms

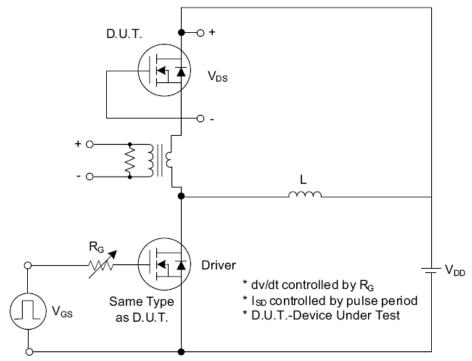


Fig. 1.1 Peak Diode Recovery dv/dt Test Circuit

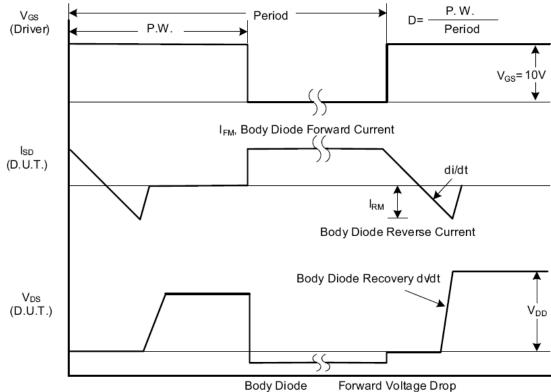


Fig. 1.2 Peak Diode Recovery dv/dt Waveforms



# Test Circuits and Waveforms (Cont.)

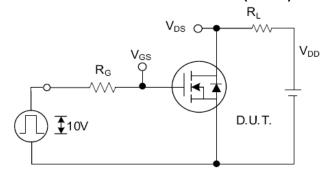


Fig. 2.1 Switching Test Circuit

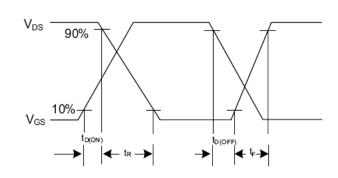


Fig. 2.2 Switching Waveforms

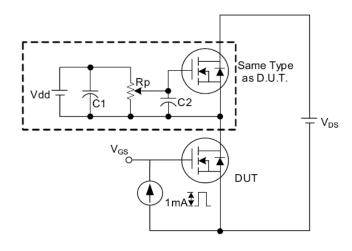


Fig. 3 . 1 Gate Charge Test Circuit

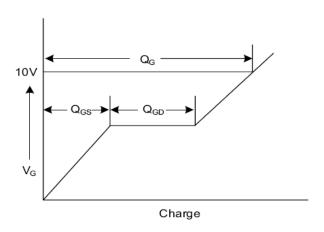


Fig. 3.2 Gate Charge Waveform

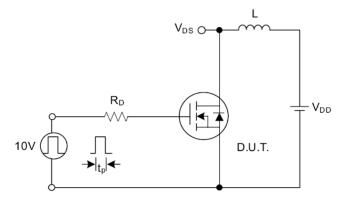


Fig. 4.1 Unclamped Inductive Switching Test Circuit

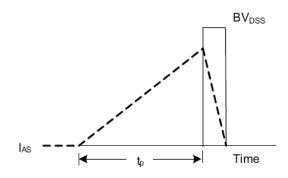
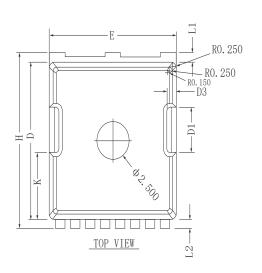
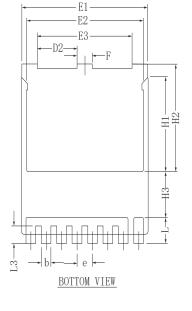


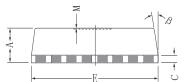
Fig. 4.2 Unclamped Inductive Switching Waveforms



## •Dimensions (TOLL)







Symbols	MIIIIMOUCIS				
3ymb018	MIN.	NOM.	MAX.		
A	2.20	2.30	2.40		
b	0.65	0.75	0.85		
С	0. 508 REF				
D	10.25	10. 25 10. 40 10. 55			
D1	2.85	2. 85 3. 00 3. 1			
D2	2.95	3. 10	3. 25		
D3		0.75 REF			
Е	9.75	9.90	10.05		
E1	9.65	9.80	9. 95		
E2	8. 95	9.10	9. 25		
E3	7. 25	7.40	7. 55		
е	1.20 BSC				
F	1.05	1.20	1.35		
Н	11. 55	11.70	11.85		
H1	6.03	6. 18	6. 33		
H2	6.85	7.00	7. 15		
Н3	3.00 BSC				
L	1.55	1.70	1.85		
L1	0.55	0.70	0.85		
L2	0.45	0.60	0.75		
L3	1.00	1. 15	1.15 1.30		
M	0.08 REF				
β	8°	10°	12°		
K	4. 25	4. 40	4. 55		
IX	7. 20	7. 10	7.00		

MILLIMETER

NOM.

2.300

1.800

0.700

9.800

0.750

1.200

0.500

10.400

2.400

1.900

0.800

9.900

0.850

1.300

0.600

10.500

0.600

3.200

SYMBOL

A1

b

b1

b2

D

MIN.

2.200

1.700

0.600

9.700

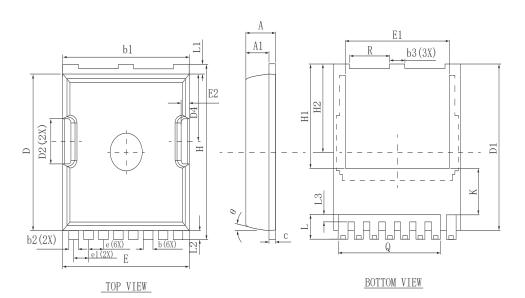
0.650

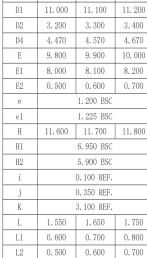
1.100

0.400

10.300

Millimeters





0.400

3.000

0.500 7.950 REF.

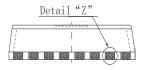
3. 100 10° REF.

L3

Q

R

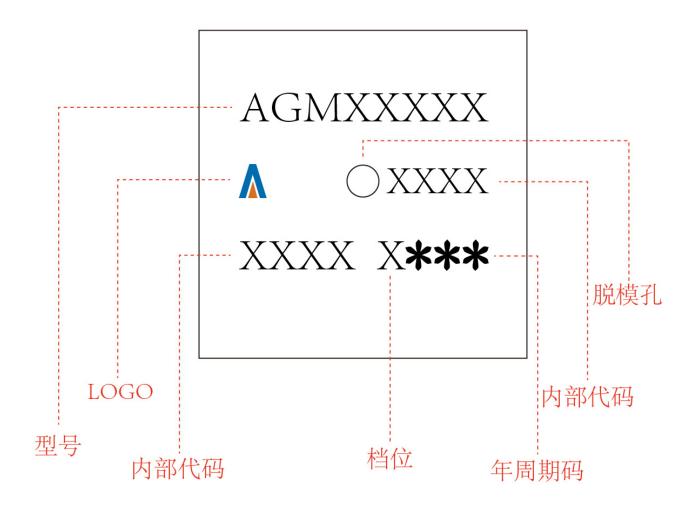
θ







TOLL
Marking Instructions:





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