



# N-Channel 200 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY		
V <sub>DS</sub> (V)	200	
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 10 V	0.0216	
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 7.5 \text{ V}$	0.0235	
Q <sub>g</sub> typ. (nC)	31.6	
I <sub>D</sub> (A)	64	
Configuration	Single	

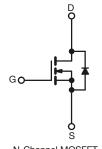
#### **FEATURES**

- ThunderFET® power MOSFET
- Low R<sub>DS</sub> Q<sub>g</sub> figure-of-merit (FOM)
- Maximum 175 °C junction temperature
- 100 % R<sub>a</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



#### **APPLICATIONS**

- · Synchronous rectification
- Power supplies
- DC/AC inverter
- DC/DC converter
- · Solar micro inverter
- · Motor drive switch



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ORDERING INFORMATION	
Package	TO-263
Lead (Pb)-free and halogen-free	SUM90220E-GE3

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	200	
Gate-source voltage		V <sub>GS</sub>	± 20	V
Continuous dunin suurant	T <sub>C</sub> = 25 °C		64	
Continuous drain current	T <sub>C</sub> = 125 °C	l <sub>D</sub>	37	
Pulsed drain current (t = 100 μs)  Continuous source-drain diode current		I <sub>DM</sub>	100	A
		I <sub>S</sub>	64.7	
Single pulse avalanche current <sup>a</sup>	ulse avalanche current a		45	
Single pulse avalanche energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	101	mJ
	T <sub>C</sub> = 25 °C	Б	230 <sup>b</sup>	10/
Maximum power dissipation	T <sub>C</sub> = 125 °C	P <sub>D</sub>	77 b	W
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	
Soldering recommendations (peak temperature) <sup>c</sup>			260	°C

THERMAL RESISTANCE RATING	is			
PARAMETER		SYMBOL	MAXIMUM	UNIT
Maximum junction-to-ambient (PCB mount) c		R <sub>thJA</sub>	40	°C/W
Maximum junction-to-case (drain)	Steady state	R <sub>thJC</sub>	0.65	C/W

#### Notes

- a. Duty cycle  $\leq 1$  %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).



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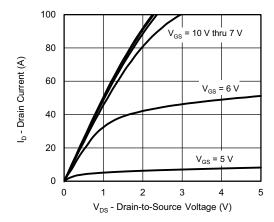
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200	-	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	-	4	V
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	250	nA
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V	-	-	1	
Zero gate voltage drain current	$I_{DSS}$	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	150	μΑ
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C	-	-	5	mA
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	30	-	-	Α
Duning and an atota projetoway 2	_	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A	-	0.0180	0.0216	
Drain-source on-state resistance a	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, I_D = 10 \text{ A}$	-	0.0188	0.0235	Ω
Forward transconductance a	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A	-	37	-	S
Dynamic <sup>b</sup>						
Input capacitance	C <sub>iss</sub>		-	1950	-	
Output capacitance	C <sub>oss</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	170	-	рF
Reverse transfer capacitance	C <sub>rss</sub>		-	15	-	
Total gate charge	Qg		-	31.6	48	
Gate-source charge	Q <sub>gs</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	-	8.6	-	nC
Gate-drain charge	Q <sub>gd</sub>		-	7.6	-	
Gate resistance	Rg	f = 1 MHz	0.6	3	6	Ω
Turn-on delay time	t <sub>d(on)</sub>		-	15	30	
Rise time	t <sub>r</sub>	$V_{DD}$ = 100 V, $R_L$ = 8.3 $\Omega$ , $I_D \cong$ 12 A,	-	35	53	
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	28	42	ns
Fall time	t <sub>f</sub>		-	38	57	
<b>Drain-Source Body Diode Characteristic</b>	s					
Pulse diode forward current (t = 100 μs)	I <sub>SM</sub>		-	-	100	Α
Body diode voltage	V <sub>SD</sub>	I <sub>F</sub> = 12 A, V <sub>GS</sub> = 0 V	-	0.85	1.5	V
Body diode reverse recovery time	t <sub>rr</sub>		-	120	180	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	L = 12 A di/dt = 100 A/::a	-	0.91	1.37	μC
Reverse recovery fall time	ta	I <sub>F</sub> = 12 A, di/dt = 100 A/μs	-	95	-	·
Reverse recovery rise time	t <sub>b</sub>		-	25	-	ns
Body diode peak reverse recovery charge	I <sub>RM(REC)</sub>		-	12	18	Α

## Notes

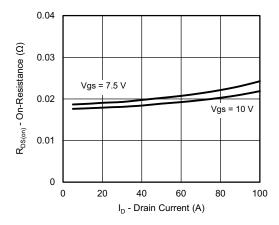
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

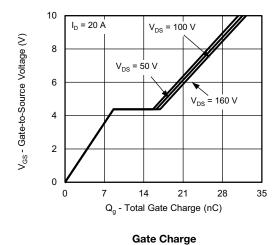


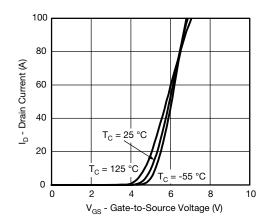


#### **Output Characteristics**

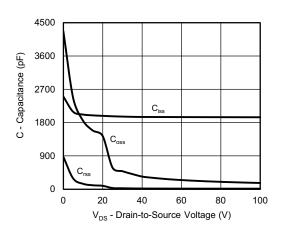


On-Resistance vs. Drain Current and Gate Voltage

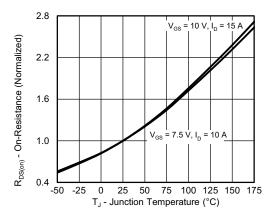




**Transfer Characteristics** 

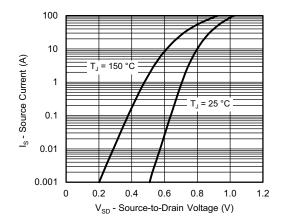


Capacitance

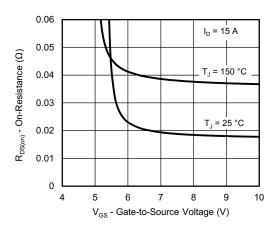


On-Resistance vs. Junction Temperature

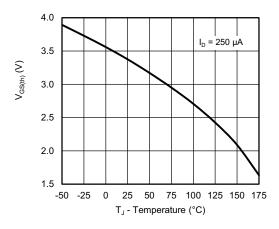




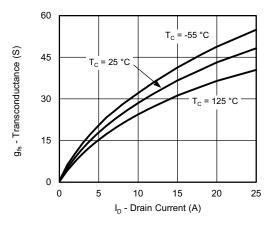
Source-Drain Diode Forward Voltage



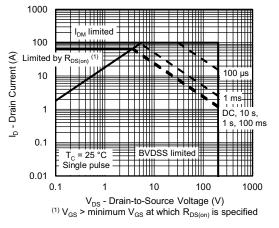
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 

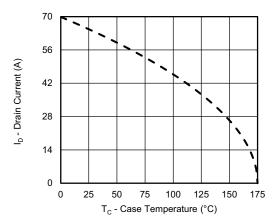


Transconductance

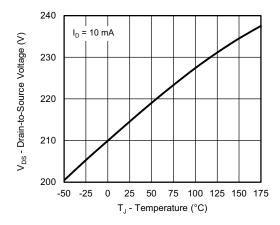


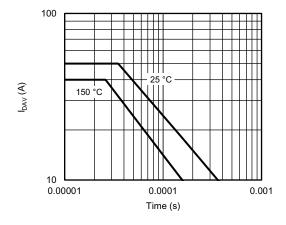
Safe Operating Area, Junction-to-Ambient





### Current Derating a





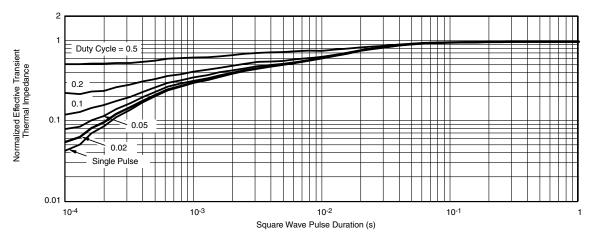
Drain Source Breakdown vs. Junction Temperature

I<sub>DAV</sub> vs. Time

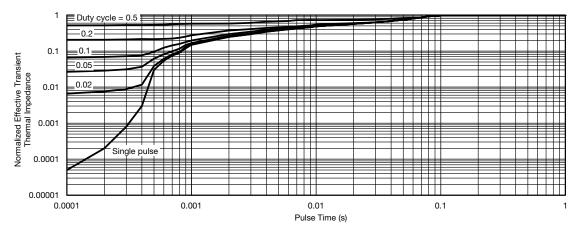
#### Note

a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 25 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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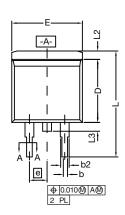
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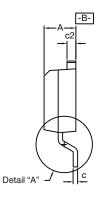


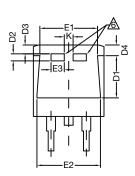
# TO-263 (D<sup>2</sup>PAK): 3-LEAD

### **VERSION 1: FACILITY CODE = T**

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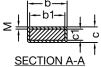








**DETAIL A (ROTATED 90°)** 



<u> </u>	b	1	<u> </u>
< T		10	ပ
SI	FCTION	1 A-A	Ŧ

## **Notes**

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

6. This feature is for thick lead.

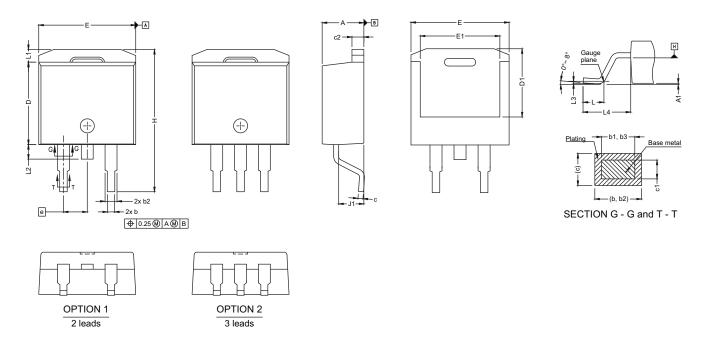
		INC	HES	MILLIMETERS		
	DIM.	MIN.	MAX.	MIN.	MAX.	
	Α	0.160	0.190	4.064	4.826	
	b	0.020	0.039	0.508	0.990	
	b1	0.020	0.035	0.508	0.889	
	b2	0.045	0.055	1.143	1.397	
c*	Thin lead	0.013	0.018	0.330	0.457	
C	Thick lead	0.023	0.028	0.584	0.711	
c1	Thin lead	0.013	0.017	0.330	0.431	
CI	Thick lead	0.023	0.027	0.584	0.685	
	c2	0.045	0.055	1.143	1.397	
	D	0.340	0.380	8.636	9.652	
	D1	0.220	0.240	5.588	6.096	
	D2	0.038	0.042	0.965	1.067	
	D3	0.045	0.055	1.143	1.397	
	D4	0.044	0.052	1.118	1.321	
	E	0.380	0.410	9.652	10.414	
	<u>E1</u>	0.245	-	6.223	-	
	E2	0.355	0.375	9.017	9.525	
	E3	0.072	0.078	1.829	1.981	
	е	0.100	) BSC	2.54	BSC	
K		0.045	0.055	1.143	1.397	
	L	0.575	0.625	14.605	15.875	
	L1	0.090	0.110	2.286	2.794	
L2		0.040	0.055	1.016	1.397	
	L3	0.050	0.070	1.270	1.778	
	L4	0.010	BSC	0.254	BSC	
	М	-	0.002	-	0.050	



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## **VERSION 2: FACILITY CODE = N**



DIM.	MIN.	MAX.
A	4.36	4.56
A1	0	0.25
b	0.70	0.90
b1	0.51	0.89
b2	1.20	1.46
b3	1.17	1.37
С	0.38	0.694
c1	0.38	0.534
c2	1.19	1.34
D	8.60	9.00
D1	6.9	7.5
E	10.15	10.55
E1	8.1	8.7
е	2.54	BSC
Н	15.0	15.6
L	1.9	2.5
L1	-	1.65
L2	-	1.78
L3	0.25	5 typ.
L4	4.78	5.28
J1	2.56	2.96

DWG: 5843





# RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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