## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)TYP</sub>	l <sub>D</sub>
100V	2.6mΩ@10V	240A



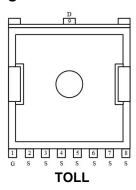
#### **Feature**

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

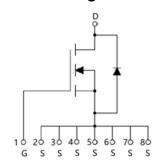
## **Applications**

- PWM Application
- Hard switched and high frequency circuits
- Power Management

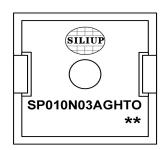
#### **Package**



## Circuit diagram



## Marking



SP010N03AGHTO: Product code
\*\*: Week code

#### **Order Information**

Device	Package	Unit/Tape
SP010N03AGHTO	TOLL	2000



100V N-Channel Power MOSFET

## Absolute maximum ratings (Ta=25°C,unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current1 (Tc=25°C)	I <sub>D</sub>	240	Α
Continuous Drain Current1 (Tc=100°C)	I <sub>D</sub>	160	Α
Pulsed Drain Current	I <sub>DM</sub>	960	Α
Single Pulse Avalanche Energy <sup>1</sup>	Eas	1225	mJ
Power Dissipation (Tc=25°C)	P <sub>D</sub>	260	W
Thermal Resistance Junction-to-Case	R <sub>eJC</sub>	0.48	°C/W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	$^{\circ}$
Operating Junction Temperature Range	TJ	-55 to 150	$^{\circ}$ C

## Electrical characteristics (Ta=25°C, unless otherwise noted)

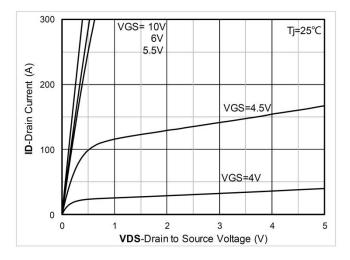
Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	ID = 250μA, VGS = 0V	100	110	-	V
Drain Cut-Off Current	I <sub>DSS</sub>	VDS = 80V, VGS = 0V	-	-	1	
Gate Leakage Current	I <sub>GSS</sub>	VGS = ±20V, VDS = 0V	-	-	±0.1	μA
Gate Threshold Voltage	$V_{GS(th)}$	VDS = VGS, ID = 250μA	2.0	3.0	4.0	V
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	VGS = 10V, ID = 30A	-	2.6	3.0	mΩ
Dynamic Characteristics						
Input Capacitance	Ciss		-	7162	-	
Output Capacitance	Coss	VDS =50V, VGS = 0V, f = 1.0MHz	-	1067	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	35	-	
Switching Characteristics						
Total Gate Charge	Qg		-	105	-	
Gate-Source Charge	Q <sub>gs</sub>	VDS=50V , VGS=10V , ID=100A	-	47	-	nC
Gate-Drain Charge	$Q_{gd}$		-	23	-	
Turn-On Delay Time	t <sub>d(on)</sub>		-	26	-	
Rise Time	t <sub>r</sub>	VGS = 10V, VDS =50V, ID=100A	-	75	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$RG = 6\Omega$	-	87	-	113
Fall Time	t <sub>f</sub>		-	30	-	
Drain-Source Body Diode Characteris	tics		_			
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1A, VGS = 0V	-	-	1.2	V
Maximum Body-Diode Continuous Current	Is		-	_	240	Α
Body Diode Reverse Recovery Time	T <sub>rr</sub>	1 400A di/dt 400A/v- T1 05°C	-	72	-	nS
Body Diode Reverse Recovery Charge	Qrr	I <sub>S</sub> =100A, di/dt=100A/us, TJ=25℃		180	-	nC

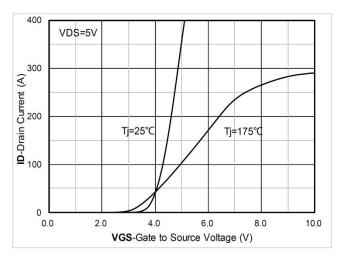
#### Note:

1. The test condition is VDD=50V,VGS=10V,L=0.5mH,RG=25 $\Omega$ ;

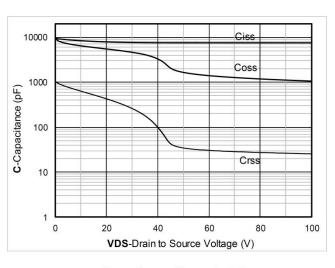


#### **Typical Characteristics**

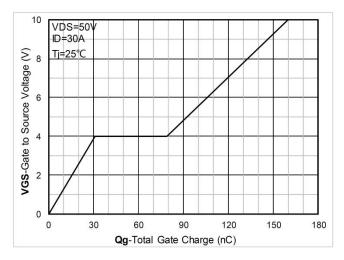




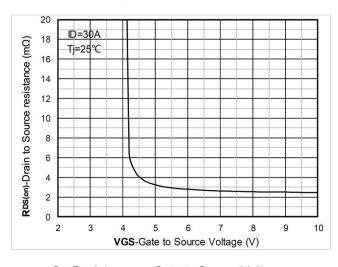
**Output Characteristics** 



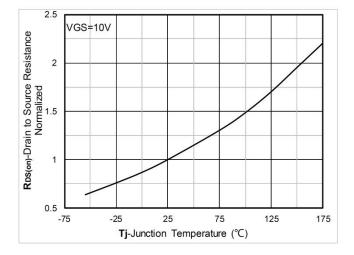
**Transfer Characteristics** 



Capacitance Characteristics



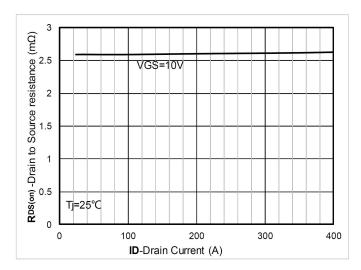
Gate Charge

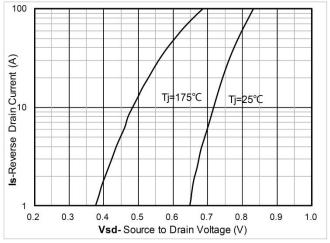


On-Resistance vs Gate to Source Voltage

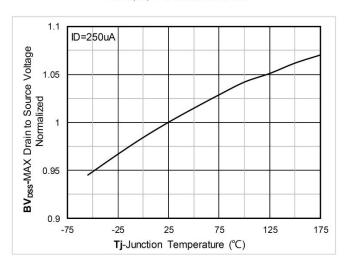
Normalized On-Resistance

# 100V N-Channel Power MOSFET

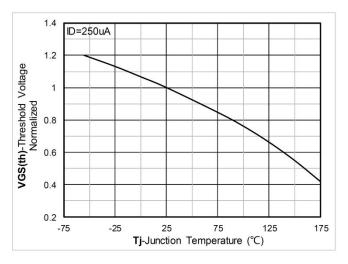




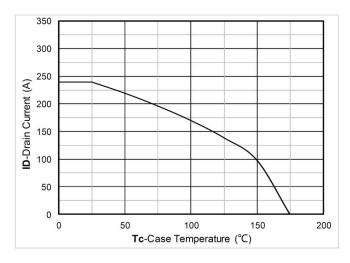
RDS(on) VS Drain Current



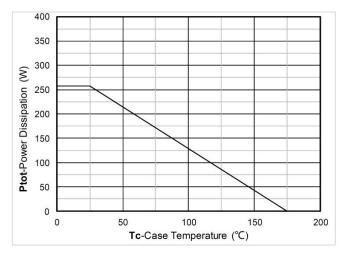
Forward characteristics of reverse diode



Normalized breakdown voltage

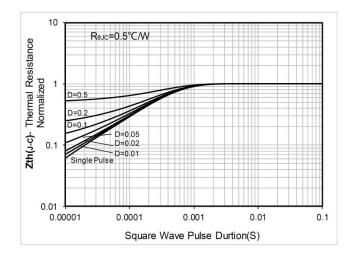


Normalized Threshold voltage

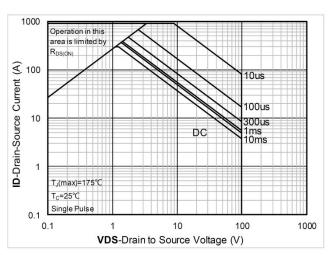


Current dissipation

Power dissipation

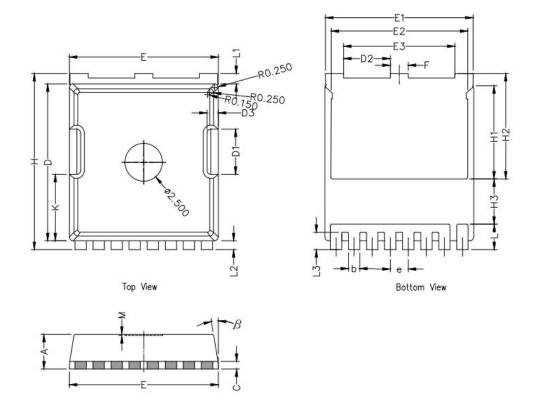






Safe Operation Area

# TOLL Package Information



Symbol	Dimensions In Millimeters				
	Min.	Nom.	Max.		
A	2.20	2.30	2.40		
b	0.65	0.75	0.85		
С		0.508 REF			
D	10.25	10.40	10.55		
D1	2.85	3.00	3.15		
E	9.75	9.90	10.05		
E1	9.65	9.80	9.95		
E2	8.95	9.10	9.25		
E3	7.25	7.40	7.55		
е	1.20 BSC				
F	1.05	1.20	1.35		
Н	11.55	11.70	11.85		
H1	6.03	6.18	6.33		
H2	6.85	7.00	7.15		
H3	3.00 BSC				
L	1.55	1.70	1.85		
L1	0.55	0.7	0.85		
L2	0.45	0.6	0.75		
М	0.08 REF.				
β	8°	10°	12°		
K	4.25	4.40	4.55		