

MOSFETs Silicon N-channel MOS (U-MOSVIII-H)

# TK22E10N1

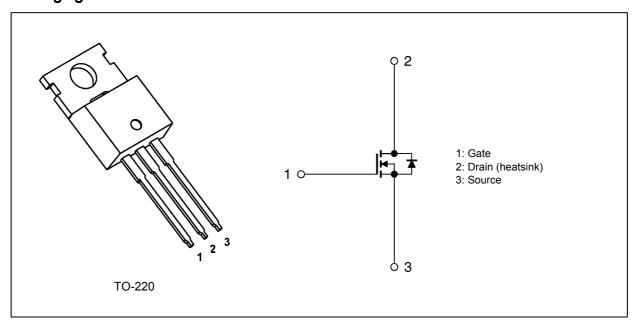
#### 1. Applications

• Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 11.5 \text{ m}\Omega$  (typ.) ( $V_{GS} = 10 \text{ V}$ )
- (2) Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 100 \text{ V)}$
- (3) Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 0.3 mA)

#### 3. Packaging and Internal Circuit



# 4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V <sub>DSS</sub>	100	V
Gate-source voltage			V <sub>GSS</sub>	±20	
Drain current (DC)	$(T_c = 25^{\circ}C)$	(Note 1)	I <sub>D</sub>	52	Α
Drain current (DC)		(Note 1,2)	I <sub>D</sub>	22	
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I <sub>DP</sub>	102	
Power dissipation	$(T_c = 25^{\circ}C)$		$P_{D}$	72	W
Single-pulse avalanche energy		(Note 3)	E <sub>AS</sub>	48	mJ
Avalanche current			I <sub>AR</sub>	22	Α
Channel temperature	•		T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	1.72	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	83.3	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Device mounted with heatsink so that  $R_{th(ch-a)}$  becomes 4.16°C/W.

Note 3:  $V_{DD}$  = 80 V,  $T_{ch}$  = 25°C (initial), L = 77.6  $\mu H$ ,  $I_{AR}$  = 22 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



#### 6. Electrical Characteristics

### 6.1. Static Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V	_	-	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	100	_	_	V
Drain-source breakdown voltage (Note 4)	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	65	_	_	
Gate threshold voltage	V <sub>th</sub>	$V_{DS}$ = 10 V, $I_{D}$ = 0.3 mA	2.0	_	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11 A	_	11.5	13.8	mΩ

Note 4: If a reverse bias is applied between gate and source, this device enters  $V_{(BR)DSX}$  mode. Note that the drain-source breakdown voltage is lowered in this mode.

### 6.2. Dynamic Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	1800	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	18	_	
Output capacitance	C <sub>oss</sub>		_	310	_	
Gate resistance	r <sub>g</sub>	_	_	2.0		Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1	_	11	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	27	_	
Switching time (fall time)	t <sub>f</sub>		_	11	_	
Switching time (turn-off time)	t <sub>off</sub>		_	38	_	

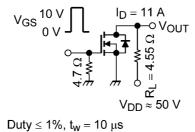


Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}$ C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	$Q_g$	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 22 \text{ A}$	_	28	-	nC
Gate-source charge 1	Q <sub>gs1</sub>			8.7		
Gate-drain charge	$Q_{gd}$			8.1		
Gate switch charge	$Q_SW$		_	12		



## 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I <sub>DR</sub>	_	_	_	22	Α
Reverse drain current (pulsed)	(Note 5)	I <sub>DRP</sub>	_	_	_	102	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 22 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time	(Note 6)	t <sub>rr</sub>	I <sub>DR</sub> = 22 A, V <sub>GS</sub> = 0 V	_	54		ns
Reverse recovery charge	(Note 6)	Q <sub>rr</sub>	-dl <sub>DR</sub> /dt = 100 A/μs	_	94		nC

Note 5: Ensure that the channel temperature does not exceed 150°C.

Note 6: Ensure that  $V_{DS}$  peak does not exceed  $V_{DSS}$ .

### 7. Marking

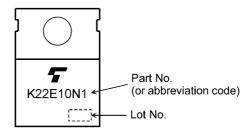
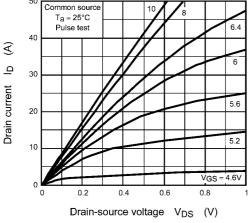
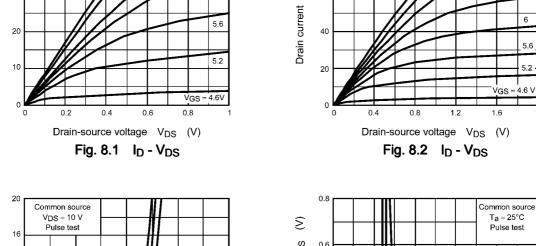


Fig. 7.1 Marking

#### 8. Characteristics Curves (Note)





100

80

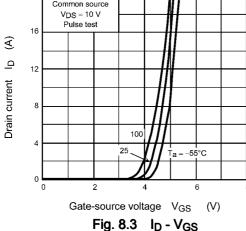
60

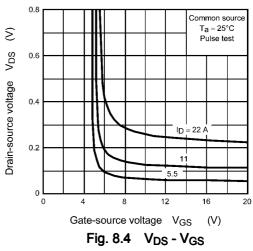
 $\overline{\mathfrak{C}}$ 

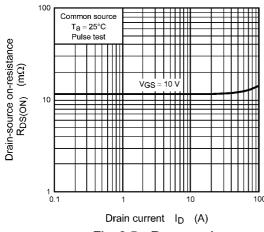
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Common source

T<sub>a</sub> = 25°C Pulse test







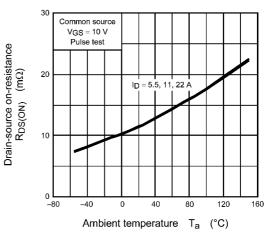


Fig. 8.5 R<sub>DS(ON)</sub> - I<sub>D</sub>

Fig. 8.6 R<sub>DS(ON)</sub> - T<sub>a</sub>

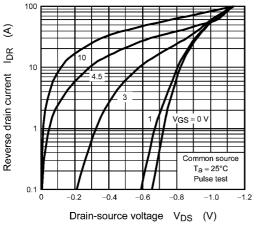


Fig. 8.7  $I_{DR}$  -  $V_{DS}$ 

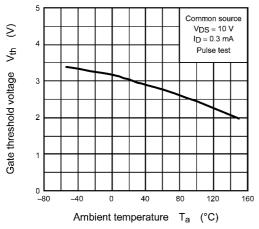


Fig. 8.9 Vth - Ta

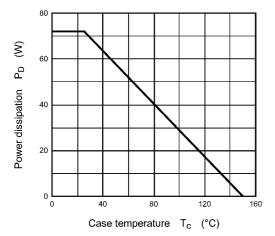


Fig. 8.11 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

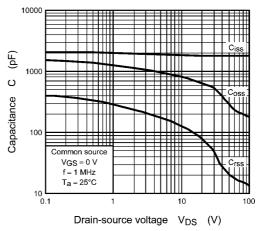


Fig. 8.8 Capacitance - V<sub>DS</sub>

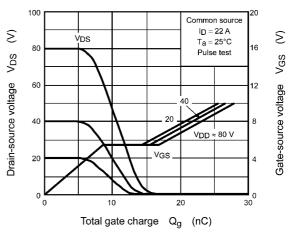


Fig. 8.10 Dynamic Input/Output Characteristics

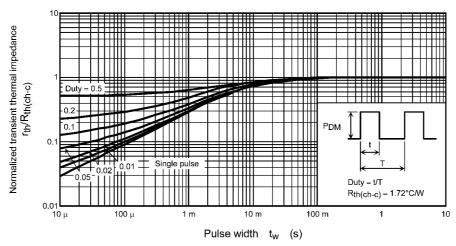


Fig. 8.12  $r_{th}/R_{th(ch-c)} - t_w$ (Guaranteed Maximum)

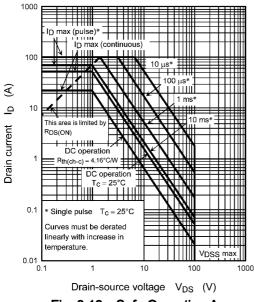


Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

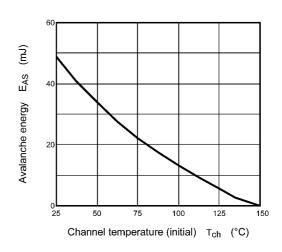


Fig. 8.14 E<sub>AS</sub> - T<sub>ch</sub> (Guaranteed Maximum)

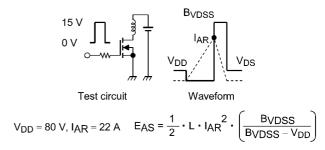


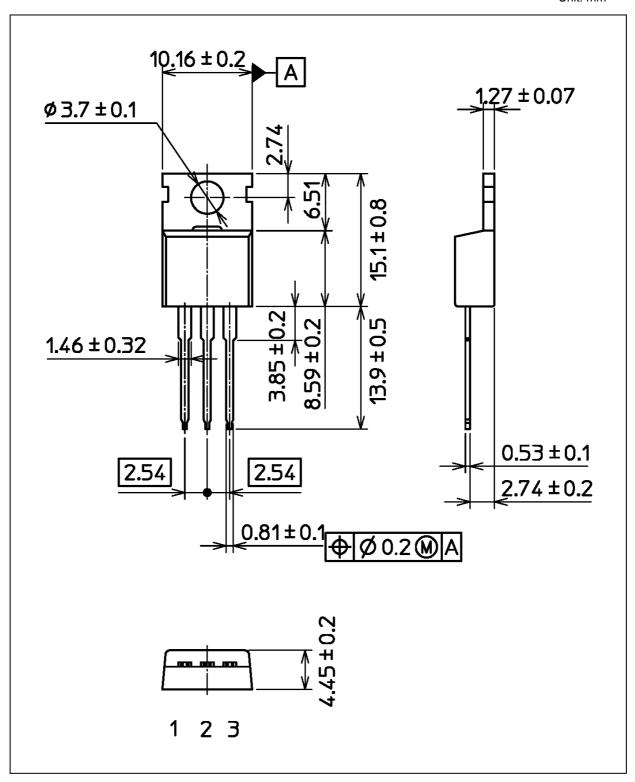
Fig. 8.15 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



### **Package Dimensions**

Unit: mm



Weight: 1.93 g (typ.)

	Package Name(s)
TOSHIBA: 2-10X1A	
Nickname: TO-220	



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