

MOSFET - N-Channel, POWERTRENCH®

75 V, 235 A, 3.2 m Ω

FDA032N08

Description

This N-Channel MOSFET is produced using onsemi's advanced POWERTRENCH process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Features

- $R_{DS(on)} = 2.5 \text{ m}\Omega$ (Typ.) @ $V_{GS} = 10 \text{ V}$, $I_D = 75 \text{ A}$
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low
- High Power and Current Handling Capability
- RoHS Compliant

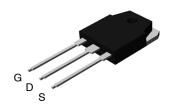
Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies

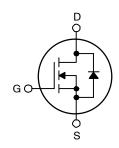
MOSFET MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Value	Unit	
V _{DSS}	Drain to Source Voltage	75	V	
V _{GSS}	Gate to Source Voltage	±20	V	
I _D	Drain Current Continuous (T _C = 25°C, Silicon Limited) Continuous (T _C = 100°C, Silicon Limited) Continuous (T _C = 25°C, Package Limited)	235 165 120	Α	
I _{DM}	Drain Current - Pulsed (Note 1)	940	Α	
E _{AS}	AS Single Pulsed Avalanche Energy (Note 2)		mJ	
dv/dt	dv/dt Peak Diode Recovery dv/dt (Note 3)		V/ns	
P _D	Power Dissipation (T _C = 25°C) – Derate above 25°C	375 2.5	W W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range	–55 to +175	°C	
TL	T _L Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



TO-3P-3LD / EIAJ SC-65, ISOLATED CASE 340BZ



MARKING DIAGRAM

FDA 032N08 **AYWWZZ**

FDA032N08 = Specific Device Code = Assembly Location

YWW = Date Code (Year and Week)

ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FDA032N08	TO-3P-3L (Pb-Free)	450 Units / Tube

THERMAL CHARACTERISTICS

Symbo	Parameter	FDA032N08	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max	0.4	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max	40	°C/W

FLECTRICAL CHARACTERISTICS (T.

	CAL CHARACTERISTICS (T _C = 25°C, t	,	T	I _		1
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHAR	ACTERISTIC				_	
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$, $T_C = 25^{\circ}C$	75	-	-	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.05	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 75 V, V _{GS} = 0 V	-	-	1	μΑ
		V _{DS} = 75 V, T _C = 150°C	-	_	10	
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA
ON CHARA	CTERISTICS		-			
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2.5	3.5	4.5	V
R _{DS(on)}	Static Drain to Source On-Resistance	V _{GS} = 10 V, I _D = 75 A	-	2.5	3.2	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 75 A	-	180	-	S
DYNAMIC (CHARACTERISTICS		•		•	
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	11400	15160	pF
C _{oss}	Output Capacitance		-	1360	1810	pF
C _{rss}	Reverse Transfer Capacitance		-	595	800	pF
Q _{g(TOT)}	Total Gate Charge at 10 V	V _{DS} = 60 V, I _D = 75 A,	-	169	220	nC
Q_{gs}	Gate to Source Gate Charge	V _{GS} = 10 V (Note 4)	-	60	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	47	-	nC
SWITCHING	G CHARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 37.5 V, I _D = 75 A,	_	230	470	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega, V_{GS} = 10 V \text{ (Note 4)}$	_	191	392	ns
t _{d(off)}	Turn-Off Delay Time	7	_	335	680	ns
t _f	Turn-Off Fall Time	7	_	121	252	ns
DRAIN-SO	URCE DIODE CHARACTERISTICS		•			
IS	Maximum Continuous Drain to Source Diode Forward Current		-	_	235	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	940	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 75 A	-	-	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 75 A,	-	53	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	77	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. L = 0.71 mH, $I_{AS} = 75$ A, $V_{DD} = 50$ V, $R_{G} = 25$ Ω , Starting $T_{J} = 25^{\circ}C$.

3. $I_{SD} \le 75$ A, $I_{SD} \le 75$ A, $I_{SD} \le 8V_{DSS}$, Starting $I_{SD} \le 75$ C.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

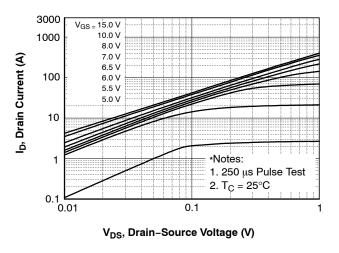
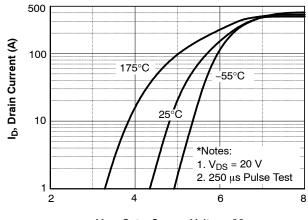


Figure 1. On-Region Characteristics



 V_{GS} , Gate-Source Voltage (V) Figure 2. Transfer Characteristics

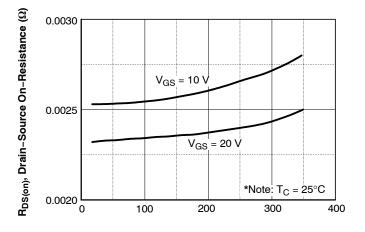
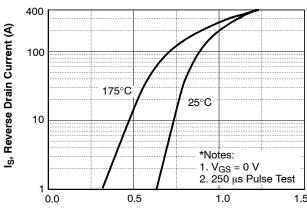
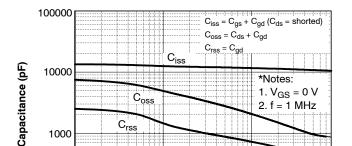


Figure 3. On–Resistance Variation vs. Drain Current and Gate Voltage

I_D, Drain Current (A)



V_{SD}, Source-Drain Voltage (V)



100

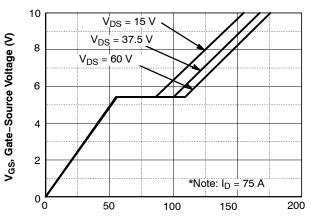
0.1

V_{DS}, Drain-Source Voltage (V)

10

Figure 5. Capacitance Characteristics





Q_G, Total Gate Charge (nC)

Figure 6. Gate Charge Characteristics

80

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

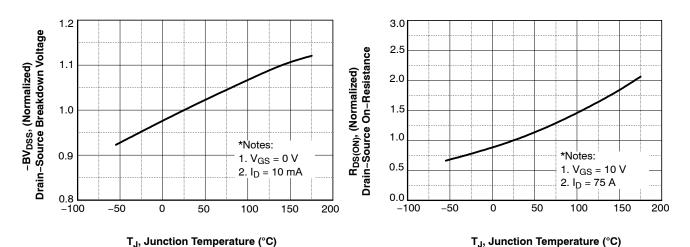


Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature

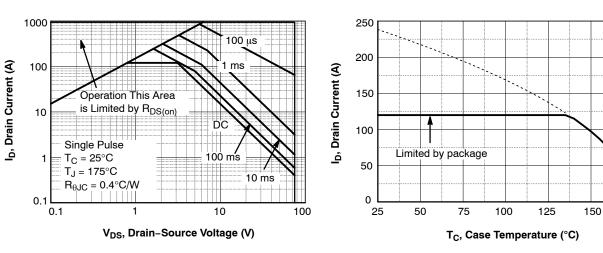
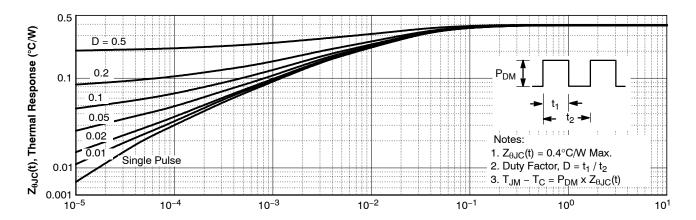


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

175



t₁, Rectangular Pulse Duration (s)

Figure 11. Transient Thermal Response Curve

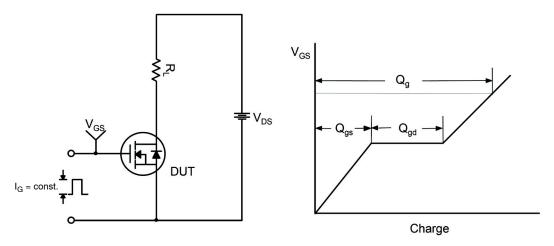


Figure 12. Gate Charge Test Circuit & Waveform

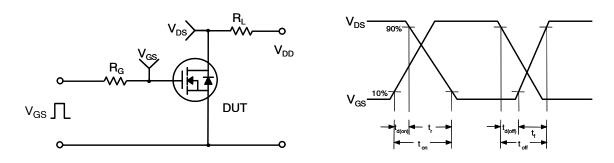


Figure 13. Resistive Switching Test Circuit & Waveforms

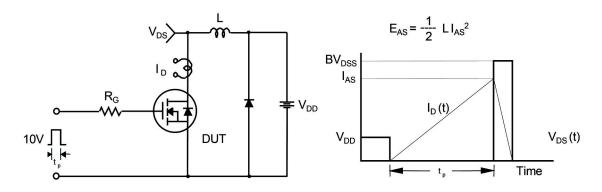
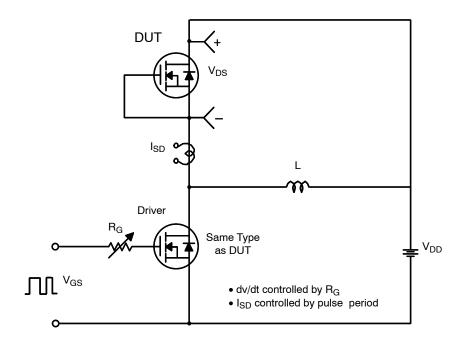


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



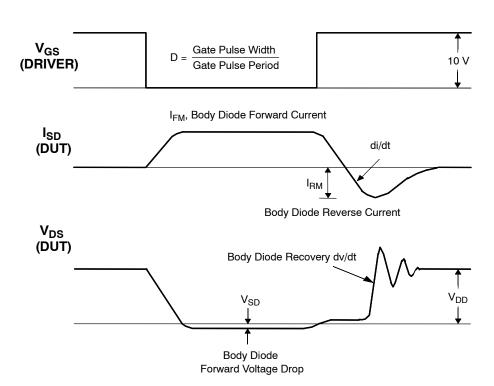


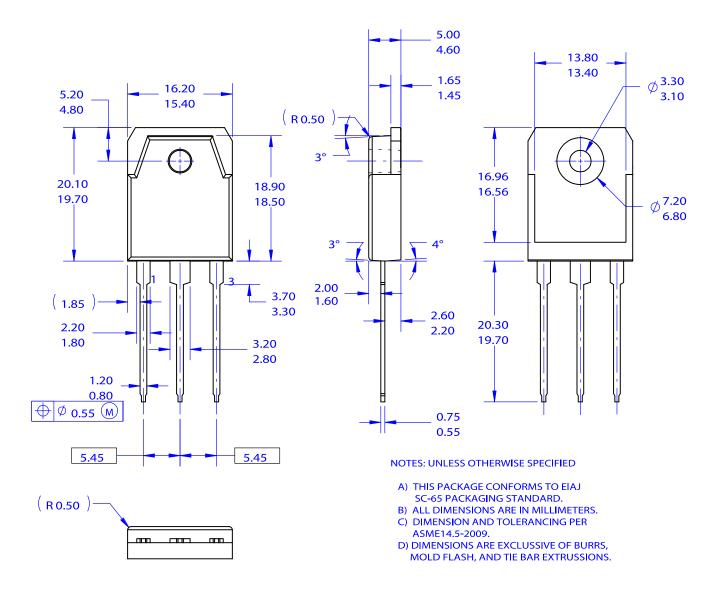
Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

POWERTRENCH is a registered trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.



TO-3P-3LD / EIAJ SC-65, ISOLATED CASE 340BZ ISSUE O

DATE 31 OCT 2016



DOCUMENT NUMBER:	98AON13862G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-3P-3LD / EIAJ SC-65, ISOLATED		PAGE 1 OF 1	

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales