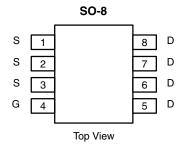


N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$ Max.	I _D (A) ^a	Q _g (Typ.)			
40	0.0024 at V _{GS} = 10 V	42.5	28 nC			
	0.0032 at V _{GS} = 4.5 V	36.8				



Ordering Information:

Si4038DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

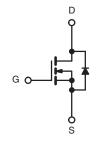
FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



APPLICATIONS

- Synchronous Rectification
- DC/DC Converters
- DC/AC Inverters
- Industrial



N-Channel MOSFET

ABSOLUTE MAXIMUM RATIN	IGS (T _A = 25 °C	, unless othe	erwise noted)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	40	V	
Gate-Source Voltage		V_{GS}	± 20	v
	T _C = 25 °C		42.5	
Continuous Drain Current (T = 150 °C)	T _C = 70 °C	1 , [34	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	- I _D	28.4 ^{b, c}	
	T _A = 70 °C		22.4 ^{b, c}	^
Pulsed Drain Current (t = 100 μs)		I _{DM}	150	Α
0 " 0 0 0 0	T _C = 25 °C		7	
Continuous Source-Drain Diode Current	T _A = 25 °C	- I _S	3.1 ^{b, c}	
Single Pulse Avalanche Current	. 0.1 11	I _{AS}	40	
Avalanche Energy	L = 0.1 mH	E _{AS}	80	mJ
	T _C = 25 °C		7.8	
Maximum Davier Dissination	T _C = 70 °C] , [5	W
Maximum Power Dissipation	T _A = 25 °C	P _D	3.5 ^{b, c}	VV
	T _A = 70 °C		2.2 ^{b, c}	
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	29	35	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16	C/VV		

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 80 °C/W.



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L = 250 uA		24		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- I _D = 250 μA		- 5.2			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1		2.1	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V			1	μА	
	500	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	r.	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.0020	0.0024	Ω	
Drain-Source On-State Hesistance	1 108(011)	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.0026	0.0032		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_D = 15 \text{ A}$		97		S	
Dynamic ^b							
Input Capacitance	C _{iss}			4070		pF	
Output Capacitance	C _{oss}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		2420			
Reverse Transfer Capacitance	C _{rss}			210			
Total Gate Charge	Q_g	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		58	87		
Gate-Source Charge	Q _{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		28 9.1	42	nC	
Gate-Drain Charge	Q _{gd}	T DS = 20 1, 1GS = 1.0 1, 1D = 10 11		6.8			
Output Charge	Q _{oss}	V _{DS} = 20 V, V _{GS} = 0 V		66	100		
Gate Resistance	R _g	f = 1 MHz	0.4	1.15	1.8	Ω	
Turn-On Delay Time	t _{d(on)}	2	0.1	13	26		
Rise Time	t _r	$V_{DD} = 20 \text{ V}, R_{L} = 2 \Omega$		14	28		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A, } V_{GEN} = 10 \text{ V, } R_q = 1 \Omega$		14	28	ns	
Fall Time	t _f			10	20		
Turn-On Delay Time	t _{d(on)}			40	80		
Rise Time	t _r	$V_{DD} = 20 \text{ V, R}_{L} = 2 \Omega$		75	150		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		35	70		
Fall Time	t _f	J		13	26		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7		
Pulse Diode Forward Current (t = 100 μs)	I _{SM}				150	Α	
Body Diode Voltage	V _{SD}	I _S = 5 A		0.72	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			55	100	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 10 A 11/4L 100 A / - T 05 00		55	100	nC	
Reverse Recovery Fall Time t _a		$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		22			
Reverse Recovery Rise Time	t _b			33		ns	

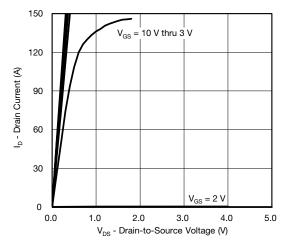
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

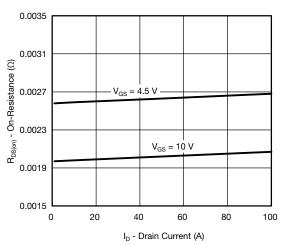
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



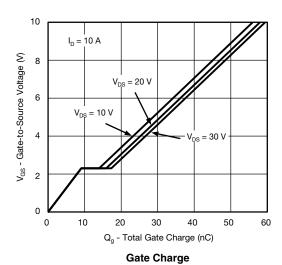
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

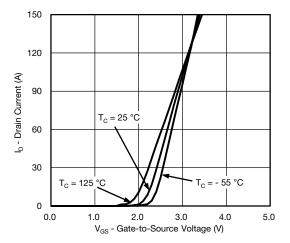


Output Characteristics

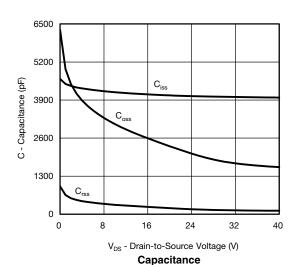


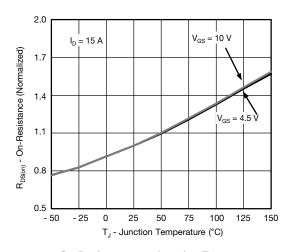
On-Resistance vs. Drain Current





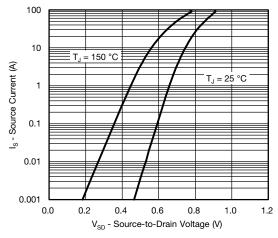
Transfer Characteristics



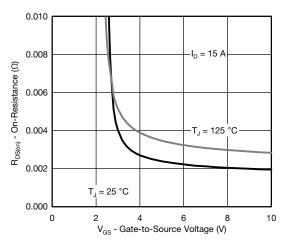


On-Resistance vs. Junction Temperature

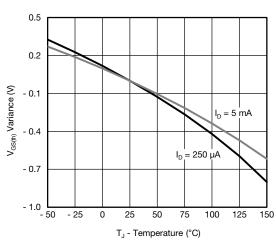
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



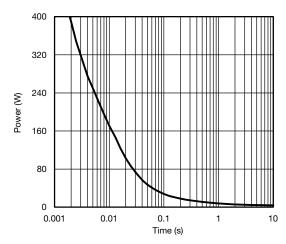
Source-Drain Diode Forward Voltage



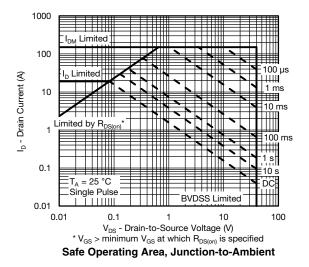
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

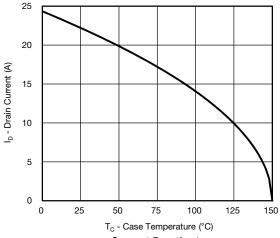


Single Pulse Power, Junction-to-Ambient

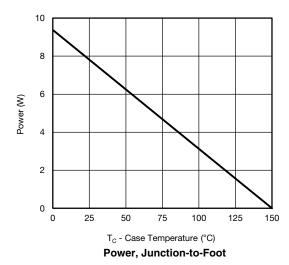


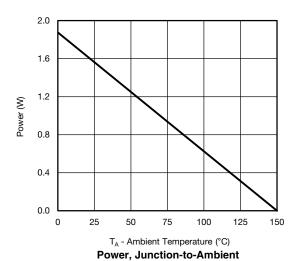


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*

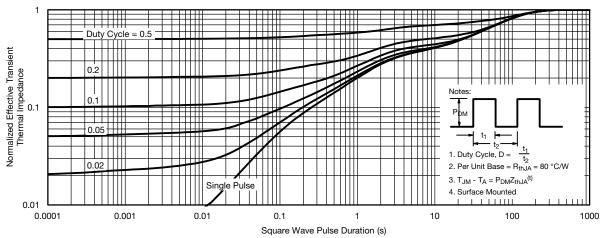




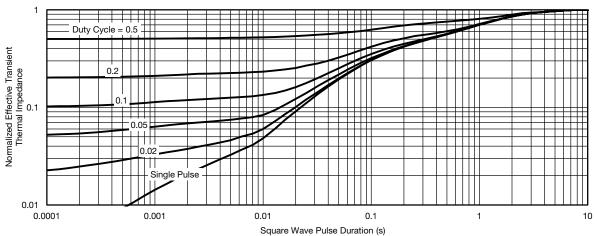
^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06

LON NOTE



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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