

## • General Description

The AGM14N10AP combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

#### Features

- Advance high cell density Trench technology
- Low R<sub>DS(ON)</sub> to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

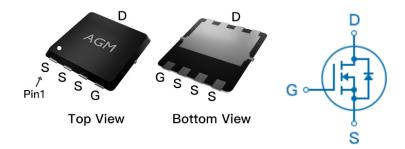
#### Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

# **Product Summary**

BVDSS	RDSON	ID
100V	12mΩ	40A

## PDFN3.3\*3.3 Pin Configuration



# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM14N10AP	AGM14N10AP	PDFN3.3*3.3	330mm	12mm	5000

## Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	40	А
_	Drain Current-Continuous(Tc=100℃)	28	Α
IDM (pluse)	Drain Current-Pulsed (Note 2)	160	Α
PD	Maximum Power Dissipation(Tc=25℃)	68	w
	Maximum Power Dissipation(Tc=100℃)	27	w
EAS	Avalanche energy (Note 3)	100	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>		55	°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup>		1.85	°C/W



Table 3. Electrical Characteristics (TJ=25℃unless otherwise noted)

Table 3. Electrical Characteristics (TJ=25 ℃ unless otherwise noted)						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off Sta	ates					
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	100			V
IDSS	Zero Gate Voltage Drain Current	VDS=100V,VGS=0V			1	μΑ
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250µA	1.2	1.6	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=15A		18		S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A		12	17	mΩ
	Brain Godioo on Gtate Neciotario	VGS=4.5V, ID=15A		17	21	mΩ
Dynamic	Characteristics					
Ciss	Input Capacitance			1090		pF
Coss	Output Capacitance	VDS=50V,VGS=0V, F=1MHZ		470		pF
Crss	Reverse Transfer Capacitance			60		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz		1.3		Ω
Switching	Times					
td(on)	Turn-on Delay Time			45		nS
tr	Turn-on Rise Time	VGS=10V,VDS=50V,ID=		54.5		nS
td(off)	Turn-Off Delay Time	10A,RGEN=6Ω		249		nS
tf	Turn-Off Fall Time			60		nS
Qg	Total Gate Charge			30.5		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=50V, ID=8.5A		6.1		nC
Qgd	Gate-Drain Charge	1D-0.3A		8.3		nC
Source-D	rain Diode Characteristics					
ISD	Source-Drain Current(Body Diode)				40	Α
VSD	Forward on Voltage	VGS=0V,IS=20A		0.7	1.2	V
trr	Reverse Recovery Time	Isd=20A ,		43		ns
Qrr	Reverse Recovery Charge	dl/dt=100A/µs , TJ=25℃		90		nc

Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25  $^{\circ}\text{C},\text{VDD}=50\text{V},\text{Vgs}=10\text{V}$  , ID=20A, L=0.5mH,RG=25ohm



# **Typical Performance Characteristics**

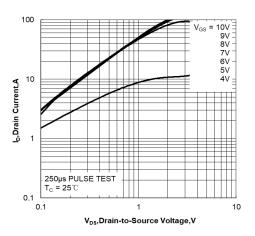


Figure 1. Output Characteristics

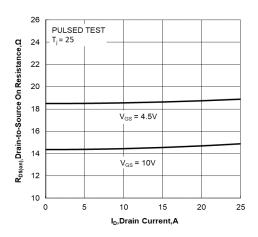


Figure 3. Drain-to-Source On Resistance vs Drain Current

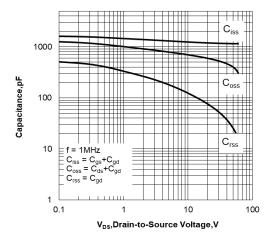


Figure 5. Capacitance Characteristics

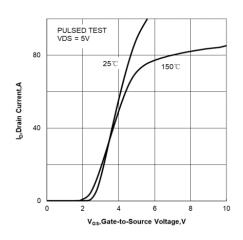


Figure 2. Transfer Characteristics

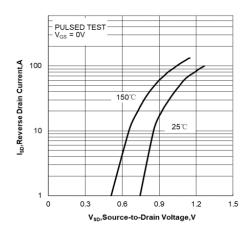


Figure 4. Body Diode Forward Voltage vs Source Current and Temperature

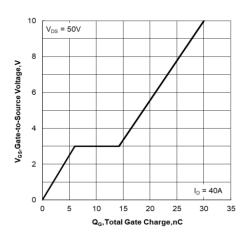


Figure 6. Gate Charge Characteristics



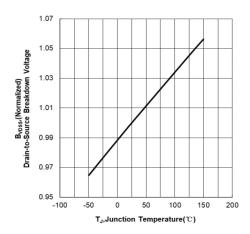


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

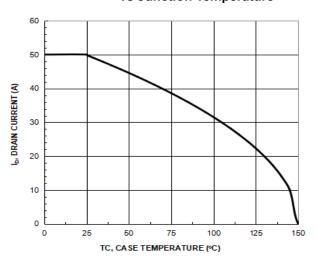


Figure 9. Maximum Continuous Drain Current vs Case Temperature

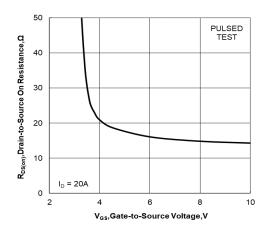


Figure11. Drain-to-Source On Resistance vs Gate

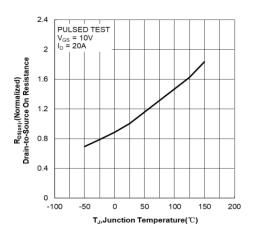


Figure 8. Normalized On Resistance vs

Junction Temperature

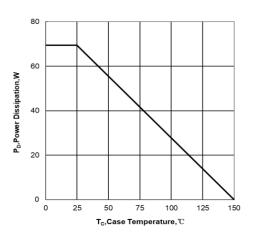


Figure 10. Maximum Power Dissipation vs Case Temperature

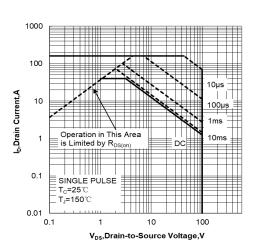


Figure 12. Maximum Safe Operating Area

MAX

MILLIMETER

0.700 0.800 0.900

0.152REF. 0~0.05

3.000 3.100 3.200 2. 300 | 2. 450 | 2. 600

2. 900 | 3. 000 | 3. 100 3. 150 | 3. 300 | 3. 450

1. 320 | 1. 520 | 1. 720

0. 200 | 0. 300 | 0. 400

0.550 0.650 0.750

0.300 0.400 0.500

0.180 0.330 0.480

0~0.100

0~0.100 0. 315 | 0. 415 | 0. 515

10°

12°

8°

MIN Typ.

SYMBOL

A A1

A2 D

D1 Е

E1

E2

b

L

L1

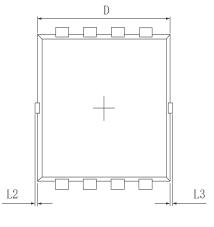
L2

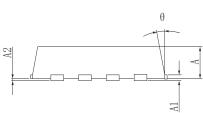
L3

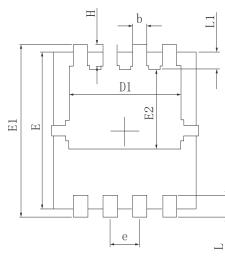
Н θ



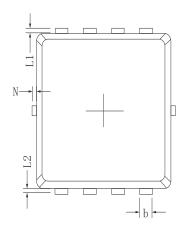
# •Dimensions (PDFN3.3\*3.3)

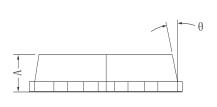


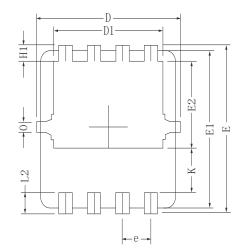


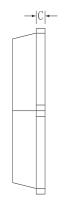


1		D1 Z2	
E1	H		
ļ		e	





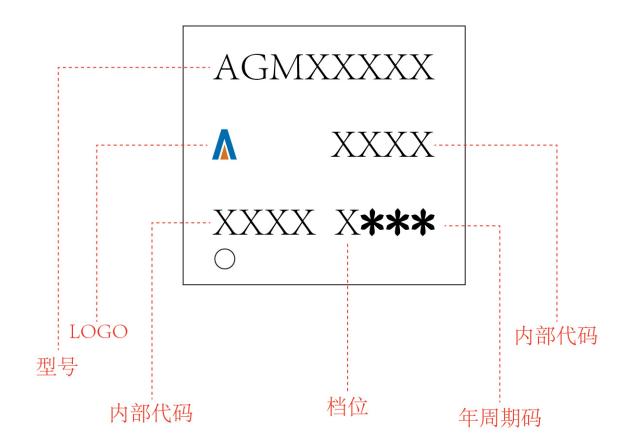




C 1 1	Millimeters			
Symbols	MIN.	NOM.	MAX.	
A	0.65	0.75	0.85	
b	0.25	0.30	0.35	
С	0.15	0.20	0. 25	
D	3.00	3.10	3. 20	
D1	2.40	2.50	2.60	
Е	3.20	3.30	3.40	
E1	3.00	3.10	3. 20	
E2	1.60	1.70	1.80	
е	0.	65 BSC	· /•	
H1	0.21	0.31	0.41	
Н2	0.30	0.40	0.50	
K	0.78	0.88	0.98	
L1/L2	0.10 REF.			
θ	11°	12°	13°	
N	0	_	0.15	
0	0.2 REF.			



PDFN3.3\*3.3 Marking Instructions:





#### Disclaimer:

The information provided in this document is believed to be accurate and reliable. However, Shenzhen Core Control Source Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Source Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the sixth version issued on April 20th, 2024. This document replaces all previously provided information.

It is a registered trademark of Shenzhen Core Control Source Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Source Electronics Technology Co., Ltd. all rights reserved.