

#### N-Channel Enhancement Mode MOSFET

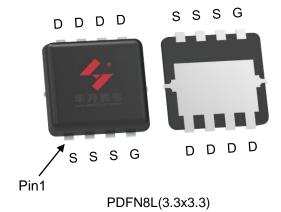
#### **Feature**

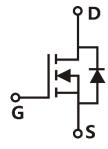
- 40V/125A R<sub>DS(ON)</sub>= 2.0 mΩ(typ.) @VGS = 10V R<sub>DS(ON)</sub>= 3.2 mΩ(typ.) @VGS = 4.5V
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

## **Applications**

- Battery Protection
- DC-DC

### **Pin Description**





Single N-Channel MOSFET

# **Ordering and Marking Information**



Package Code

C1: PDFN8L(3.3x3.3)

Date Code XYMXXXXXX

Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
Common Ra	ntings (Tc=25°C Unless Otherwise Noted)		-	1
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range			°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)	Tc=25°C	125	Α
Mounted on	Large Heat Sink	1	-	
Ірм	Pulsed Drain Current *	Tc=25°C	396	А
	Continuous Danie Comment	Tc=25°C	125	А
lo	Continuous Drain Current	Tc=100°C	88	А
		Tc=25°C	76	W
PD	P <sub>D</sub> Maximum Power Dissipation		38	W
R₀c	Thermal Resistance, Junction-to-Case		1.98	°C/W
R <sub>eJA</sub>	Thermal Resistance, Junction-to-Ambient	Thermal Resistance, Junction-to-Ambient **		°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.1mH	154	mJ

Note:

- \* Repetitive rating; pulse width limited by max.junction temperature.
- \*\* Surface mounted on 1in2 FR-4 board.
- \*\*\* Limited by TJmax , starting TJ=25°C, L = 0.1mH, Rg=  $25\Omega$ , Vgs =10V.

# **Electrical Characteristics**(Tc =25°C Unless Otherwise Noted)

Compleal	Develope	Took Conditions	HY	HYG022N04LS1		11
Symbol	pol Parameter Test Conditions		Min	Тур.	Max	Unit
Static Char	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>DS</sub> =250μA	40	-	-	V
Ibss Drain-to-Source Leakage Current		VDS=40V,VGS=0V	-	-	1	μΑ
		TJ=125°C	-	-	50	μΑ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	1	2	3	V
lgss	Gate-Source Leakage Current	Vgs=±20V,Vps=0V	-	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V,I <sub>DS</sub> =20A	-	2.0	2.7	mΩ
RDS(ON)	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V,I <sub>DS</sub> =20A	-	3.2	4.5	mΩ
Diode Cha	racteristics		•			
VsD	Diode Forward Voltage	IsD=20A,Vgs=0V	-	0.80	1.20	V
trr	Reverse Recovery Time	log 20 A dlog /dt 400 A /u.o	-	23	-	ns
Qrr	Reverse Recovery Charge	- Isb=20A,dIsb/dt=100A/μs	-	14	-	nC



# Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumala al	Barrantan	To at Complitions	HY	HYG022N04LS1		
Symbol Parameter		Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	1.0	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	2034	-	
Coss	Output Capacitance	V <sub>DS</sub> =25V,	-	405	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1MHz	-	22	-	
td(ON)	Turn-on Delay Time		-	10	-	
Tr	Turn-on Rise Time	$V_{DD}=20V,R_{G}=4\Omega,$	-	40	-	]
td(OFF)	Turn-off Delay Time	Ips=20A,Vgs=10V	-	26	-	ns
Tf	Turn-off Fall Time		-	6	-	
Gate Cha	rge Characteristics					
Qg	Total Gate Charge(V <sub>GS</sub> =10V)		-	31	-	
Qg	Total Gate Charge(V <sub>GS</sub> =4.5V)			15		·- C
Qgs	Gate-Source Charge	V <sub>DS</sub> =32V, I <sub>DS</sub> =20A	-	8	-	nC
Qgd	Gate-Drain Charge		-	5	-	
V <sub>plateau</sub>	Gate plateau voltage		-	3.7	-	V

Note: \*Pulse test, pulse width  $\leq 300$ us, duty cycle  $\leq 2\%$ 



## **Typical Operating Characteristics**

**Figure 1: Power Dissipation** 

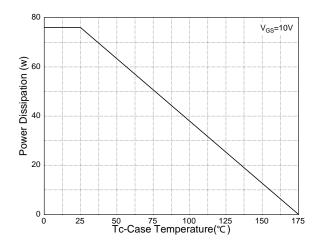


Figure 3: Safe Operation Area

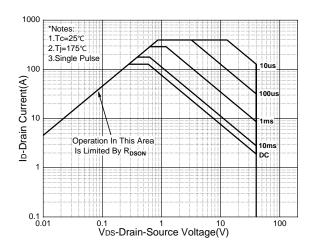
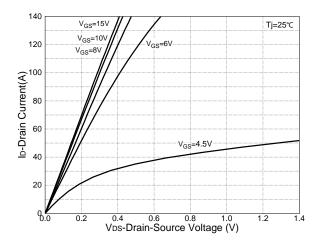


Figure 5: Output Characteristics



**Figure 2: Drain Current** 

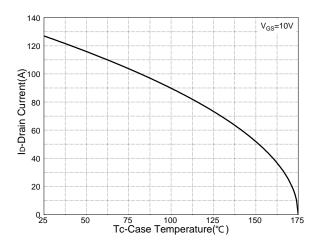


Figure 4: Thermal Transient Impedance

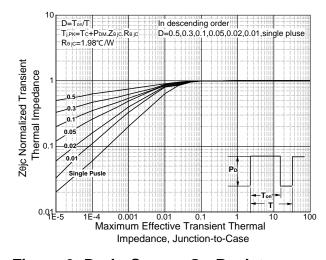
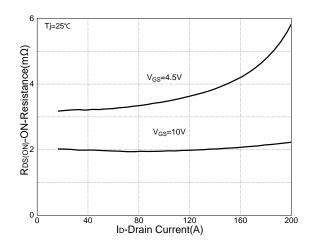


Figure 6: Drain-Source On Resistance





## **Typical Operating Characteristics(Cont.)**

Figure 7: On-Resistance vs. Temperature

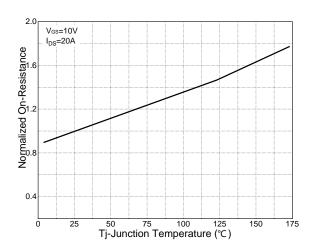


Figure 8: Source-Drain Diode Forward

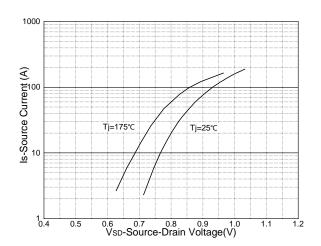
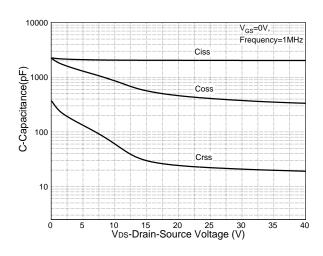
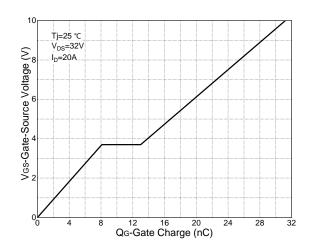


Figure 9: Capacitance Characteristics

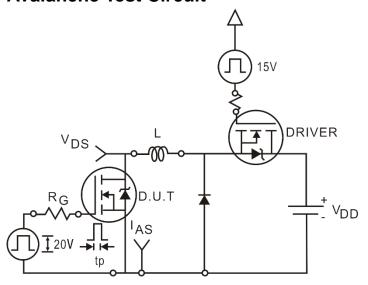


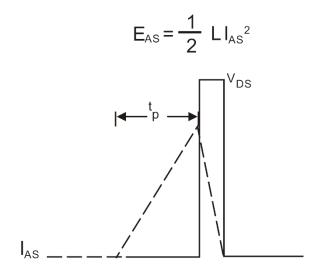
**Figure 10: Gate Charge Characteristics** 



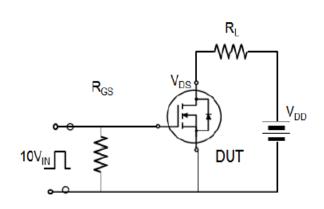


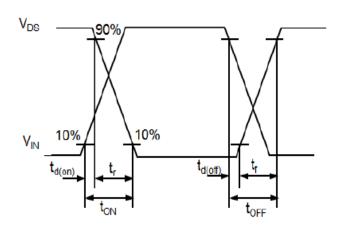
## **Avalanche Test Circuit**



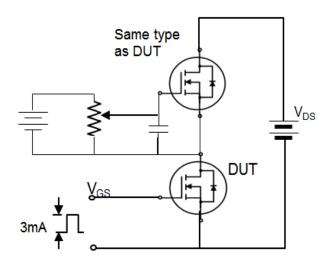


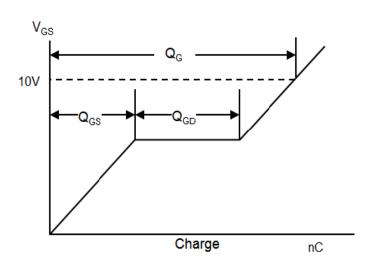
# **Switching Time Test Circuit**





# **Gate Charge Test Circuit**





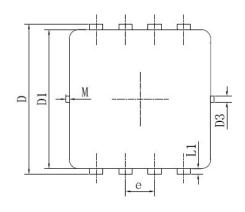


# **Device Per Unit**

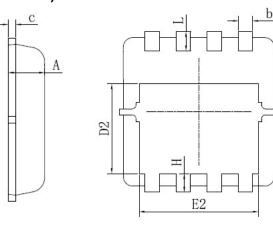
Package Type	Unit	Quantity
PDFN8L(3.3x3.3)	Reel	6500

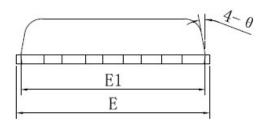
# **Package Information**

## PDFN8L(3.3x3.3)





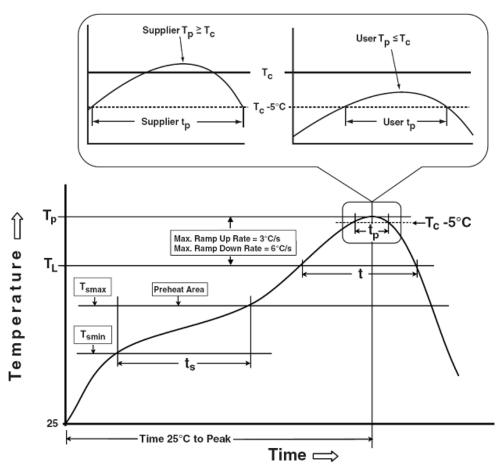




COMMON DIMENSIONS				
SYMBOL	mm			
STIVIBOL	MIN	NOM	MAX	
А	0.715	0.75	0.785	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.78	1.88	1.98	
D3	\	0.20	\	
E	3.20	3.30	3.40	
E1	3.10	3.20	3.30	
E2	2.44	2.54	2.64	
е	e 0.65BSC			
Н	0.34	0.39	0.44	
L	0.35	0.40	0.45	
L1	\	0.13	\	
θ	\	10°	10°	
М	\	\	0.10	
*Not specified				



#### **Classification Profile**



### **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly				
	Preheat & Soak					
Temperature min (T <sub>smin</sub> )	100 °C	150 °C				
Temperature max (T <sub>smax</sub> )	150 °C	200 °C				
Time (Tsmin to Tsmax) (t <sub>s</sub> )	60-120 seconds	60-120 seconds				
Average ramp-up rate	2 °C/cocond mov	3°C/second max.				
(T <sub>smax</sub> to T <sub>P</sub> )	3 °C/second max.					
Liquidous temperature (T <sub>L</sub> )	183 °C	217 °C				
Time at liquidous (t <sub>L</sub> )	60-150 seconds	60-150 seconds				
Peak package body Temperature	See Classification Temp in table 1	SeeClassification Tempin table 2				
(T <sub>P</sub> )*	'	, , , , , , , , , , , , , , , , , , , ,				
Time (t <sub>P</sub> )** within 5°C of the specified	20** seconds	30** seconds				
classification temperature (T <sub>c</sub> )	20 3600103					
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.				
Time 25°C to peak temperature	6 minutes max.	8 minutes max.				

<sup>\*</sup>Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup> Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

# **HYG022N04LS1C1**



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

### Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## **Reliability Test Program**

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500 Cycles, -55°C~150°C

### **Customer Service**

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