

# **Opti**MOS<sup>™</sup>3 Power-Transistor

#### **Features**

- N-channel, normal level
- Excellent gate charge x R<sub>DS(on)</sub> product (FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1)</sup> for target application
- Halogen-free according to IEC61249-2-21
- Ideal for high-frequency switching and synchronous rectification

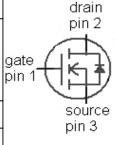
#### **Product Summary**

V <sub>DS</sub>	200	V
R <sub>DS(on),max</sub>	32	mΩ
I <sub>D</sub>	34	Α





Туре	IPB320N20N3 G	IPP320N20N3 G	IPI320N20N3 G
	1 3 2 (tab)	123	123
Package	PG-TO263-3	PG-TO220-3	PG-TO262-3
Marking	320N20N	320N20N	320N20N



#### **Maximum ratings,** at $T_i$ =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	ID	T <sub>C</sub> =25 °C	34	А
		T <sub>C</sub> =100 °C	22	
Pulsed drain current <sup>2)</sup>	I <sub>D,pulse</sub>	T <sub>C</sub> =25 °C	136	
Avalanche energy, single pulse	E <sub>AS</sub>	$I_{\rm D}$ =34 A, $R_{\rm GS}$ =25 $\Omega$	190	mJ
Reverse diode dv/dt	dv/dt		10	kV/μs
Gate source voltage	$V_{GS}$		±20	V
Power dissipation	$P_{\text{tot}}$	T <sub>C</sub> =25 °C	136	W
Operating and storage temperature	$T_{\rm j}$ , $T_{\rm stg}$		-55 175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

<sup>1)</sup>J-STD20 and JESD22

<sup>&</sup>lt;sup>2)</sup> See figure 3



# IPB320N20N3 G IPP320N20N3 G

## **IPI320N20N3 G**

Parameter	Symbol	Conditions	Values		Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	$R_{\mathrm{thJC}}$		-	-	1.1	K/W
Thermal resistance, junction -	$R_{\mathrm{thJA}}$	minimal footprint	-	-	62	
ambient		6 cm2 cooling area <sup>3)</sup>	-	-	40	

# **Electrical characteristics,** at $T_j$ =25 °C, unless otherwise specified

#### Static characteristics

Drain-source breakdown voltage $V_{(BR)DSS}$		V <sub>GS</sub> =0 V, I <sub>D</sub> =1 mA	200	-	-	V
Gate threshold voltage	$V_{\rm GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =90 μA	2	3	4	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{\rm DS}$ =160 V, $V_{\rm GS}$ =0 V, $T_{\rm j}$ =25 °C	1	0.1	1	μΑ
		V <sub>DS</sub> =160 V, V <sub>GS</sub> =0 V, T <sub>j</sub> =125 °C	-	10	100	
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =20 V, V <sub>DS</sub> =0 V	-	1	100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =34 A	1	28	32	mΩ
Gate resistance	R <sub>G</sub>		1	2.5	1	Ω
Transconductance	$g_{ ext{fs}}$	$ V_{\rm DS}  > 2 I_{\rm D} R_{\rm DS(on)max},$ $I_{\rm D} = 34~{\rm A}$	27	54	1	S

 $<sup>^{3)}</sup>$  Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm $^{2}$  (one layer, 70  $\mu$ m thick) copper area for drain connection. PCB is vertical in still air.



# IPB320N20N3 G IPP320N20N3 G

## **IPI320N20N3 G**

Parameter	Symbol	Conditions		Values		Unit
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	Ciss		-	1770	2350	pF
Output capacitance	Coss	V <sub>GS</sub> =0 V, V <sub>DS</sub> =100 V, f=1 MHz	-	135	180	
Reverse transfer capacitance	C <sub>rss</sub>		-	4	-	1
Turn-on delay time	t <sub>d(on)</sub>		-	11	-	ns
Rise time	t <sub>r</sub>	V <sub>DD</sub> =100 V, V <sub>GS</sub> =10 V, I <sub>D</sub> =17 A,	-	9	-	
Turn-off delay time	$t_{\text{d(off)}}$	$R_{\rm G}$ =1.6 $\Omega$	-	21	-	
Fall time	$t_{f}$	]	-	4	-	
Gate Charge Characteristics <sup>4)</sup>		T		ı	I	
Gate to source charge	Q <sub>gs</sub>		-	8	-	nC
Gate to drain charge	$Q_{gd}$	],, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-	3	-	
Switching charge	$Q_{sw}$	V <sub>DD</sub> =100 V, I <sub>D</sub> =17 A, V <sub>GS</sub> =0 to 10 V	-	5	-	
Gate charge total	Qg		-	22	29	
Gate plateau voltage	$V_{ m plateau}$		-	4.4	-	٧
Output charge	Q <sub>oss</sub>	V <sub>DD</sub> =100 V, V <sub>GS</sub> =0 V	-	54	72	nC
Reverse Diode						
Diode continous forward current	Is	T <sub>C</sub> =25 °C	-	-	34	Α
Diode pulse current	I <sub>S,pulse</sub>	7 <sub>C</sub> -25 C	-	-	136	
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0 V, I <sub>F</sub> =34 A, T <sub>j</sub> =25 °C	-	0.9	1.2	V
Reverse recovery time	t <sub>rr</sub>	V <sub>R</sub> =100 V, I <sub>F</sub> =17 A,	-	110	-	ns
Reverse recovery charge	Q <sub>rr</sub>	di <sub>F</sub> /dt=100 A/µs	-	500	-	nC

<sup>&</sup>lt;sup>4)</sup> See figure 16 for gate charge parameter definition

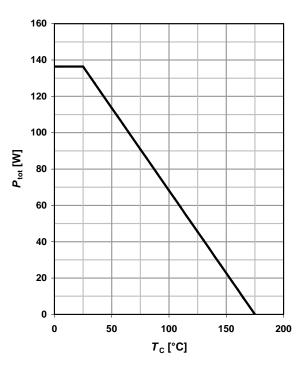


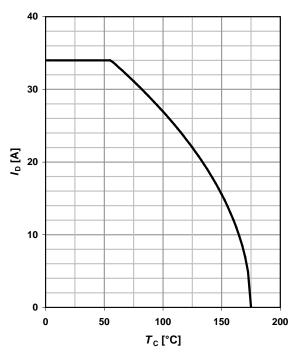
## 1 Power dissipation

## $P_{\text{tot}}$ =f( $T_{\text{C}}$ )

# 2 Drain current

$$I_D = f(T_C); V_{GS} \ge 10 \text{ V}$$

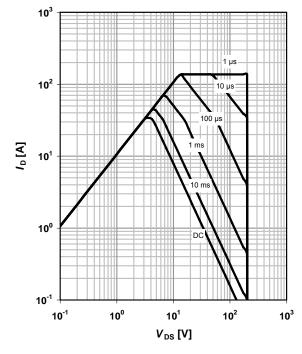




## 3 Safe operating area

$$I_{\rm D}$$
=f( $V_{\rm DS}$ );  $T_{\rm C}$ =25 °C;  $D$ =0

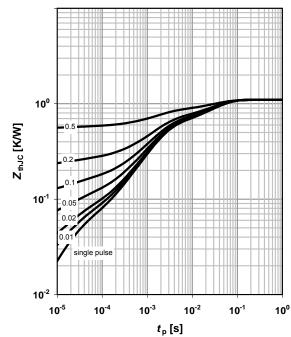
parameter:  $t_{\rm p}$ 



## 4 Max. transient thermal impedance

$$Z_{\text{thJC}}$$
=f( $t_{p}$ )

parameter:  $D=t_p/T$ 





#### 5 Typ. output characteristics

 $I_D = f(V_{DS}); T_j = 25 °C$ 

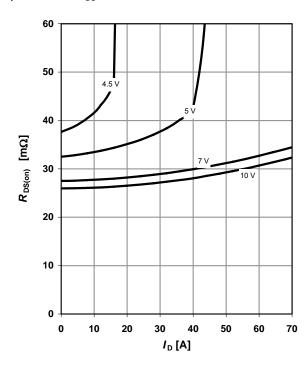
parameter:  $V_{\rm GS}$ 

# 

#### 6 Typ. drain-source on resistance

 $R_{DS(on)}$ =f( $I_D$ );  $T_j$ =25 °C

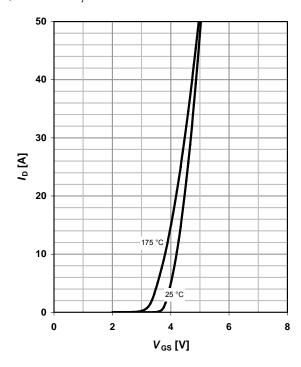
parameter: V<sub>GS</sub>



## 7 Typ. transfer characteristics

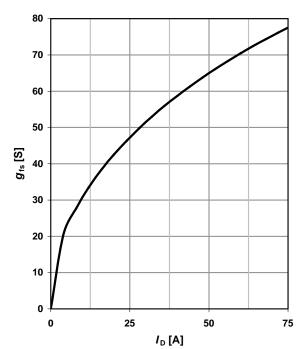
 $I_{D}$ =f( $V_{GS}$ );  $|V_{DS}|$ >2 $|I_{D}|R_{DS(on)max}$ 

parameter:  $T_{\rm j}$ 



## 8 Typ. forward transconductance

 $g_{fs}$ =f( $I_D$ );  $T_j$ =25 °C





#### 9 Drain-source on-state resistance

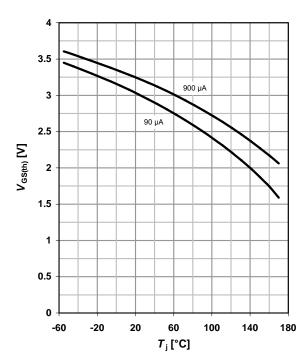
$$R_{DS(on)}$$
=f( $T_j$ );  $I_D$ =34 A;  $V_{GS}$ =10 V

# 

#### 10 Typ. gate threshold voltage

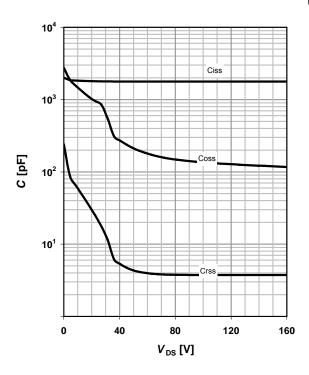
$$V_{GS(th)}$$
=f( $T_j$ );  $V_{GS}$ = $V_{DS}$ 

parameter:  $I_D$ 



## 11 Typ. capacitances

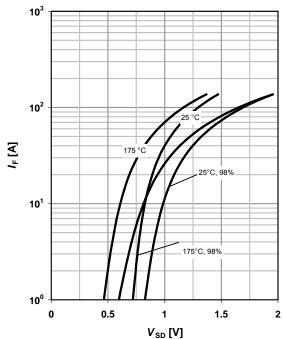
$$C=f(V_{DS}); V_{GS}=0 V; f=1 MHz$$



## 12 Forward characteristics of reverse diode

$$I_{\mathsf{F}} = \mathsf{f}(V_{\mathsf{SD}})$$

parameter:  $T_{\rm j}$ 





#### 13 Avalanche characteristics

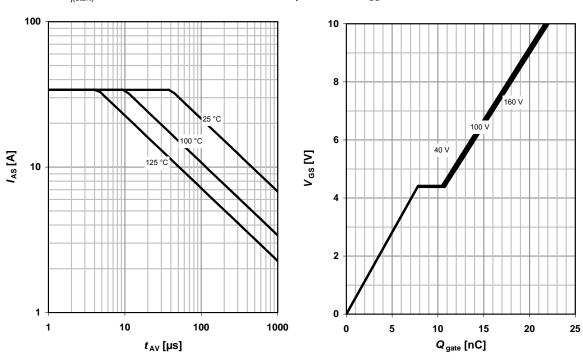
 $I_{\mathsf{AS}}$ =f( $t_{\mathsf{AV}}$ );  $R_{\mathsf{GS}}$ =25  $\Omega$ 

parameter:  $T_{j(start)}$ 

# 14 Typ. gate charge

 $V_{\rm GS}$ =f(Q<sub>gate</sub>);  $I_{\rm D}$ =17 A pulsed

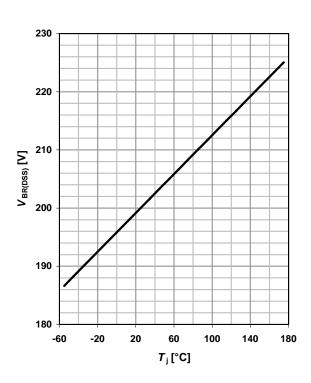
parameter:  $V_{\rm DD}$ 

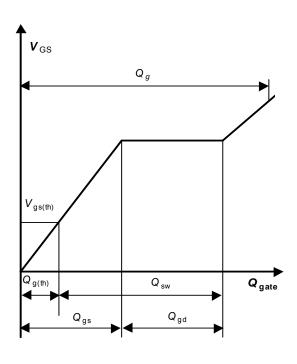


## 15 Drain-source breakdown voltage

 $V_{BR(DSS)}$ =f( $T_i$ );  $I_D$ =1 mA

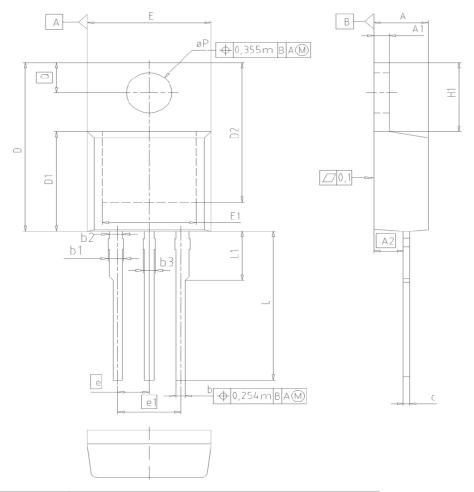
## 16 Gate charge waveforms



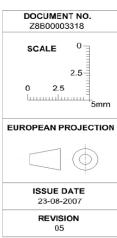




## PG-TO220-3: Outline

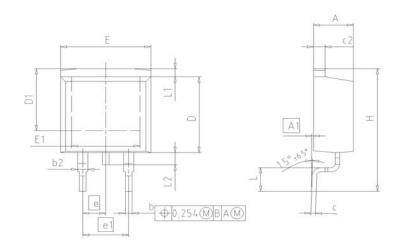


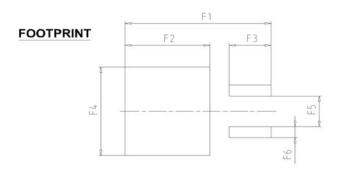
DIM	MILLI	METERS	INCHES		
DIN	MIN	MAX	MIN	MAX	
Α	4.30	4.57	0.169	0.180	
A1	1.17	1.40	0.046	0.055	
A2	2.15	2.72	0.085	0.107	
b	0.65	0.86	0.026	0.034	
b1	0.95	1.40	0.037	0.055	
b2	0.95	1.15	0.037	0.045	
b3	0.65	1.15	0.026	0.045	
С	0.33	0.60	0.013	0.024	
D	14.81	15.95	0.583	0.628	
D1	8.51	9.45	0.335	0.372	
D2	12.19	13.10	0.480	0.516	
E	9.70	10.36	0.382	0.408	
E1	6.50	8.60	0.256	0.339	
е	2.54		0.1	00	
e1	5	.08	0.2	200	
N		3 3		3	
H1	5.90	6.90	0.232	0.272	
L	13.00	14.00	0.512	0.551	
L1	-	4.80	-	0.189	
øΡ	3.60	3.89	0.142	0.153	
Q	2.60	3.00	0.102	0.118	



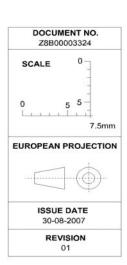


#### PG-TO263-3: Outline



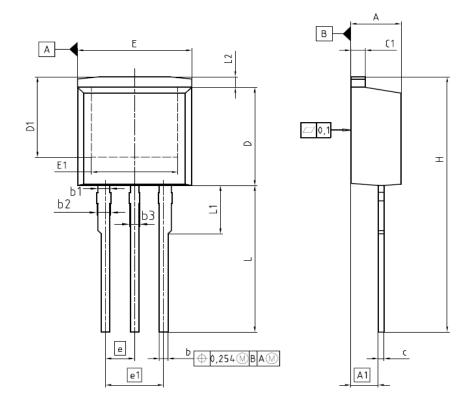


DIM	MILLIN	IETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	4.30	4.57	0.169	0.180	
A1	0.00	0.25	0.000	0.010	
b	0.65	0.85	0.026	0.033	
b2	0.95	1.15	0.037	0.045	
С	0.33	0.65	0.013	0.026	
c2	1.17	1.40	0.046	0.055	
D	8.51	9.45	0.335	0.372	
D1	7.10	7.90	0.280	0.311	
E	9.80	10.31	0.386	0.406	
E1	6.50	8.60	0.256	0.339	
e	2.5	2.54		0.100	
e1	5.0	08	0.200		
N		2		2	
н	14.61	15.88	0.575	0.625	
L	2.29	3.00	0.090	0.118	
L1	0.70	1.60	0.028	0.063	
L2	1.00	1.78	0.039	0.070	
F1	16.05	16.25	0.632	0.640	
F2	9.30	9.50	0.366	0.374	
F3	4.50	4.70	0.177	0.185	
F4	10.70	10.90	0.421	0.429	
F5	3.65	3.85	0.144	0.152	
F6	1.25	1.45	0.049	0.057	

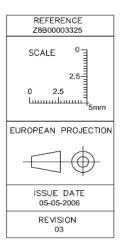




#### PG-TO262-3: Outline



	MILLIMI	ETERS	INCH	ES	
DIM	MIN	MAX	MIN	MAX	
Α	4.300	4.572	0.169	0.180	
A1	2.150	2,718	0.085	0.107	
b	0.650	0.864	0.026	0.034	
b1	0.950	1.093	0.037	0.043	
b2	0.950	1.400	0.037	0.055	
b3	0.650	1.118	0.026	0.044	
С	0,330	0,600	0,013	0,024	
c1	1.170	1.400	0.046	0.055	
D	8,509	9.450	0.335	0.372	
D1	6.900	-	0.272	-	
E	9.700	10.363	0.382	0.408	
E1	6,500	8,600	0,256	0,339	
e	2.5	2.540		00	
e1	5.0	080	0,200		
N	3		3	1	
L	13.000	14.000	0.512	0.551	
L1	-	4.800	-	0.189	
L2	-	1.727	-	0.068	





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