

Single N-Channel Enhancement Mode MOSFET

Feature

30V/100A

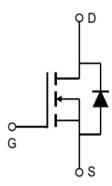
 $R_{DS(ON)} = 2.5 \text{ m}\Omega(\text{typ.}) \text{ @Vgs} = 10V$

 $R_{DS(ON)} = 3.5 \text{ m}\Omega(typ.) \text{ @Vgs} = 4.5 \text{V}$

- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free Devices Available

Pin Description





Single N-Channel MOSFET

Applications

- Battery Protection
- Motor drives

Ordering and Marking Information



Package Code

D: TO-252-2L

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Rat	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		30	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		-55 to 175	$^{\circ}$
Tstg	Storage Temperature Range		-55 to 175	$^{\circ}$
Is	Source Current-Continuous(Body Diode) Tc=25°C		100	А
Mounted on	Large Heat Sink		•	
Ідм	Pulsed Drain Current *	Tc=25℃	390	А
1	Continuous Drain Compart	Tc=25℃	100	А
lσ	Continuous Drain Current	Tc=100°C	71	А
Б	Marrian na Davian Dissination	Tc=25℃	57	W
PD	P _D Maximum Power Dissipation Tc=100°C		28	W
R₀JC	Thermal Resistance, Junction-to-Case	2.6	°CMV	
R _{eJA}	Thermal Resistance, Junction-to-Ambient **		110	°CMV
Eas	SinglePulsed-Avalanche Energy *** L=0.3mH		148	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
** Surface mounted on FR-4 board.

Limited by TJmax , starting TJ=25 $^{\circ}\text{C}$, L = 0.3mH, Rg=25 Ω , Vgs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol Parameter		Test Conditions		HYG030N03LQ1		11:0:4	
Symbol	Parameter	rest Conditions		Min	Тур.	Max	Unit
Static Cha	Static Characteristics						
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =2	250µA	30	-	-	V
Inno	IDSS Drain-to-Source Leakage Current		=0V	-	-	1	μΑ
IDSS			TJ=125℃	-	-	50	μΑ
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA		1	1.3	3	V
Igss	Gate-Source Leakage Current	Vgs=+20V/-20V,Vps=0V		-	-	±100	nA
Process	RDS(ON) Drain-Source On-State Resistance		=30A	-	2.5	3	mΩ
Rds(on)	Drain-Source On-State Resistance	V _{GS} =4.5V,I _{DS} =30A		-	3.5	4.2	mΩ
Diode Cha	Diode Characteristics						
Vsp*	Diode Forward Voltage	IsD=30A,Vgs=0V		-	0.83	1.2	V
trr	Reverse Recovery Time	Isb=30A,dIsb/dt=100A/μs		-	14.0	-	ns
Qrr	Reverse Recovery Charge			-	5.9	-	nC

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Electrical Characteristics (Cont.) (Tc =25℃ Unless Otherwise Noted)

Cumbal	Parameter	Took Conditions	HY	HYG030N03LQ1		
Symbol		Test Conditions	Min	Тур.	Max	Unit
Dynamic (Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	3.9	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	1958	-	
Coss	Output Capacitance	VDS=25V,	-	308	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	292	-	
td(ON)	Turn-on Delay Time		-	9.1	-	
Tr	Turn-on Rise Time	$V_{DD}=24V,R_{G}=4\Omega,$	-	70.2	-	
td(OFF)	Turn-off Delay Time	lps=30A,Vgs=10V	-	42.8	-	ns
Tf	Turn-off Fall Time		-	90.2	-	
Gate Cha	Gate Charge Characteristics					
Qg	Total Gate Charge (V _{GS} =10V)		-	54.8	-	
Qg	Total Gate Charge (V _{GS} =4.5V)	\/ -24\/ -20\	-	29.7	-	»C
Qgs	Gate-Source Charge	$V_{DS} = 24V, I_D = 30A$	-	6.6	-	nC
Qgd	Gate-Drain Charge		-	18.4	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

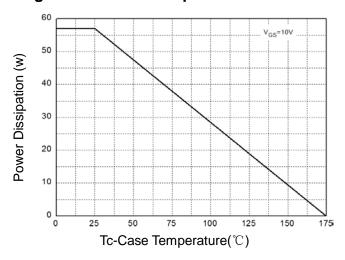


Figure 3: Safe Operation Area

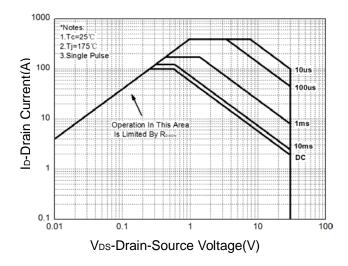


Figure 5: Output Characteristics

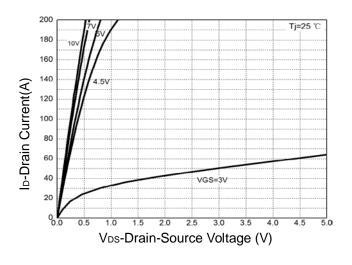


Figure 2: Drain Current

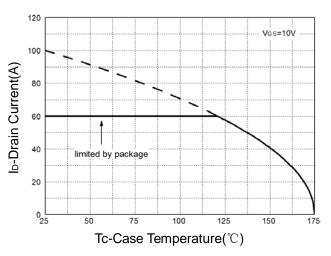
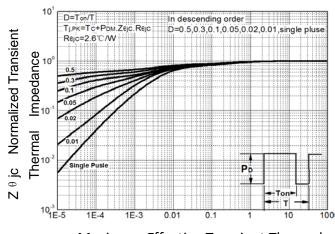
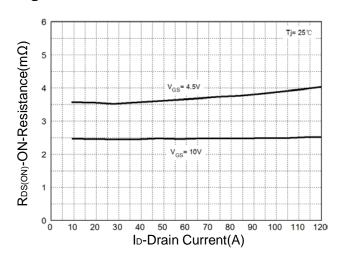


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

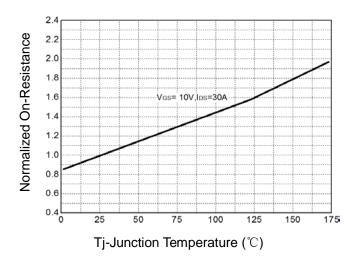


Figure 9: Capacitance Characteristics

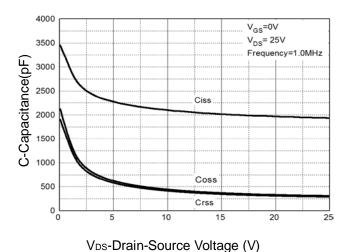


Figure 8: Source-Drain Diode Forward

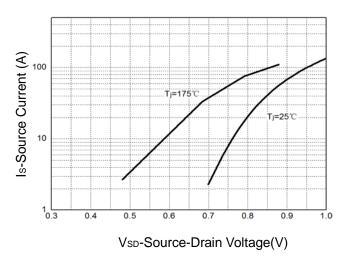
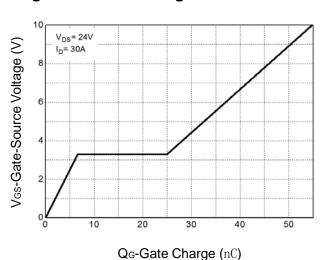


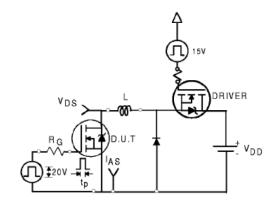
Figure 10: Gate Charge Characteristics

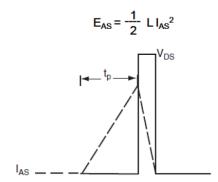


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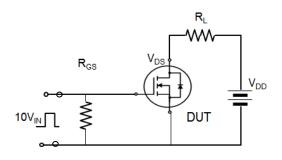


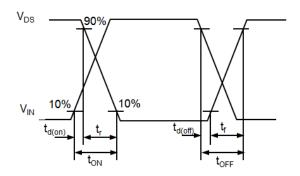
Avalanche Test Circuit



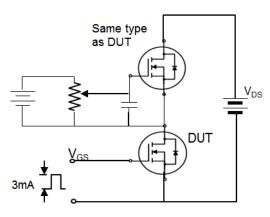


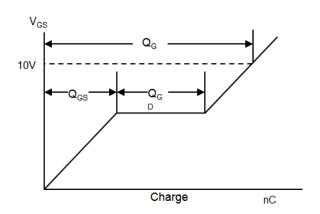
Switching Time Test Circuit





Gate Charge Test Circuit





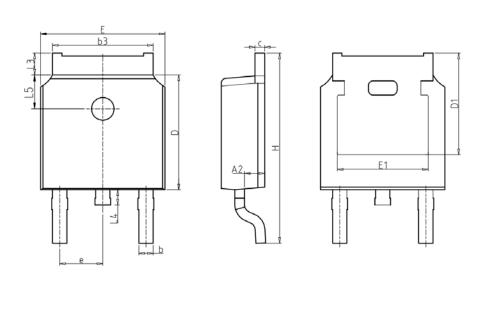


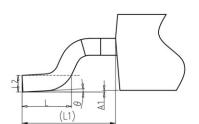
Device Per Unit

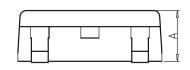
Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-252-2L	Reel	2500

PackageInformation

TO-252-2L





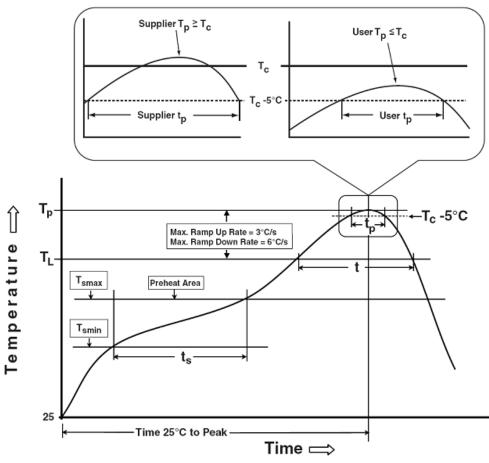


COMMONDIMENSIONS

	mm		
SYMBOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
Е	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BS	
Н	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	-	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T _{smin}) Temperature max (T _{smax}) Time (Tsmin to Tsmax) (t _s)	100 ℃ 150 ℃ 60-120 seconds	150 ℃ 200 ℃ 60-120 seconds
Average ramp-up rate (T _{smax} to T _P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T _L)	183 ℃	217 ℃
Time at liquidous (t∟)	60-150 seconds	60-150 seconds
Peak package body Temperature (T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2
Time (t _P)** within 5°C of the specified classification temperature (T _c)	20** seconds	30** seconds
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.
Time 25℃ to peak temperature	6 minutes max.	8 minutes max.

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (t₂) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 ℃	220 ℃
≥2.5 mm	220 ℃	220 ℃

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 ℃	260 ℃	260 ℃
1.6 mm – 2.5 mm	260 ℃	250 ℃	245 ℃
≥2.5 mm	250 ℃	245 ℃	245 ℃

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
PRECON	JESD-22, A113	30°C/60%/192Hrs
HTRB	JESD-22, A108	168Hrs//500Hrs/1000Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 Hrs/500Hrs/1000Hrs,Vgs100%@ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com Technical Support:Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: www.hymexa.com