

MOSFET - N-Channel, SUPERFET® III, FAST 650 V, 19.3 m Ω , 75 A NTHL019N65S3H

Description

SUPERFET III MOSFET is **onsemi**'s brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET FAST series helps minimize various power systems and improve system efficiency.

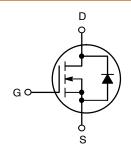
Features

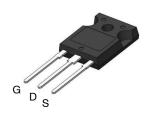
- 700 V @ $T_J = 150$ °C
- Typ. $R_{DS(on)} = 15 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 282 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 2495 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar

V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
650 V	19.3 mΩ @ 10 V	75 A	





TO-247 Long Leads CASE 340CX

MARKING DIAGRAM



T019N65S3H = Specific Device Code
A = Assembly Plant Code
YWW = Data Code (Year & Week)

ZZ = Lot

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ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		650	V
V_{GSS}	Gate to Source Voltage – DC		±30	V
		- AC (f > 1 Hz)	±30	
I _D	Drain Current	– Continuous (T _C = 25°C)	75	Α
		- Continuous (T _C = 100°C)	73	
I _{DM}	Drain Current	- Pulsed (Note 1)	328	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		1421	mJ
I _{AS}	Avalanche Current (Note 2)		12.5	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		6.25	mJ
dv/dt	MOSFET dv/dt		120	V/ns
	Peak Diode Recovery dv/dt (Note 3)		20	
P_{D}	Power Dissipation	(T _C = 25°C)	625	W
		- Derate Above 25°C	5.0	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. Repetitive rating: pulse–width limited by maximum junction temperature.
2. $I_{AS} = 12.5 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$.
3. $I_{SD} \le 37.5 \text{ A}$, $di/dt \le 200 \text{ A}/\mu\text{s}$, $V_{DD} \le 400 \text{ V}$, starting $T_J = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ hetaJC}$	Thermal Resistance, Junction to Case, Max.	0.20	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient, Max.	40	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTHL019N65S3H	T019N65S3H	TO-247	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS			•		
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA, T _J = 25°C	650	_	_	V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700	-	_	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, Referenced to 25°C	-	0.63	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	_	-	5	μΑ
		V _{DS} = 520 V, T _C = 125°C	_	7.1	_	
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V	_	-	±100	nA
ON CHARACTE	ERISTICS				-	
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 14.3 \text{ mA}$	2.4	_	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 37.5 A	_	15	19.3	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 37.5 A	-	97.4	-	S
YNAMIC CHA	RACTERISTICS		•	•		
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 250 kHz	_	15993	_	pF
C _{oss}	Output Capacitance		_	188	_	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	_	2495	_	pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	_	344	_	pF
Q _{g(tot)}	Total Gate Charge at 10V	$V_{DS} = 400 \text{ V}, I_D = 37.5 \text{ A}, V_{GS} = 10 \text{ V}$	-	282	_	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	-	73	_	nC
Q _{gd}	Gate to Drain "Miller" Charge		-	77	_	nC
ESR	Equivalent Series Resistance	f = 1 MHz	_	1.1	_	Ω
WITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, I_D = 37.5 \text{ A},$	_	54	_	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_g = 2.2 \Omega$ (Note 4)	_	22	_	ns
t _{d(off)}	Turn-Off Delay Time		-	182	_	ns
t _f	Turn-Off Fall Time		_	4.0	_	ns
OURCE-DRAI	N DIODE CHARACTERISTICS		-	-	•	
I _S	Maximum Continuous Source to Drain Diode Forward Current		_	-	75	Α
I _{SM}	Maximum Pulsed Source to Drain Diod	e Forward Current	_	-	328	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 37.5 A	-	-	1.2	٧
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 37.5 A,	-	540	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	_	13.2	_	μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

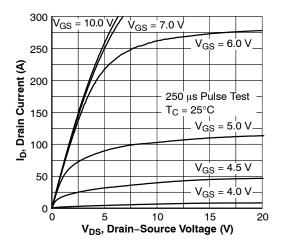


Figure 1. On-Region Characteristics

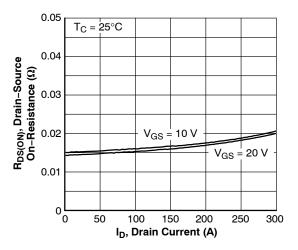


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

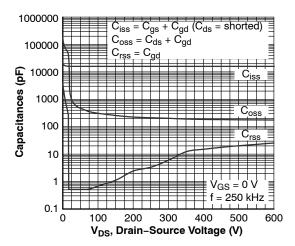


Figure 5. Capacitance Characteristics

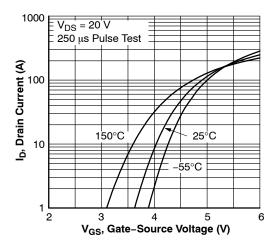


Figure 2. Transfer Characteristics

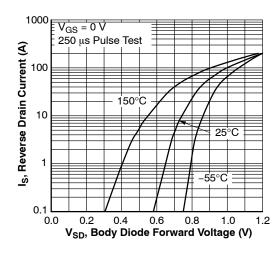


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

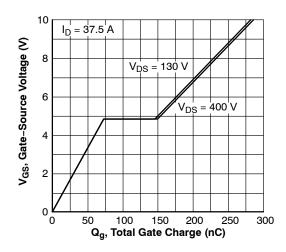


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

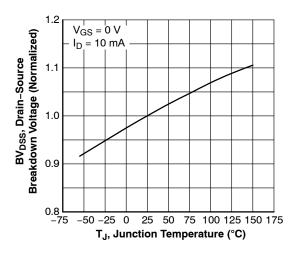


Figure 7. Breakdown Voltage Variation vs. Temperature

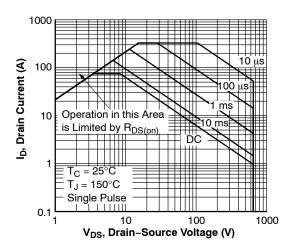


Figure 9. Maximum Safe Operating Area

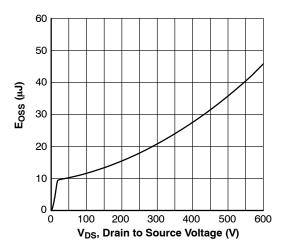


Figure 11. E_{OSS} vs. Drain to Source Voltage

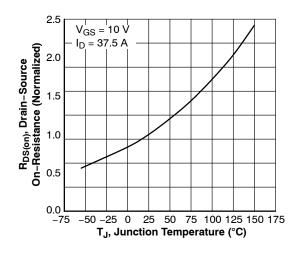


Figure 8. On–Resistance Variation vs. Temperature

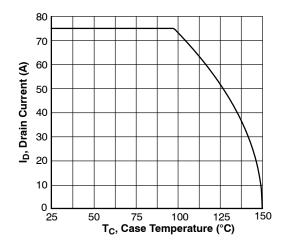


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

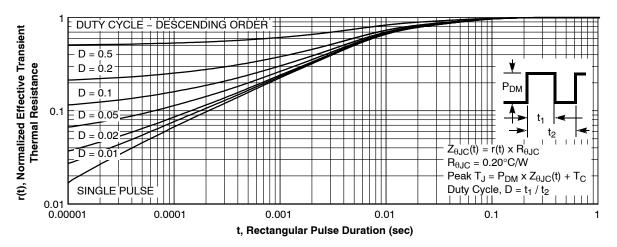


Figure 12. Transient Thermal Response Curve

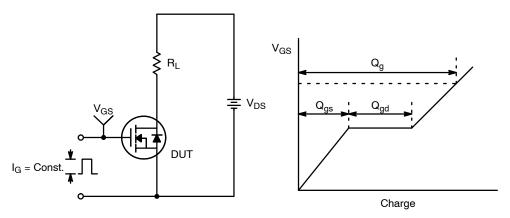


Figure 13. Gate Charge Test Circuit & Waveform

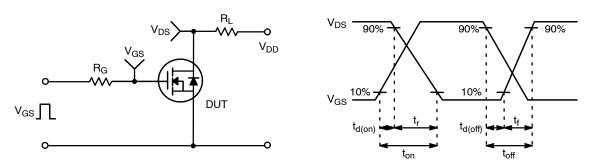


Figure 14. Resistive Switching Test Circuit & Waveforms

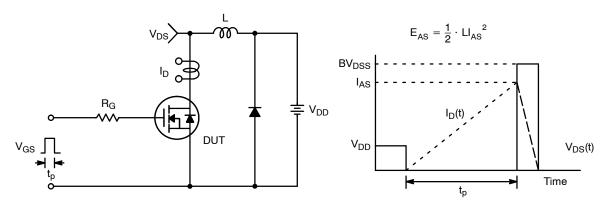


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

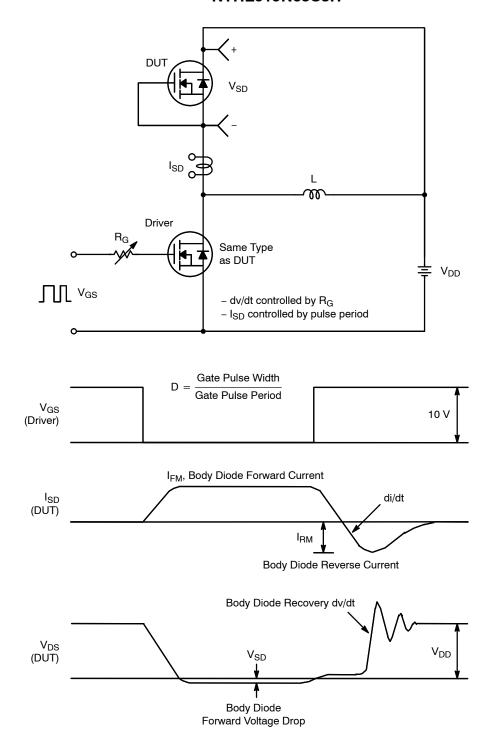
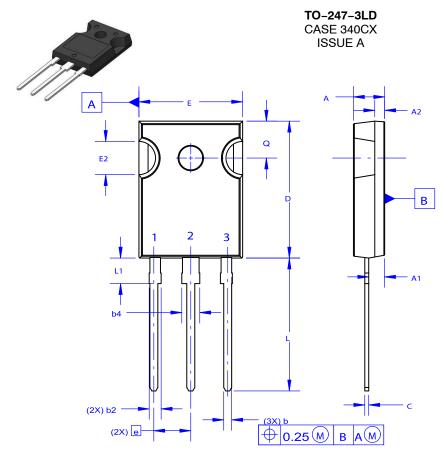


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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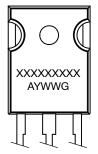




NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present. Some products may not follow the Generic Marking.

Ø _P —		Φ _{P1} D2
E1 -	2	D1

DIM	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	4.58	4.70	4.82	
A 1	2.20	2.40	2.60	
A2	1.40	1.50	1.60	
D	20.32	20.57	20.82	
E	15.37	15.62	15.87	
E2	4.96	5.08	5.20	
е	~	5.56	~	
L	19.75	20.00	20.25	
L1	3.69	3.81	3.93	
ØΡ	3.51	3.58	3.65	
Q	5.34	5.46	5.58	
S	5.34	5.46	5.58	
b	1.17	1.26	1.35	
b2	1.53	1.65	1.77	
b4	2.42	2.54	2.66	
С	0.51	0.61	0.71	
D1	13.08	~	~	
D2	0.51	0.93	1.35	
E1	12.81	~	~	
ØP1	6.60	6.80	7.00	

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