Power MOSFET

40 V, 70 A, Single N-Channel, DPAK

Features

- Low R_{DS(on)}
- High Current Capability
- Low Gate Charge
- STD Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Electronic Brake Systems
- Electronic Power Steering
- Bridge Circuits

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

| Parameter | | | Symbol | Value | Unit |
|--|------------------|------------------------|--------------------------------------|---------------|------|
| Drain-to-Source Voltage | | | V _{DSS} | 40 | V |
| Gate-to-Source Voltage | е | | V_{GS} | ±20 | V |
| Continuous Drain | Steady | T _C = 25°C | I _D | 70 | Α |
| Current – R _{θJC} | State | T _C = 125°C | | 40 | |
| Power Dissipation – R _{θJC} | Steady State | T _C = 25°C | P _D | 100 | W |
| Continuous Drain | Steady State | T _A = 25°C | I _D | 12.2 | Α |
| Current – R _{θJA} (Note 1) | State | T _A = 125°C | | 7.0 | |
| Power Dissipation – R _{θJA} (Note 1) | Steady State | T _A = 25°C | P _D | 3.0 | W |
| Pulsed Drain Current | t _p = | = 10 μs | I _{DM} | 150 | Α |
| Operating Junction and Storage Temperature | | | T _J , T _{STG} | –55 to 175 | °C |
| Source Current (Body Diode) Pulsed | | | I _S | 63.5 | Α |
| Single Pulse Drain–to Source Avalanche Energy – (V_{DD} = 50 V, V_{GS} = 10 V, I_{PK} = 30 A, L = 1 mH, R_G = 25 Ω) | | | EAS | 450 | mJ |
| Lead Temperature for Soldering Purposes (1/8" from case for 10 s) | | | TL | 260 | °C |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS (Note 1)

| Parameter | Symbol | Max | Unit |
|------------------------------|-----------------|-----|------|
| Junction-to-Case (Drain) | $R_{\theta JC}$ | 1.5 | °C/W |
| Junction-to-Ambient (Note 1) | $R_{\theta JA}$ | 49 | |

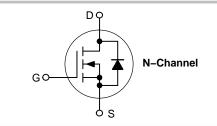
Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [2 oz] including traces).



ON Semiconductor®

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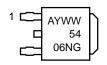
| V _{(BR)DSS} | R _{DS(ON)} TYP | I _D MAX (Note 1) |
|----------------------|-------------------------|--------------------------------|
| 40 V | 8.7 mΩ @ 10 V | 70 A |





DPAK CASE 369C STYLE 2

MARKING DIAGRAM



A = Assembly Location*

Y = Year WW = Work Week

5406N = Specific Device Code G = Pb-Free Device

* The Assembly Location code (A) is front side optional. In cases where the Assembly Location is stamped in the package, the front side assembly code may be blank.

ORDERING INFORMATION

| Device | Package | Shipping† |
|------------------|-------------------|-----------------------|
| NTD5406NT4G | DPAK (Pb-Free) | 2500 / Tape & Reel |
| STD5406NT4G* | DPAK (Pb-Free) | 2500 / Tape & Reel |
| STD5406NT4G-VF01 | DPAK (Pb-Free) | 2500 / Tape & Reel |

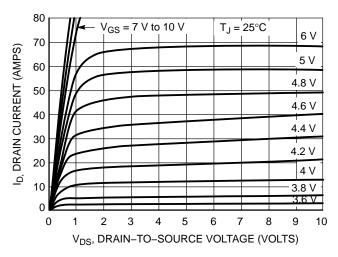
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise stated)

| Parameter | Symbol | Test Cond | dition | Min | Тур | Max | Unit |
|--|--------------------------------------|--|---------------------------|-----|------|------|-------|
| OFF CHARACTERISTICS | - | | • | | • | • | • |
| Drain-to-Source Breakdown Voltage | V _{(BR)DSS} | $V_{GS} = 0 \text{ V}, I_D$ | = 250 μΑ | 40 | | | V |
| Drain-to-Source Breakdown Voltage Temperature Coefficient | V _{(BR)DSS} /T _J | | | | 42 | | mV/°C |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{GS} = 0 V$ | T _J = 25°C | | | 1.0 | μΑ |
| | | $V_{DS} = 40 \text{ V}$ | T _J = 100°C | | | 10 | 1 |
| Gate-to-Source Leakage Current | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{G}$ | _{iS} = ±30 V | | | ±100 | nA |
| ON CHARACTERISTICS (Note 2) | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | $V_{GS} = V_{DS}, I_{D}$ | = 250 μΑ | 1.5 | | 3.5 | V |
| Gate Threshold Temperature Coefficient | V _{GS(TH)} /T _J | | | | -7.0 | | mV/°C |
| Drain-to-Source On Resistance | R _{DS(on)} | V _{GS} = 10 V, I | I _D = 30 A | | 8.7 | 10 | mΩ |
| | | $V_{GS} = 5.0 \text{ V},$ | I _D = 10 A | | 13.2 | 17 | 7 |
| Forward Transconductance | 9FS | V _{GS} = 10 V, I | I _D = 10 A | | 19 | | S |
| CHARGES AND CAPACITANCES | | | | | | | |
| Input Capacitance | C _{ISS} | | | | 1375 | 2500 | pF |
| Output Capacitance | C _{OSS} | $V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = 32 \text{ V}$ | | | 370 | 700 | |
| Reverse Transfer Capacitance | C _{RSS} | | | | 160 | 300 | |
| Total Gate Charge | Q _{G(TOT)} | | | 45 | | nC | |
| Threshold Gate Charge | Q _{G(TH)} | V _{GS} = 10 V, V _I | | 2.0 | | - | |
| Gate-to-Source Charge | Q_{GS} | $V_{GS} = 10 \text{ V}, V_{DS} = 32 \text{ V},$ $I_{D} = 30 \text{ A}$ | | | 5.4 | | |
| Gate-to-Drain Charge | Q_{GD} | | | | 20 | | |
| SWITCHING CHARACTERISTICS, V_0 | _{SS} = 10 V (Note | 3) | | | | | |
| Turn-On Delay Time | t _{d(ON)} | | | | 7.2 | | ns |
| Rise Time | t _r | $V_{GS} = 10 \text{ V}, V_{DD} = 32 \text{ V},$ $I_{D} = 30 \text{ A}, R_{G} = 2.5 \Omega$ | | | 57 | | 7 |
| Turn-Off Delay Time | t _{d(OFF)} | $I_D = 30 \text{ A, R}_C$ | $_{\rm G}$ = 2.5 Ω | | 30 | | 7 |
| Fall Time | t _f | | Ī | | 67 | | |
| SWITCHING CHARACTERISTICS, Vo | SS = 5 V (Note 3 |) | | | | | |
| Turn-On Delay Time | t _{d(ON)} | | | | 15 | | ns |
| Rise Time | t _r | V _{GS} = 5.0 V, V | nn = 20 V, | | 147 | | |
| Turn-Off Delay Time | t _{d(OFF)} | $I_D = 30 \text{ A, R}_C$ | $_{\rm G}$ = 2.5 Ω | | 20 | | |
| Fall Time | t _f | 1 | | | 29 | | 1 |
| DRAIN-SOURCE DIODE CHARACTE | RISTICS | | | | | | |
| Forward Diode Voltage | V_{SD} | Voc = 0 V T _J = 25°C | | | 0.82 | 1.1 | V |
| | | $V_{GS} = 0 \text{ V},$ $I_{S} = 10 \text{ A}$ | T _J = 125°C | | 0.67 | | |
| Reverse Recovery Time | t _{RR} | | • | | 46 | | ns |
| Charge Time | ta | $V_{GS} = 0 \text{ V, dl}_{SD}/d$ | t = 100 A/us. | | 24 | | |
| Discharge Time | t _b | $I_{S} = 10 \text{ A}$ | | | 22 | | |
| Reverse Recovery Charge | Q _{RR} | † † | | | 65 | | nC |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Pulse Test: pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$. 3. Switching characteristics are independent of operating junction temperatures.

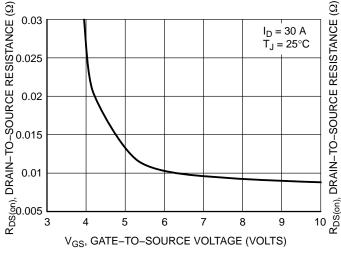
TYPICAL PERFORMANCE CURVES



80 $V_{DS} \ge 10 \text{ V}$ 70 ID, DRAIN CURRENT (AMPS) 60 50 40 30 T_J = 100°C 20 $T_J = 25^{\circ}C$ 10 $T_{.1} = -55^{\circ}C$ 0 0 2 5 6 3 V_{GS}, GATE-TO-SOURCE VOLTAGE (VOLTS)

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



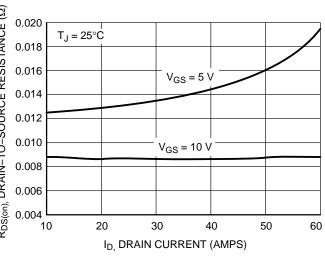
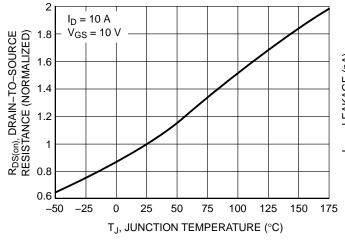


Figure 3. On-Resistance vs. Gate-to-Source Voltage

Figure 4. On-Resistance vs. Drain Current and Gate Voltage



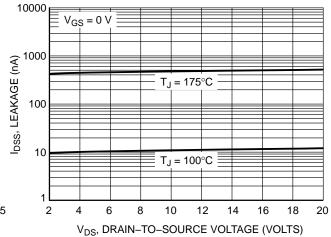
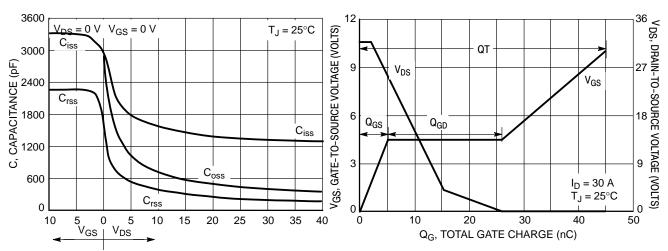


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL PERFORMANCE CURVES



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-To-Source and Drain-To-Source Voltage vs. Total Charge

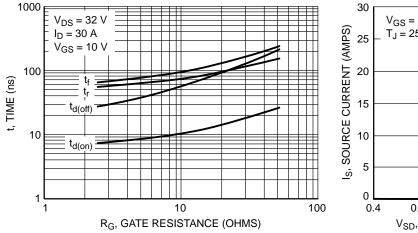


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

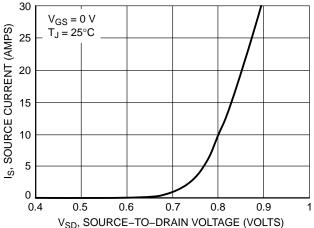


Figure 10. Diode Forward Voltage vs. Current

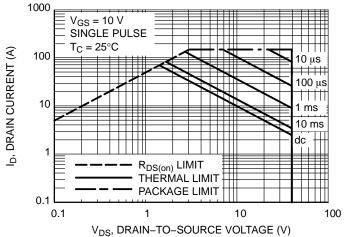


Figure 11. Maximum Rated Forward Biased Safe Operating Area

TYPICAL PERFORMANCE CURVES

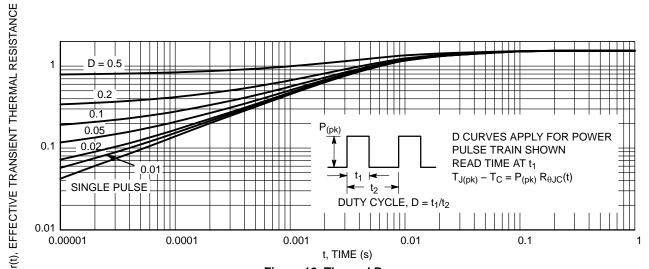
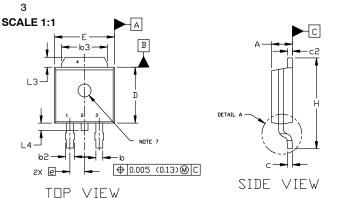


Figure 12. Thermal Response

DPAK (SINGLE GAUGE)

CASE 369C **ISSUE G**

DATE 31 MAY 2023

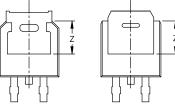


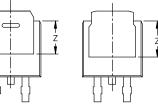


- DIMENSIONING AND TOLERANCING ASME Y14.5M, 1994. CONTROLLING DIMENSION: INCHES
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS 63,
- L3. AND Z. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
 PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR
 GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
- DIMENSIONS D AND E ARE DETERMINED AT THE DUTERMOST EXTREMES OF THE PLASTIC BODY.

 DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.
- OPTIONAL MOLD FEATURE.

| DIM | INCHES | | MILLIMETERS | | |
|------------|--------|--------------|-------------|-------|--|
| MIM | MIN. | MAX. | MIN. | MAX. | |
| Α | 0.086 | 0.094 | 2.18 | 2.38 | |
| A1 | 0.000 | 0.005 | 0.00 | 0.13 | |
| ھ | 0.025 | 0.035 | 0.63 | 0.89 | |
| b2 | 0.028 | 0.045 | 0.72 | 1.14 | |
| b 3 | 0.180 | 0.215 | 4.57 | 5.46 | |
| Ū | 0.018 | 0.024 | 0.46 | 0.61 | |
| -2 | 0.018 | 0.024 | 0.46 | 0.61 | |
| D | 0.235 | 0.245 | 5.97 | 6.22 | |
| E | 0.250 | 0.265 | 6.35 | 6.73 | |
| е | 0.090 | BSC | 2.29 BSC | | |
| Η | 0.370 | 0.410 | 9.40 | 10.41 | |
| L | 0.055 | 0.070 | 1.40 | 1.78 | |
| L1 | 0.114 | REF | 2.90 | REF | |
| L2 | 0.020 | 0 BSC 0.51 B | | BSC | |
| L3 | 0.035 | 0.050 | 0.89 | 1.27 | |
| L4 | | 0.040 | - | 1.01 | |
| Z | 0.155 | | 3.93 | | |

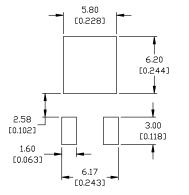




BOTTOM VIEW

BOTTOM VIEW

ALTERNATE CONSTRUCTIONS





CW ROTATED 90°

GENERIC MARKING DIAGRAM*



| XXXXXX | = Device Code |
|--------|---------------------|
| Α | = Assembly Location |
| L | = Wafer Lot |
| Υ | = Year |
| WW | = Work Week |
| G | = Pb-Free Package |

RECOMMENDED MOUNTING FOOTPRINT* *FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DUWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

S

| STYLE 1: | STYLE 2: | STYLE 3: | STYLE 4: | STYLE 5: |
|-----------------------------|--------------------------|---------------------------|-------------------------|---------------------------|
| PIN 1. BASE | PIN 1. GATE | PIN 1. ANODE | PIN 1. CATHODE | PIN 1. GATE |
| 2. COLLECTOR | 2. DRAIN | CATHODE | 2. ANODE | ANODE |
| EMITTER | SOURCE | ANODE | 3. GATE | CATHODE |
| COLLECTOR | 4. DRAIN | CATHODE | ANODE | ANODE |

STYLE 7: PIN 1. GATE 2. COLLECTOR STYLE 6: STYLE 8: STYLE 9: STYLE 10: PIN 1. CATHODE 2. ANODE 3. CATHODE PIN 1. MT1 2. MT2 PIN 1. N/C 2. CATHODE 3. ANODE PIN 1. ANODE 2. CATHODE 3 FMITTER 3 RESISTOR ADJUST 3 GATE 4. COLLECTOR 4. CATHODE 4. ANODE 4. CATHODE

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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| DESCRIPTION: | DPAK (SINGLE GAUGE) | | PAGE 1 OF 1 | |

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