

• General Description

The AGM056N10A combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{\text{DS(ON)}}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

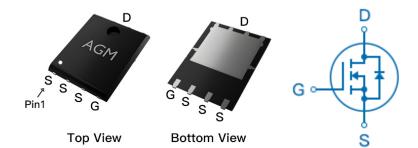
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
100V	4.7mΩ	100A

PDFN5*6 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM056N10A	AGM056N10A	PDFN5*6	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	100	А
	Drain Current-Continuous(Tc=100℃)	75	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	400	А
PD	Maximum Power Dissipation(Tc=25℃)	113	W
	Maximum Power Dissipation(Tc=100℃)	45	w
EAS	Avalanche energy (Note 3)	72	mJ
TJ,TSTG	TG Operating Junction and Storage Temperature Range		$^{\circ}\!\mathbb{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹		20	°C/W
RøJC	Thermal Resistance Junction-Case ¹		1.1	°C/W



Table 3. Electrical Characteristics (TJ=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off Sta	ites					
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	100			V
IDSS	Zero Gate Voltage Drain Current	VDS=100V,VGS=0V			1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	1.7	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=15A		38		S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A		4.7	6.5	mΩ
1.00(011)		VGS=4.5V, ID=15A		6.5	8.5	mΩ
Dynamic (Characteristics					
Ciss	Input Capacitance	VDS=40V,VGS=0V,		3100		pF
Coss	Output Capacitance	F=1MHZ		705		pF
Crss	Reverse Transfer Capacitance			26		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz		1.7		Ω
Switching	Times					
td(on)	Turn-on Delay Time			10		nS
tr	Turn-on Rise Time	VGS=10V,VDS=50V,		6.5		nS
td(off)	Turn-Off Delay Time	ID=14A,RGEN=3.3Ω		45		nS
tf	Turn-Off Fall Time			7.5		nS
Qg	Total Gate Charge			44		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=50V, ID=20A		10.1		nC
Qgd	Gate-Drain Charge	ID-20A		8.4		nC
	ain Diode Characteristics			1		
ISD	Source-Drain Current(Body Diode)				100	А
VSD	Forward on Voltage	VGS=0V,IS=20A			1.2	V
trr	Reverse Recovery Time	IF=20A , dl/dt=100A/μs ,		30		ns
	Reverse Recovery Charge			148		nc

Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25 $^{\circ}$ C,VDD=50V,Vgs=10V,ID=17A,L=0.5mH,RG=25ohm



Typical Characteristics

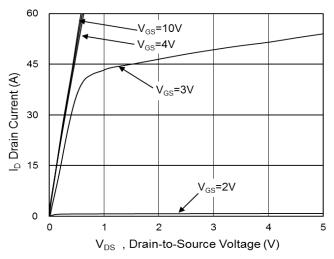


Fig.1 Typical Output Characteristics

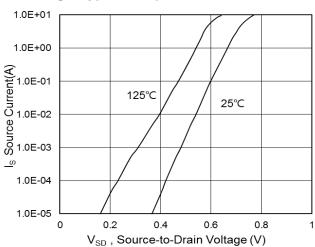


Fig.3 Source-Drain Forward Characteristics

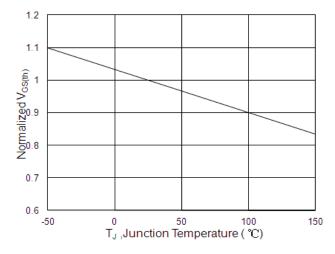


Fig.5 Normalized V_{GS(th)} vs. T_J

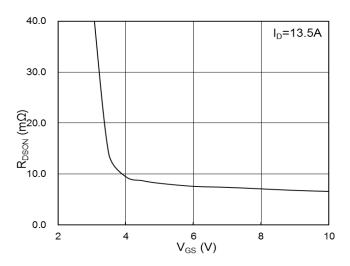


Fig.2 On-Resistance vs. G-S Voltage

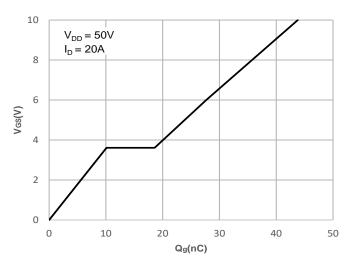


Fig.4 Gate-Charge Characteristics

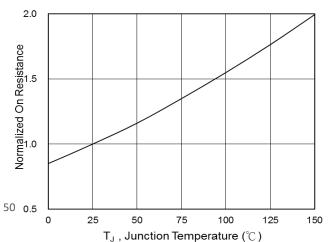
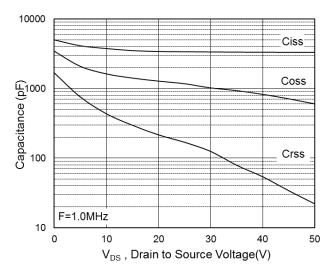


Fig.6 Normalized R_{DSON} vs. T_J





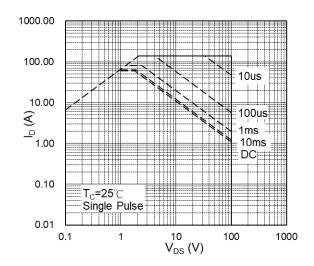


Fig.7 Capacitance

Fig.8 Safe Operating Area

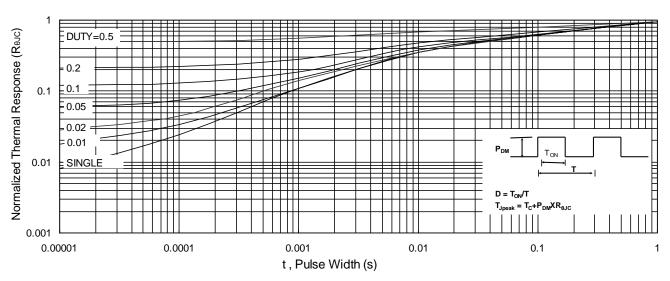


Fig.9 Normalized Maximum Transient Thermal Impedance

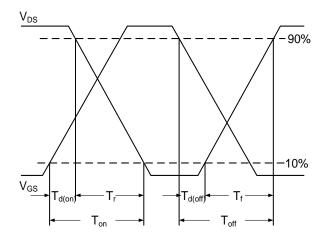


Fig.10 Switching Time Waveform

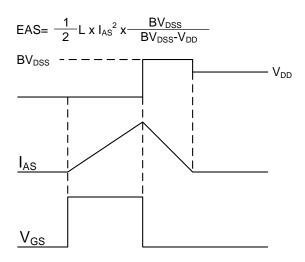
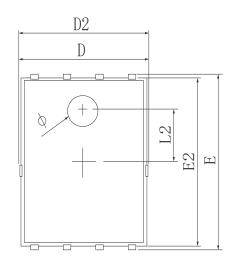
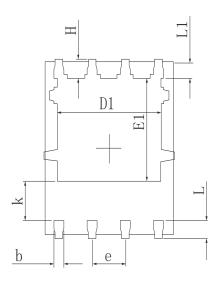


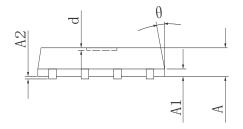
Fig.11 Unclamped Inductive Switching Waveform



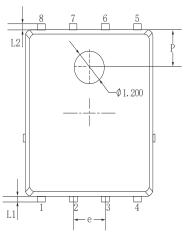
•Dimensions (PDFN5*6)

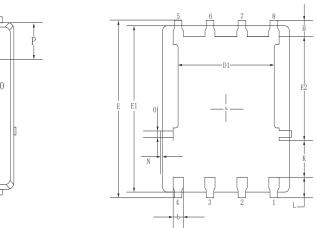


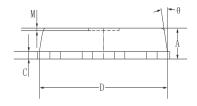




	MILLIMETER			
SYMBOL	MIN	Typ.	MAX	
A	0.900	1.000	1.100	
A1		0.254 REF.		
A2		0~0.05		
D	4. 824	4.900	4. 976	
D1	3. 910	4.010	4. 110	
D2	4. 924	5.000	5. 076	
E	5. 924	6.000	6.076	
E1	3. 375	3. 475	3. 575	
E2	5. 674	5. 750	5.826	
b	0.350	0.400	0.450	
е	1.270 TYP.			
L	0. 534	0.610	0.686	
L1	0.424	0.500	0.576	
L2	1.800 REF.			
k	1.190	1. 290	1. 390	
Н	0.549	0.625	0.701	
θ	8°	10°	12°	
Ф	1.100	1.200	1. 300	
d			0.100	





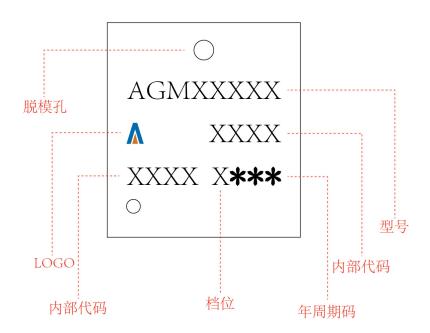


	Millimeters			
Symbol	MIN.	NOM.	MAX.	
A	0.90	1.05	1. 20	
b	0.35	0.40	0.50	
С	0.20	0. 25	0.35	
D	4.90	5. 05	5. 20	
D1	3. 72	3. 82	3. 92	
Е	6.00	6. 15	6.30	
E1	5. 60	5. 75	5. 90	
E2	3. 47	3. 57	3. 67	
е	1.27 BSC.			
Н	0.48	0.58	0.68	
K	1.17	1. 27	1. 37	
L	0.64	0.74	0.84	
L1/L2	0.20 REF.			
θ	8°	10°	12°	
M	0.08 REF.			
N	0	-	0. 15	
0	0.25 REF.			
P		1.28 REF.		

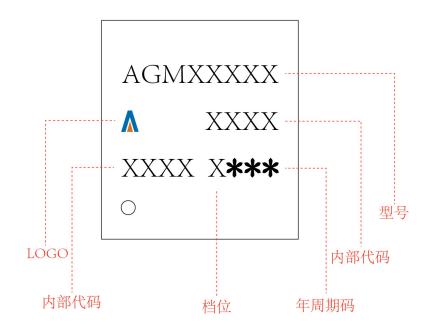


PDFN5*6 Marking Instructions:

Model1:



Model2:





Disclaimer:

The information provided in this document is believed to be accurate and reliable. However, Shenzhen Core Control Source Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Source Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the sixth version issued on Oct. 10th, 2024. This document replaces all previously provided information.

It is a registered trademark of Shenzhen Core Control Source Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Source Electronics Technology Co., Ltd. all rights reserved.