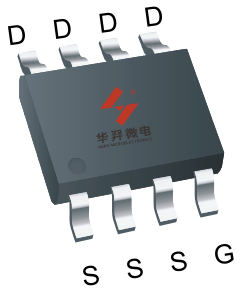
N-Channel Enhancement Mode MOSFET

Feature

- 40V/9A
  $R_{DS(ON)}$= 16 mΩ(typ.) @VGS = 10V
  $R_{DS(ON)}$= 19 mΩ(typ.) @VGS = 4.5V
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available
  (RoHS Compliant)

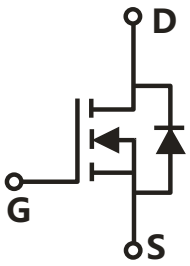
Pin Description



SOP8L

Applications

- Switching application
- Li-battery protection
- DC-DC
- Motor control



Single N-Channel MOSFET

Ordering and Marking Information

S HYG190N04 XYMXXXXXX	Package Code S:SOP8L Date Code XYMXXXXXX

Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish;which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines “Green” to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25℃ Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		40	V
V_{GSS}	Gate-Source Voltage		±20	V
T_J	Junction Temperature Range		-55 to 175	℃
T_{STG}	Storage Temperature Range			℃
I_S	Source Current-Continuous(Body Diode)	Tc=25°C	9	A
Mounted on Large Heat Sink				
I_{DM}	Pulsed Drain Current *	Tc=25°C	32	A
I_D	Continuous Drain Current	Tc=25°C	9	A
		Tc=100°C	7	A
P_D	Maximum Power Dissipation	Tc=25°C	3	W
		Tc=100°C	1.5	W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		50	°C/W
E_{AS}	Single Pulsed-Avalanche Energy ***	L=0.1mH	18	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
** Surface mounted on 1in2 FR-4 board.
*** Limited by $T_J$max , starting T_J=25°C, L = 0.1mH, R_G= 25Ω, V_{GS} =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG190N04LR1			Unit
			Min	Typ.	Max	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}=250μA	40	-	-	V
I_{DSS}	Drain-to-Source Leakage Current	V_{DS}=40V,V_{GS}=0V	-	-	1	μA
		T_J=125°C	-	-	50	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1	1.7	3	V
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=10V,I_{DS}=8A	-	16	19	mΩ
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=4.5V,I_{DS}=6A	-	19	25	mΩ
Diode Characteristics						
V_{SD}	Diode Forward Voltage	I_{SD}=8A,V_{GS}=0V	-	0.83	1.20	V
t_{rr}	Reverse Recovery Time	I_{SD}=8A,dI_{SD}/dt=100A/μs	-	8	-	ns
Q_{rr}	Reverse Recovery Charge		-	4	-	nC

Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG190N04LR1			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	2.3	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V,	-	555	-	pF
C_{oss}	Output Capacitance	V_{DS}=25V,	-	63	-	
C_{rss}	Reverse Transfer Capacitance	Frequency=1MHz	-	51	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=20V,R_G=2.5Ω, I_{DS}=8A,V_{GS}=10V	-	6	-	ns
T_r	Turn-on Rise Time		-	15	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	17	-	
T_f	Turn-off Fall Time		-	3	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge(V_{GS}=10V)	V_{DS}=32V,I_{DS}=8A	-	14	-	nC
Q_g	Total Gate Charge(V_{GS}=4.5V)			7		
Q_{gs}	Gate-Source Charge		-	2	-	
Q_{gd}	Gate-Drain Charge		-	3	-	
$V_{plateau}$	Gate plateau voltage		-	3.2	-	V

Note：*Pulse test，pulse width ≤ 300us，duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

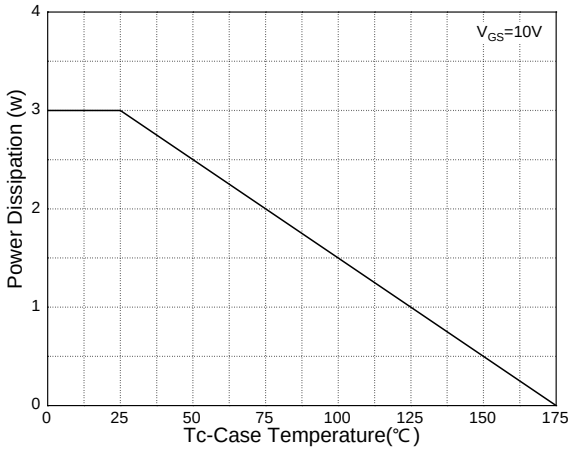


Figure 2: Drain Current

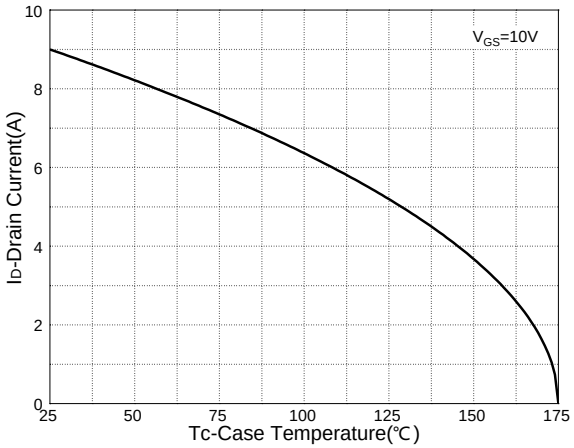


Figure 3: Safe Operation Area

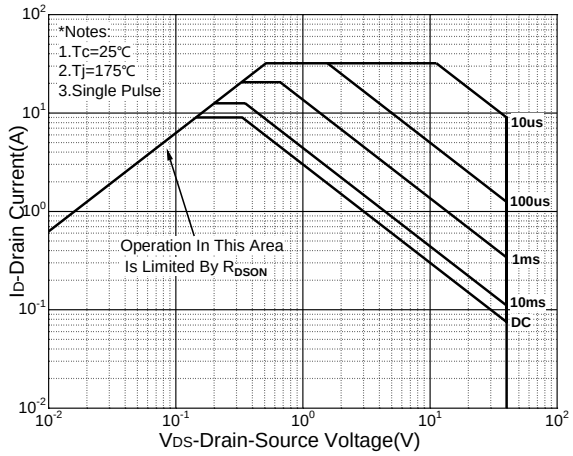


Figure 4: Thermal Transient Impedance

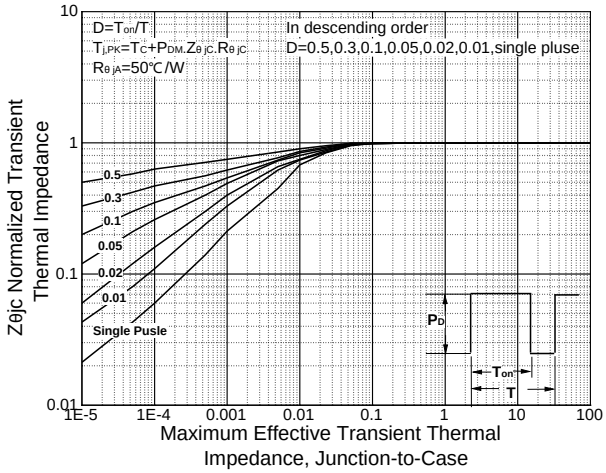


Figure 5: Output Characteristics

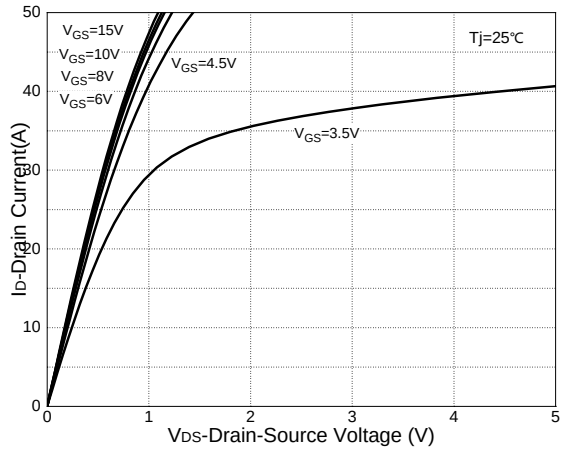
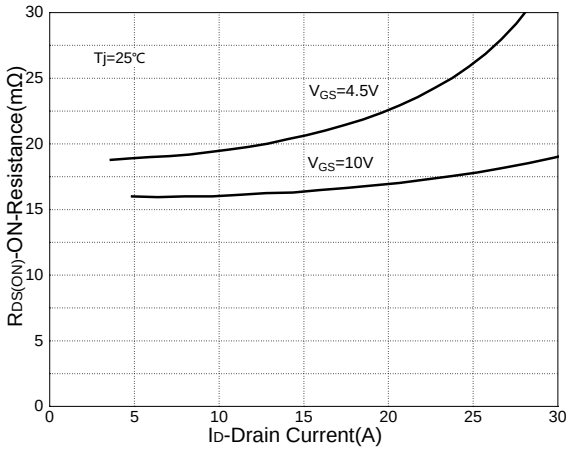


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

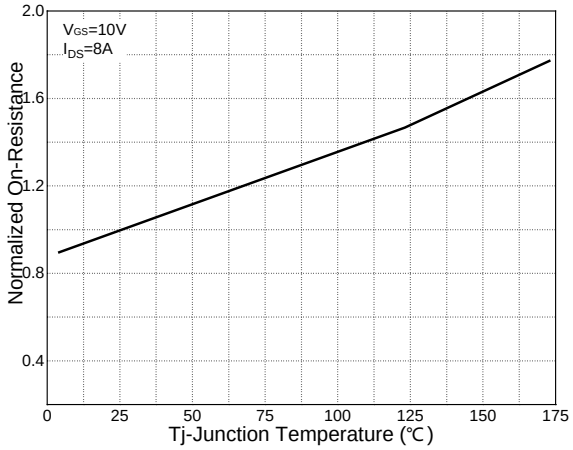


Figure 8: Source-Drain Diode Forward

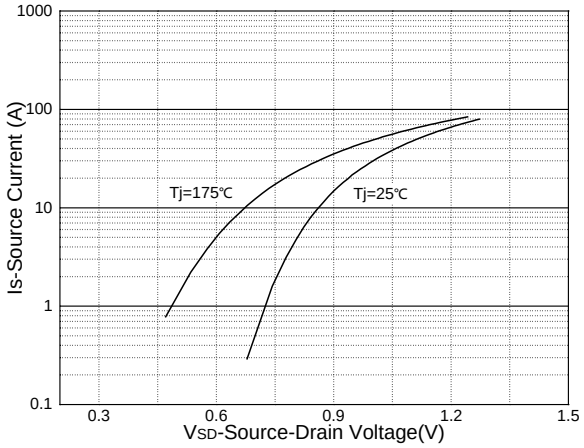


Figure 9: Capacitance Characteristics

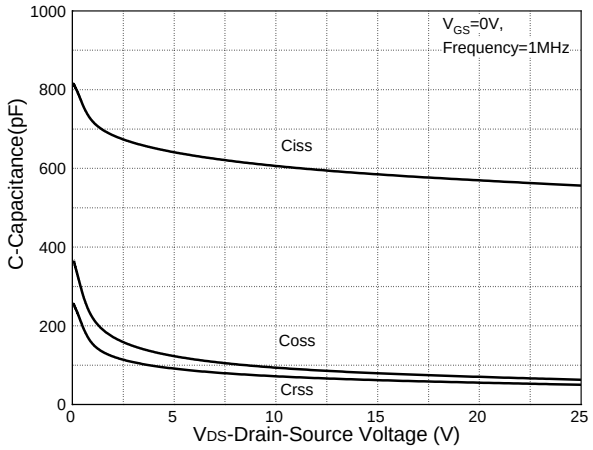
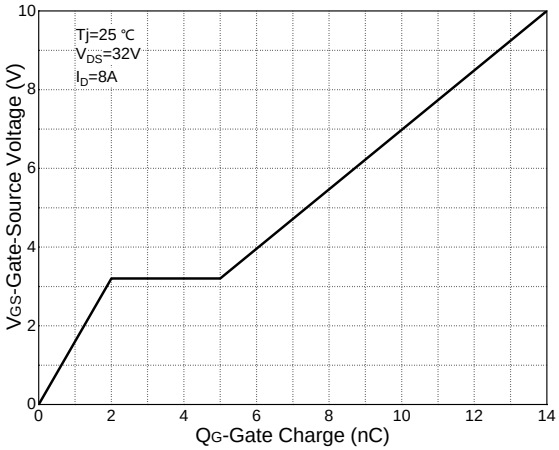
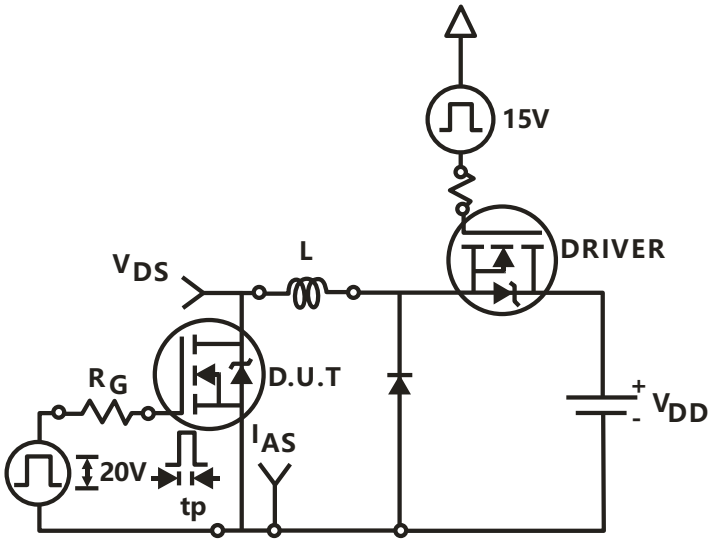


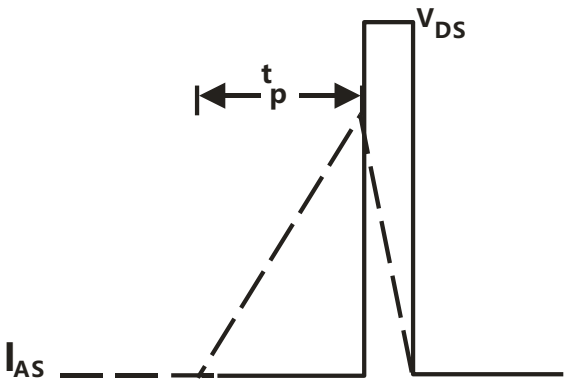
Figure 10: Gate Charge Characteristics



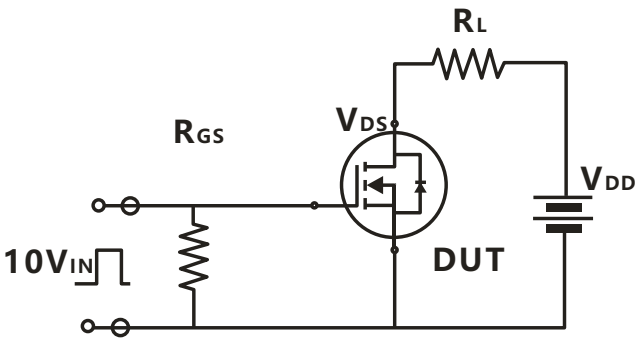
Avalanche Test Circuit

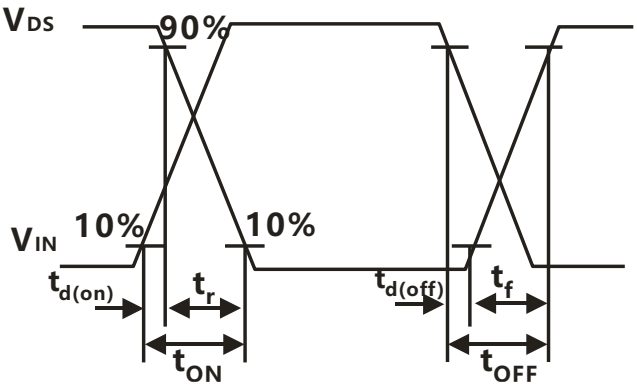


$$E_{AS} = \frac{1}{2} L I_{AS}^2$$

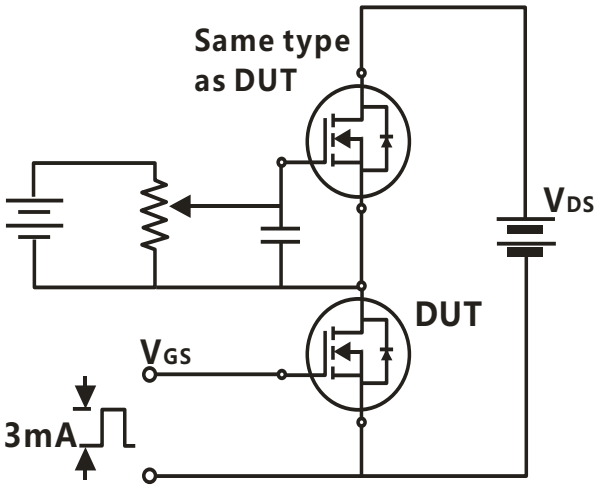


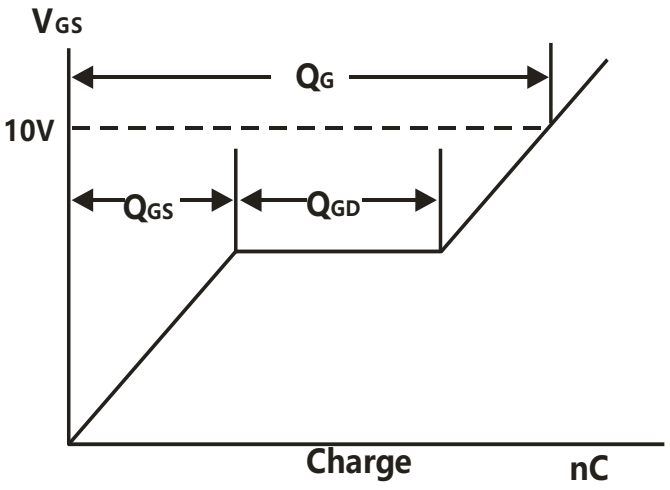
Switching Time Test Circuit





Gate Charge Test Circuit

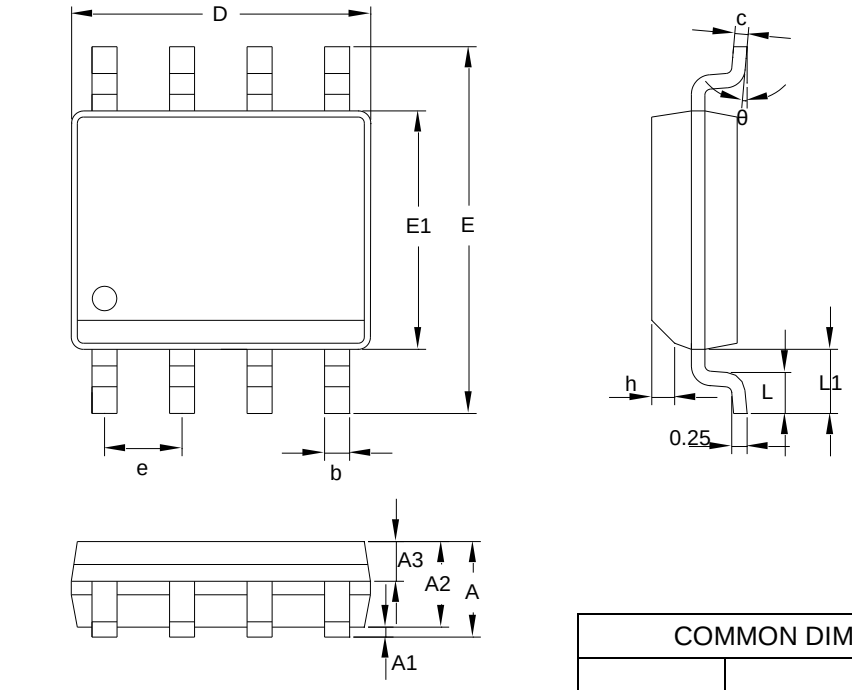




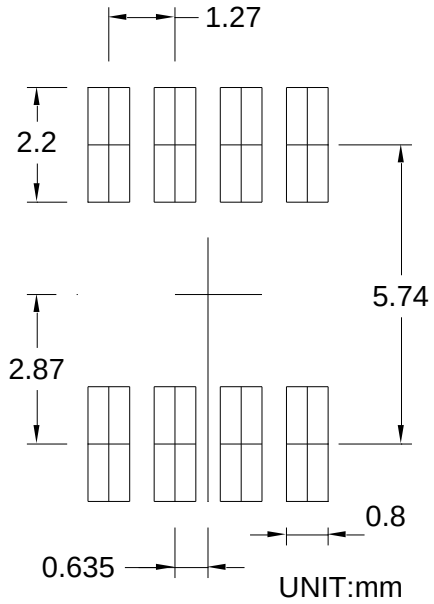
Device Per Unit

Package Type	Unit	Quantity
SOP8L	Reel	2500

Package Information



RECOMMENDED LAND PATTERN

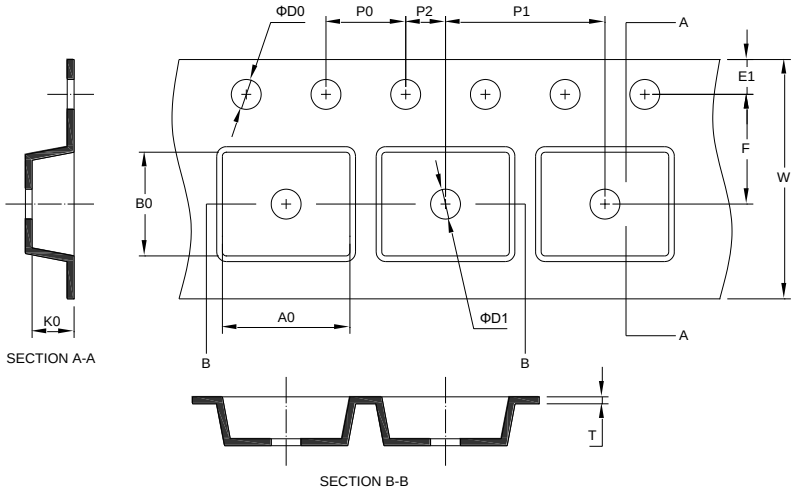


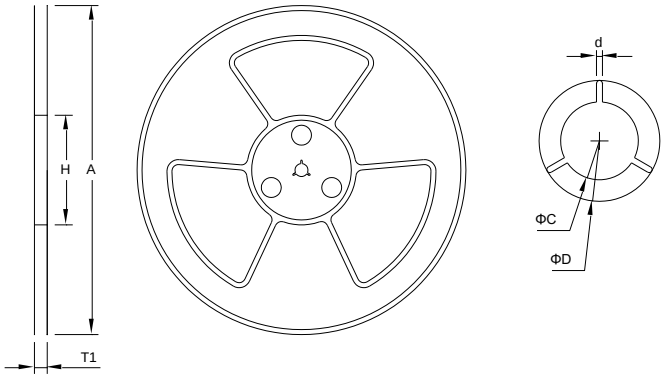
COMMON DIMENSIONS			
SYMBOL	mm		
	MIN	NOM	MAX
A	-	-	1.75
A1	0.10	-	0.225
A2	1.30	1.40	1.50
A3	0.60	0.65	0.70
b	0.39	-	0.47
c	0.20	-	0.24
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27 BSC		
h	0.25	-	0.50
L	0.50	-	0.80
L1	1.05 REF		
θ	0°	-	8°

Note：

1. Follow JEDEC MS-012AA。
2. Dimension D" does not include mold flash，protrusions or gate burrs.
   Mold flash，protrusion or gate burrs shall not exceed 6 mil per side.
3. Dimension E" does not include inter-lead flash or protrusions.
   Inter-lead flash and protrusions shall not exceed 10 mil per side.

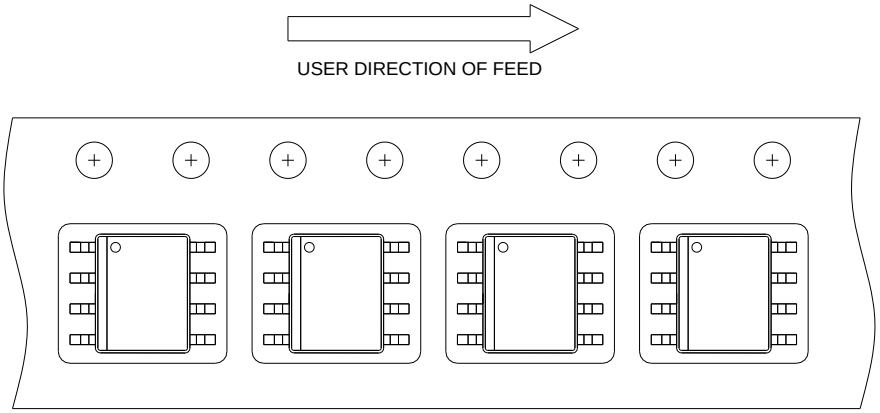
Carrier Tape & Reel Dimensions



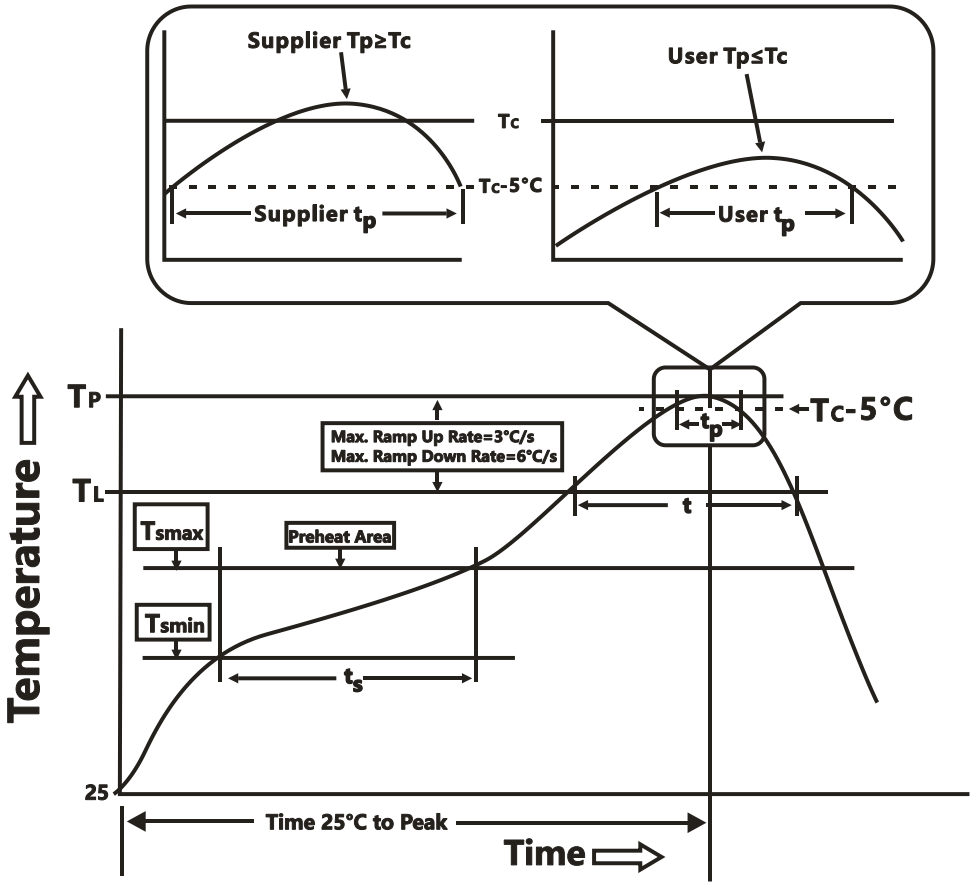


Application	A	H	T1	C	d	D	W	E1	F
SOP8L	330 2.00	50 MIN	12.4+2.00 -0.20	13.0+0.50 -0.20	1.5 MIN	20.2 MIN	12.0 0.30	1.75 0.10	5.5 0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 0.10	8.0 0.10	2.0 0.05	1.5+0.10 -0.00	1.5 MIN	0.6+0.00 -0.40	6.40 0.20	5.20 0.20	2.10 0.20

Taping Direction Information



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin}) Temperature max (T_{smax}) Time (Tsmin to Tsmax) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate ($T_{smax}$to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	SeeClassification Tempin table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate ($T_p$to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ⩾350
＜2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350-2000	Volume mm³ ⩾2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
⩾2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500 Hrs, V_{gs}100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500 Cycles, -55°C~150°C

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