

Product Summary

V _{(BR)DSS}	R _{DS(on)TYP}	l _D	
1001/	15mΩ@10V	8.4	
100V	18mΩ@4.5V	oA .	



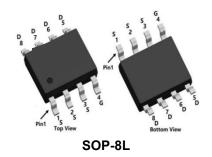
Feature

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

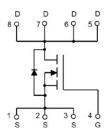
Applications

- Power switching application
- Battery management
- Uninterruptible power supply

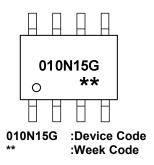
Package



Circuit Diagram



Marking



Order Information

Device	Package	Unit/Tape	
SP010N15GP8	SOP-8L	4000	



Absolute maximum ratings (Ta=25℃ unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	8	Α
Continuous Drain Current (Ta=100°C)	I _D	5.5	Α
Pulse Drain Current Tested	I _{DM}	32	Α
Single Pulse Avalanche Energy ¹	Eas	72	mJ
Power Dissipation	P _D	3	W
Thermal Resistance Junction-to-Ambient	R _{θJA}	41.67	°C/W
Storage Temperature Range	T _{STG}	55 to 150	$^{\circ}$ C
Operating Junction Temperature Range	T₃	55 to 150	$^{\circ}$

Electrical characteristics (Ta=25°C, unless otherwise noted)

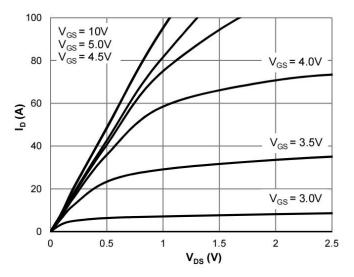
Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	100	-	-	V	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V	-	-	1	uA	
Gate Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±0.1	nA	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.8	2.5	V	
	_	V _{GS} = 10V, I _D = 8A	-	15	19	mΩ	
Drain-Source ON Resistance	R _{DS(ON)}	$V_{GS} = 4.5V, I_D = 6A$	-	18	24	11177	
Dynamic Characteristics							
Input Capacitance	Ciss	V _{DS} =50V, V _{GS} = 0V, f = 1.0MHz	-	1069	-		
Output Capacitance	Coss		-	356	-	pF	
Reverse Transfer Capacitance	Crss		-	17	-		
Total Gate Charge	Qg	V _{DS} =50V , VGS=10V , ID=8A	-	14	-		
Gate-Source Charge	Q _{gs}		-	5	-	nC	
Gate-Drain Charge	Q_{gd}		-	2.7	-		
Switching Characteristics							
Turn-On Delay Time	t _{d(on)}		-	38	-		
Rise Time	t _r	V _{GS} = 10V, V _{DS} =50V, ID=8A	-	12	-	nS	
Turn-Off Delay Time	$t_{d(off)}$	$R_G = 2.2\Omega$	-	51	-	ns	
Fall Time	t _f		-	17	-		
Drain-Source Body Diode Characteris	stics						
Source-Drain Diode Forward Voltage	V _{SD}	V _{GS} =0V , I _S =1A , T _J =25℃	-	-	1.2	V	
Maximum Body-Diode Continuous Current	Is		-	-	8	А	
Reverse Recovery Time	Trr	1 -04 di/dt-4004/us T -05%	-	40	-	nS	
Reverse Recovery Charge	Qrr	I _S =8A, di/dt=100A/us, T _J =25℃	-	42	-	nC	

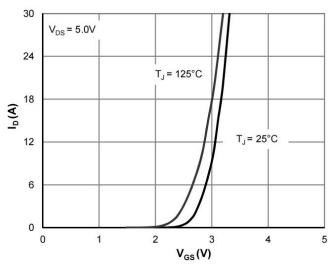
Note:

1. The EAS test condition is VDD=50V,VGS=10V,L=0.5mH,RG=25 Ω



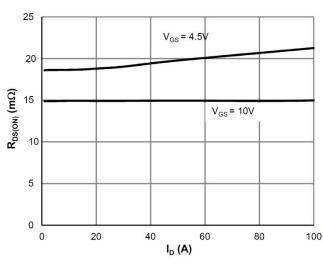
Typical Characteristics

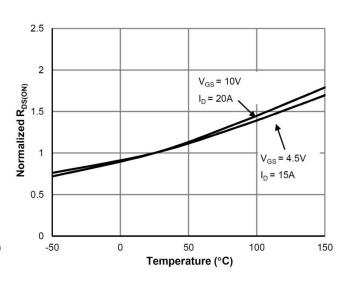




Typical Output Characteristics

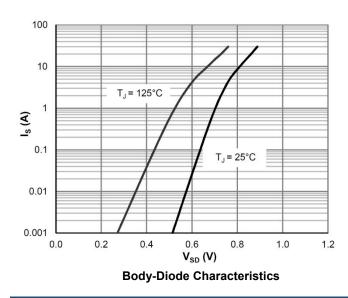


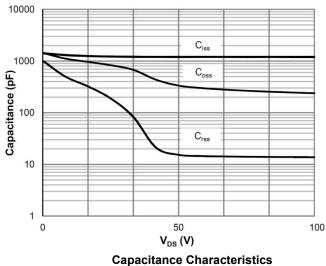




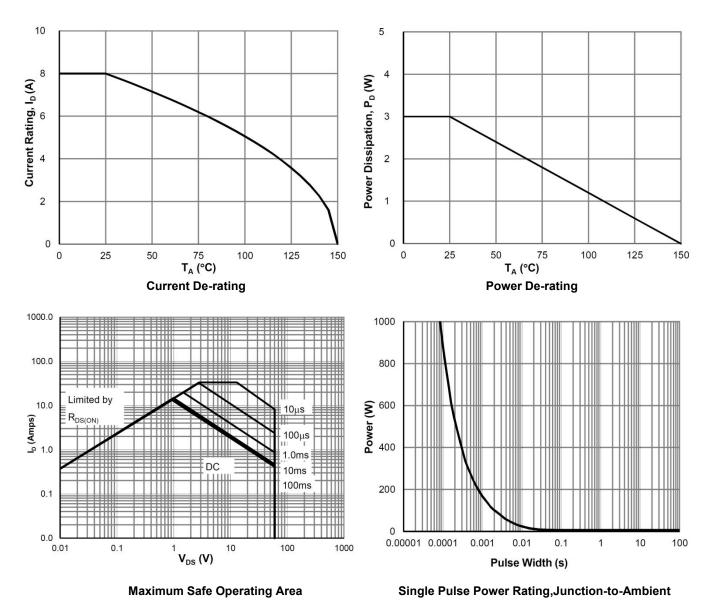
On-Resistance vs.Drain Current

On-Resistance vs. Junction Temperature





Siliup Semiconductor Technology Co. Ltd. www.siliup.com



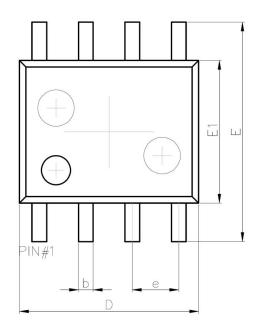
Duty Cycle = T_{on}/T
Peak T_J = T_C + P_D x Z_{0,JC} x R_{0,JC}
R_{0,JC} = 1.6°C/W
Duty = 0.5, 0.3, 0.1, 0.05, 0.02, 0.01, single pulse

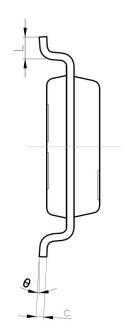
0.01
0.00001
0.0001
0.0001
0.001
0.01
1
1
10
Pulse Width (s)

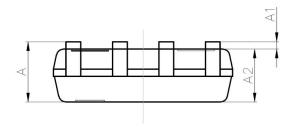
Normalized Maximum Transient Thermal Impedance



SOP-8L Package Information







Symbol	Dimensions In Millimeters		
Symbol	Min.	Max.	
Α	1.35	1.75	
A1	0.10	0.25	
A2	1.35	1.55	
b	0.33	0.51	
С	0.17	0.25	
D	4.80	5.00	
е	1.27	1.27 REF.	
E	5.80	6.20	
E1	3.80	4.00	
L	0.40	1.27	
θ	0°	8°	