

N-Channel Enhancement Mode MOSFET

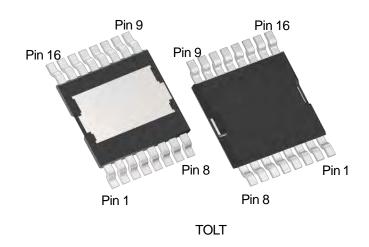
Feature

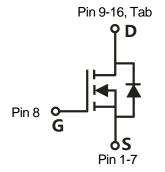
- 80V/440A $R_{DS(ON)} = 0.9 \text{ m}\Omega(typ.) @VGS = 10V$
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

Applications

- Switching application
- DC-DC Converter
- Motor control

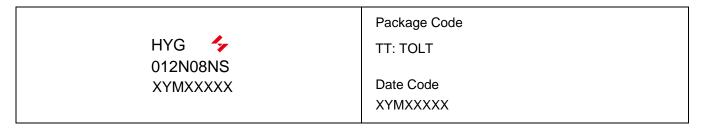
Pin Description





Single N-Channel MOSFET

Ordering and Marking Information



Note: HUAYI halogen free products contain molding compounds and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		80	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55. 475	°C
Tstg	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode) Tc=25°C		440	А
Mounted on	Large Heat Sink	•	•	1
Ідм	Pulsed Drain Current *	Tc=25°C	1500	А
1	Outing a Paris Out of	Tc=25°C	440	Α
lσ	Continuous Drain Current	Tc=100°C	311	Α
	M : 5 5: : ::	Tc=25°C	500	W
PD	P _D Maximum Power Dissipation		250	W
R₀uc	Thermal Resistance, Junction-to-Case		0.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		45	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		1660	mJ

- Note: * Repetitive rating; pulse width limited by max.junction temperature.
 - Surface mounted on 1in2 FR-4 board.
 - Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 Ω , VGs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Cumbal	Parameter	Test Conditions	HYG012N08NS2		Unit	
Symbol	Farameter	rest Conditions	Min	Тур.	Max	Unit
Static Char	acteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} = 250μA	80	-	-	V
Inno	Drain to Source Lookage Current	VDS=80V,VGS=0V	-	-	1	μA
IDSS	Ibss Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	Vos=Vgs, I _{DS} =250µA	2.2	3	3.8	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =100A	-	0.9	1.2	mΩ
Diode Char	Diode Characteristics					
VsD	Diode Forward Voltage	IsD=100A,Vgs=0V	-	0.83	1.2	٧
trr	Reverse Recovery Time	lon_1004_dlon/dt_1004/up	-	107	-	ns
Qrr	Reverse Recovery Charge	Isp=100A,dIsp/dt=100A/µs	-	185	-	nC

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Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Double of the state of the stat	Tank Cam dikinga	HY	HYG012N08NS2		
Symbol Parameter		Test Conditions	Min	Тур.	Max	Unit
Dynamic (Dynamic Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,f=500kHz	-	2.3	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	12686	-	
Coss	Output Capacitance	V _{DS} =25V,	-	4721	-	pF
Crss	Reverse Transfer Capacitance	frequency=500kHz	-	121	-	
td(ON)	Turn-on Delay Time		-	12	-	
Tr	Turn-on Rise Time	$V_{DD}=40V,R_{G}=2.5\Omega,$	-	46	-	
td(OFF)	Turn-off Delay Time	Ips=100A,Vgs=10V	-	34	-	ns
Tf	Turn-off Fall Time		-	47	-	
Gate Char	Gate Charge Characteristics					
Qg	Total Gate Charge(V _{GS} =10V)		-	205	-	
Qgs	Gate-Source Charge	\/ _64\/ _100A	-	66	-	nC
Qgd	Gate-Drain Charge	V_{DS} =64V, I_{DS} =100A	-	43	-	
V _{plateau}	Gate plateau voltage		-	5	-	V

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

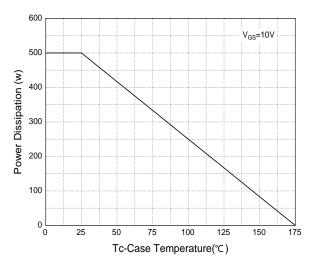


Figure 3: Safe Operation Area

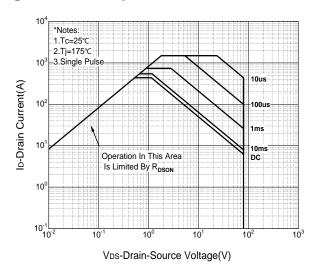


Figure 5: Output Characteristics

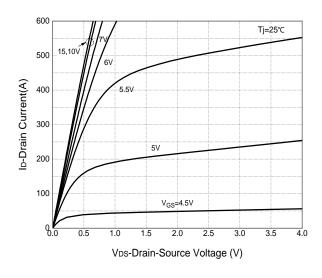


Figure 2: Drain Current

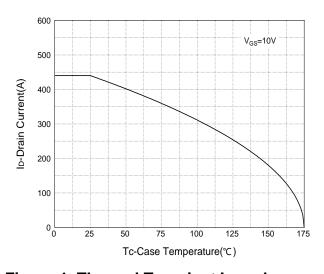


Figure 4: Thermal Transient Impedance

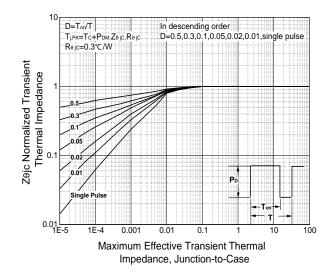
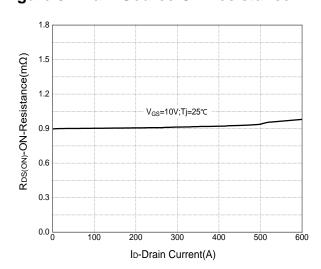


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

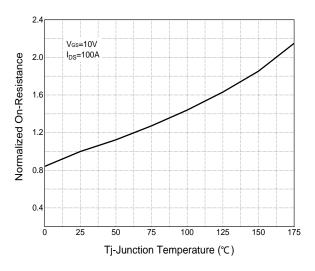


Figure 9: Capacitance Characteristics

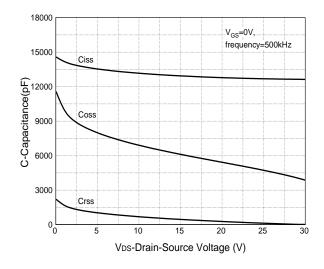


Figure 8: Source-Drain Diode Forward

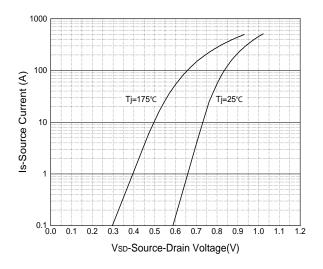
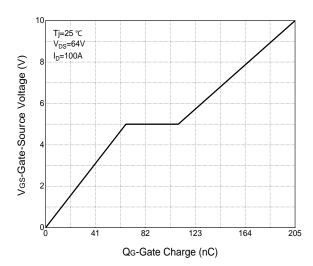
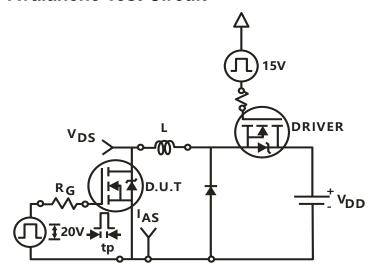


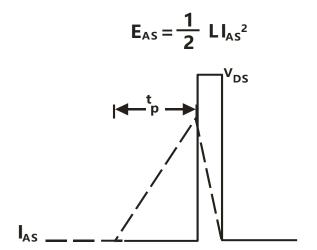
Figure 10: Gate Charge Characteristics



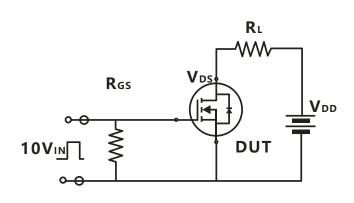


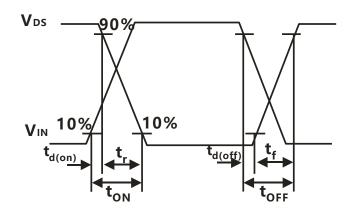
Avalanche Test Circuit



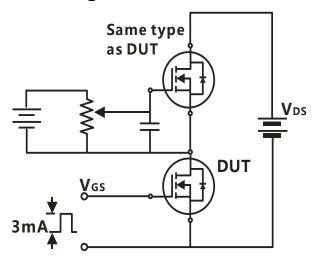


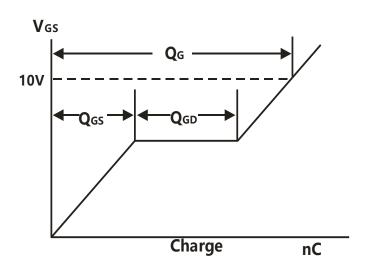
Switching Time Test Circuit





Gate Charge Test Circuit





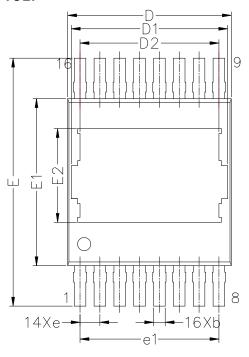


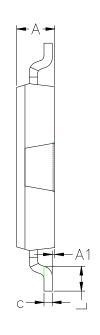
Device Per Unit

Package Type	Unit	Quantity
TOLT	Reel	1800

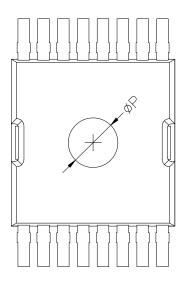
Package Information

TOLT







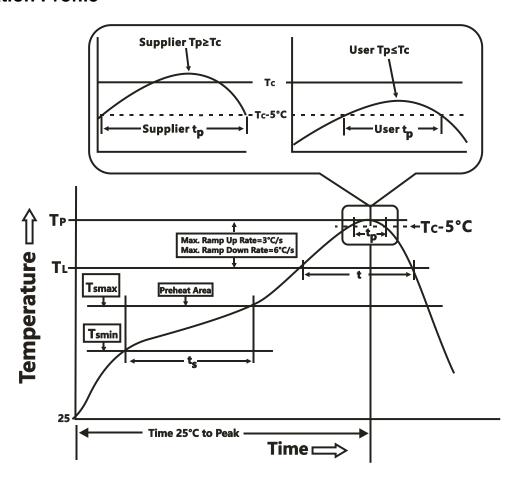


COMMON DIMENSIONS

SYMBOL	mm				
STIVIBUL	MIN	NOM	MAX		
А	2.25	2.30	2.35		
A1	0.01	0.08	0.16		
b	0.60	0.70	0.80		
С	0.40	0.50	0.60		
D	9.70	9.90	10.10		
D1	9.46 REF				
D2	8.30	8.40	8.50		
Е	14.80	15.00	15.20		
E1	10.00	10.10	10.30		
E2	5.57	5.67	5.77		
е	1.20 BSC				
e1	8.40 BSC				
L	1.40	1.50	1.60		
Р	2.90	3.00	3.10		



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly			
Preheat & Soak					
Temperature min (T _{smin})	100 °C	150 °C			
Temperature max (T _{smax})	150 °C	200 °C			
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds			
Average ramp-up rate (T _{smax} to T _P)	3 °C/second max.	3°C/second max.			
Liquidous temperature (T _L)	183 °C	217 °C			
Time at liquidous (t∟)	60-150 seconds	60-150 seconds			
Peak package body Temperature (T _P)*	See Classification Temp in table 1	SeeClassification Tempin table 2			
Time (t _P)** within 5°C of the specified	20** seconds	30** seconds			
classification temperature (T _c)					
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.			
Time 25°C to peak temperature	6 minutes max.	8 minutes max.			
*Tolerance for neak profile Temperature (T _o) is defined as a supplier minimum and a user maximum					

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm ³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168/500/1000 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500/1000 Cycles, -55°C~150°C

Customer Service

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