



N-Channel 200-V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{(BR)DSS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A)	
200	0.033 at V _{GS} = 10 V	57	

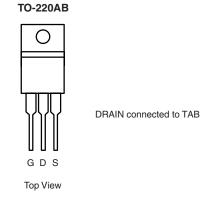
FEATURES

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature



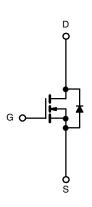
APPLICATIONS

- Isolated DC/DC converters
 - Primary-Side Switch



Ordering Information: SUP57N20-33

SUP57N20-33-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	200	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C		57		
Continuous Diam Current (1) = 175 C)	T _C = 125 °C	I _D	33		
Pulsed Drain Current		I _{DM}	140	A	
Avalanche Current		I _{AS}	35		
Single Pulse Avalanche Energy ^a	L = 0.1 mH	E _{AS}	61	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	В	300 ^b	w	
	T _A = 25 °C ^c	P _D	3.75		
Operating Junction and Storage Temperature F	Range	T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.5		

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.

SUP57N20-33

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SPECIFICATIONS $T_J = 25$	°C, unless of	otherwise noted				
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	200			V
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		V _{DS} = 160 V, V _{GS} = 0 V			1	
	I _{DSS}	$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	μΑ
		V _{DS} = 160 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V, I _D = 30 A		0.027	0.033	
	r _{DS(on)}	V _{GS} = 10 V, I _D = 30 A, T _J = 125 °C			0.069	Ω
		V _{GS} = 10 V, I _D = 30 A, T _J = 175 °C			0.093	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	25			S
Dynamic ^b						
Input Capacitance	C _{iss}			5100		pF
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		480		
Reverse Transfer Capacitance	C _{rss}			210		
Total Gate Charge ^c	Q_g			90	130	nC
Gate-Source Charge ^c	Q_{gs}	V _{DS} = 100 V, V _{GS} = 10 V, I _D = 85 A		23		
Gate-Drain Charge ^c	Q_{gd}			34		
Turn-On Delay Time ^c	t _{d(on)}			24	35	ns
Rise Time ^c	t _r	$V_{DD} = 100 \text{ V}, R_L = 1.5 \Omega$		220	330	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D\cong 65$ A, V_{GEN} = 10 V, R_G = 2.5 Ω		45	70	
Fall Time ^c	t _f			200	300	
Source-Drain Diode Ratings and Ch	aracteristics	(T _C = 25 °C) ^b				
Continuous Current	Is				65	
Pulsed Current	I _{SM}				140	A
Forward Voltage ^a	V_{SD}	I _F = 65 A, V _{GS} = 0 V		1.0	1.5	٧
Reverse Recovery Time	t _{rr}			130	200	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 50 A, di/dt = 100 A/μs		8	12	Α
Reverse Recovery Charge	Q _{rr}			0.52	1.2	μC

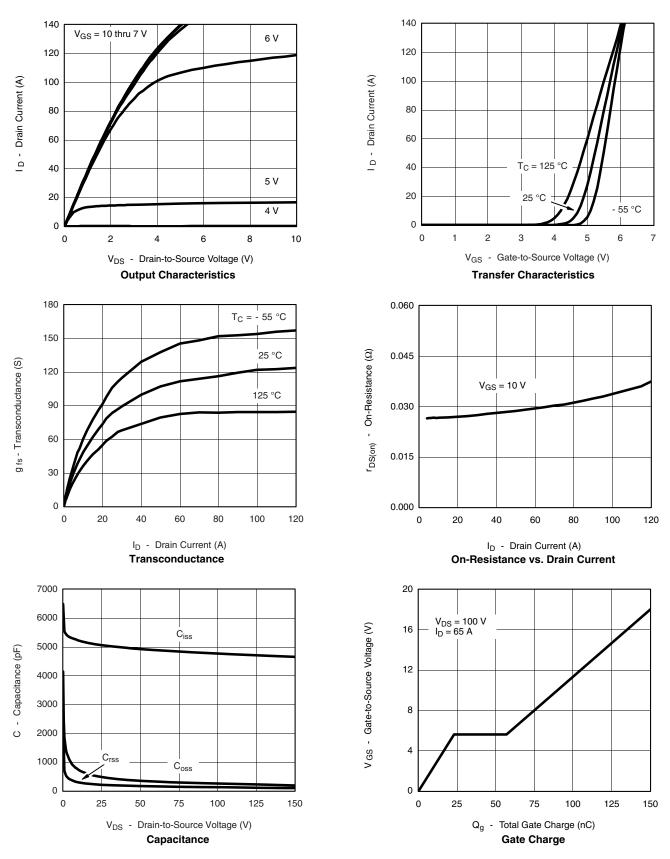
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



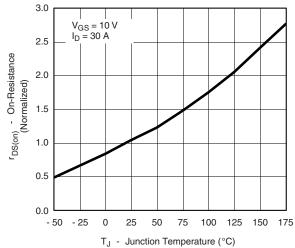
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



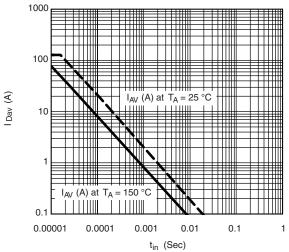
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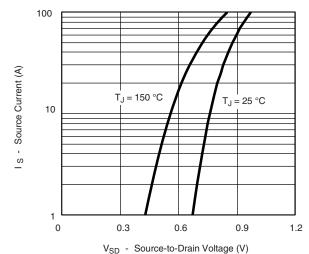
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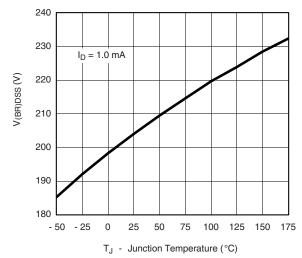
On-Resistance vs. Junction Temperature



Avalanche Current vs. Time



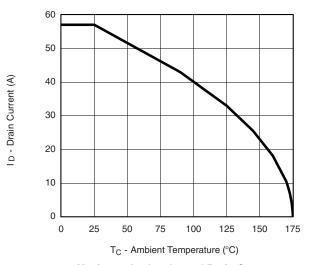
Source-Drain Diode Forward Voltage



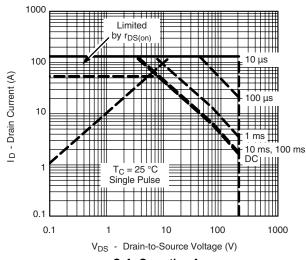
Drain Source Breakdown vs. Junction Temperature



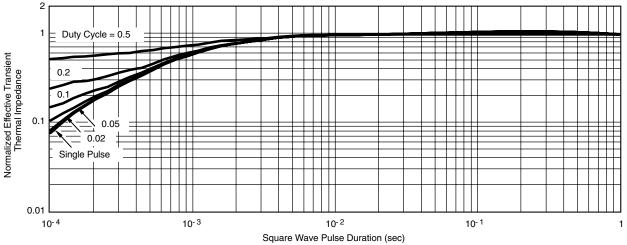
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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