

OptiMOSTM3 Power-Transistor

Features

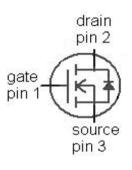
- N-channel, normal level
- Excellent gate charge x R_{DS(on)} product (FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating; RoHS compliant; Halogen free
- Qualified according to JEDEC¹⁾ for target application
- · Ideal for high-frequency switching and synchronous rectification
- •Halogen-free according to IEC61249-2-21

V_{DS}	150	٧
R _{DS(on),max (TO263)}	10.8	mΩ
I _D	83	Α





Туре	IPB108N15N3 G	IPP111N15N3 G	IPI111N15N3 G
	1 2 (tab)	123	123
Package	PG-TO263	PG-TO220-3	PG-TO262-3
Marking	108N15N	111N15N	111N15N



Maximum ratings, at T_i =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I _D	T _C =25 °C	83	А
		T _C =100 °C	59	
Pulsed drain current ²⁾	I _{D,pulse}	T _C =25 °C	332	
Avalanche energy, single pulse	E _{AS}	$I_{\rm D}$ =83 A, $R_{\rm GS}$ =25 Ω	330	mJ
Gate source voltage	V_{GS}		±20	V
Power dissipation	P_{tot}	T _C =25 °C	214	W
Operating and storage temperature	$T_{\rm j}, T_{\rm stg}$		-55 175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

¹⁾J-STD20 and JESD22

²⁾ See figure 3



IPB108N15N3 G IPP111N15N3 G

IPI111N15N3 G

Parameter	Symbol	Conditions	Values		Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	R_{thJC}		-	-	0.7	K/W
Thermal resistance, junction -	R_{thJA}	minimal footprint	-	-	62	
ambient		6 cm2 cooling area ³⁾	-	-	40	

Electrical characteristics, at T_j =25 °C, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =0 V, I _D =1 mA	150	-	-	V
Gate threshold voltage	$V_{\rm GS(th)}$	$V_{\rm DS} = V_{\rm GS}, I_{\rm D} = 160 \ \mu {\rm A}$	2	3	4	
Zero gate voltage drain current	I _{DSS}	$V_{\rm DS}$ =120 V, $V_{\rm GS}$ =0 V, $T_{\rm j}$ =25 °C	1	0.1	1	μA
		V _{DS} =120 V, V _{GS} =0 V, T _j =125 °C	-	10	100	
Gate-source leakage current	I _{GSS}	V _{GS} =20 V, V _{DS} =0 V	-	1	100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =83 A, (TO220; TO262)	1	9.4	11.1	mΩ
		V _{GS} =10 V, I _D =83 A, (TO263)	-	9.1	10.8	
		V _{GS} =8 V, I _D =41 A, (TO220; TO262)	-	9.5	11.3	
		V _{GS} =8 V, I _D =41 A, (TO263)	-	9.2	11	
Gate resistance	R_{G}		-	2.4	-	Ω
Transconductance	g_{fs}	$ V_{\rm DS} > 2 I_{\rm D} R_{\rm DS(on)max},$ $I_{\rm D} = 83~{\rm A}$	47	94	-	s

 $^{^{3)}}$ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm 2 (one layer, 70 μ m thick) copper area for drain connection. PCB is vertical in still air.



IPB108N15N3 G IPP111N15N3 G

IPI111N15N3 G

Parameter	Symbol	Conditions		Values		Uni
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	Ciss		-	3230	-	pF
Output capacitance	Coss	V _{GS} =0 V, V _{DS} =75 V, f=1 MHz	-	378	-	1
Reverse transfer capacitance	C _{rss}		-	7	-	1
Turn-on delay time	$t_{d(on)}$		-	17	-	ns
Rise time	t _r	V _{DD} =75 V, V _{GS} =10 V,	-	35	-	1
Turn-off delay time	$t_{d(off)}$	$I_{\rm D}$ =83 A, $R_{\rm G}$ =1.6 Ω	-	32	-	
Fall time	t_{f}		-	9	-	1
Gate Charge Characteristics ⁴⁾				ı		
Gate to source charge	Q _{gs}]	-	18	-	nC
Gate to drain charge	Q_{gd}	75.77.00.0	-	7	-	
Switching charge	Q_{sw}	V_{DD} =75 V, I_{D} =83 A, V_{GS} =0 to 10 V	-	16	-	
Gate charge total	Q_g		-	41	55	
Gate plateau voltage	V_{plateau}		-	5.7	-	٧
Output charge	Q _{oss}	V_{DD} =75 V, V_{GS} =0 V	-	106	141	nC
Reverse Diode						
Diode continous forward current	Is	T _25 °C	-	-	83	А
Diode pulse current	I _{S,pulse}	- T _C =25 °C	-	-	332	
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =83 A, T _j =25 °C	-	1	1.2	V
Reverse recovery time	t _{rr}	V _R =75 V, I _F =I _S ,	-	132	-	ns
Reverse recovery charge	Q _{rr}	di _F /dt=100 A/µs	_	415	-	nC

⁴⁾ See figure 16 for gate charge parameter definition

 $^{^{5)}}$ Plotted values correspond to IPP11N15N3 G and IPI111N15N3 G. Corresponding values for IPB108N15N3 G are $0.3m\Omega$ lower

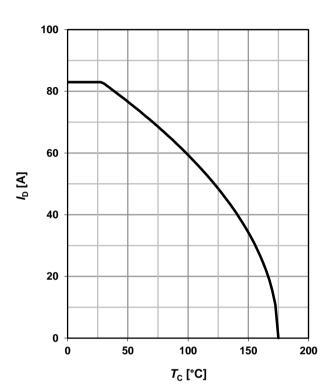


1 Power dissipation

$P_{\text{tot}} = f(T_{\text{C}})$

240 200 160 80 40 40 0 0 50 100 150 200 T_C [°C]

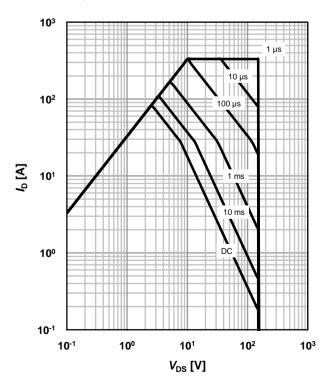
2 Drain current



3 Safe operating area

 $I_D=f(V_{DS}); T_C=25 \text{ °C}; D=0$

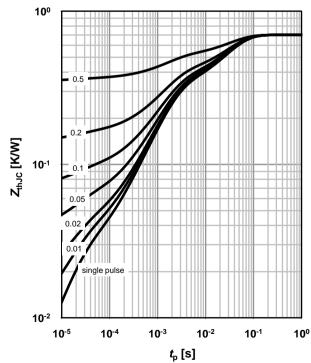
parameter: t_p



4 Max. transient thermal impedance

 $Z_{\text{thJC}} = f(t_p)$

parameter: $D=t_p/T$

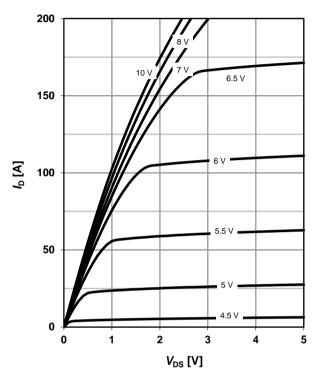




5 Typ. output characteristics

 $I_D=f(V_{DS}); T_i=25 °C$

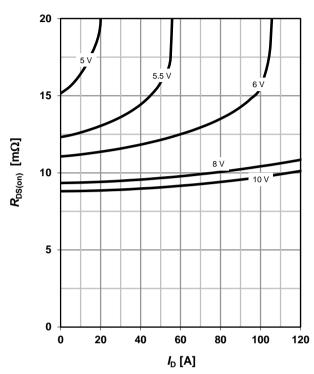
parameter: V_{GS}



6 Typ. drain-source on resistance 5)

 $R_{DS(on)}=f(I_D); T_i=25 \text{ °C}$

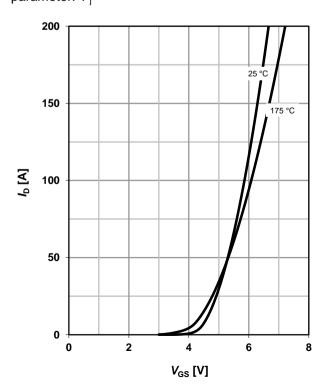
parameter: V_{GS}



7 Typ. transfer characteristics

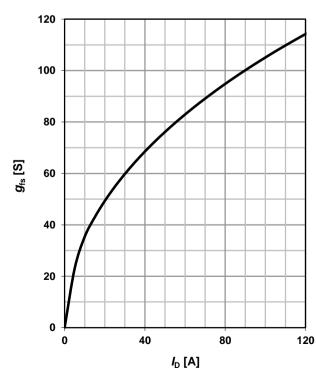
 $I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$

parameter: $T_{\rm j}$



8 Typ. forward transconductance

 g_{fs} =f(I_D); T_j =25 °C





9 Drain-source on-state resistance

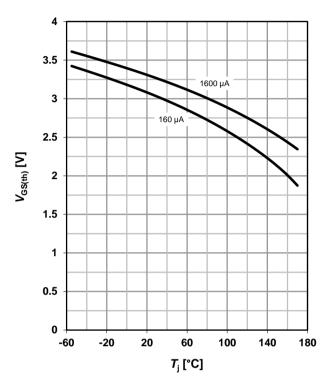
 $R_{DS(on)} = f(T_i); I_D = 83 \text{ A}; V_{GS} = 10 \text{ V}$

35 30 25 $R_{\mathrm{DS(on)}}$ [m Ω] 20 15 10 5 0 -60 -20 20 60 100 140 180 *T*_i [°C]

10 Typ. gate threshold voltage

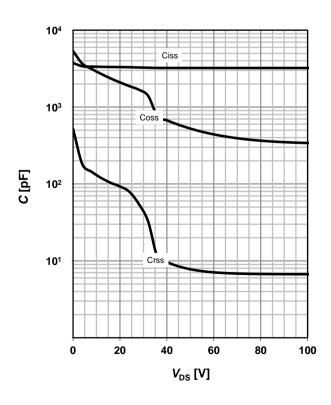
 $V_{GS(th)} = f(T_j); V_{GS} = V_{DS}$

parameter: I_D



11 Typ. capacitances

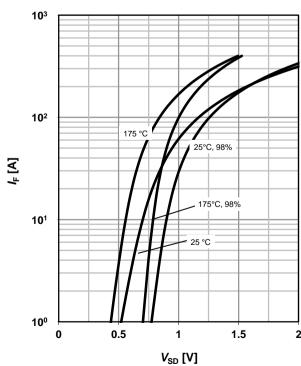
 $C=f(V_{DS}); V_{GS}=0 V; f=1 MHz$



12 Forward characteristics of reverse diode

 $I_F=f(V_{SD})$

parameter: T_i

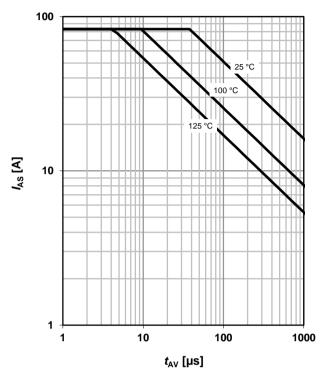




13 Avalanche characteristics

 $I_{AS}=f(t_{AV}); R_{GS}=25 \Omega$

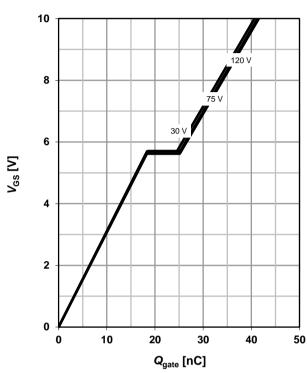
parameter: $T_{j(start)}$



14 Typ. gate charge

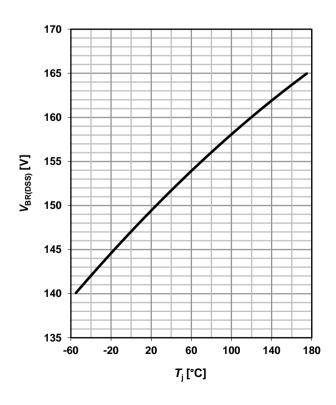
 V_{GS} =f(Q_{gate}); I_D =83 A pulsed

parameter: $V_{\rm DD}$

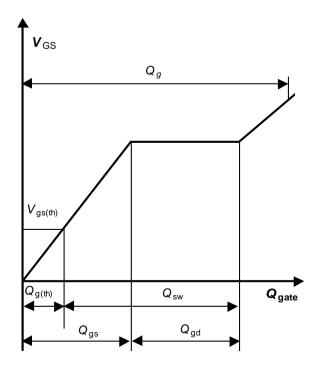


15 Drain-source breakdown voltage

 $V_{BR(DSS)}=f(T_j); I_D=1 \text{ mA}$

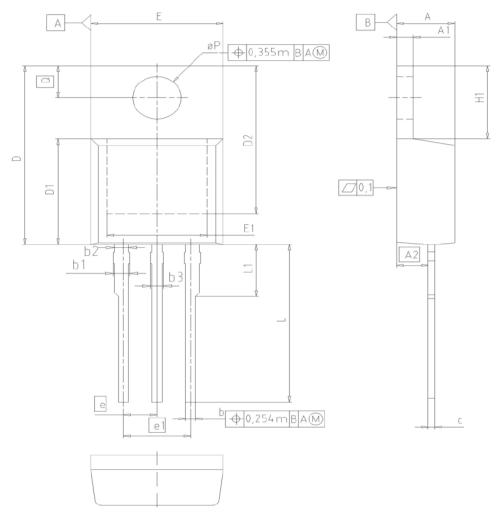


16 Gate charge waveforms





PG-TO220-3: Outline

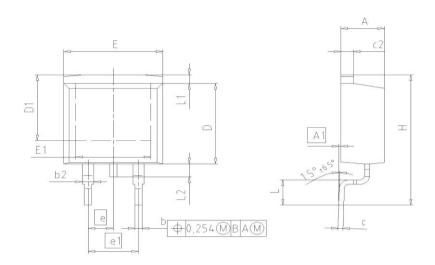


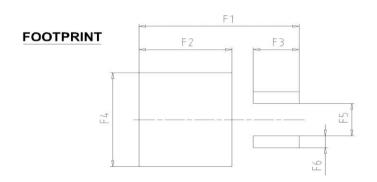
DIM	MILLIM	IETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	4.30	4.57	0.169	0.180	
A1	1.17	1.40	0.046	0.055	
A2	2.15	2.72	0.085	0.107	
b	0.65	0.86	0.026	0.034	
b1	0.95	1.40	0.037	0.055	
b2	0.95	1.15	0.037	0.045	
b3	0.65	1.15	0.026	0.045	
С	0.33	0.60	0.013	0.024	
D	14.81	15.95	0.583	0.628	
D1	8.51	9.45	0.335	0.372	
D2	12.19	13.10	0.480	0.516	
E	9.70	10.36	0.382	0.408	
E1	6.50	8.60	0.256	0.339	
е	2.5	54	0.1	100	
e1	5.0	08	0.2	200	
N		3	3		
H1	5.90	6.90	0.232	0.272	
L	13.00	14.00	0.512	0.551	
L1	-	4.80	-	0.189	
øΡ	3.60	3.89	0.142	0.153	
Q	2.60	3.00	0.102	0.118	

DOCUMEN Z8B0000	
SCALE	0
0 2.5	2.5 5mm
EUROPEAN P	ROJECTION
ISSUE D 23-08-2	
REVISI 05	ON

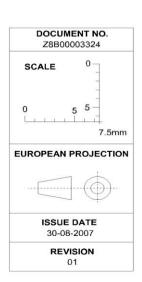


PG-TO263: Outline



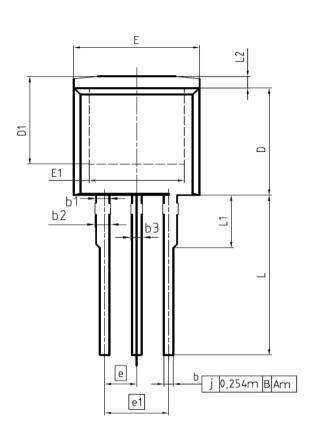


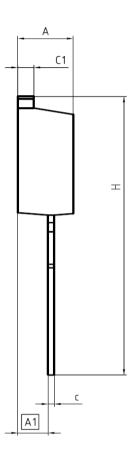
DIM	MILLIN	IETERS	INCI	HES
DIW	MIN	MAX	MIN	MAX
Α	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
С	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
е	2.5	54	0.1	100
e1	5.0	08	0.200	
N		2		2
Н	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057





PG-TO262-3: Outline





DIM	MILLIM	ETERS	INCH	IES
DIM	MIN	MAX	MIN	MAX
Α	4,300	4,572	0.169	0.180
A1	2.150	2,718	0.085	0.107
Ь	0.650	0.864	0.026	0.034
b1	0.950	1.093	0.037	0.043
b2	0.950	1,400	0.037	0.055
ь3	0.650	1.118	0.026	0.044
С	0.330	0.600	0.013	0.024
c1	1.170	1.400	0.046	0.055
D	8,509	9,450	0.335	0,372
D1	6.900	-	0.272	-
E	9.700	10.363	0.382	0.408
E1	6.500	8.600	0.256	0.339
е	2,5	540	0.100	
e1	5.080		0.200	
N	3		3	3
L	13.000	14.000	0.512	0.551
L1	-	4,800	-	0.189
L2	-	1.727	-	0.068

REFERENCE JEDEC TO262
SCALE 0
2.5- 0 2.5 Luuuuduuuu 5
EUROPEAN PROJECTION
ISSUE DATE 05-05-2006
FILE TO262_1



Published by Infineon Technologies AG 81726 Munich, Germany © 2009 Infineon Technologies AG All Rights Reserved.

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

on the types in question, please contact the nearest Infineon Technologies Office. Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.