

Single N-Channel Enhancement Mode MOSFET

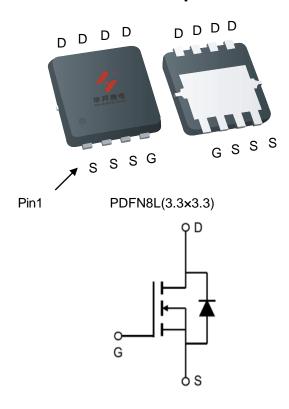
Feature

- 40V/50A $R_{DS(ON)}=6.2m\Omega$ (typ.) $@V_{GS}=10V$ $R_{DS(ON)}=9.5m\Omega$ (typ.) $@V_{GS}=4.5V$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

Applications

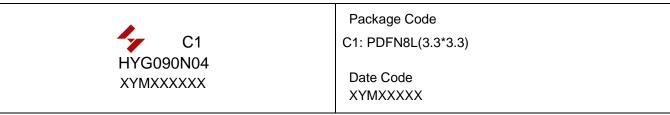
- Power Management for DC/DC
- Switching Application

Pin Description



Single N-Channel MOSFET

Ordering and Marking Information



Note:HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plateTermi-Nationfinish; which are fully compliant with RoHS.HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature.HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Rat	tings (Tc=25°C Unless Otherwise Noted)		<u>, </u>	
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		-55 to 175	°C
Tstg	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode) Tc=25°C		50	А
Mounted on	Large Heat Sink	•	,	1
Ідм	Pulsed Drain Current *	Tc=25°C	200	А
1	Outilities - Paris Outub	Tc=25°C	50	Α
lσ	Continuous Drain Current	Tc=100°C	30	Α
	M	Tc=25°C	34	W
PD	P _D Maximum Power Dissipation		11.6	W
R₀c	Thermal Resistance, Junction-to-Case		4.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		60	°C/W
Eas	SinglePulsed-Avalanche Energy *** L=0.3mH		50	mJ

- Repetitive rating: pulse width limited by max.junction temperature. Surface mounted on 1in2 FR-4 board.
- Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 4Ω , VGS =10V.

Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Symbol Parameter		Toot Conditions	HYC	HYG090N04LS1		l lmi4
Symbol	Parameter	Test Conditions		Тур.	Max	Unit
Static Cha	Static Characteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	40	-		V
Ipss	Drain to Source Leekage Current	VDS=40V,VGS=0V	-	-	1	μA
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	1	2.1	3	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA
RDS(ON)*	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =20A		6.2	7.5	mΩ
NDS(ON)	Diain-Source On-State Resistance	V _{GS} =4.5V,I _{DS} =20A		9.5	13	mΩ
Diode Characteristics						
Vsp*	Diode Forward Voltage	Isp=20A,Vgs=0V	-	0.91	1.2	V
t rr	Reverse Recovery Time	IsD=20A,dIsD/dt=100A/µ	-	10.9	-	ns
Qrr	Reverse Recovery Charge	s	-	4.1	-	nC

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Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Council of	Parameter	Tarak Orana Piti ana a	HYG090N04LS1			11
Symbol		Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V, Frequency=500KHz	-	2.1	-	Ω
Ciss	Input Capacitance	V _G s=0V,	-	798	-	
Coss	Output Capacitance	V _{DS} =25V,	-	164	-	pF
Crss	Reverse Transfer Capacitance	Frequency=500KHz		12.5	-	
td(ON)	Turn-on Delay Time		-	7.3	-	
Tr	Turn-on Rise Time	$V_{DD}=10V,R_{G}=4\Omega,$	-	21.8	-	ns
td(OFF)	Turn-off Delay Time	Ips=20A,Vgs=10V		14.9	-	115
Tf	Turn-off Fall Time		-	2.4	-	
Gate Cha	Gate Charge Characteristics					
Qg(10V)	Total Gate Charge		-	12.6	-	
Qg(4.5V)	Total Gate Charge	$V_{DS} = 32V, V_{GS} = 10V,$		5.8		»C
Qgs	Gate-Source Charge	I _D =20A	-	3.8	-	nC
Qgd	Gate-Drain Charge		-	1.5	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

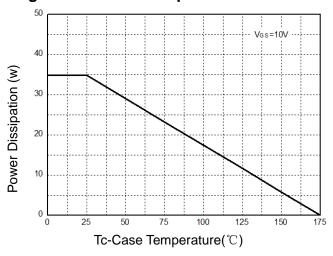
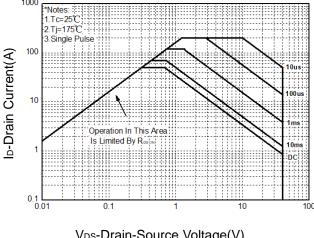


Figure 3: Safe Operation Area



V_{DS}-Drain-Source Voltage(V)

Figure 5: Output Characteristics

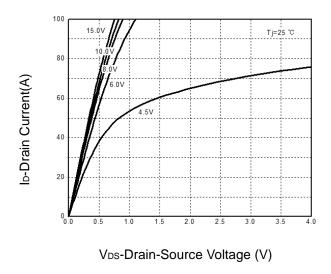


Figure 2: Drain Current

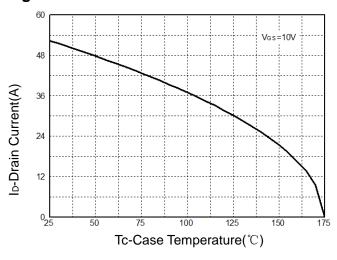
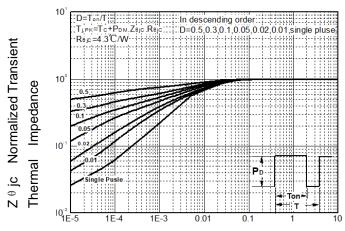
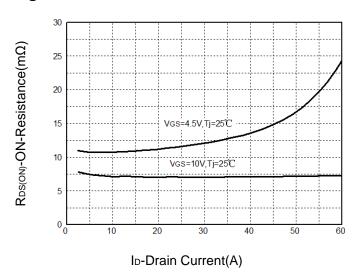


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

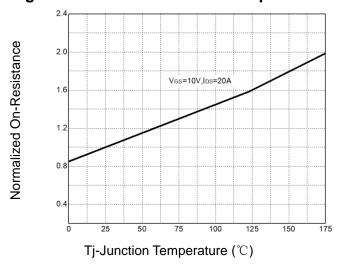


Figure 9: Capacitance Characteristics

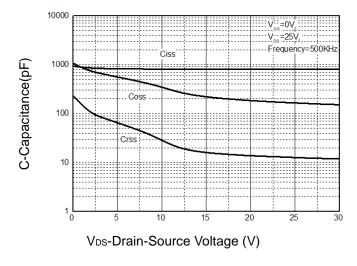


Figure 8: Source-Drain Diode Forward

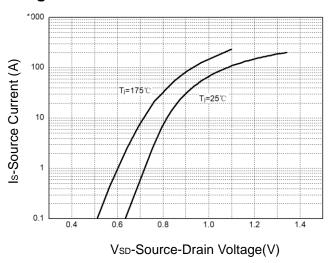
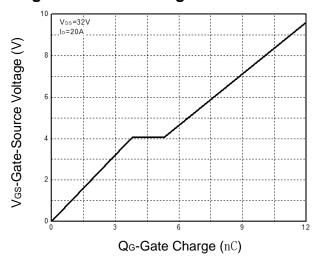
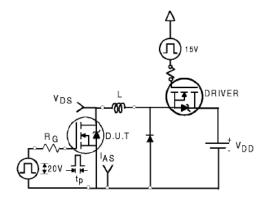


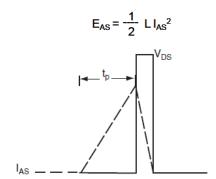
Figure 10: Gate Charge Characteristics



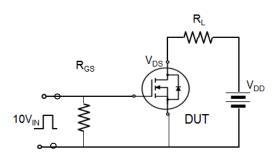


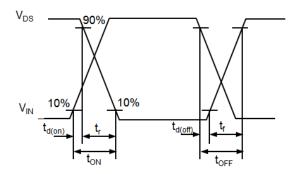
Avalanche Test Circuit



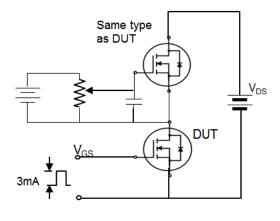


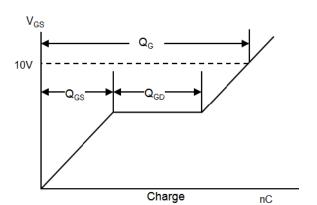
Switching Time Test Circuit





Gate Charge Test Circuit





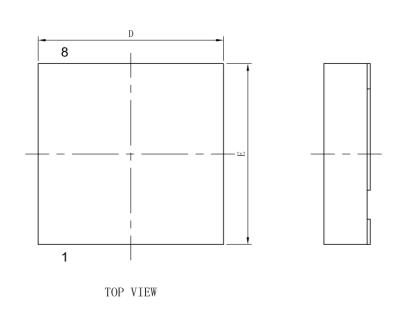


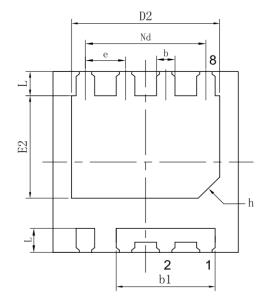
Device Per Unit

Package Type	Unit	Quantity
PDFN8L(3.3*3.3)	Reel	3000

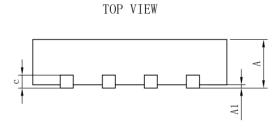
Package Information

PDFN8L(3.3*3.3)





BOTTOM VIEW



CVMDOL	MILLIMETER			
SYMBOL	MIN	NOM	MAX	
Α	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
b	0.25	0.30	0.35	
b1	1.55	1.60	165.00	
С	0.19	0.20	0.21	
D	2.90	3.00	3.10	
D2	2.30	2.40	2.50	
Nd	1.90	1.95	2.00	
E	2.90	3.00	3.10	
E2	1.60	1.70	1.80	
е	0.65bsc			
L	0.35	0.40	0.45	
h	0.30	0.35	0.40	



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak	100 °C	150 °C	
Temperature min (T _{smin})	150 °C	200 °C	
Temperature max (T _{smax})			
Time (Tsmin to Tsmax) (ts)	60-120 seconds	60-120 seconds	
Average ramp-up rate	3 °C/second max.	3°C/second max.	
(T _{smax} to T _P)	3 C/second max.		
Liquidous temperature (T _L)	183 °C	217 °C	
Time at liquidous (t _L)	60-150 seconds	60-150 seconds	
Peak package body Temperature	Soc Classification Town in table 1	SeeClassification Tempin table 2	
(T _p)*	See Classification Temp in table 1		
Time (t _P)** within 5°C of the specified	20** seconds	30** seconds	
classification temperature (T _c)	20 seconds	30 seconds	
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245℃
HTRB	JESD-22, A108	168/500/1000Hrs, Bias @ 150℃
HTGB	JESD-22, A108	168 /500/1000Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121℃
тст	JESD-22, A104	500 Cycles, -55℃~150℃

Customer Service

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