

STB76NF75, STI76NF75 STP76NF75

N-channel 75 V, 0.0095 Ω, 80 A TO-220, D²PAK, I²PAK STripFET™ II Power MOSFET

Features

Туре	V _{DSS}	R _{DS(on)} max	I _D
STB76NF75	75 V	< 0.011 Ω	80 A ⁽¹⁾
STI76NF75	75 V	< 0.011 Ω	80 A ⁽¹⁾
STP76NF75	75 V	< 0.011 Ω	80 A ⁽¹⁾

- 1. Current limited by package
- Exceptional dv/dt capability
- 100% avalanche tested

Application

- Switching applications
 - Automotive



This Power MOSFET is the latest development of STMicroelectronics unique "single feature size" strip-based process. The resulting transistor shows extremely high packing density for low onresistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

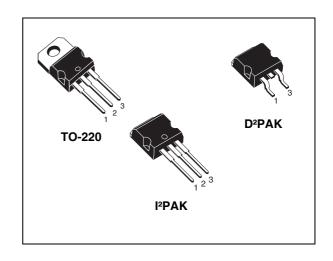


Figure 1. Internal schematic diagram

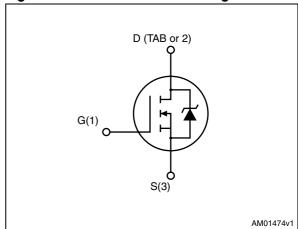


Table 1. Device summary

Order codes	Marking	Package	Packaging
STB76NF75	B76NF75	D ² PAK	Tape and reel
STI76NF75	176NF75	I ² PAK	Tube
STP76NF75	P76NF75	TO-220	Tube

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-source voltage (V _{GS} = 0)	75	V
V _{DGR}	Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)	75	V
V _{GS}	Gate-source voltage	± 20	V
I _D ⁽¹⁾	Drain current (continuous) at T _C = 25 °C	80	Α
I _D ⁽¹⁾	Drain current (continuous) at T _C =100 °C	70	Α
I _{DM} ⁽²⁾	Drain current (pulsed)	320	Α
P _{TOT}	Total dissipation at T _C = 25 °C	300	W
	Derating factor	2.0	W/°C
dv/dt (3)	Peak diode recovery voltage slope	12	V/ns
E _{AS} (4)	Single pulse avalanche energy	700	mJ
T _J T _{stg}	Operating junction temperature Storage temperature	-55 to 175	°C

^{1.} Current limited by package

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case max	0.5	°C/W
R _{thJA}	Thermal resistance junction-ambient max	62.5	°C/W
T _I	Maximum lead temperature for soldering purpose ⁽¹⁾	300	°C

^{1. 1.6}mm from case for 10 sec

^{2.} Pulse width limited by safe operating area

^{3.} $I_{SD} \leq$ 80 A, di/dt \leq 300 A/ μ s, $V_{DD} \leq$ $V_{(BR)DSS}$, $T_{j} \leq$ T_{JMAX}

^{4.} Starting $T_J = 25~^{\circ}C$, $I_D = 40~A$, $V_{DD} = 37.5~V$

2 Electrical characteristics

(T_{CASE}=25 °C unless otherwise specified)

Table 4. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$I_D = 250 \mu A, V_{GS} = 0$	75			V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V_{DS} = Max rating, V_{DS} = Max rating @125 °C			1 10	μ Α μ Α
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ± 20 V			±100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	2	3	4	V
R _{DS(on)}	Static drain-source on resistance	V _{GS} = 10 V, I _D = 40 A		0.0095	0.011	W

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
g _{fs} ⁽¹⁾	Forward transconductance	$V_{DS} = 15 \text{ V}, I_{D} = 40 \text{ A}$	-	20		S
C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 25 \text{ V, f} = 1 \text{ MHz,}$ $V_{GS} = 0$	-	3700 730 240		pF pF pF
Q _g Q _{gs} Q _{gd}	Total gate charge Gate-source charge Gate-drain charge	V _{DD} = 60 V, I _D = 80 A V _{GS} =10 V	-	117 27 47	160	nC nC nC

^{1.} Pulsed: pulse duration = 300 μ s, duty cycle 1.5%

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r t _{d(off)} t _f	Turn-on delay time Rise time Turn-off delay time Fall time	V_{DD} = 37.5 V, I_{D} = 45 A, R_{G} =4.7 Ω , V_{GS} =10 V Figure 14 on page 8	-	25 100 66 30	-	ns ns ns ns

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min	Тур.	Max	Unit
I _{SD}	Source-drain current		-		80	Α
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)		-		320	Α
V _{SD} ⁽²⁾	Forward on voltage	$I_{SD} = 80 \text{ A}, V_{GS} = 0$	-		1.5	V
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I_{SD} = 80 A, di/dt = 100 A/ μ s, V_{DD} = 25 V, T_{J} = 150 °C Figure 16 on page 8	-	132 660 10		ns nC A

^{1.} Pulse width limited by safe operating area

^{2.} Pulsed: pulse duration=300µs, duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

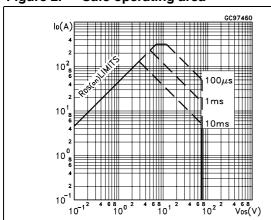


Figure 3. Thermal impedance

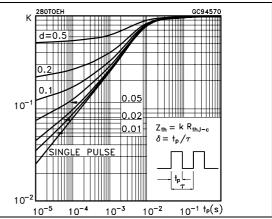


Figure 4. Output characteristics

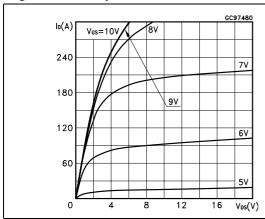


Figure 5. Transfer characteristics

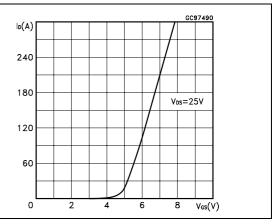


Figure 6. Transconductance

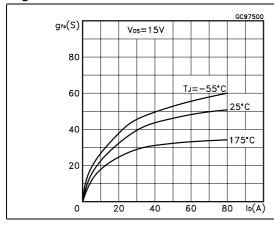
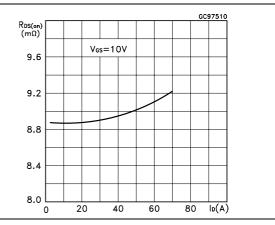


Figure 7. Static drain-source on resistance



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GC97530 GC97520 C(pF) Vgs(V) f=1MHz Vgs=0V V_{DS}=60V 10=80A 8000 12 6000 4000 2000 30 60 90 120 Q₉(nC) 0 10 V_{DS}(V)

Figure 8. Gate charge vs gate-source voltage Figure 9. Capacitance variations

Figure 10. Normalized gate threshold voltage Figure 11. Normalized on resistance vs vs temperature temperature

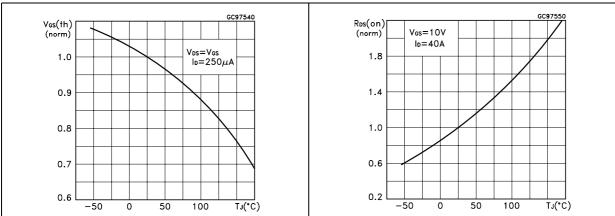
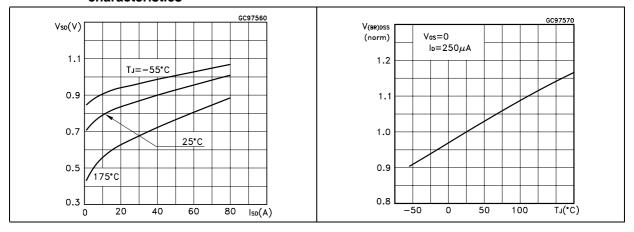


Figure 12. Source-drain diode forward characteristics

Figure 13. Normalized B_{VDSS} vs temperature



3 Test circuits

Figure 14. Switching times test circuit for resistive load

Figure 15. Gate charge test circuit

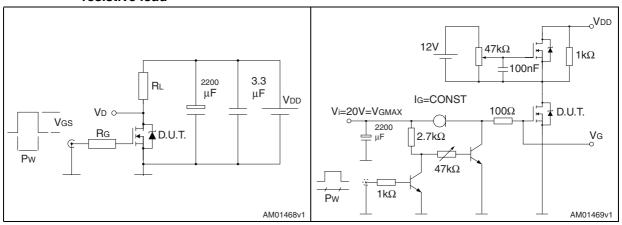


Figure 16. Test circuit for inductive load switching and diode recovery times

Figure 17. Unclamped inductive load test circuit

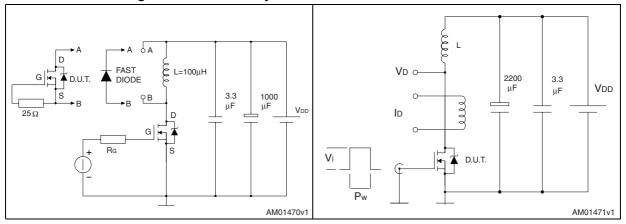
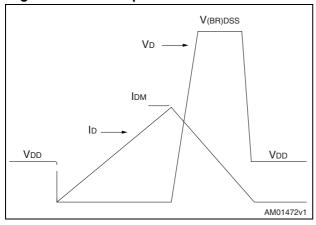


Figure 18. Unclamped inductive waveform



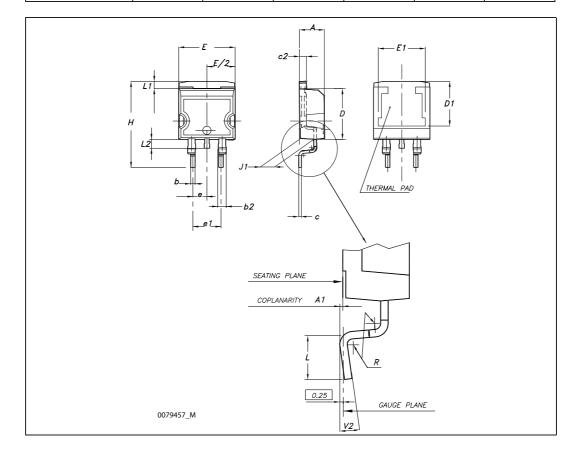
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4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

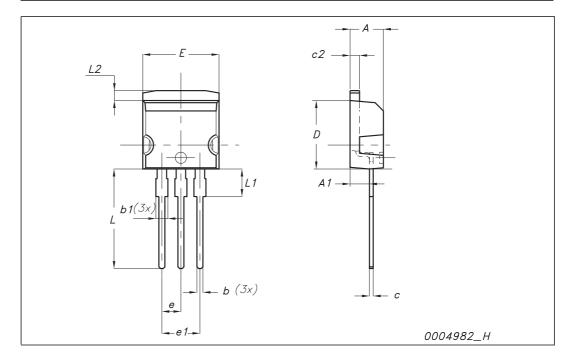
D²PAK (TO-263) mechanical data

Dim		mm			inch		
DIIII	Min	Тур	Max	Min	Тур	Max	
А	4.40		4.60	0.173		0.181	
A1	0.03		0.23	0.001		0.009	
b	0.70		0.93	0.027		0.037	
b2	1.14		1.70	0.045		0.067	
С	0.45		0.60	0.017		0.024	
c2	1.23		1.36	0.048		0.053	
D	8.95		9.35	0.352		0.368	
D1	7.50			0.295			
E	10		10.40	0.394		0.409	
E1	8.50			0.334			
е		2.54			0.1		
e1	4.88		5.28	0.192		0.208	
Н	15		15.85	0.590		0.624	
J1	2.49		2.69	0.099		0.106	
L	2.29		2.79	0.090		0.110	
L1	1.27		1.40	0.05		0.055	
L2	1.30		1.75	0.051		0.069	
R		0.4			0.016		
V2	0°		8°	0°		8°	



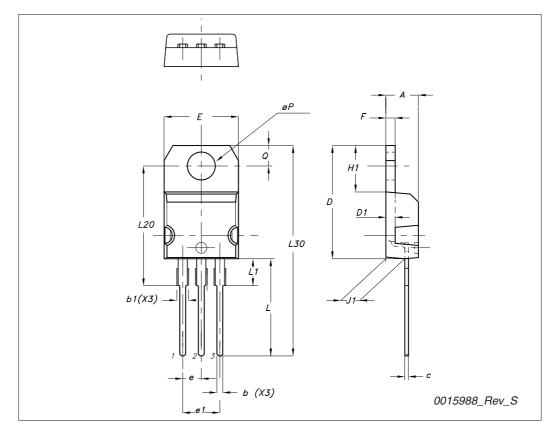
I²PAK (TO-262) mechanical data

Dim		mm			inch		
Dim	Min	Тур	Max	Min	Тур	Max	
А	4.40		4.60	0.173		0.181	
A1	2.40		2.72	0.094		0.107	
b	0.61		0.88	0.024		0.034	
b1	1.14		1.70	0.044		0.066	
С	0.49		0.70	0.019		0.027	
c2	1.23		1.32	0.048		0.052	
D	8.95		9.35	0.352		0.368	
е	2.40		2.70	0.094		0.106	
e1	4.95		5.15	0.194		0.202	
E	10		10.40	0.393		0.410	
L	13		14	0.511		0.551	
L1	3.50		3.93	0.137		0.154	
L2	1.27		1.40	0.050		0.055	



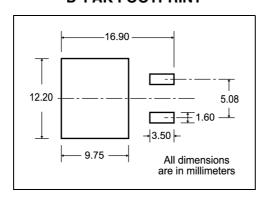
TO-220 type A mechanical data

Dim	mm				
Dim	Min	Тур	Max		
A	4.40		4.60		
b	0.61		0.88		
b1	1.14		1.70		
С	0.48		0.70		
D	15.25		15.75		
D1		1.27			
E	10		10.40		
е	2.40		2.70		
e1	4.95		5.15		
F	1.23		1.32		
H1	6.20		6.60		
J1	2.40		2.72		
L	13		14		
L1	3.50		3.93		
L20		16.40			
L30		28.90			
ØP	3.75		3.85		
Q	2.65		2.95		

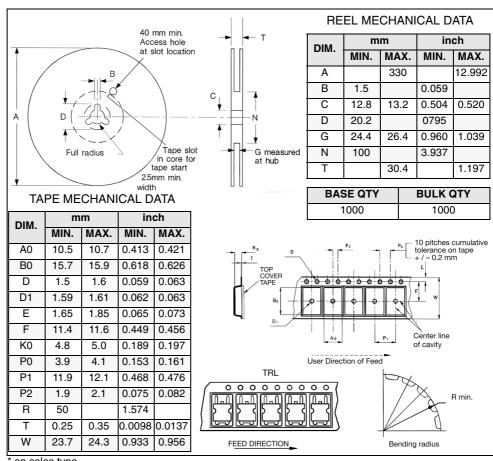


5 Packaging mechanical data

D²PAK FOOTPRINT



TAPE AND REEL SHIPMENT



* on sales type

6 Revision history

Table 8. Document revision history

Date	Revision	Changes
25-Jul-2007	1	Initial release
14-Dec-2009	2	Added new package, mechanical data: I ² PAK

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