

Single N-Channel Enhancement Mode MOSFET

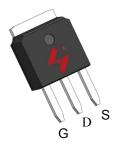
Feature

30V/85A

 $R_{DS(ON)}$ =3.3 m Ω (typ.) @VGS = 10V $R_{DS(ON)}$ =4.6 m Ω (typ.) @VGS = 4.5V

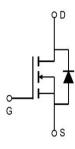
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free Devices Available

Pin Description



Applications

- Load Switch
- Lithium battery protect board



Single N-Channel MOSFET

Ordering and Marking Information



Package Code

V: TO-251-3S

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit			
Common Rat	Common Ratings (Tc=25°C Unless Otherwise Noted)					
VDSS	Drain-Source Voltage		30	V		
Vgss	Gate-Source Voltage		±20	V		
TJ	Junction Temperature Range		-55 to 175	$^{\circ}$		
Tstg	Storage Temperature Range		-55 to 175	$^{\circ}$		
Is	Source Current-Continuous(Body Diode)		85	Α		
Mounted on I	Large Heat Sink					
І дм	Pulsed Drain Current *	Tc=25℃	300	А		
1	Cantinua Dania Cumant	Tc=25℃	85	А		
lσ	Continuous Drain Current	Tc=100°C	60	А		
-	Mariana Barra Bioriantina	Tc=25℃	57.6	W		
P□	Maximum Power Dissipation Tc=100℃		28.8	W		
R₀Jc	Thermal Resistance, Junction-to-Case		2.6	°C/W		
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		60.0	°C/W		
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		160***	mJ		

Note: * Repetitive rating; pulse width limited by max.junction temperature.
** Surface mounted on FR-4 board.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Cumbal	Davamatar.	Test Conditions		HYG045N03LA1		11	
Symbol	Parameter			Min	Тур.	Max	Unit
Static Cha	racteristics						
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =2	250µA	30	-	-	V
Inno	loss Drain-to-Source Leakage Current		=0V	-	-	1	μΑ
IDSS			TJ=125℃	-	-	50	μΑ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA		1.0	1.6	3.0	V
Igss	Gate-Source Leakage Current	V _{GS} =±20V,V _{DS} =0V		-	-	±100	nA
Descent	Drain Source On State Registeres	V _{GS} =10V,I _{DS}	=20A	-	3.3	4.0	mΩ
Rds(on)	Drain-Source On-State Resistance	V _{GS} =4.5V,I _{DS}	=20A	-	4.6	5.5	mΩ
Diode Cha	Diode Characteristics						
V _{SD} *	Diode Forward Voltage	I _{SD} =20A,V _{GS} =0V		-	8.0	1.1	V
trr	Reverse Recovery Time	Isp=20A,dIsp/dt=100A/μs		-	13	-	ns
Qrr	Reverse Recovery Charge			-	5.3	-	nC

Limited by TJmax , starting TJ=25 $^{\circ}$ C, L = 0.3mH, Rg =25 Ω ., Vgs =10V.

HYG045N03LA1V



Electrical Characteristics (Cont.) (Tc =25 °C Unless Otherwise Noted)

Comple ed	Parameter	Took Conditions	HY	HYG045N03LA1		
Symbol		Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics		•			
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	4.5	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	2166	-	
Coss	Output Capacitance	V _{DS} =25V,	-	267	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	223	-	
td(ON)	Turn-on Delay Time		-	15.0	-	
Tr	Turn-on Rise Time	V_{DD} =15 V , R_{G} =4 Ω ,	-	55.7	-	
t d(OFF)	Turn-off Delay Time	lps=20A,Vgs=10V	-	60.4	-	ns
Tf	Turn-off Fall Time		-	71.8	-	
Gate Cha	Gate Charge Characteristics					•
Qg	Total Gate Charge (V _{GS} =10V)		-	47.8	-	
Qg	Total Gate Charge (V _{GS} =4.5V)	V _{DS} =24V, V _{GS} =10V,	-	23.8	-	·- O
Qgs	Gate-Source Charge	I _D =20A	-	7.9	-	nC
Qgd	Gate-Drain Charge		-	11.1	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

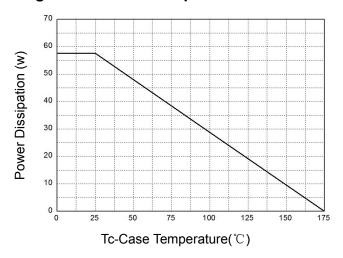
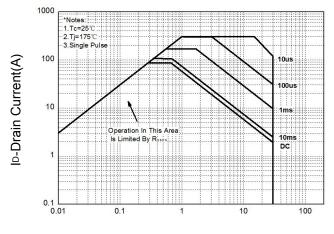


Figure 3: Safe Operation Area



Vps-Drain-Source Voltage(V)

Figure 5: Output Characteristics

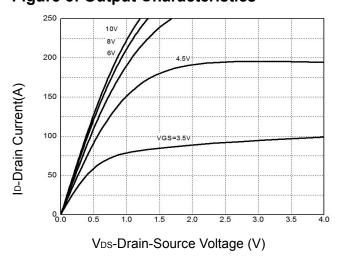


Figure 2: Drain Current

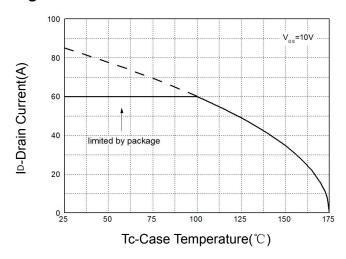
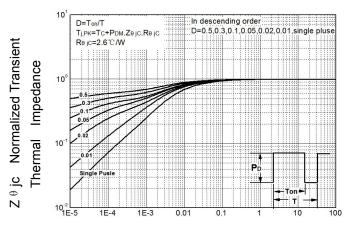
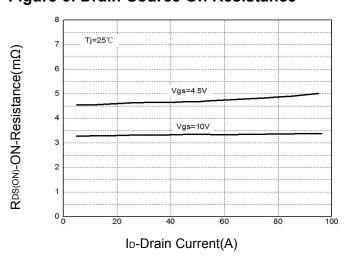


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance



4



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

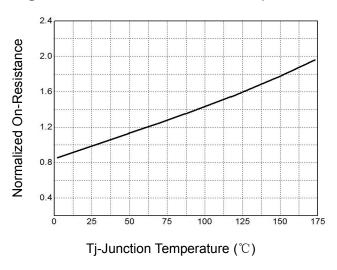
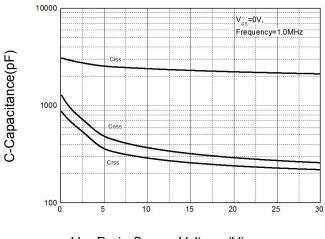


Figure 9: Capacitance Characteristics



VDS-Drain-Source Voltage (V)

Figure 8: Source-Drain Diode Forward

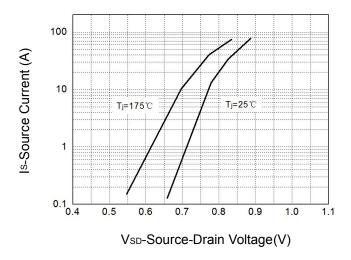
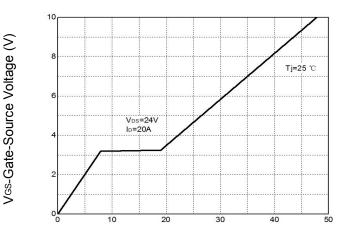


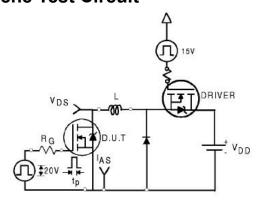
Figure 10: Gate Charge Characteristics

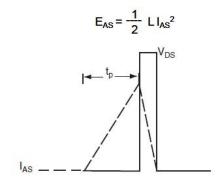


QG-Gate Charge (nC)

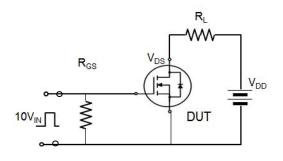


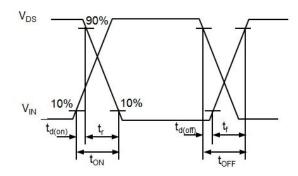
Avalanche Test Circuit



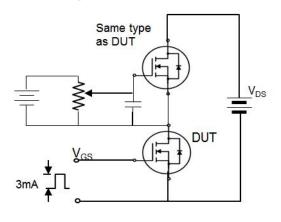


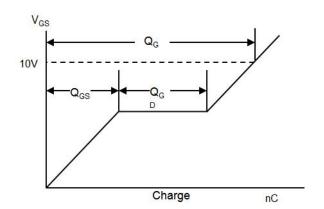
Switching Time Test Circuit





Gate Charge Test Circuit





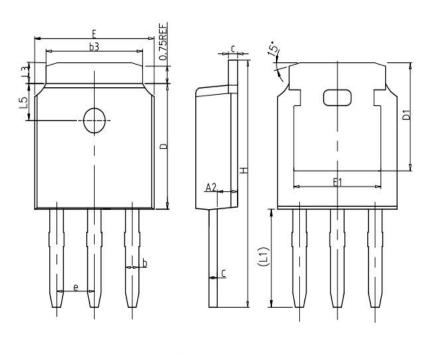


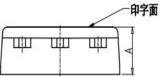
Device Per Unit

Package Type	Unit	Quantity
TO-251-3S	Tube	75

Package Information

TO-251-3S



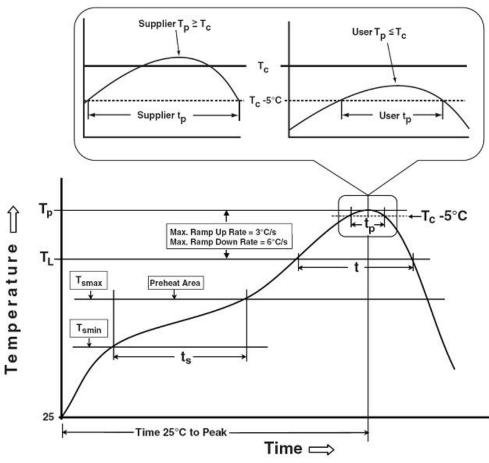


COMMON DIMENSIONS

SYMBOL	mm			
STIVIBUL	MIN	NOM	MAX	
Α	2.20	2.30	2.40	
A2	0.97	1.07	1.17	
b	0.68	0.78	0.90	
b3	5.20	5.33	5.50	
С	0.43	0.53	0.63	
D	5.98	6.10	6.22	
D1	5.30REF			
E	6.40	6.60	6.80	
E1	4.63	-	-	
е	2.286BSC			
Н	10.00	11.22	11.44	
L1	3.90	4.10	4.30	
L3	0.88	1.02	1.28	
L5	1.65	1.80	1.95	



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak Temperature min (T _{smin})	100 ℃	150 ℃	
Temperature max (T _{smax}) Time (Tsmin to Tsmax) (t _s)	150 °C 60-120 seconds	200 °C 60-120 seconds	
Average ramp-up rate (T _{smax} to T _P)	3 °C/second max.	3℃/second max.	
Liquidous temperature (T∟)	183 ℃	217 ℃	
Time at liquidous (t∟)	60-150 seconds	60-150 seconds	
Peak package body Temperature (T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2	
Time (t _P)** within 5°C of the specified classification temperature (T _c)	20** seconds	30** seconds	
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 ℃/second max.	
Time 25℃ to peak temperature	6 minutes max.	8 minutes max.	
*Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.			

** Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 ℃	220 °C
≥2.5 mm	220 ℃	220 ℃

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 ℃	260 ℃	260 ℃
1.6 mm – 2.5 mm	260 ℃	250 ℃	245 ℃
≥2.5 mm	250 ℃	245 ℃	245 ℃

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, V _{gs} 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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