

# X4-Class Power MOSFET™

## IXTP94N20X4

= 200V94A 10.6m $\Omega$ 

N-Channel Enhancement Mode Avalanche Rated



TO-220 (IXTP)	
	D <sub>S</sub> D (Tab)

G = Gate	D	= Drain
S = Source	Tab	= Drain

Test Conditions	Maximum Ra	atings
T <sub>J</sub> = 25°C to 175°C	200	V
$T_J = 25$ °C to 175°C, $R_{GS} = 1M\Omega$	200	V
Continuous	±20	V
Transient	±30	V
T <sub>c</sub> = 25°C	94	A
$T_{\rm c}$ = 25°C, Pulse Width Limited by $T_{\rm JM}$	220	Α
T <sub>c</sub> = 25°C	47	Α
$T_{c} = 25^{\circ}C$	1	J
$I_{_{S}} \le I_{_{DM}}, V_{_{DD}} \le V_{_{DSS}}, T_{_{J}} \le 150^{\circ}C$	20	V/ns
T <sub>c</sub> = 25°C	360	W
	-55 +175	°C
	175	°C
	-55 <b>+</b> 15	°C
Maximum Lead Temperature for Soldering	300	°C
1.6 mm (0.062 in.) from Case for 10s		
Mounting Torque	1.13 / 10	Nm/lb.in
	3	g
	$\begin{split} &T_{_J}=25^\circ\text{C to }175^\circ\text{C}\\ &T_{_J}=25^\circ\text{C to }175^\circ\text{C},R_{_{GS}}=1\text{M}\Omega\\ &\text{Continuous}\\ &\text{Transient}\\ &T_{_C}=25^\circ\text{C}\\ &T_{_C}=25^\circ\text{C},\text{Pulse Width Limited by }T_{_{JM}}\\ &T_{_C}=25^\circ\text{C}\\ &T_{_C}=25^\circ\text{C}\\ &I_{_S}\leq I_{_{DM}},V_{_{DD}}\leq V_{_{DSS}},T_{_J}\leq 150^\circ\text{C}\\ &T_{_C}=25^\circ\text{C}\\ &I_{_C}=25^\circ\text{C}\\ &I_{_{C}}=25^\circ\text{C}\\ &I_{$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

#### **Features**

- International Standard Package
- Low  $R_{DS(ON)}$  and  $Q_G$  Avalanche Rated
- Low Package Inductance

#### **Advantages**

- High Power Density
- Easy to Mount
- Space Savings

### **Applications**

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

		Charae Min.	cteristic Typ.	etic Values p. <sub> </sub> Max.	
BV <sub>DSS</sub>	$V_{GS} = 0V, I_{D} = 250\mu A$	200			V
V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V
I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
I <sub>DSS</sub>	$V_{DS} = V_{DSS}, V_{GS} = 0V$ $T_{J} = 150^{\circ}C$			20 500	μA μA
R <sub>DS(on)</sub>	$V_{GS} = 10V$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1			10.6	mΩ

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Symbol	Test Conditions	Char	acteristic	: Values
$(T_{J} = 25^{\circ}C, L)$	Inless Otherwise Specified)	Min.	Тур.	Max
g <sub>fs</sub>	$V_{DS} = 10V, I_{D} = 0.5 \cdot I_{D25}, Note 1$	60	100	S
$R_{Gi}$	Gate Input Resistance		5.3	Ω
C <sub>iss</sub>			5050	pF
C <sub>oss</sub>	$V_{GS} = 0V$ , $V_{DS} = 25V$ , $f = 1MHz$		750	pF
C <sub>rss</sub>			4	pF
	Effective Output Capacitance			
C <sub>o(er)</sub>	Energy related $\bigvee_{GS} = 0V$		390	pF
$C_{o(tr)}$	Time related $\int_{DS} V_{DS} = 0.8 \cdot V_{DSS}$		1670	pF
t <sub>d(on)</sub>	Resistive Switching Times		18	ns
t <sub>r</sub>	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 0.5 \cdot I_{D25}$		9	ns
t <sub>d(off)</sub>	50 50 5		97	ns
$\mathbf{t}_{f}$	$R_{\rm G} = 5\Omega$ (External)		7	ns
$Q_{g(on)}$			77	nC
Q <sub>gs</sub>	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 0.5 \cdot I_{D25}$		21	nC
$\mathbf{Q}_{gd}$			25	nC
R <sub>thJC</sub>				0.42 °C/W
R <sub>thCS</sub>			0.50	°C/W

#### Source-Drain Diode

Symbol	Test Conditions	Chara	cteristic	Values	
$(T_{J} = 25^{\circ}C, l)$	Jnless Otherwise Specified)	Min.	Тур.	Max	
l <sub>s</sub>	$V_{GS} = 0V$			94	Α
I <sub>SM</sub>	Repetitive, pulse Width Limited by $T_{_{\rm JM}}$			376	Α
V <sub>sD</sub>	$I_F = I_S$ , $V_{GS} = 0V$ , Note 1			1.4	V
$\left. egin{array}{c} oldsymbol{t_{rr}} \ oldsymbol{Q_{RM}} \ oldsymbol{I_{RM}} \end{array}  ight.  ight.$	$I_F = 47A$ , -di/dt = 200A/ $\mu$ s $V_R = 100V$		130 1.1 17.0		ns µC A

Note 1. Pulse test,  $t \le 300 \mu s$ , duty cycle,  $d \le 2\%$ .



Fig. 1. Output Characteristics @ T<sub>.I</sub> = 25°C

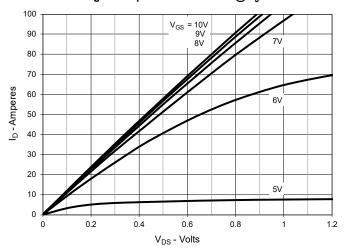


Fig. 3. Output Characteristics @  $T_J = 150$ °C

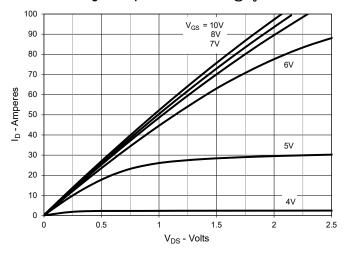


Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D$  = 47A Value vs. Drain Current

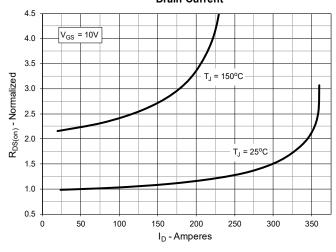


Fig. 2. Extended Output Characteristics @ T<sub>J</sub> = 25°C

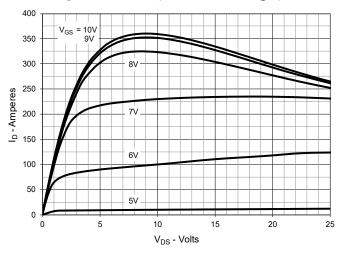


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D$  = 47A Value vs. Junction Temperature

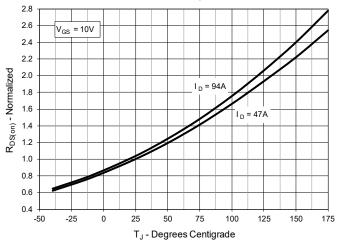


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature

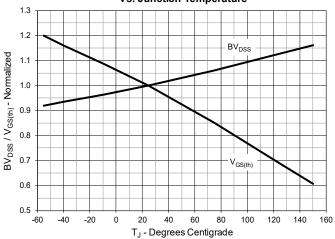




Fig. 7. Maximum Drain Current vs. Case Temperature

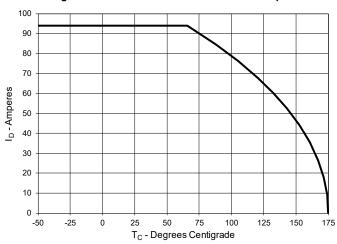


Fig. 8. Input Admittance

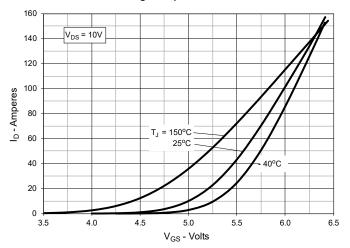


Fig. 9. Transconductance

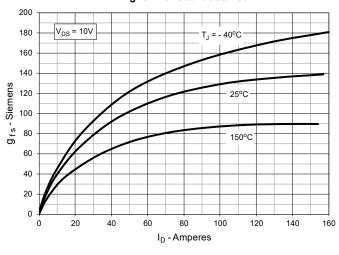


Fig. 10. Forward Voltage Drop of Intrinsic Diode

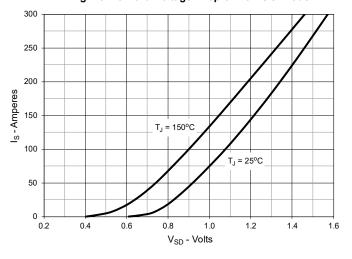


Fig. 11. Gate Charge

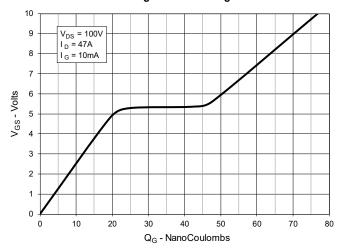
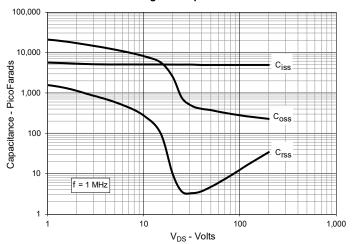
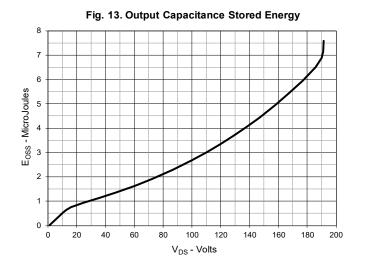


Fig. 12. Capacitance



Littelfuse reserves the right to change limits, test conditions and dimensions.





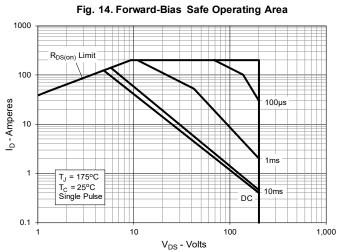
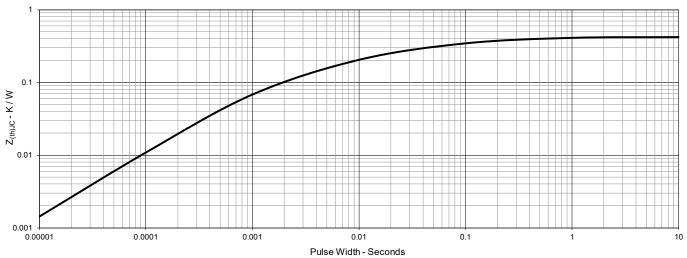
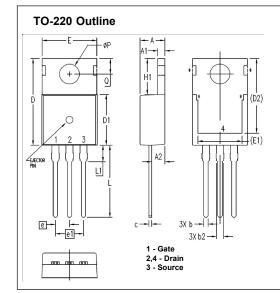


Fig. 15. Maximum Transient Thermal Impedance



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SYM	INCHES		MILLIMETERS	
2114	MIN	MAX	MIN	MAX
Α	.169	.185	4.30	4.70
A1	.047	.055	1.20	1.40
A2	.079	.106	2.00	2.70
Ь	.024	.039	0.60	1.00
b2	.045	.057	1.15	1.45
С	.014	.026	0.35	0.65
D	.587	.626	14.90	15.90
D1	.335	.370	8.50	9.40
(D2)	.500	.531	12.70	13.50
E	.382	.406	9.70	10.30
(E1)	.283	.323	7.20	8.20
е	.100 BSC		SC 2.54 BSC	
e1	.200 BSC		5.08 BSC	
H1	.244	.268	6.20	6.80
L	.492	.547	12.50	13.90
L1	.110	.154	2.80	3.90
ØΡ	.134	.150	3.40	3.80
Q	.106	.126	2.70	3.20







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