

Final datasheet

EasyPACK™ module with CoolSiC™ Trench MOSFET and High Current Pin / NTC

Features

- · Electrical features
 - V_{DSS} = 2000 V
 - $I_{DN} = 400 \text{ A} / I_{DRM} = 640 \text{ A}$
 - Low switching losses
 - High current density
 - Suitable Infineon gate drivers can be found under https://www.infineon.com/gdfinder
- Mechanical features
 - 3.2 kV AC 1 minute insulation
 - High current pin
 - PressFIT contact technology
 - Rugged mounting due to integrated mounting clamps
 - Integrated NTC temperature sensor

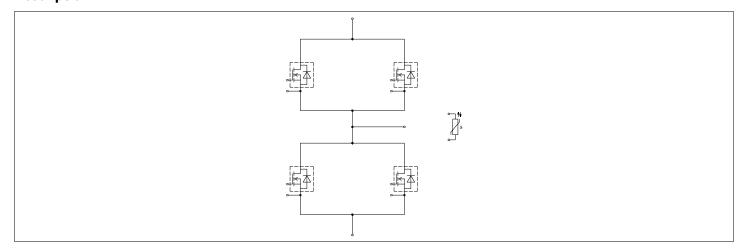
Potential applications

- Energy storage systems (ESS)
- EV charging
- UPS systems
- · Solar applications

Product validation

• Qualified for industrial applications according to the relevant tests of IEC 60747, 60749 and 60068

Description





EasyPACK™ module

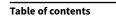




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EasyPACK™ module

1 Package



1 Package

Table 1 Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS, f = 50 Hz, t = 1 min	3.2	kV
Isolation test voltage NTC	V _{ISOL(NTC)}	RMS, f = 50 Hz, t = 1 min	3.2	kV
Internal isolation		basic insulation (class 1, IEC 61140)	Al ₂ O ₃	
Comparative tracking index	СТІ		> 400	
Relative thermal index (electrical)	RTI	housing	140	°C

Table 2 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур.	Max.	
Stray inductance module	L _{sCE}				17		nH
Module lead resistance, terminals - chip	R _{CC'+EE'}	T _H = 25 °C, per switch			1		mΩ
Storage temperature	$T_{\rm stg}$			-40		125	°C
Mounting torque for module mounting	М	- Mounting according to valid application note	M5, Screw	1.3		1.5	Nm
Weight	G				78		g

Note: The current under continuous operation is limited to 50A rms per high current pin.

2 MOSFET, T1.1-T1.2 / T2.1-T2.2

Table 3 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Drain-source voltage	V_{DSS}		T _{vj} = 25 °C	2000	V
Implemented drain current	I _{DN}			400	А
Continuous DC drain current	I _{DDC}	$T_{\rm vj}$ = 175 °C, $V_{\rm GS}$ = 18 V	T _H = 65 °C	275	А
Repetitive peak drain current	/ _{DRM}	verified by design, t _p limited by T _{vjmax}		640	А
Gate-source voltage, max. transient voltage	V_{GS}	D < 0.01		-10/23	V
Gate-source voltage, max. static voltage	V_{GS}			-7/20	V

EasyPACK™ module

2 MOSFET, T1.1-T1.2 / T2.1-T2.2



Table 4 Recommended values

Parameter	Symbol	Note or test condition	Values	Unit
On-state gate voltage	V _{GS(on)}		1518	V
Off-state gate voltage	V _{GS(off)}		-50	V

Table 5 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур.	Max.	
Drain-source on-resistance	R _{DS(on)}	I _D = 320 A	$V_{\rm GS} = 18 \text{ V},$ $T_{\rm vj} = 25 ^{\circ}\text{C}$		2.6	4.1	mΩ
			$V_{\rm GS} = 18 \text{ V},$ $T_{\rm vj} = 125 ^{\circ}\text{C}$		5.4		
			V _{GS} = 18 V, T _{vj} = 175 °C		7.7		
			$V_{\rm GS} = 15 \text{ V},$ $T_{\rm vj} = 25 ^{\circ}\text{C}$		2.8		
Gate threshold voltage	V _{GS(th)}	I_D = 224 mA, V_{DS} = V_{GS} , (te pulse at V_{GS} = +20 V), T_{Vj} =		3.45	4.3	5.15	V
Total gate charge	Q_{G}	$V_{\rm DD}$ = 1200 V, $V_{\rm GS}$ = -3/18 V	/, T _{vj} = 25 °C		1.56		μC
Internal gate resistor	R _{Gint}	T _{vj} = 25 °C			0.9		Ω
Input capacitance	C _{ISS}	$f = 100 \text{ kHz}, V_{DS} = 1500 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		47.9		nF
Output capacitance	C _{OSS}	$f = 100 \text{ kHz}, V_{DS} = 1500 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		1.12		nF
Reverse transfer capacitance	C _{rss}	$f = 100 \text{ kHz}, V_{DS} = 1500 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		0.084		nF
C _{OSS} stored energy	Eoss	$V_{\rm DS}$ = 1500 V, $V_{\rm GS}$ = -3/18 V	, T _{vj} = 25 °C		1470		μJ
Drain-source leakage current	I _{DSS}	$V_{\rm DS}$ = 2000 V, $V_{\rm GS}$ = -3 V	T _{vj} = 25 °C		0.08	660	μA
Gate-source leakage current	I _{GSS}	$V_{\rm DS}$ = 0 V, $T_{\rm vj}$ = 25 °C	V _{GS} = 20 V			400	nA
Turn-on delay time	t _{d on}	$I_{\rm D} = 320 \text{ A}, R_{\rm Gon} = 3 \Omega,$	T _{vj} = 25 °C		86		ns
(inductive load)		$V_{DD} = 1500 \text{ V},$ $V_{GS} = -3/18 \text{ V},$	T _{vj} = 125 °C		82		
		$t_{\text{dead}} = 1000 \text{ ns}$	T _{vj} = 175 °C		81		
Rise time (inductive load)	t _r	$I_{\rm D} = 320 \text{A}, R_{\rm Gon} = 3 \Omega,$	T _{vj} = 25 °C		44		ns
		$V_{DD} = 1500 \text{ V},$ $V_{GS} = -3/18 \text{ V},$	T _{vj} = 125 °C		39		1
		$t_{\text{dead}} = 1000 \text{ ns}$	T _{vj} = 175 °C		39		

(table continues...)

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3 Body diode (MOSFET, T1.1-T1.2 / T2.1-T2.2)



Table 5 (continued) Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Min. Typ.		
Turn-off delay time	t _{d off}	$I_{\rm D} = 320 \text{A}, R_{\rm Goff} = 1 \Omega,$	T _{vj} = 25 °C		123		ns
(inductive load)	$V_{DD} = 1500 \text{ V},$ $V_{GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		136			
		V _{GS} = -5/16 V	T _{vj} = 175 °C		142		
Fall time (inductive load)	t _f	$I_{\rm D} = 320 \text{ A}, R_{\rm Goff} = 1 \Omega,$	<i>T</i> _{vj} = 25 °C		62		ns
		$V_{DD} = 1500 \text{ V},$ $V_{GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		65		
		VGS - 3/10 V	T _{vj} = 175 °C		67		
Turn-on energy loss per	Eon	$I_{\rm D}$ = 320 A, $V_{\rm DD}$ = 1500 V,	<i>T</i> _{vj} = 25 °C		44.3		mJ
•	$L_{\sigma} = 8 \text{ nH}, V_{GS} = -3/18 \text{ V},$ $R_{Gon} = 3 \Omega, \text{ di/dt} = 9$	T _{vj} = 125 °C		49.7			
		$kA/\mu s$ ($T_{vj} = 175$ °C), $t_{dead} = 1000 \text{ ns}$	T _{vj} = 175 °C		56.5		
Turn-on energy loss per	E _{on,o}	$I_{\rm D} = 320 \text{ A}, V_{\rm DD} = 1500 \text{ V},$ $L_{\sigma} = 8 \text{ nH}, V_{\rm GS} = -3/18 \text{ V},$ $R_{\rm Gon,o} = 1 \Omega, \text{ di/dt} =$	T _{vj} = 25 °C		22.2		mJ
pulse, optimized			T _{vj} = 125 °C		23.7		
		$17 \text{ kA/µs } (T_{\text{vj}} = 175 \text{ °C}),$ $t_{\text{dead}} = 100 \text{ ns}$	T _{vj} = 175 °C		26.6		
Turn-off energy loss per	E _{off}	$I_{\rm D}$ = 320 A, $V_{\rm DD}$ = 1500 V,	T _{vj} = 25 °C		12		mJ
pulse	,	$L_{\sigma} = 8 \text{ nH}, V_{GS} = -3/18 \text{ V},$ $R_{Goff} = 1 \Omega, \text{ dv/dt} = 26.6$	T _{vj} = 125 °C		13.5		
		$kV/\mu s (T_{vj} = 175 °C)$	T _{vj} = 175 °C		14		
Thermal resistance, junction to heat sink	R _{thJH}	per MOSFET, $\lambda_{\text{grease}} = 3.3$	W/(m·K)		0.12		K/W
Temperature under switching conditions	T _{vj op}			-40		175	°C

Note:

The selection of positive and negative gate-source voltages impacts losses and the long-term behavior of the MOSFET and body diode. The design guidelines described in Application Notes AN 2018-09 and AN 2021-13 must be considered to ensure sound operation of the device over the planned lifetime.

 $T_{\rm vj\,op}$ > 150 °C is allowed for operation at overload conditions. For detailed specifications, please refer to AN 2018-14.

3 Body diode (MOSFET, T1.1-T1.2 / T2.1-T2.2)

Table 6 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
DC body diode forward current	I _{SD}	$T_{\rm vj} = 175 {\rm ^{\circ}C}, V_{\rm GS} = -3 {\rm V}$	T _H = 65 °C	210	А

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4 NTC-Thermistor



Table 7 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур.	Max.	
Forward voltage	V _{SD}	$I_{SD} = 320 \text{ A}, V_{GS} = -3 \text{ V}$	T _{vj} = 25 °C		4.4	5.95	V
			T _{vj} = 125 °C		4		
			T _{vj} = 175 °C		3.85		
Peak reverse recovery	I _{rrm}	I _{SD} = 320 A, di _s /dt =	T _{vj} = 25 °C		156		А
current		9 kA/ μ s, V_{DD} = 1500 V, V_{GS} = -3 V, t_{dead} = 1000 ns	T _{vj} = 125 °C		229		
		ν _{GS} – -3 ν , ι _{dead} – 1000 113	T _{vj} = 175 °C		292		
Recovered charge	Q _{rr}	I_{SD} = 320 A, di _s /dt = 9 kA/µs, V_{DD} = 1500 V, V_{GS} = -3 V, t_{dead} = 1000 ns	T _{vj} = 25 °C		9.2		μC
			T _{vj} = 125 °C		14.8		
			T _{vj} = 175 °C		19.7		
Reverse recovery energy	E _{rec}	$I_{SD} = 320 \text{ A, di}_{s}/\text{dt} =$	T _{vj} = 25 °C		8.1		mJ
		9 kA/ μ s (T _{vj} = 175 °C), V_{DD} = 1500 V, V_{GS} = -3 V,	T _{vj} = 125 °C		10.2		
		$t_{\text{dead}} = 1000 \text{ ns}$	T _{vj} = 175 °C		11.9		
Reverse recovery energy, optimized	E _{rec,o}	$I_{SD} = 320 \text{ A, di}_{s}/\text{dt} =$	T _{vj} = 25 °C		10.8		mJ
		17 kA/ μ s (T _{vj} = 175 °C),	T _{vj} = 125 °C		12.5		
		$V_{DD} = 1500 \text{ V}, V_{GS} = -3 \text{ V},$ $t_{dead} = 100 \text{ ns}$	T _{vj} = 175 °C		13		

4 NTC-Thermistor

Table 8 Characteristic values

Parameter	Symbol	ol Note or test condition		Values		
			Min.	Тур.	Max.	
Rated resistance	R ₂₅	T _{NTC} = 25 °C		5		kΩ
Deviation of R ₁₀₀	∆R/R	$T_{\rm NTC} = 100 {}^{\circ}{\rm C}$, $R_{100} = 493 \Omega$	-5		5	%
Power dissipation	P ₂₅	T _{NTC} = 25 °C			20	mW
B-value	B _{25/50}	$R_2 = R_{25} \exp[B_{25/50}(1/T_2-1/(298,15 \text{ K}))]$		3375		K
B-value	B _{25/80}	$R_2 = R_{25} \exp[B_{25/80}(1/T_2-1/(298,15 \text{ K}))]$		3411		K
B-value	B _{25/100}	$R_2 = R_{25} \exp[B_{25/100}(1/T_2-1/(298,15 \text{ K}))]$		3433		K

Note: For an analytical description of the NTC characteristics please refer to AN2009-10, chapter 4

5 Characteristics diagrams

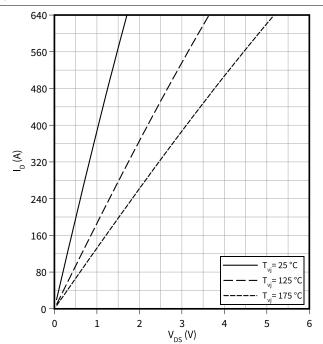


5 Characteristics diagrams

Output characteristic (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $I_D = f(V_{DS})$

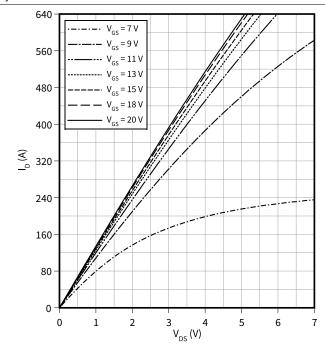
 $V_{GS} = 18 V$



Output characteristic field (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $I_D = f(V_{DS})$

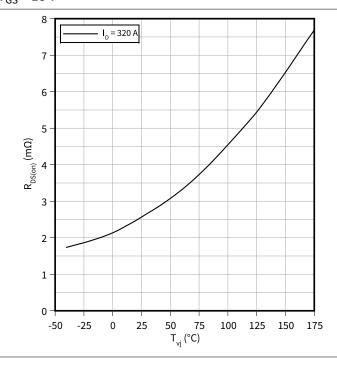
T_{vi} = 175 °C



Drain source on-resistance (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $R_{DS(on)} = f(T_{vj})$

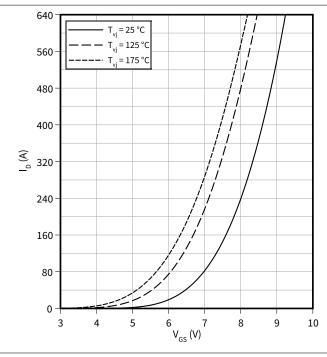
 $V_{GS} = 18 V$



Transfer characteristic (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $I_D = f(V_{GS})$

 $V_{DS} = 20 V$



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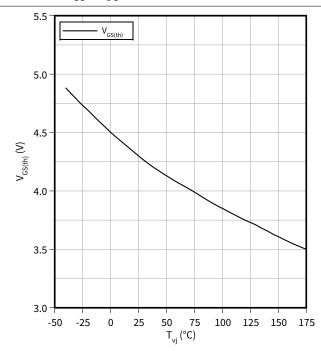
5 Characteristics diagrams



Gate-source threshold voltage (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

$$V_{GS(th)} = f(T_{vj})$$

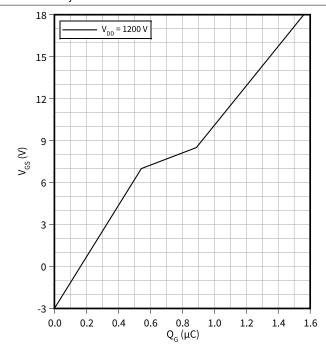
 $I_D = 224 \text{ mA}, V_{GS} = V_{DS}$



Gate charge characteristic (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

$$V_{GS} = f(Q_G)$$

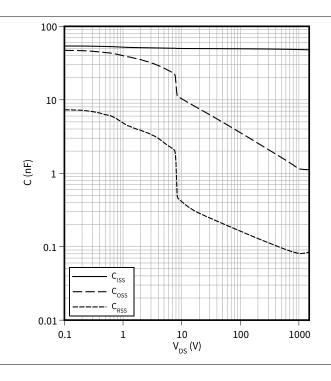
 $I_D = 320 \text{ A}, T_{vi} = 25 ^{\circ}\text{C}$



Capacity characteristic (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

$$C = f(V_{DS})$$

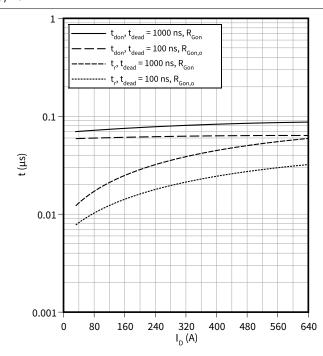
f = 100 kHz, T_{vj} = 25 °C, V_{GS} = 0 V



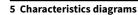
Switching times (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

$$t = f(I_D)$$

 $V_{DD} = 1500$ V, $R_{Gon} = 3$ Ω, $R_{Gon,o} = 1$ Ω, $T_{vj} = 175$ °C, $V_{GS} = -3/18$ V



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Switching times (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

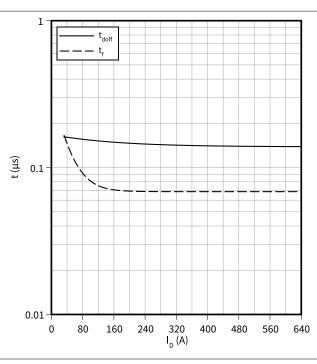
 $t = f(I_D)$

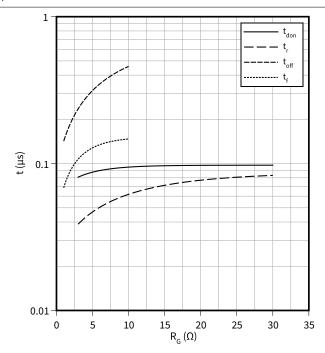
$$R_{Goff} = 1 \Omega$$
, $V_{DD} = 1500 V$, $T_{vi} = 175 °C$, $V_{GS} = -3/18 V$

Switching times (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $t = f(R_G)$

 V_{DD} = 1500 V, t_{dead} = 1000 ns, I_{D} = 320 A, T_{vj} = 175 °C, V_{GS} = -3/18 V

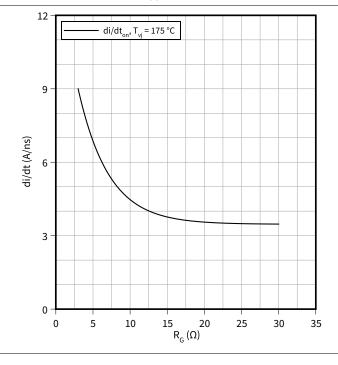




Current slope (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $di/dt = f(R_G)$

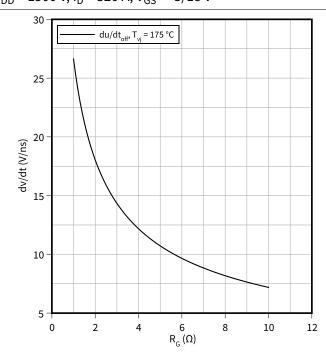
 $V_{DD} = 1500 \text{ V}, I_D = 320 \text{ A}, V_{GS} = -3/18 \text{ V}$



Voltage slope (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $dv/dt = f(R_G)$

 $V_{DD} = 1500 \text{ V}, I_D = 320 \text{ A}, V_{GS} = -3/18 \text{ V}$



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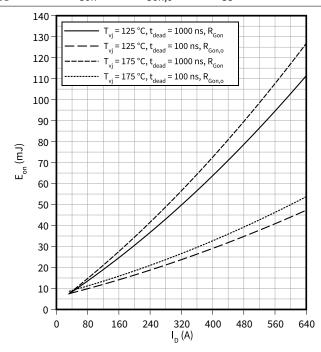
5 Characteristics diagrams



Switching losses (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{on} = f(I_D)$

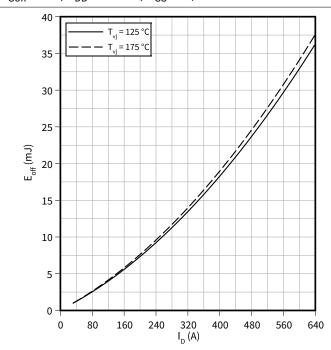
 $V_{DD} = 1500 \text{ V}, R_{Gon} = 3 \Omega, R_{Gon,o} = 1 \Omega, V_{GS} = -3/18 \text{ V}$



Switching losses (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{off} = f(I_D)$

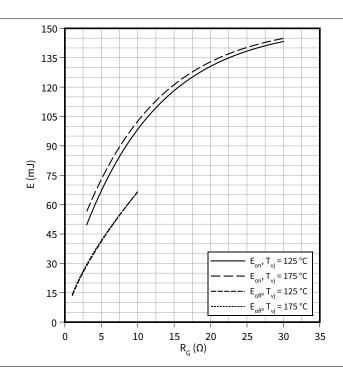
 $R_{Goff} = 1 \Omega, V_{DD} = 1500 V, V_{GS} = -3/18 V$



Switching losses (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E = f(R_G)$

 $V_{DD} = 1500 \text{ V}, t_{dead} = 1000 \text{ ns}, I_D = 320 \text{ A}, V_{GS} = -3/18 \text{ V}$

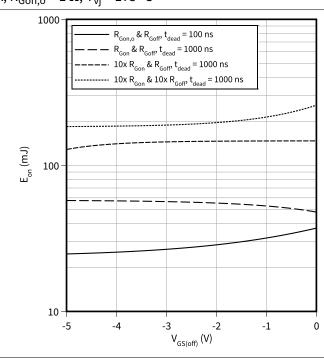


Switching losses (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{on} = f(V_{GS(off)})$

 $R_{Goff} = 1 \Omega$, $V_{DD} = 1500 V$, $R_{Gon} = 3 \Omega$, $V_{GS(on)} = 18 V$, $I_D = 320$

A, $R_{Gon,o} = 1 \Omega$, $T_{vj} = 175 °C$



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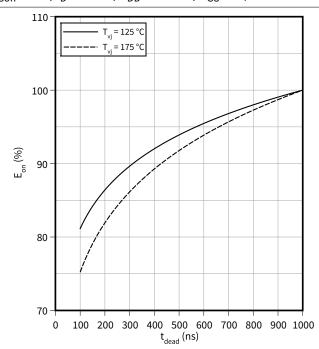
5 Characteristics diagrams



Switching losses (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{on} = f(t_{dead})$

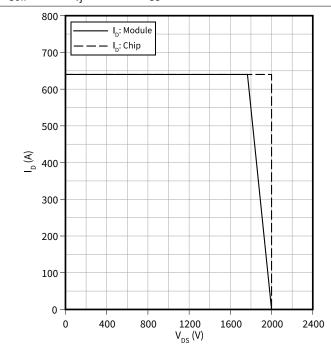
$$R_{Gon} = 3 \Omega$$
, $I_D = 320 A$, $V_{DD} = 1500 V$, $V_{GS} = -3/18 V$



Reverse bias safe operating area (RBSOA), MOSFET, T1.1-T1.2 / T2.1-T2.2

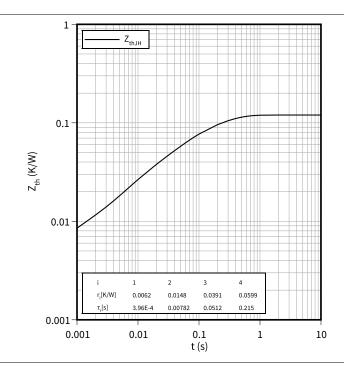
 $I_D = f(V_{DS})$

$$R_{Goff} = 1 \Omega, T_{vi} = 175 \, ^{\circ}C, V_{GS} = -3/18 \, V$$



Transient thermal impedance, MOSFET, T1.1-T1.2 / T2.1-T2.2

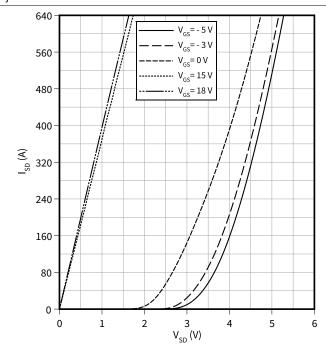
 $Z_{th} = f(t)$



Forward characteristic body diode (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $I_{SD} = f(V_{SD})$

$$T_{vi} = 25 \,^{\circ}C$$



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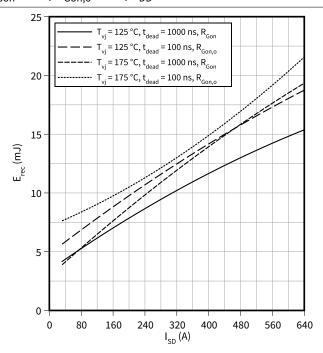
5 Characteristics diagrams



Switching losses body diode (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{rec} = f(I_{SD})$

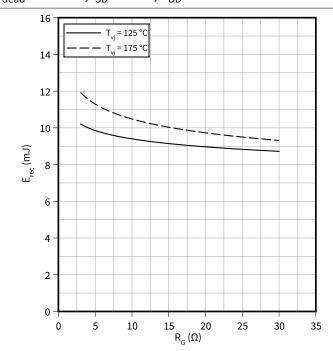
$$R_{Gon} = 3 \Omega, R_{Gon,o} = 1 \Omega, V_{DD} = 1500 V$$



Switching losses body diode (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{rec} = f(R_G)$

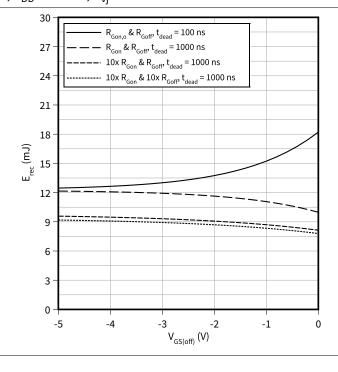
$$t_{dead} = 1000 \text{ ns}, I_{SD} = 320 \text{ A}, V_{DD} = 1500 \text{ V}$$



Switching losses body diode (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{rec} = f(V_{GS(off)})$

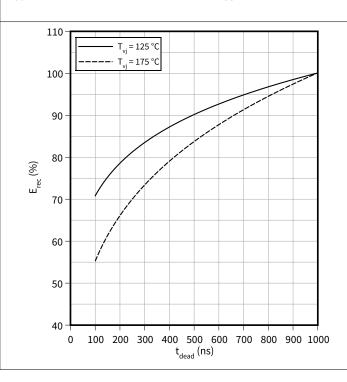
$$R_{Goff}$$
 = 1 Ω , R_{Gon} = 3 Ω , $V_{GS(on)}$ = 18 V, I_{SD} = 320 A, $R_{Gon,o}$ = 1 Ω , V_{DD} = 1500 V, T_{vj} = 175 °C



Switching losses body diode (typical), MOSFET, T1.1-T1.2 / T2.1-T2.2

 $E_{rec} = f(t_{dead})$

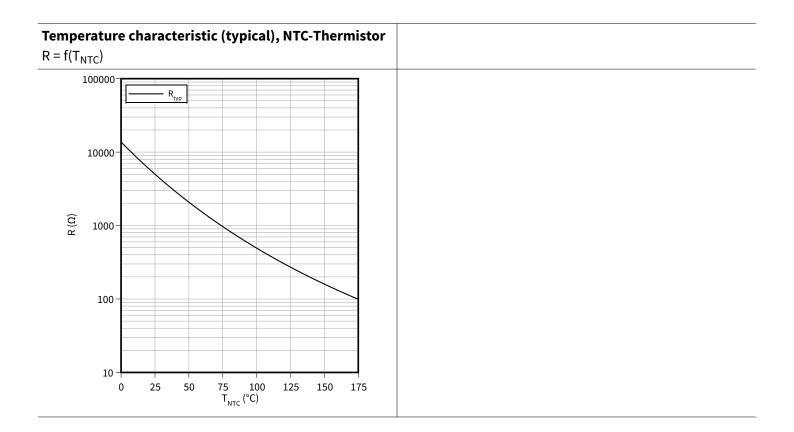
$$R_{Gon} = 3 \Omega$$
, $I_{D} = 320 A$, $V_{DD} = 1500 V$, $V_{GS} = -3/18 V$



EasyPACK™ module



5 Characteristics diagrams



13

6 Circuit diagram



6 Circuit diagram

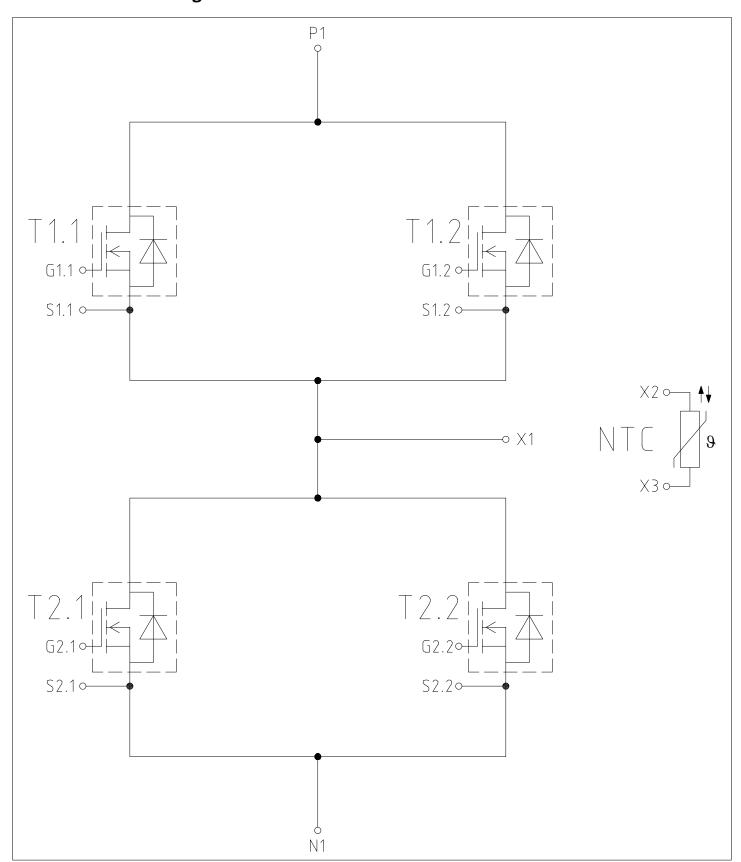


Figure 1

7 Package outlines



7 Package outlines

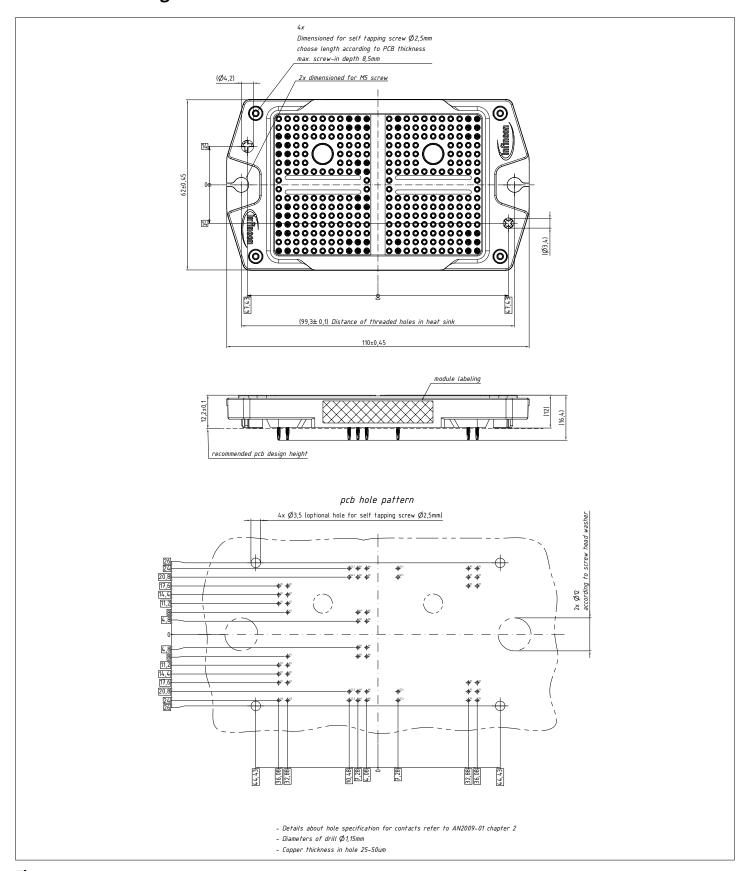


Figure 2

EasyPACK™ module

8 Module label code



8 Module label code

Code format	Data Matrix		Barcode C	Code128
Encoding	ASCII text		Code Set	Ą
Symbol size	16x16		23 digits	
Standard	IEC24720 and IEC16022		IEC8859-1	
Code content	ContentDigitModule serial number1 - 5Module material number6 - 11Production order number12 - 19Date code (production year)20 - 21Date code (production week)22 - 23			Example 71549 142846 55054991 15 30
Example	71549142846550549911530			6550549911530

Figure 3

EasyPACK™ module

Revision history



Revision history

Document version	Date of release	Description of changes
0.10	2024-02-12	Initial version
1.00	2025-01-13	Final datasheet

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