

• General Description

The AGM1005E combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

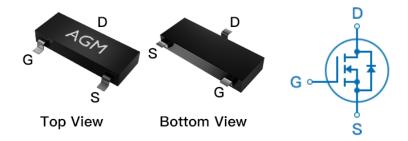
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
100V	95mΩ	5A

SOT23-3 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
1005	AGM1005E	SOT23-3	178mm	8mm	3000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(TA=25℃) (Note 1)	5.0	А
	Drain Current-Continuous(TA=70℃)	3.2	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	20	А
PD	Maximum Power Dissipation(TA=25℃)	1.25	W
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	${\mathbb C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA Thermal Resistance Junction-ambient (Steady State) ¹			100	°C/W



Table 3. Electrical Characteristics (TJ=25^oC unless otherwise noted)

Table 3. Electrical Characteristics (TJ=25 ℃ unless otherwise noted) Symbol Parameter Conditions Min Typ Max Unit						
Symbol		Conditions	IVIII	Тур	IVIAX	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	100			V
IDSS	Zero Gate Voltage Drain Current	VDS=100V,VGS=0V			1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250µA	1.2	1.5	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=2A		5		S
		VGS=10V, ID=3A		95	120	mΩ
RDS(on)	Drain-Source On-State Resistance	VGS=4.5V, ID=2A		98	137	mΩ
Dynamic	Characteristics					
Ciss	Input Capacitance	VDC-50VV0C-0V		765		pF
Coss	Output Capacitance	VDS=50V,VGS=0V, F=1MHZ		38		pF
Crss	Reverse Transfer Capacitance			33		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz				Ω
Switching	Times					
td(on)	Turn-on Delay Time			7.5		nS
tr	Turn-on Rise Time	VGS=10V,VDS=50V,		6.0		nS
td(off)	Turn-Off Delay Time	RGEN=1.8Ω,ID=3A		21		nS
tf	Turn-Off Fall Time			9.0		nS
Qg	Total Gate Charge			18		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=50V, ID=2A		2.5		nC
Qgd	Gate-Drain Charge	_ ID-ZA		4.0		nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)				5.0	А
VSD	Forward on Voltage	VGS=0V,IS=3A			1.2	V
trr	Reverse Recovery Time	IF=3A , dI/dt=100A/μs ,		21		ns
Qrr	Reverse Recovery Charge	TJ=25℃		22		nc

Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25℃



Figure1: Output Characteristics

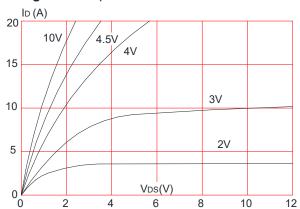


Figure 3:On-resistance vs. Drain Current

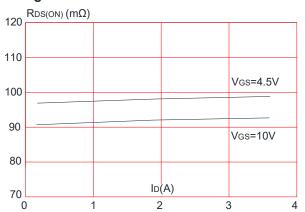


Figure 5: Gate Charge Characteristics

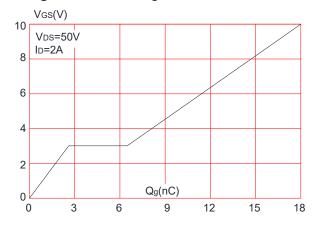


Figure 2: Typical Transfer Characteristics

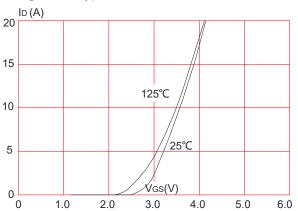


Figure 4: Body Diode Characteristics

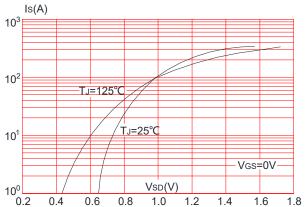


Figure 6: Capacitance Characteristics

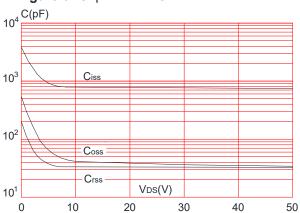




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

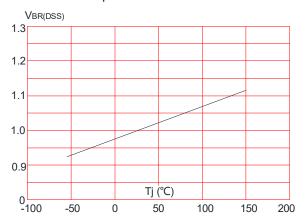


Figure 9: Maximum Safe Operating Area

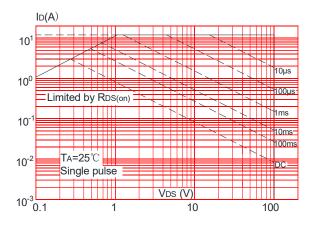


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

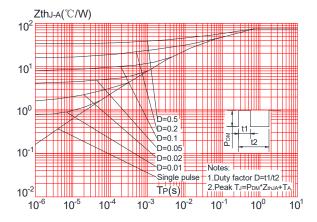


Figure 8: Normalized on Resistance vs. Junction Temperature

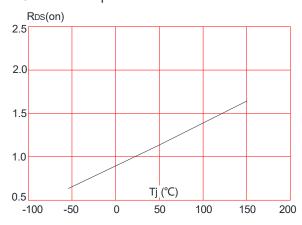
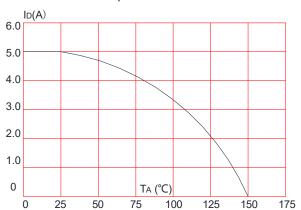
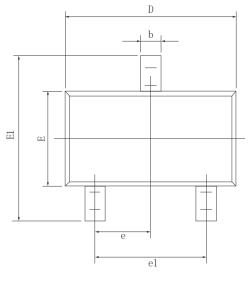


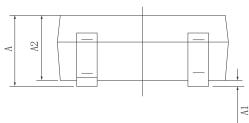
Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

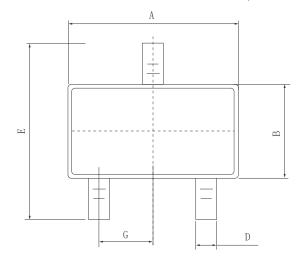


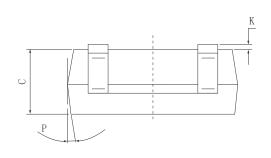


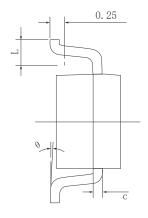
•Dimensions (SOT23-3)

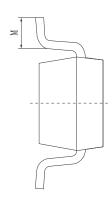










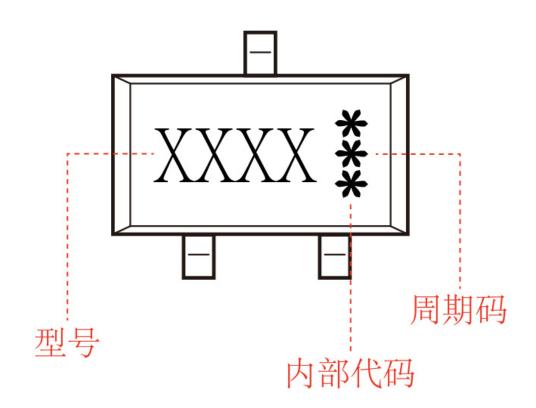


CVANDOI	MILLIMETER			
SYMBOL	MIN	NOM	MAX	
A	_	_	1.25	
A1	0.03	_	0.10	
A2	1.05	1.10	1. 15	
b	0.30	0.35	0.40	
С	0.13	_	0. 17	
D	2.87	2. 92	2. 97	
Е	1. 55	1.60	1.65	
E1	2.70	2.85	3.00	
е	0. 95 BSC.			
e1	1.80		2.00	
L	0.35	0.45	0.55	
θ	0°	_	8°	

DIM	MILLIMETERS
A	2.82 [~] 3.02
В	1.60±0.10
С	1.10±0.05
D	0.40 ± 0.10
Е	2. 65 ² . 95
G	0.95typ
K	0.00~0.10
M	0.20MIN
Р	9±2°



SOT23-3 Marking Instructions:





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