

N-Channel Enhancement Mode MOSFET

Feature

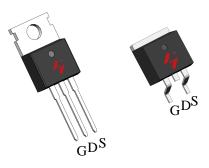
• 30V/275A

 $R_{DS(ON)}$ = 1.3m $\Omega(typ.)$ @Vgs = 10V

 $R_{DS(ON)} = 1.6 m\Omega(typ.) @V_{GS} = 4.5 V$

- 100% Avalanche Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

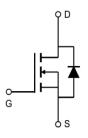
Pin Description



TO-220FB-3L TO-263-2L

Applications

- Switching application
- Power Management for DC/DC



N-Channel MOSFET

Ordering and Marking Information



Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)		•	1
VDSS	Drain-Source Voltage		30	V
Vgss	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		175	°C
Tstg	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode) Tc=25°C		275	А
Mounted on	Large Heat Sink		1	
Ідм	Pulsed Drain Current *	Tc=25°C	990	А
	Continuous Paris Courset	Tc=25°C	275	А
lσ	Continuous Drain Current	Tc=100°C	194	А
D	Mariana Barra Biratastina	Tc=25°C	166	W
Po	Maximum Power Dissipation	Maximum Power Dissipation Tc=100°C		W
R ₀ JC	Thermal Resistance, Junction-to-Case**		0.9	°C/W
R _{euA}	Thermal Resistance, Junction-to-Ambient		62.5	°C/W
Eas	SinglePulsed-Avalanche Energy *** L=0.3mH		750	mJ

Note: *

- * Repetitive rating; pulse width limited by max.junction temperature.
- ** Drain current is limited by junction temperature
- *** Limited by TJmax , starting TJ= 25° C, L = 0.3mH, VD= 24V, VGs =10V.

Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Cumbal	Parameter	Took Conditions	HY4703			11:4
Symbol	Parameter	Test Conditions		Тур.	Max	Unit
Static Char	acteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	30	-	-	V
L Dusin to Course Lealings Course		VDS=30V,VGS=0V	-	-	1	μA
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	100	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	1.0	1.4	3.0	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA
RDS(ON)*	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =150A	-	1.3	1.7	mΩ
RDS(ON)*	Drain-Source On-State Resistance	V _{GS} =4.5V,I _{DS} =150A	-	1.6	2.3	mΩ
Diode Char	Diode Characteristics					
V _{SD} *	Diode Forward Voltage	Isp=150A,Vgs=0V	-	0.8	1.0	V
trr	Reverse Recovery Time	I _{SD} =150A,	-	35	-	ns
Qrr	Reverse Recovery Charge	dI _{SD} /dt=100A/μs	-	28	-	nC

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Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Parameter Test Conditions	Took Conditions	HY4703			I I so i t
Symbol		Min	Тур.	Max	Unit	
Dynamic (Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	2.7	-	Ω
Ciss	Input Capacitance	V _G s=0V,	-	9946	-	
Coss	Output Capacitance	V _{DS} =25V,	-	1012	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	647	-	
td(ON)	Turn-on Delay Time		-	16	-	
Tr	Turn-on Rise Time	V _{DD} =15V,R _G =6Ω, I _{DS} =150A,V _{GS} =10V	-	80	•	no
td(OFF)	Turn-off Delay Time		-	56	-	ns
Tf	Turn-off Fall Time		-	38	-	
Gate Charge Characteristics						
Qg	Total Gate Charge	- V _{DS} =24V, V _{GS} =10V,	-	240	-	
Qgs	Gate-Source Charge		-	20	-	nC
Qgd	Gate-Drain Charge	- I _D =150A	-	52	-	

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%



Typical Operating Characteristics

Figure 1: Power Dissipation

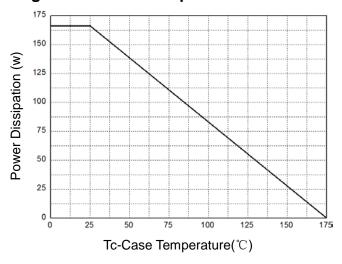


Figure 3: Safe Operation Area

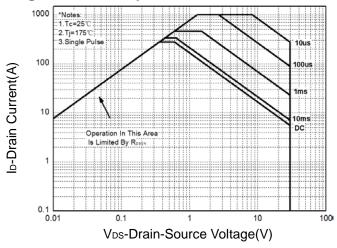


Figure 5: Output Characteristics

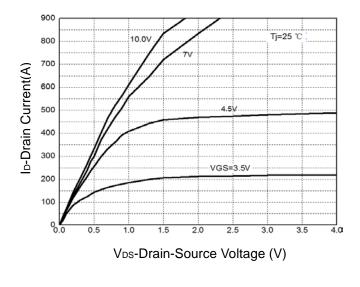


Figure 2: Drain Current

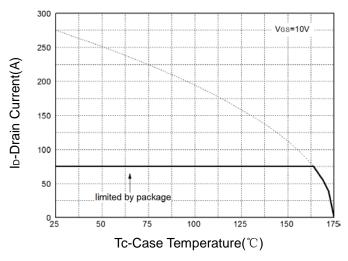


Figure 4: Thermal Transient Impedance

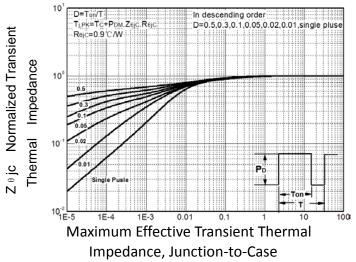
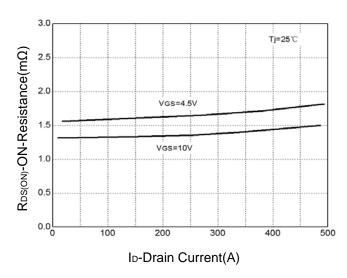


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

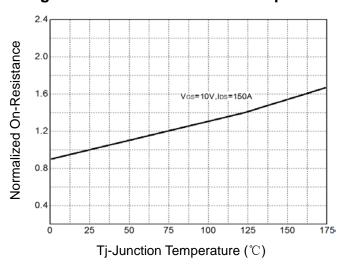


Figure 9: Capacitance Characteristics

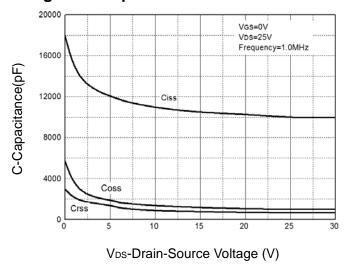


Figure 8: Source-Drain Diode Forward

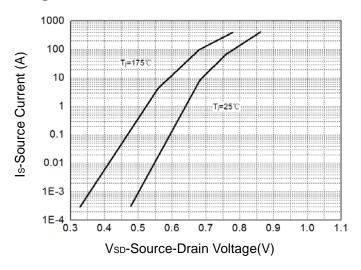
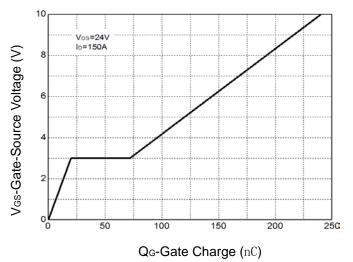
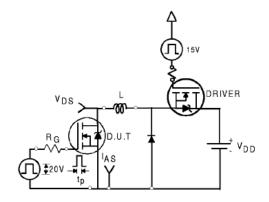


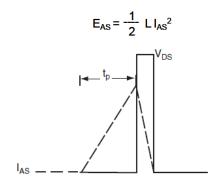
Figure 10: Gate Charge Characteristics



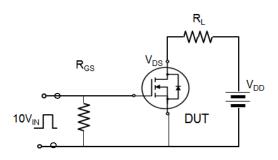


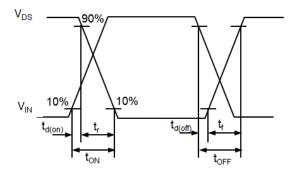
Avalanche Test Circuit



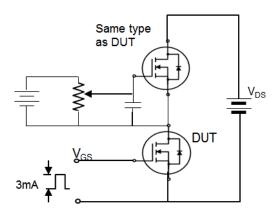


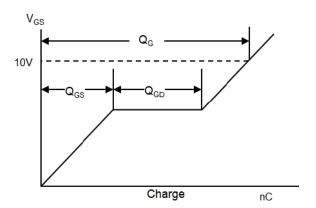
Switching Time Test Circuit





Gate Charge Test Circuit





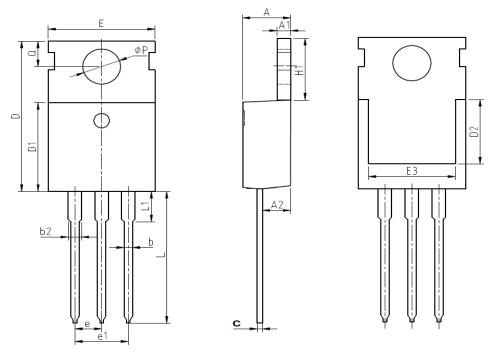


Device Per Unit

Package Type	Unit	Quantity
TO-220FB-3L	Tube	50
TO-263-2L	Tube	50

Package Information

TO-220FB-3L

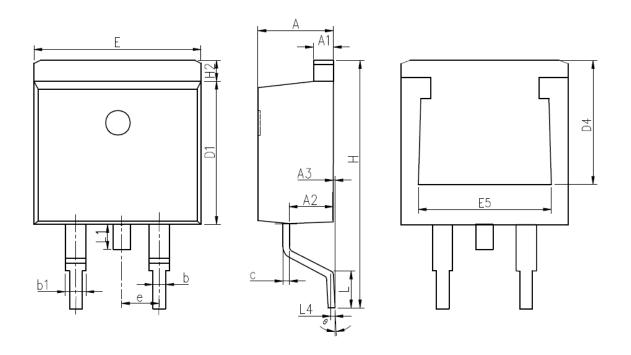


COMMON DIMENSIONS

CVMDOL	mm			
SYMBOL	MIN	NOM	MAX	
А	4.37	4.57	4.77	
A1	1.25	1.30	1.45	
A2	2.20	2.40	2.60	
b	0.70	0.80	0.95	
b2	1.17	1.27	1.47	
С	0.40	0.50	0.65	
D	15.10	15.60	16.10	
D1	8.80	9.10	9.40	
D2	5.50	-	-	
Е	9.70	10.00	10.30	
E3	7.00	-	-	
е		2.54 BSC		
e1	5.08 BSC			
H1	6.25	6.50	6.85	
L	12.75	13.50	13.80	
L1	-	3.10	3.40	
ФР	3.40	3.60	3.80	
Q	2.60	2.80	3.00	



TO-263-2L

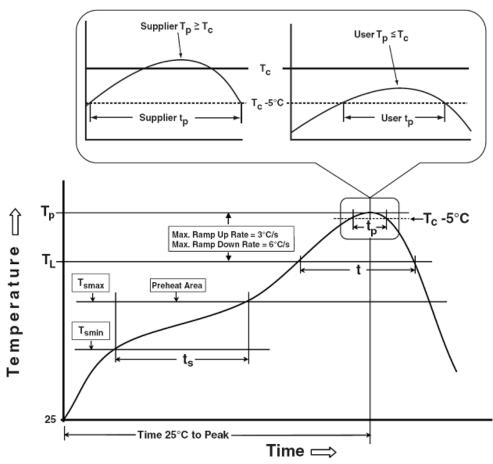


COMMON DIMENSIONS

SYMBOL	mm			
STIVIBOL	MIN	NOM	MAX	
А	4.37	4.57	4.77	
A1	1.22	1.27	1.42	
A2	2.49	2.69	2.89	
A3	0	0.13	0.25	
b	0.7	0.81	0.96	
b1	1.17	1.27	1.47	
С	0.3	0.38	0.53	
D1	8.5	8.7	8.9	
D4	6.6	-	ı	
E	9.86	10.16	10.36	
E5	7.06	-	ı	
е		2.54 BSC		
Н	14.7	15.1	15.5	
H2	1.07	1.27	1.47	
L	2	2.3	2.6	
L1	1.4	1.55	1.7	
L4	0.25 BSC			
θ	0°	5°	9°	



Classification Profile



Classification Reflow Profiles

Sn-Pb Eutectic Assembly	Pb-Free Assembly
100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
3 °C/second max.	3°C/second max.
183 °C 60-150 seconds	217 °C 60-150 seconds
See Classification Temp in table 1	SeeClassification Tempin table 2
20** seconds	30** seconds
6 °C/second max.	6 °C/second max.
6 minutes max.	8 minutes max.
	100 °C 150 °C 60-120 seconds 3 °C/second max. 183 °C 60-150 seconds See Classification Temp in table 1 20** seconds 6 °C/second max.

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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