

Preliminary Technical Information

TrenchP[™] Power MOSFET

IXTR120P20T

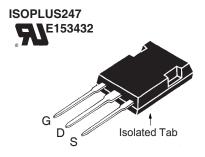
P-Channel Enhancement Mode Avalanche Rated Fast Intrinsic Rectifier



Symbol	Test Conditions	Maximum Ratings		
V _{DSS}	T _J = 25°C to 150°C	- 200	V	
V _{DGR}	$T_J = 25^{\circ}\text{C to } 150^{\circ}\text{C}, R_{gs} = 1\text{M}\Omega$	- 200	V	
V _{GSS}	Continuous	±15	V	
V _{GSM}	Transient	±25	V	
I _{D25}	T _c = 25°C	- 90	A	
I _{DM}	$T_{c} = 25^{\circ}C$, Pulse Width Limited by T_{JM}	- 400	Α	
I _A E _{AS}	T _c = 25°C T _c = 25°C	-100 3	A J	
dv/dt	$I_{_{S}} \le I_{_{DM}}, V_{_{DD}} \le V_{_{DSS}}, T_{_{J}} \le 150^{\circ}C$	10	V/ns	
P _D	T _C = 25°C	595	W	
T _J T _{JM} T _{stg}		-55 +150 150 -55 +150	°C °C °C	
T _L T _{SOLD}	1.6mm (0.062 in.) from Case for 10s Plastic Body for 10s	300 260	°C °C	
V _{ISOL}	50/60 Hz, 1 Minute	2500	V~	
F _c	Mounting Force	20120/4.527	N/lb.	
Weight		5	g	

Symbol (T _J = 25°C	Test Conditions Unless Otherwise Specified)	Cha Min.	racteristic Typ.	Values Max.	
BV _{DSS}	$V_{GS} = 0V, I_{D} = -250\mu A$	- 200			V
V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 2.5		- 4.5	V
I _{GSS}	$V_{GS} = \pm 15V, V_{DS} = 0V$			±200	nA
I _{DSS}	$V_{DS} = V_{DSS}, V_{GS} = 0V$	T _J = 125°C		- 25 - 300	•
R _{DS(on)}	$V_{GS} = -10V$, $I_{D} = -60A$, Note	1		32	mΩ

 $egin{array}{lll} V_{_{DSS}} &=& -200V \ I_{_{D25}} &=& -90A \ R_{_{DS(on)}} &\leq& 32m\Omega \ t_{_{rr}} &\leq& 300ns \end{array}$



G = Gate D = DrainS = Source

Features

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Isolated Mounting Surface
- 2500V~ Electrical Isolation
- Avalanche Rated
- Extended FBSOA
- Fast Intrinsic Rectifier
- Low R_{DS(ON)} and Q_G

Advantages

- Easy to Mount
- Space Savings
- High Power Density

Applications

- High-Side Switching
- Push Pull Amplifiers
- DC Choppers
- Automatic Test Equipment
- Current Regulators
- Battery Charger Applications



Symbol Test Conditions			Characteristic Values			
$(T_J = 25)$	°C U	nless Otherwise Specified)	Min.	Тур.	Max.	
g _{fs}		$V_{DS} = -10V, I_{D} = -60A, Note 1$	85	145	S	
C _{iss})			73	nF	
\mathbf{C}_{oss}	}	$V_{GS} = 0V, V_{DS} = -25V, f = 1MHz$		2550	pF	
\mathbf{C}_{rss}	J			480	pF	
t _{d(on)})	Desiration Control in a Times		90	ns	
t _r		Resistive Switching Times		85	ns	
t _{d(off)}	}	$V_{GS} = -10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = -60A$		200	ns	
t _f	J	$R_{G} = 1\Omega$ (External)		50	ns	
$\mathbf{Q}_{g(on)}$)			740	nC	
\mathbf{Q}_{gs}	}	$V_{GS} = -10V$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_{D} = -60A$		220	nC	
\mathbf{Q}_{gd}	J			120	nC	
R _{thJC}					0.21 °C/W	
R _{thCS}				0.15	°C/W	

ISOPLUS247 (IXTR) Outline 1 = Gate 2,4 = Drain3 = Source MILLIMETERS MYZ MAX .205 MAX 5.2L MIN .190 4.83 Α A1 .090 .100 2,29 2,54 2.16 1.40 2.15 3.20 .045 .055 .085 .115 .024 .L26 .033 D.61 0.83 ,819 .84D .620 .81 .150 .220 .172 .244

.191 .540 .640

.080

,004

.620

.065

4.3B

4.85

16.26

1.65

Source-Drain Diode

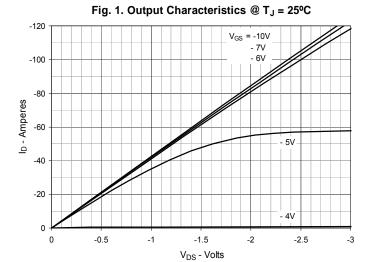
Symbol	Test Conditions	Characteristic Values			
$T_{\rm J} = 25^{\circ}$ C	Unless Otherwise Specified)	Min.	Тур.	Max.	
Is	$V_{gS} = 0V$			-120	Α
I _{SM}	Repetitive, Pulse Width Limited by $T_{_{JM}}$			- 480	Α
V _{SD}	$I_{\rm F} = -100 {\rm A}, \ V_{\rm GS} = 0 {\rm V}, \ {\rm Note} \ 1$			-1.4	V
$\left\{ egin{array}{ll} \mathbf{t}_{rr} & \\ \mathbf{Q}_{RM} & \\ \mathbf{I}_{RM} & \end{array} ight\}$	$I_F = -60A$, $-di/dt = -100A/\mu s$ $V_R = -100V$, $V_{GS} = 0V$		3.3 25.6	300	ns µC A

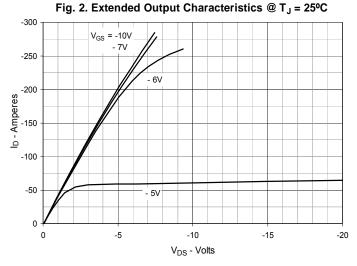
Note 1. Pulse test, $t \le 300\mu s$, duty cycle, $d \le 2\%$.

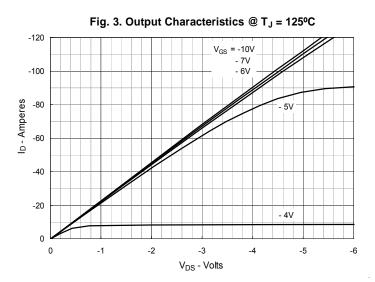
PRELIMINARY TECHNICAL INFORMATION

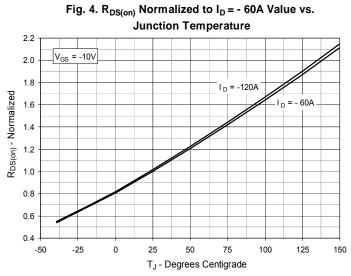
The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

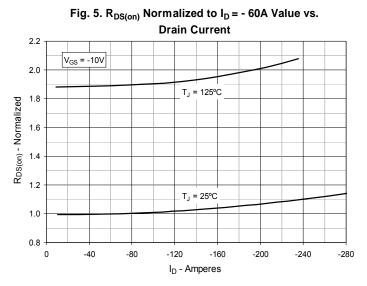


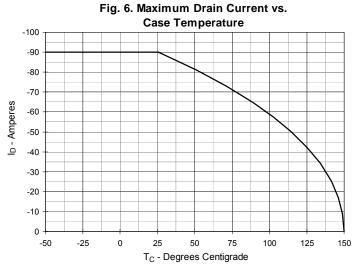




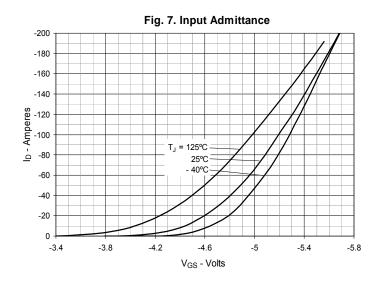


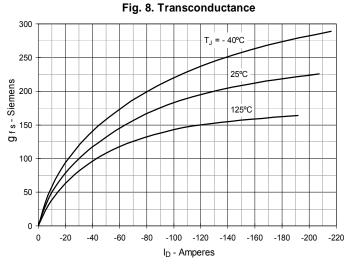


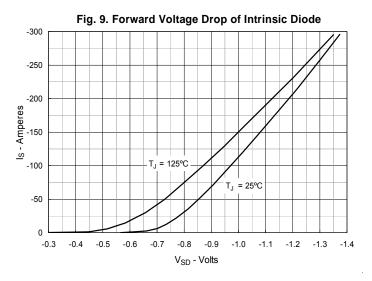


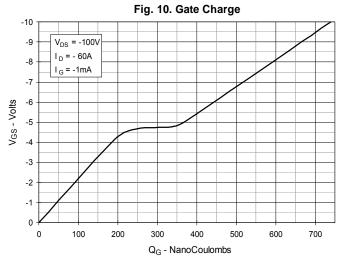


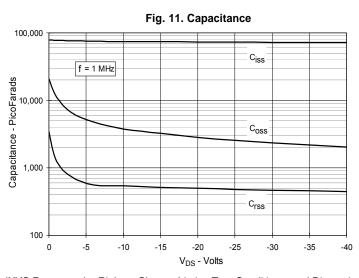


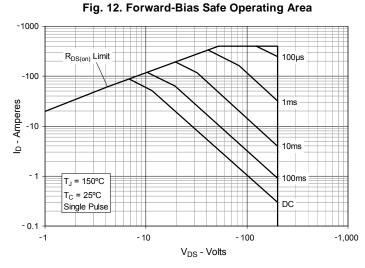












IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.



Fig. 13. Resistive Turn-on Rise Time vs.
Junction Temperature

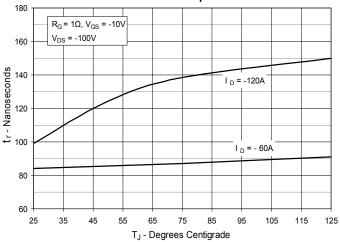


Fig. 14. Resistive Turn-on Rise Time vs.

Drain Current

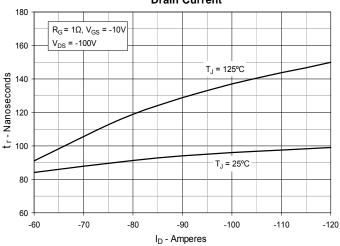


Fig. 15. Resistive Turn-on Switching Times vs.
Gate Resistance

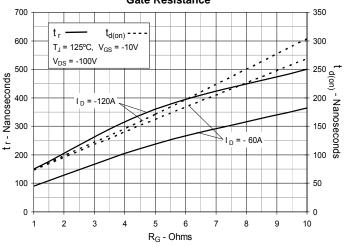


Fig. 16. Resistive Turn-off Switching Times vs.
Junction Temperature

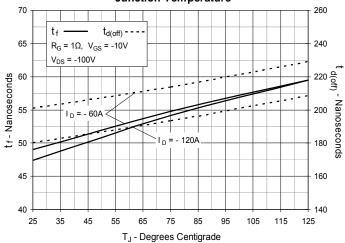


Fig. 17. Resistive Turn-off Switching Times vs.

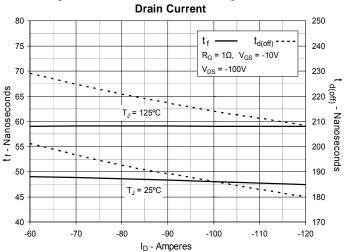
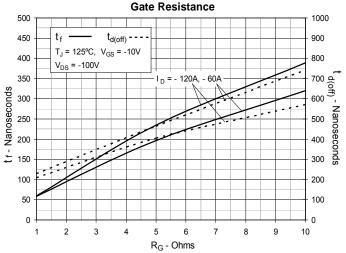


Fig. 18. Resistive Turn-off Switching Times vs.





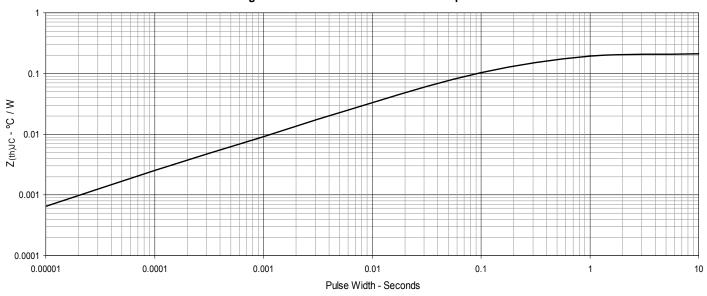


Fig. 19. Maximum Transient Thermal Impedance

