

N-Channel Enhancement Mode MOSFET

Feature

20V/105A

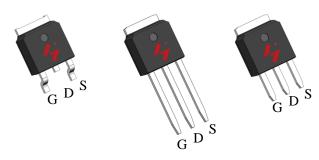
 $R_{DS(ON)} = 2.4 \text{m}\Omega \text{ (typ.)} @V_{GS} = 4.5 \text{V}$

 $R_{DS(ON)} = 3.1 \text{m}\Omega \text{ (typ.) } @V_{GS} = 2.5 \text{V}$

 $R_{DS(ON)} = 4.7 \text{m}\Omega \text{ (typ.) } @V_{GS} = 1.8 \text{V}$

- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

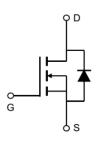
Pin Description



TO-252-2L TO-251-3L TO-251-3S

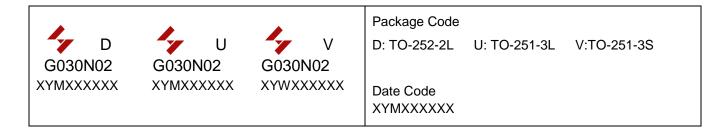
Applications

- Switching Application
- Power Management for DC/DC
- Battery Protection



N-Channel MOSFET

Ordering and Marking Information



Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plateTermi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.

HYG030N02KQ1D/U/V



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Rat	tings (Tc=25°C Unless Otherwise Noted)		·	
VDSS	Drain-Source Voltage		20	V
Vgss	Gate-Source Voltage		±12	V
TJ	Junction Temperature Range		-55 to 175	°C
Tstg	Storage Temperature Range		-55 to 175	°C
Is	Source Current-Continuous(Body Diode)	Tc=25°C	105	А
Mounted on	Large Heat Sink			•
Ідм	Pulsed Drain Current *	Tc=25°C	380	А
ī			105	Α
lσ	Continuous Drain Current	Tc=100°C	74	А
1		Tc=25°C	62.5	W
Po	Maximum Power Dissipation	Maximum Power Dissipation Tc=100°C		W
R₀c	Thermal Resistance, Junction-to-Case		2.4	°C/W
R _{eJA}	Thermal Resistance, Junction-to-Ambient **		110	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.1mH	133	mJ

Note: *

- * Repetitive rating; pulse width limited by max. junction temperature.
- ** Surface mounted on FR-4 board.
- *** Limited by TJmax , starting TJ=25°C, L = 0.1mH, Rg= 25 Ω , Vgs =4.5V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG	HYG030N02KQ1		Unit
Symbol	Parameter Test Conditions		Min	Тур.	Max	Unit
Static Char	acteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	20	-	-	V
Ipss	Drain to Source Leakage Current	VDS=20V,VGS=0V	-	-	1.0	μA
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	0.3	0.6	1.0	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	±100	nA
		V _{GS} =4.5V,I _{DS} =15A	-	2.4	3.0	mΩ
RDS(ON)*	Drain-Source On-State Resistance	V _{GS} =2.5V,I _{DS} =10A	-	3.1	4.0	
		V _{GS} =1.8V,I _{DS} =6A		4.7	6.5	
Diode Char	Diode Characteristics					
V _{SD} *	Diode Forward Voltage	Isp=10A,Vgs=0V	-	0.75	1.2	٧
trr	Reverse Recovery Time	Isp=10A,dIsp/dt=100A/µs	-	18	-	ns
Qrr	Reverse Recovery Charge	ISD=TOA,αISD/αI=TOOA/μS	-	8	-	nC

HYG030N02KQ1D/U/V



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Povemeter	Took Conditions	HYG030N02KQ1		l lmit	
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic (Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V, Frequency=1.0MHz	-	1.5	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	3946	-	
Coss	Output Capacitance	VDS=16V,	-	496	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	421	-	
td(ON)	Turn-on Delay Time		-	9	-	
Tr	Turn-on Rise Time	$V_{DD}=10V,R_{G}=2.5\Omega,$	-	73	-	
td(OFF)	Turn-off Delay Time	los=15A,Vgs=4.5V	-	85	-	ns
Tf	Turn-off Fall Time		-	97	-	
Gate Char	Gate Charge Characteristics					
Qg	Total Gate Charge	\\ 16\\\\\ A5\\\	-	61	-	
Qgs	Gate-Source Charge	$V_{DS} = 16V, V_{GS} = 4.5V,$ $I_{D} = 15A$	-	7	-	nC
Qgd	Gate-Drain Charge	ID-13A	-	21	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

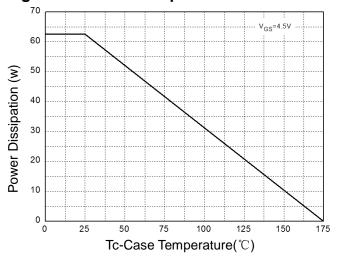


Figure 2: Drain Current

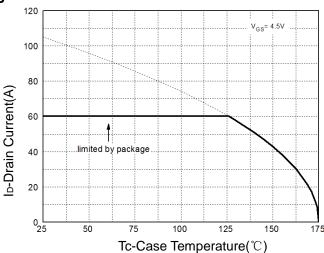


Figure 3: Safe Operation Area

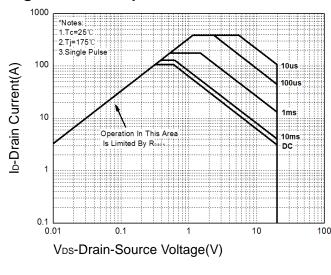


Figure 4: Thermal Transient Impedance

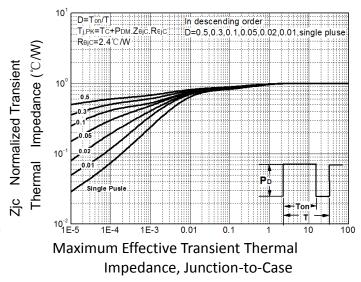


Figure 5: Output Characteristics

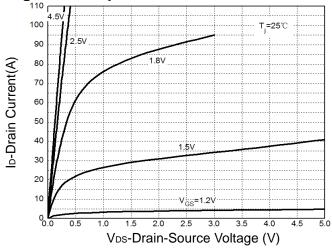
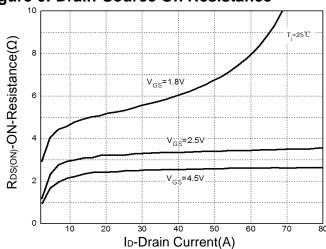


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

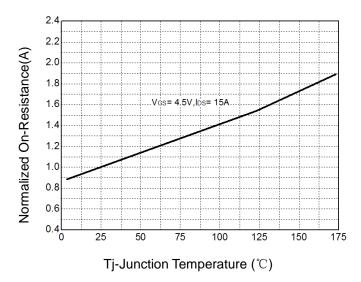
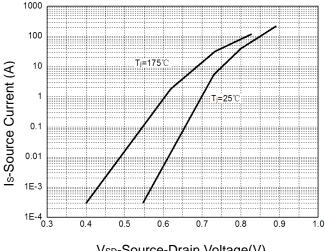


Figure 8: Source-Drain Diode Forward



Vsp-Source-Drain Voltage(V)

Figure 9: Capacitance Characteristics

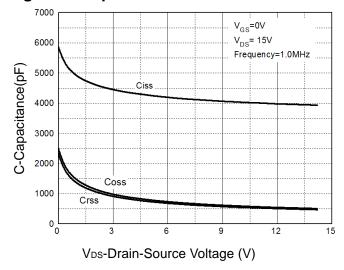
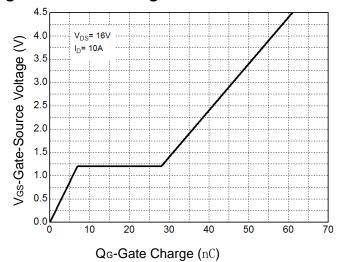
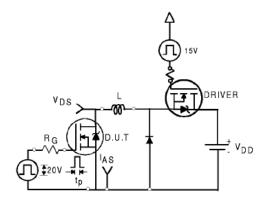


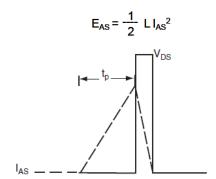
Figure 10: Gate Charge Characteristics



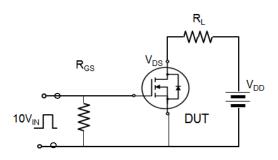


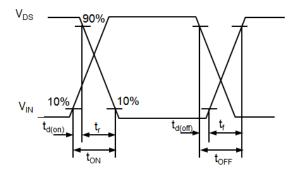
Avalanche Test Circuit



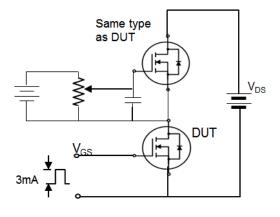


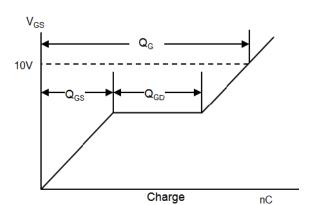
Switching Time Test Circuit





Gate Charge Test Circuit





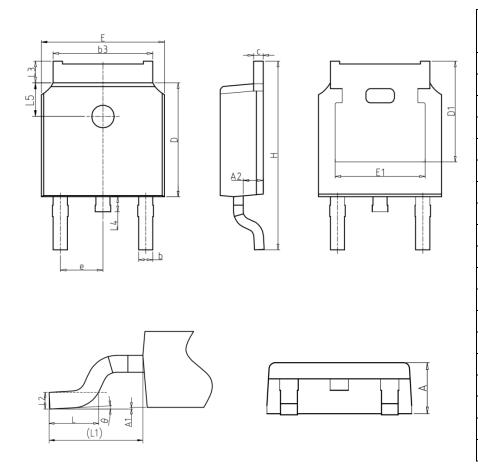


Device Per Unit

Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-251-3L	Tube	75
TO-251-3S	Tube	75

Package Information

TO-252-2L

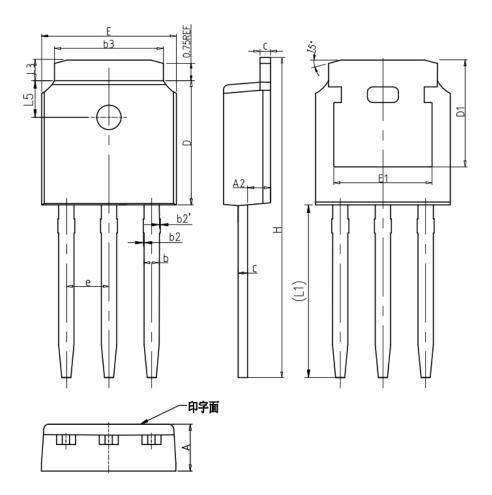


COMMON DIMENSIONS

SYMBOL		mm	
OTNIBOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
Е	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BS0)
Н	9.40	10.10	10.50
L	1.38	1.50	1.75
L1		2.90REF	•
L2	0.51BSC		
L3	0.88	-	1.28
L4	-	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°



TO-251-3L

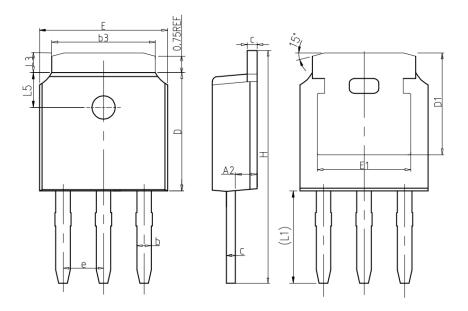


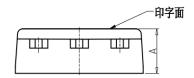
COMMON DIMENSIONS

CVMDOL		mm	
SYMBOL	MIN	NOM	MAX
А	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
E	6.40	6.60	6.80
E1	4.63	-	-
е	2.286BSC		
Н	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



TO-251-3S



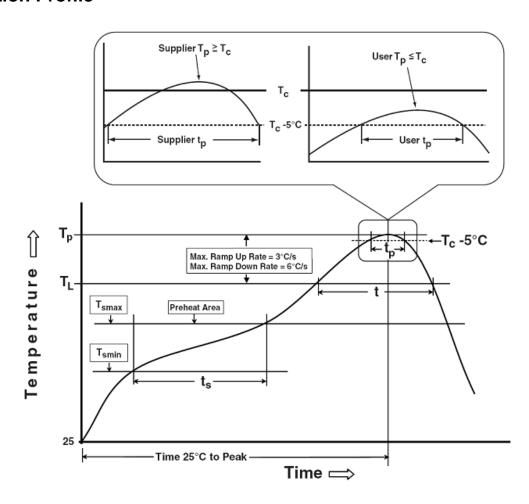


COMMON DIMENSIONS

CVMDOL		mm	
SYMBOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
E	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BSC	
Н	10.00	11.22	11.44
L1	3.90	4.10	4.30
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak	100 °C	150 °C	
Temperature min (T _{smin})			
Temperature max (T _{smax})	150 °C	200 °C	
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds	
Average ramp-up rate	2 %C/22 22 7 4 72 27	200/2001	
(T _{smax} to T _P)	3 °C/second max.	3°C/second max.	
Liquidous temperature (T _L)	183 °C	217 °C	
Time at liquidous (t _L)	60-150 seconds	60-150 seconds	
Peak package body Temperature	See Classification Temp in table 1	See Classification Temp in table 2	
(T _p)*	See Classification Temp in table 1	See Classification Temp in table 2	
Time (t _P)** within 5°C of the specified	20**	20**	
classification temperature (T _c)	20** seconds	30** seconds	
Average ramp-down rate (Tp to Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

HYG030N02KQ1D/U/V



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 125°C
PCT	JESD-22, A102	96Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com Technical Support: Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: www.hymexa.com