

N-Channel Enhancement Mode MOSFET

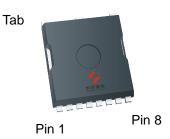
Feature

- 150V/185A RDS(ON)= 5.0 m Ω (typ.) @VGS = 10V
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

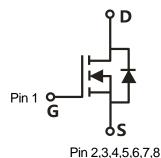
Applications

- Switching application
- Li-battery protection
- DC-DC
- Motor control

Pin Description



TOLL



Single N-Channel MOSFET

Ordering and Marking Information

HYG
TA: TOLL

058N15NS

XYMXXXXX

Date Code

XYMXXXXX

Note: HUAYI halogen free products contain molding compounds and 100% matter tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			•
VDSS	Drain-Source Voltage		150	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		55 : 475	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode) Tc=25°C		185	А
Mounted on	Large Heat Sink		-	l
I DM	Pulsed Drain Current *	Tc=25°C	665	А
ı		Tc=25°C	185	Α
lσ	Continuous Drain Current	Tc=100°C	130	Α
	M : 5 5: : :	Tc=25°C	385	W
PD	P _D Maximum Power Dissipation		192	W
R₀vc	Thermal Resistance, Junction-to-Case		0.39	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient **		45	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	1010	mJ

Note:

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Cymbal	Doromotor	Test Conditions	HYG058N15NS1		Unit	
Symbol	ymbol Parameter Test Conditions		Min	Тур.	Max	Unit
Static Char	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} = 250μA	150	-	-	V
Inno	Drain to Source Lookage Current	VDS=150V,VGS=0V	-	-	1	μΑ
loss Dra	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μA
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	2	3.2	4	V
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	1	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =80A	1	5.0	6.2	mΩ
Diode Char	Diode Characteristics					
VsD	Diode Forward Voltage	IsD=80A,Vgs=0V	1	0.88	1.20	V
trr	Reverse Recovery Time	Isp=80A,dlsp/dt=100A/µs	-	108	-	ns
Qrr	Reverse Recovery Charge	15D=00A,α15D/αt=100A/μ5	-	399	-	nC

^{*} Repetitive rating; pulse width limited by max.junction temperature.

^{**} Surface mounted on 1in2 FR-4 board.

^{***} Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25Ω , Vgs =10V.

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Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

O. mala al	Double of the state of the stat	Tank Camalikiana	HY	HYG058N15NS1		
Symbol Parameter		Test Conditions	Min	Тур.	Max	Unit
Dynamic (Dynamic Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V, f=500KHz	-	1.8	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	6415	-	
Coss	Output Capacitance	V _{DS} =25V,	-	2756	-	pF
Crss	Reverse Transfer Capacitance	f=500KHz	-	60	-	
td(ON)	Turn-on Delay Time		-	55	-	
Tr	Turn-on Rise Time	$V_{DD}=75V,R_{G}=2.5\Omega,$	-	47	-	
td(OFF)	Turn-off Delay Time	IDS=80A,VGS=10V	-	48	-	ns
Tf	Turn-off Fall Time		-	14	-	
Gate Char	ge Characteristics				•	
Qg	Total Gate Charge(V _{GS} =10V)		-	95	-	
Qgs	Gate-Source Charge	V -120V I -90A	-	37	-	nC
Qgd	Gate-Drain Charge	V_{DS} =120V, I_{DS} =80A	-	19	-	
V _{plateau}	Gate plateau voltage		-	5.8	-	V

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

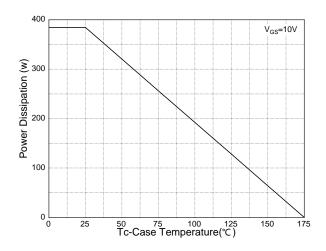


Figure 3: Safe Operation Area

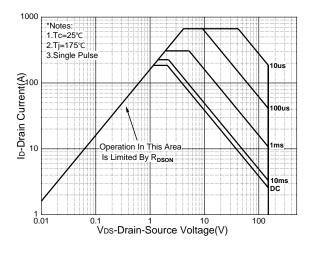


Figure 5: Output Characteristics

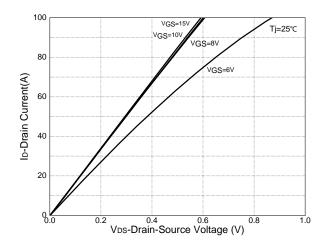


Figure 2: Drain Current

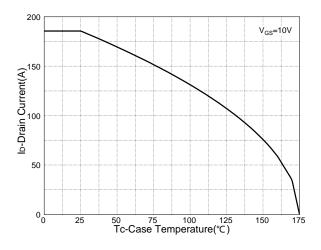


Figure 4: Thermal Transient Impedance

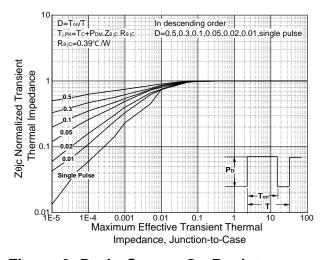
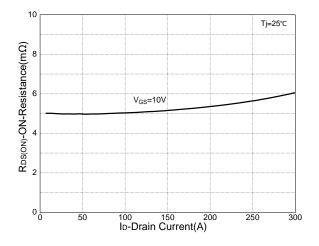


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

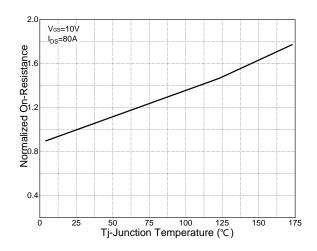


Figure 8: Source-Drain Diode Forward

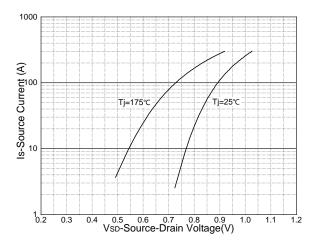


Figure 9: Capacitance Characteristics

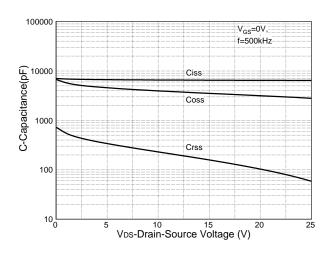
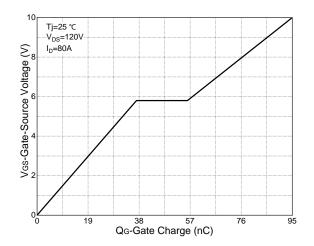
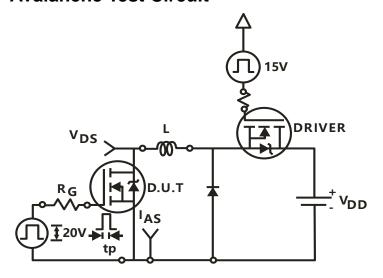


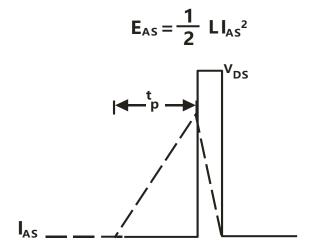
Figure 10: Gate Charge Characteristics



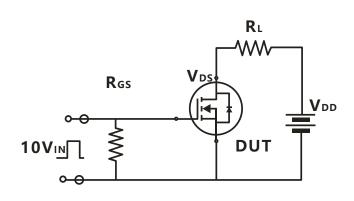


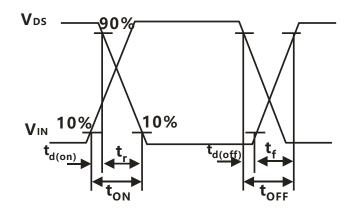
Avalanche Test Circuit



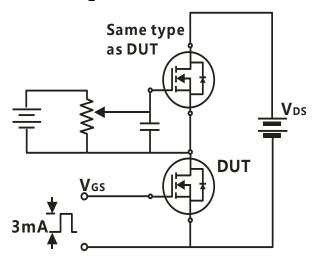


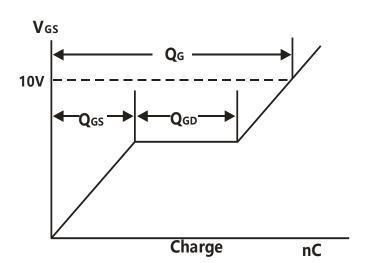
Switching Time Test Circuit





Gate Charge Test Circuit





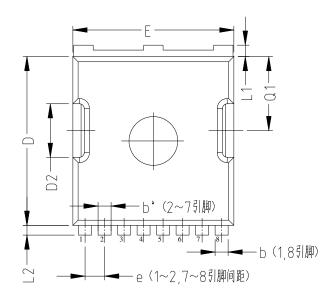


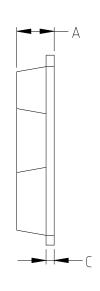
Device Per Unit

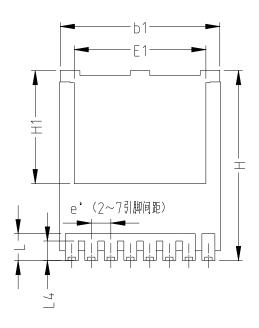
Package Type	Unit	Quantity
TOLL	Reel	1200

Package Information

TOLL





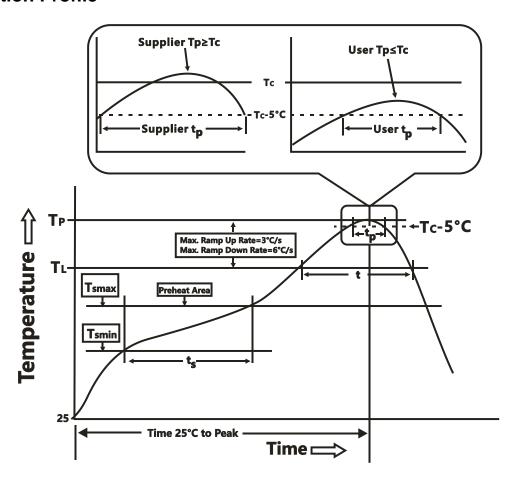


COMMON DIMENSIONS

SYMBOL	mm			
STIVIBOL	MIN	NOM	MAX	
Α	2.15	2.30	2.45	
b	0.70	0.75	0.85	
b'	0.65	0.70	0.80	
b1	9.65	9.80	9.95	
С	0.45	0.50	0.60	
D	10.18	10.38	10.58	
D2	3.15	3.30	3.45	
Е	9.70	9.90	10.10	
E1	7.95	8.10	8.25	
е	BSC 1.225			
e'	BSC 1.20			
Q1	4.40	4.55	4.70	
Н	11.48	11.68	11.88	
H1	6.80	6.95	7.10	
L	1.60	1.80	2.00	
L1	0.50	0.70	0.90	
L2	0.48	0.60	0.72	
L4	1.00	1.15	1.30	



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly			
Preheat & Soak					
Temperature min (T _{smin})	100 °C	150 °C			
Temperature max (T _{smax})	150 °C	200 °C			
Time (Tsmin to Tsmax) (t_s)	60-120 seconds	60-120 seconds			
Average ramp-up rate (T _{smax} to T _P)	3 °C/second max.	3°C/second max.			
Liquidous temperature (T _L)	183 °C	217 °C			
Time at liquidous (t∟)	60-150 seconds	60-150 seconds			
Peak package body Temperature $(T_p)^*$	See Classification Temp in table 1	SeeClassification Tempin table 2			
Time (t _P)** within 5°C of the specified classification temperature (T _c)	20** seconds	30** seconds			
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.			
Time 25°C to peak temperature	6 minutes max.	8 minutes max.			
*Tolerance for neak profile Temperature (T.) is defined as a supplier minimum and a user maximum					

^{*}Tolerance for peak profile Temperature (T_P) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (t_P) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm ³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168/500/1000 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500/1000 Cycles, -55°C~150°C

Customer Service

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