

MOSFETs Silicon N-channel MOS (U-MOSVII-H)

# **TP89R103NL**

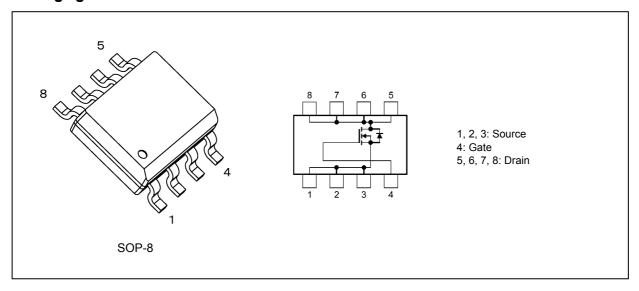
### 1. Applications

- Switching Voltage Regulators
- · DC-DC Converters

#### 2. Features

- (1) High-speed switching
- (2) Small gate charge:  $Q_{SW} = 2.5 \text{ nC (typ.)}$
- (3) Low drain-source on-resistance:  $R_{DS(ON)}$  = 10.4 m $\Omega$  (typ.) ( $V_{GS}$  = 4.5 V)
- (4) Low leakage current:  $I_{DSS}$  = 10  $\mu A$  (max) ( $V_{DS}$  = 30 V)
- (5) Enhancement mode:  $V_{th}$  = 1.3 to 2.3 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 0.1 mA)

### 3. Packaging and Internal Circuit





### 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			$V_{DSS}$	30	V
Gate-source voltage			$V_{GSS}$	±20	
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	$I_D$	15	Α
Drain current (DC)	(T <sub>c</sub> = 25 °C)	(Note 1)	$I_D$	15	
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I <sub>DP</sub>	76	
Power dissipation	(t = 10 s)	(Note 3)	$P_{D}$	1.9	W
Power dissipation	(t = 10 s)	(Note 4)	$P_{D}$	1.0	
Single-pulse avalanche energy		(Note 5)	E <sub>AS</sub>	28	mJ
Avalanche current			I <sub>AR</sub>	15	Α
Channel temperature			T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit		
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R <sub>th(ch-a)</sub>	65.7	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 4)	R <sub>th(ch-a)</sub>	125	

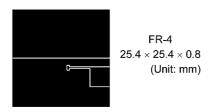
Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2: Limited by silicon chip capability.

Note 3: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 4: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 5:  $V_{DD}$  = 24 V,  $T_{ch}$  = 25 °C (initial), L = 99  $\mu$ H,  $I_{AR}$  = 15 A



 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8 \\ & \text{(Unit: mm)} \end{aligned}$ 

Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



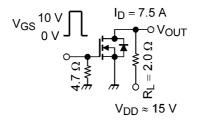
### 6. Electrical Characteristics

## 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	30	_		٧
	V <sub>(BR)DSX</sub>	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_		
Gate threshold voltage	$V_{th}$	$V_{DS} = 10 \text{ V}, I_{D} = 0.1 \text{ mA}$	1.3	_	2.3	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 6 A	_	10.4	12.9	mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.5 A	_	7.8	9.1	

# 6.2. Dynamic Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	630	820	pF
Reverse transfer capacitance	$C_{rss}$		_	21	52	
Output capacitance	C <sub>oss</sub>		_	350		
Gate resistance	r <sub>g</sub>	_		1.1	1.7	Ω
Switching time (rise time)	t <sub>r</sub>	See Fig. 6.2.1.		2.3		ns
Switching time (turn-on time)	t <sub>on</sub>			8.4		
Switching time (fall time)	t <sub>f</sub>		_	2.3	_	
Switching time (turn-off time)	t <sub>off</sub>		_	14		



Duty  $\leq$  1%,  $t_W =$  10  $\mu s$ 

Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	$Q_g$	$V_{DD} \approx 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	9.8		nC
gate-drain)		$V_{DD} \approx 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$	_	4.4		
Gate-source charge 1	Q <sub>gs1</sub>	$V_{DD} \approx 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	2.6	_	
Gate-drain charge	$Q_{gd}$		_	1.3		
Gate switch charge	$Q_{SW}$		_	2.5		

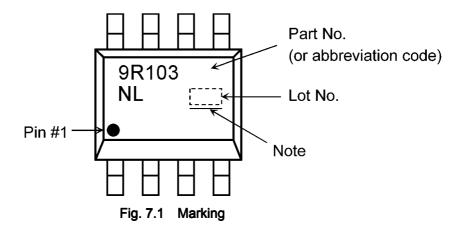
### 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 6)	I <sub>DRP</sub>	_	_	_	76	Α
Diode forward voltage		$V_{DSF}$	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V

Note 6: Ensure that the channel temperature does not exceed 150 °C.



# 7. Marking (Note)



Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Rev.2.0

# 8. Characteristics Curves (Note)

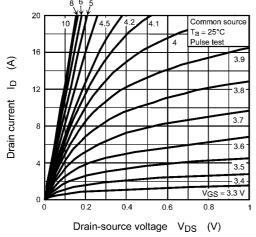
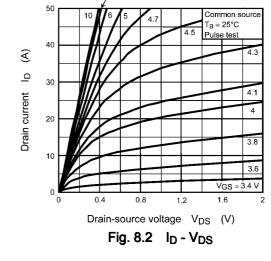


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>



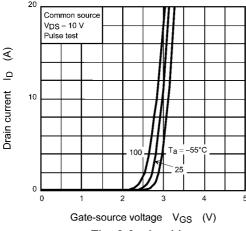


Fig. 8.3 I<sub>D</sub> - V<sub>GS</sub>

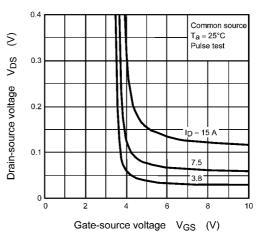


Fig. 8.4 V<sub>DS</sub> - V<sub>GS</sub>

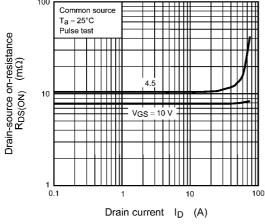


Fig. 8.5  $R_{DS(ON)}$  -  $I_D$ 

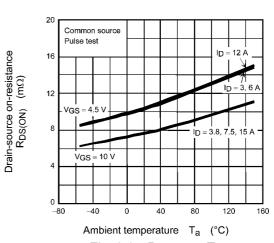


Fig. 8.6 R<sub>DS(ON)</sub> - T<sub>a</sub>

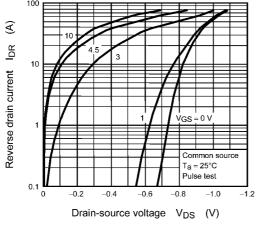


Fig. 8.7 IDR - VDS

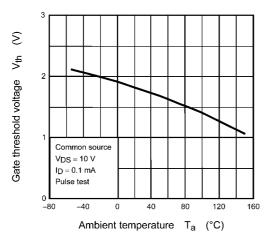


Fig. 8.9 Vth - Ta

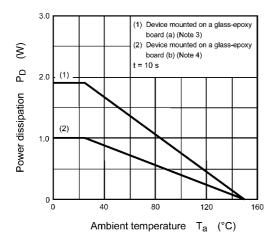


Fig. 8.11 P<sub>D</sub> - T<sub>a</sub> (Guaranteed Maximum)

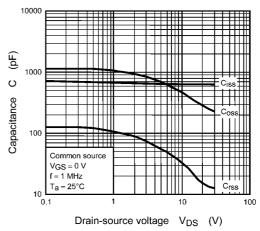


Fig. 8.8 Capacitance - V<sub>DS</sub>

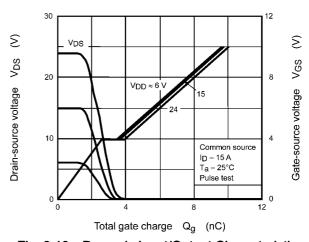


Fig. 8.10 Dynamic Input/Output Characteristics

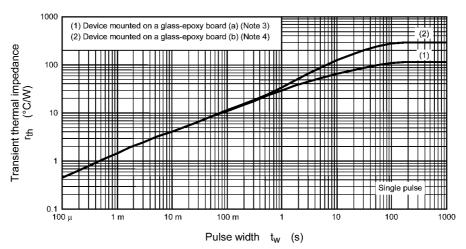


Fig. 8.12 r<sub>th</sub> - t<sub>w</sub> (Guaranteed Maximum)

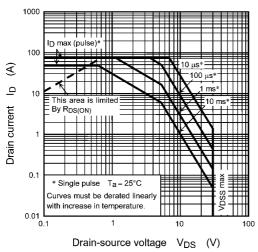


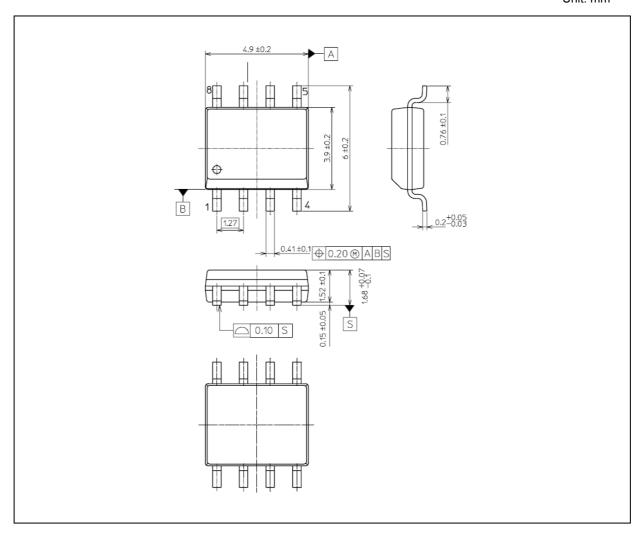
Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 0.085 g (typ.)

	Package Name(s)
TOSHIBA: 2-5R1S	
Nickname: SOP-8	



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