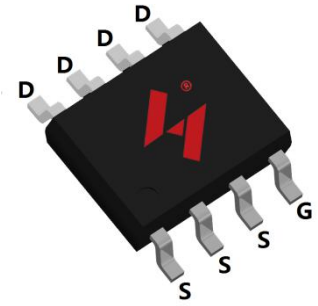
N-Channel Enhancement Mode MOSFET

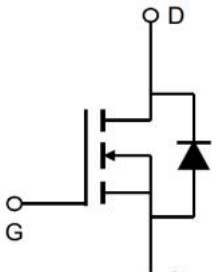
Feature

- 30V/9.5A
  $R_{DS(ON)}$= 14.5mΩ (typ.) @V_{GS} = 10V
  $R_{DS(ON)}$= 22.5mΩ (typ.) @V_{GS} = 4.5V
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

Pin Description



SOP-8L



N-Channel MOSFET

Applications

- Switching Application

Ordering and Marking Information

S G170N03 XYMXXXXXX	Package Code S: SOP-8L Date Code XYMXXXXXX

Note:HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish;which are fully compliant with RoHS.HUAYI lead-free products meet or exceed the lead-Free require-ments of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature.HUAYI defines “Green” to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this p r-oduct and/or to this document at any time without notice.

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		±20	V
T_J	Junction Temperature Range		-55 to 175	°C
T_{STG}	Storage Temperature Range		-55 to 175	°C
I_S	Source Current-Continuous(Body Diode)	Tc=25°C	9.5	A
Mounted on Large Heat Sink				
I_{DM}	Pulsed Drain Current *	Tc=25°C	60	A
I_D	Continuous Drain Current	Tc=25°C	9.5	A
		Tc=100°C	6.7	A
P_D	Maximum Power Dissipation	Tc=25°C	3.0	W
		Tc=100°C	1.5	W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient **		50	°C/W
E_{AS}	Single Pulsed-Avalanche Energy ***	L=0.1mH	13	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
** Surface Mounted on FR4 Board.
*** Limited by $T_J$max , starting T_J=25°C, L = 0.1mH, R_G= 25Ω, V_{GS} =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG170N03LR1			Unit
			Min	Typ.	Max	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}=250μA	30	-	-	V
I_{DSS}	Drain-to-Source Leakage Current	V_{DS}=30V,V_{GS}=0V	-	-	1	μA
		T_J=100°C	-	-	50	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1.0	1.9	3.0	V
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$*	Drain-Source On-State Resistance	V_{GS}=10V,I_{DS}=6A	-	14.5	17.0	mΩ
		V_{GS}=4.5V,I_{DS}=4A	-	22.5	26.0	
Diode Characteristics						
V_{SD}*	Diode Forward Voltage	I_{SD}=6A,V_{GS}=0V	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	I_{SD}=6A,dI_{SD}/dt=100A/μs	-	6.3	-	ns
Q_{rr}	Reverse Recovery Charge		-	1.9	-	nC

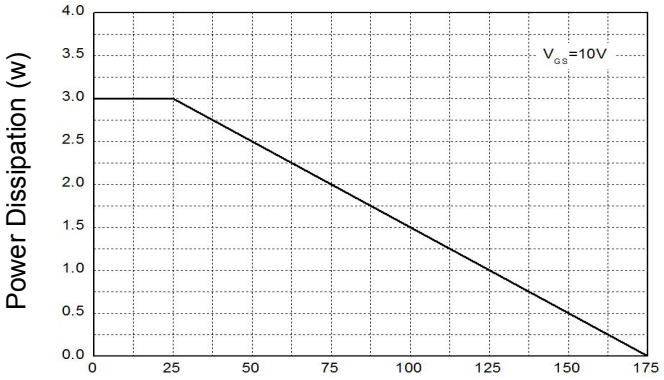
Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG170N03LR1			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V, F=1MHz	-	2.5	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V,	-	411	-	pF
C_{oss}	Output Capacitance	V_{DS}=25V,	-	51	-	
C_{rss}	Reverse Transfer Capacitance	F=1.0MHz	-	38	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=15V,R_G=4Ω, I_{DS}=6A,V_{GS}=10V;	-	5.9	-	ns
T_r	Turn-on Rise Time		-	17.5	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	11.9	-	
T_f	Turn-off Fall Time		-	3.0	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge (V_{GS}=10V)	V_{DS} =24V, V_{GS}=10V, I_D=6A;	-	9.4	-	nC
Q_g	Total Gate Charge (V_{GS}=4.5V)		-	4.8	-	
Q_{gs}	Gate-Source Charge		-	1.1	-	
Q_{gd}	Gate-Drain Charge		-	4.7	-	

Note: *Pulse test，pulse width ≤ 300us，duty cycle ≤ 2%

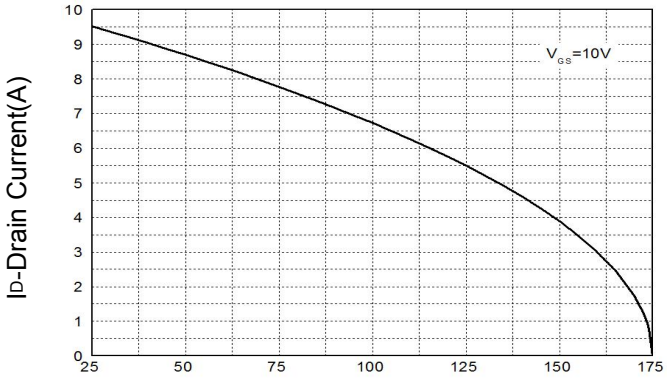
Typical Operating Characteristics

Figure 1: Power Dissipation



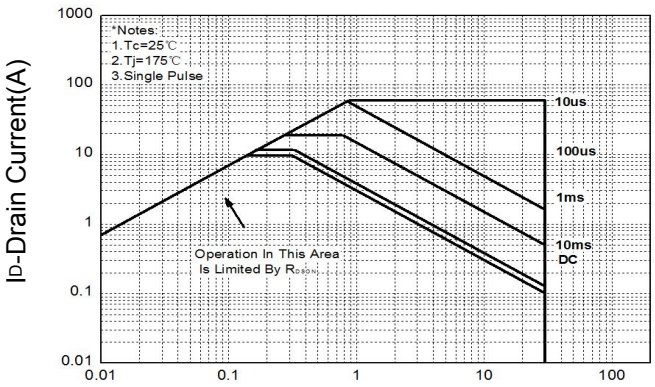
Tc-Case Temperature(℃)

Figure 2: Drain Current



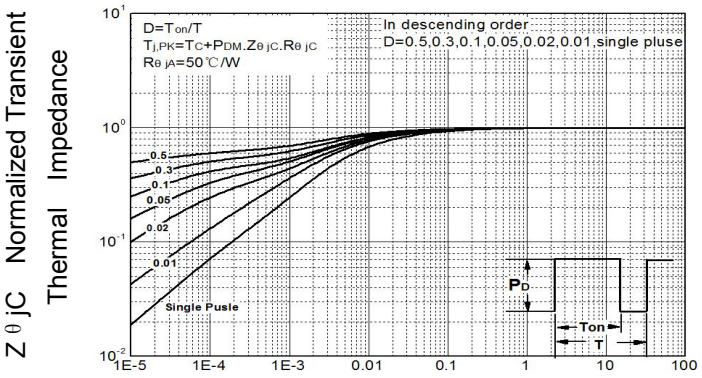
Tc-Case Temperature(℃)

Figure 3: Safe Operation Area



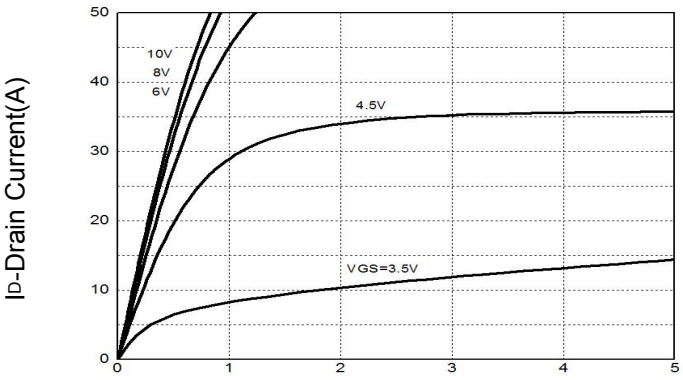
V_{DS}-Drain-Source Voltage(V)

Figure 4: Thermal Transient Impedance



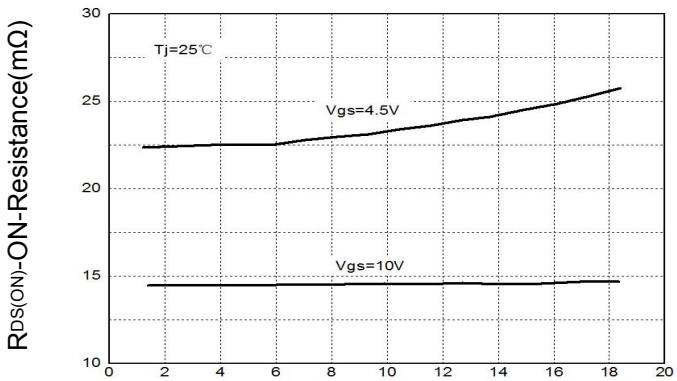
Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 5: Output Characteristics



V_{DS}-Drain-Source Voltage (V)

Figure 6: Drain-Source On Resistance



ID-Drain Current(A)

Typical Operating Characteristics(Cont.)

Figure 9: On-Resistance vs. Temperature

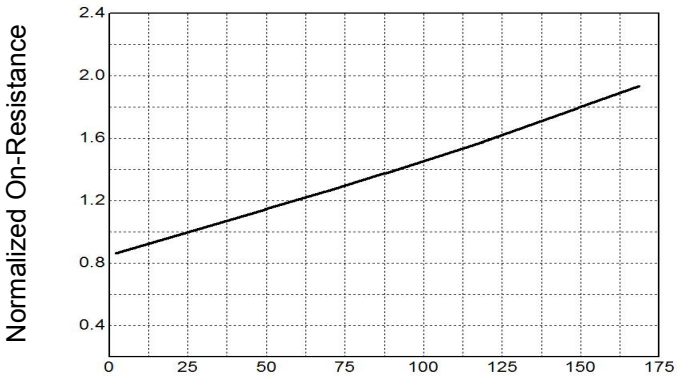


Figure 10: Source-Drain Diode Forward

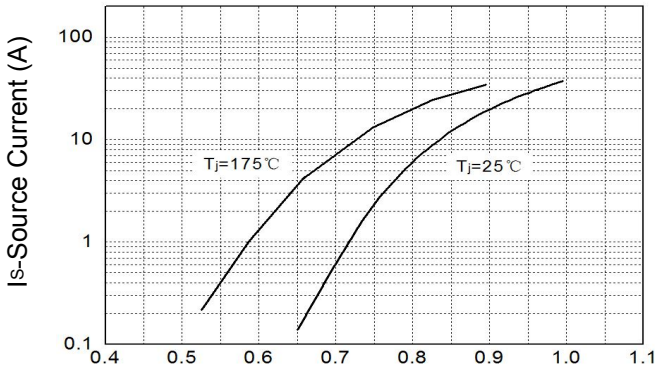


Figure 11: Capacitance Characteristics

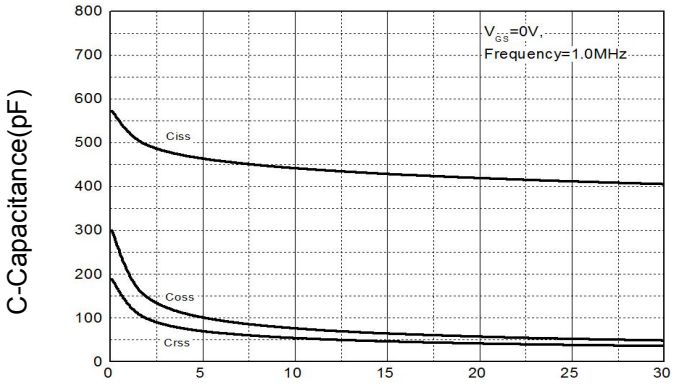
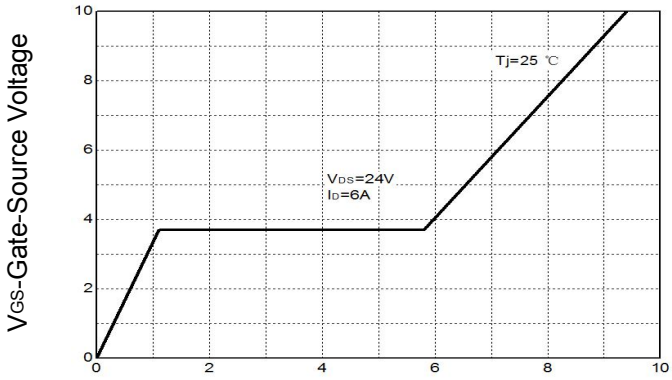
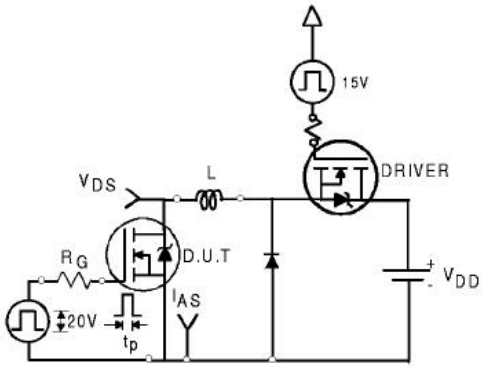


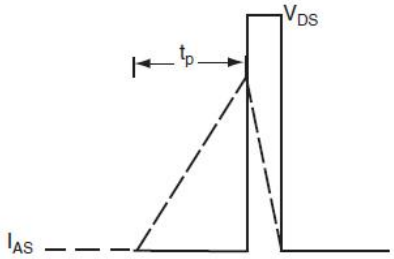
Figure 12: Gate Charge Characteristics



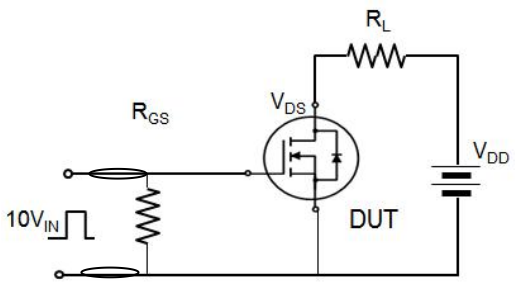
Avalanche Test Circuit

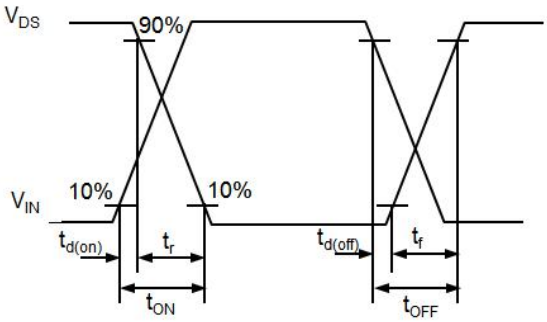


$$E_{AS} = \frac{1}{2} L I_{AS}^2$$

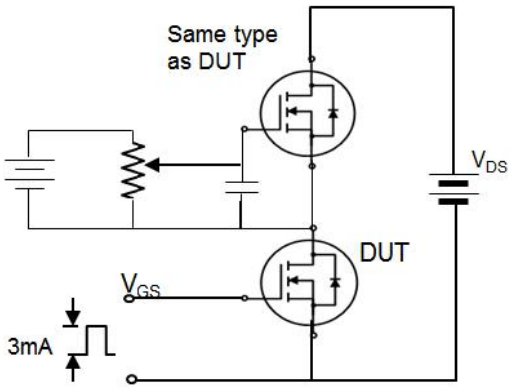


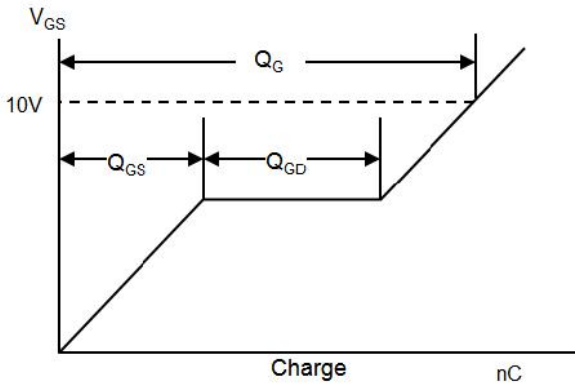
Switching Time Test Circuit





Gate Charge Test Circuit



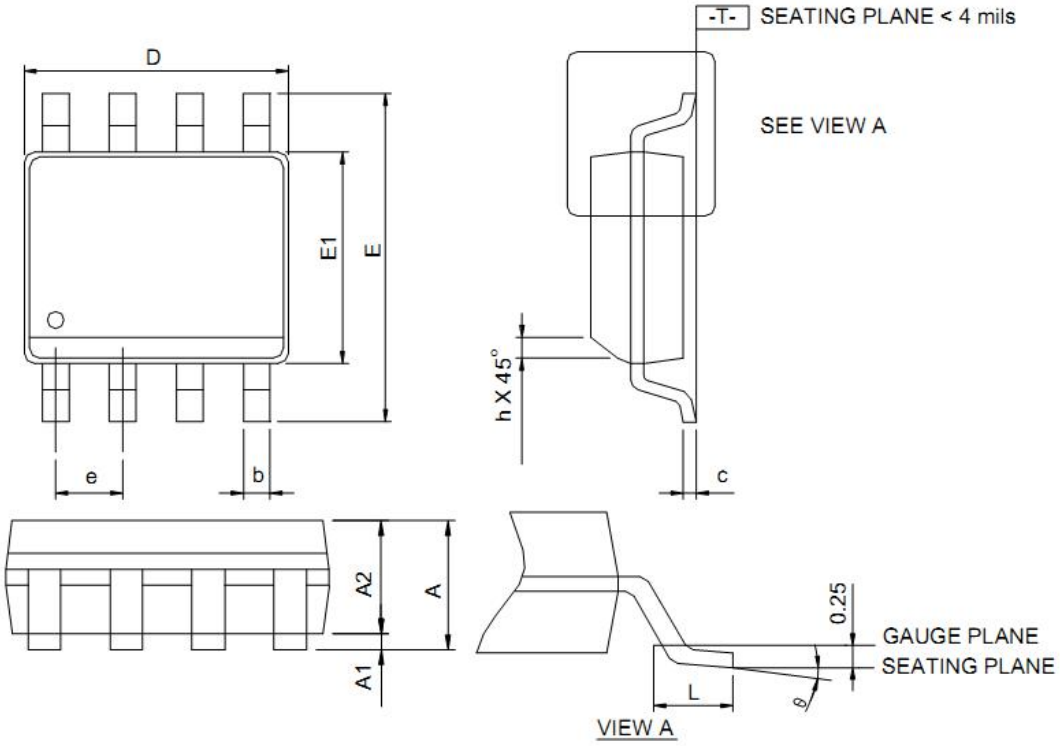


Device Per Unit

Package Type	Unit	Quantity
SOP-8L	Reel	2500

Package Information

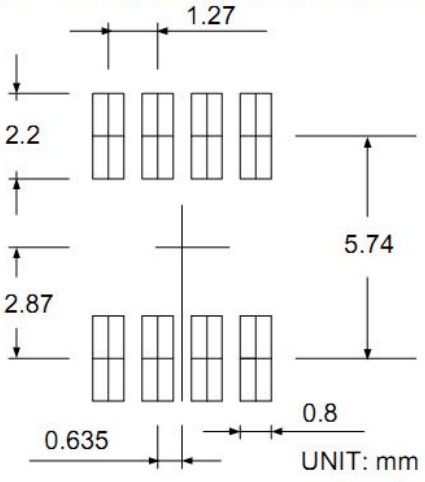
SOP-8L



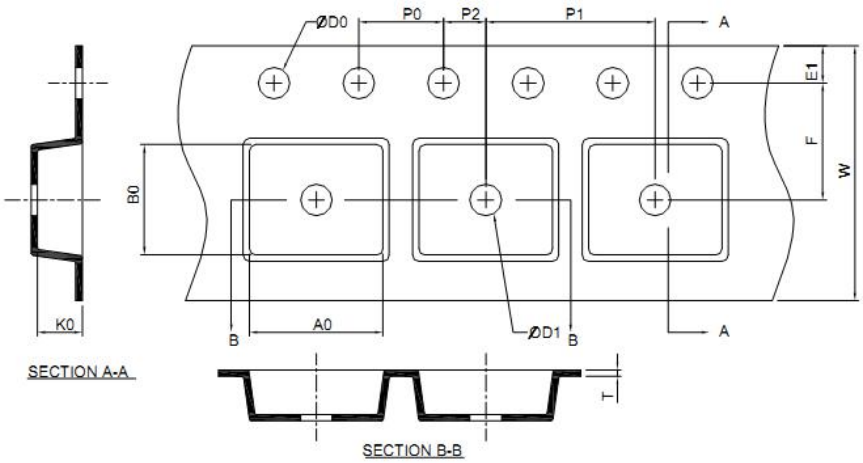
SYMBOL	SOP-8L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	1.75	-	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	-	0.049	-
b	0.31	0.51	0.012	0.020
c	0.17	0.25	0.007	0.010
D	4.80	5.00	0.189	0.197
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
h	0.25	0.50	0.010	0.020
L	0.40	1.27	0.016	0.050
θ	0°	8°	0°	8°

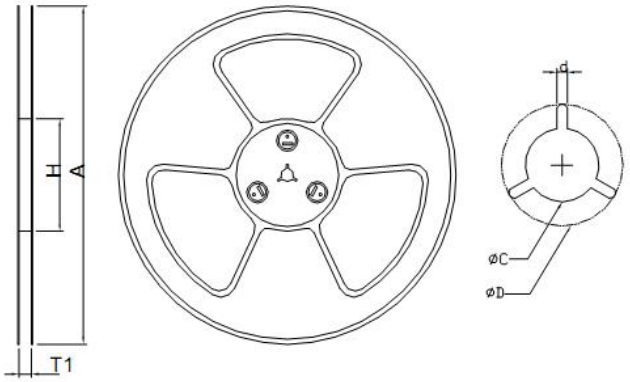
Note: 1. Follow JEDEC MS-012 AA.
2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
3. Dimension "E" does not include inter-lead flash or protrusions. Inter-lead flash and protrusions shall not exceed 10 mil per side.

RECOMMENDED LAND PATTERN



Carrier Tape & Reel Dimensions

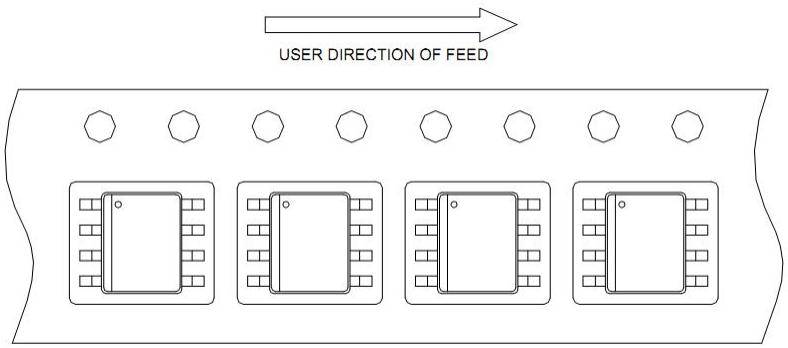




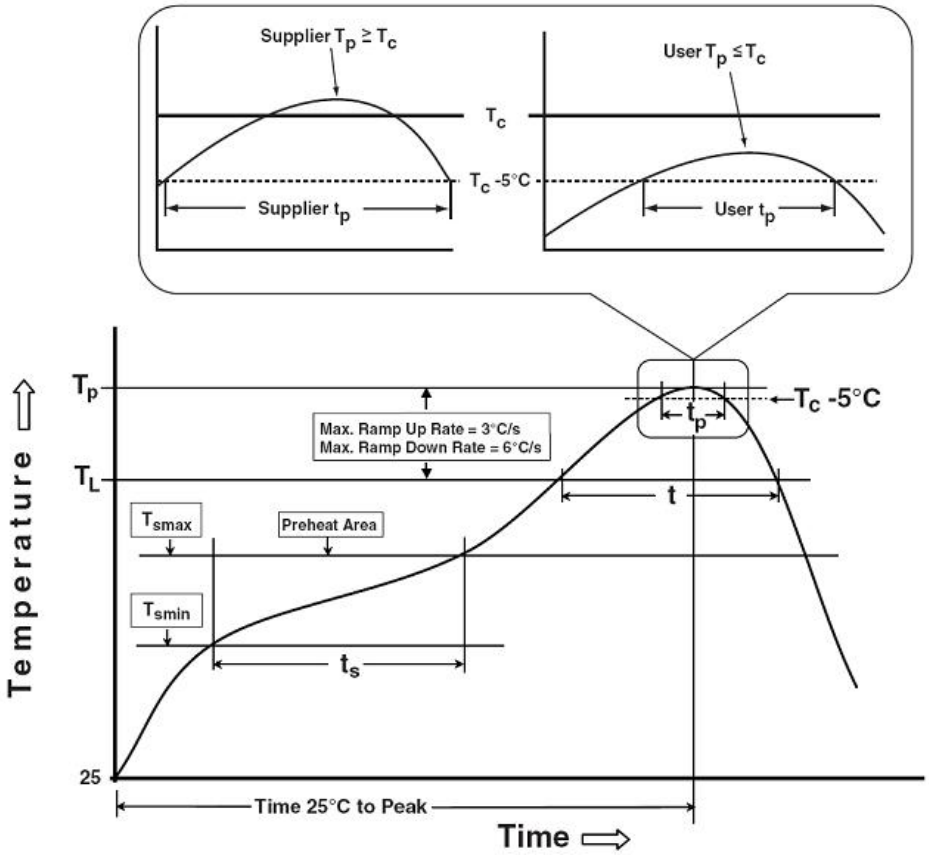
Application	A	H	T1	C	d	D	W	E1	F
SOP-8L	330.0 2.00	50 MIN.	12.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	12.0 0.30	1.75 0.10	5.5 0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 0.10	8.0 0.10	2.0 0.05	1.5+0.10 -0.00	1.5 MIN.	0.6+0.00 -0.40	6.40 0.20	5.20 0.20	2.10 0.20

(mm)

Taping Direction Information



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (Tsmin to Tsmax) (t_s)	 100 °C 150 °C 60-120 seconds	 150 °C 200 °C 60-120 seconds
Average ramp-up rate ($T_{smax}$to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	SeeClassification Tempin table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate ($T_p$to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ⩾350
＜2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350-2000	Volume mm³ ⩾2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
⩾2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, V_{gs}100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

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