

 T_L

 \mathbf{M}_{d}

T_{SOLD}

Weight

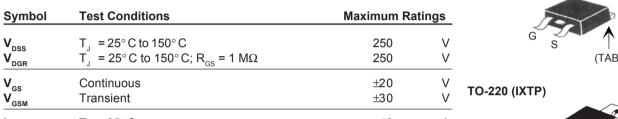
PolarHT[™] Power MOSFET

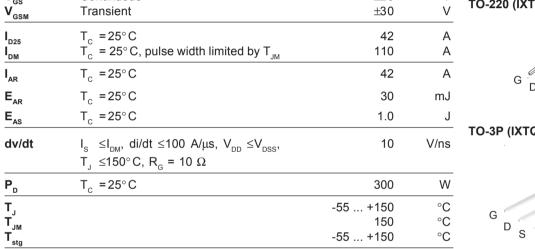
IXTA 42N25P IXTP 42N25P IXTQ 42N25P $V_{DSS} = 250 V \\ I_{D25} = 42 A \\ R_{DS(on)} \le 84 m\Omega$

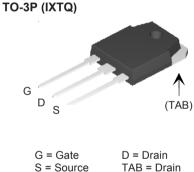
N-Channel Enhancement Mode Avalanche Rated



TO-263 (IXTA)







(TAB)

Symbol (T _J = 25° C, t	Test Conditions unless otherwise specified)		Ch Min.	istic Va Max	
BV _{DSS}	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		250		V
V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.0	5.5	V
I _{GSS}	$V_{GS} = \pm 20 V_{DC}, V_{DS} = 0$			±100	nA
I _{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$	T _J = 125° C		25 250	μA μA
R _{DS(on)}	V_{GS} = 10 V, I_{D} = 0.5 I_{D25} Pulse test, t ≤300 µs, duty	cycle d ≤ 2 %		84	mΩ

(TO-3P / TO-220)

1.6 mm (0.062 in.) from case for 10 s

Plastic body for 10 s

Mounting torque

TO-3P

TO-220

TO-263

Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
 - easy to drive and to protect

Advantages

- Easy to mount
- Space savings
- High power density

300

260

5.5

4

3

1.13/10 Nm/lb.in.

°C

°C

g

g

g

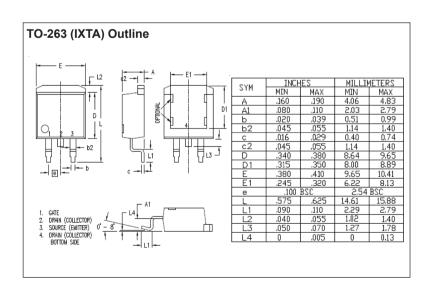


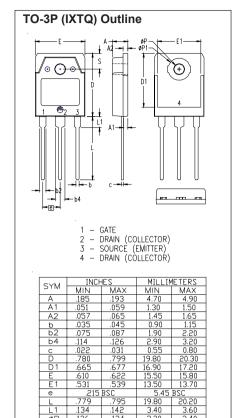
Symbol **Test Conditions Characteristic Values** (T₁ = 25° C, unless otherwise specified) Min. Typ. Max. V_{DS} = 10 V; I_{D} = 0.5 I_{D25} , pulse test 20 S $\boldsymbol{g}_{\mathsf{fs}}$ Ciss 2300 рF $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$ 430 рF 115 pF Crss 24 t_{d(on)} ns V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS} , I_{D} = 0.5 I_{D25} 28 t, ns $R_c = 10 \Omega (External)$ 81 ns $\mathbf{t}_{\mathsf{d(off)}}$ 30 t, ns $\boldsymbol{\mathsf{Q}_{\mathsf{g(on)}}}$ 70 nC \mathbf{Q}_{gs} V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS} , I_{D} = 0.5 I_{D25} 17 nC $\mathbf{Q}_{\underline{gd}}$ 37 nC $\mathbf{R}_{\mathrm{thJC}}$ 0.42°C/W °C/W (TO-3P) 0.21 R_{thCS} (TO-220) 0.25 $^{\circ}$ C/W

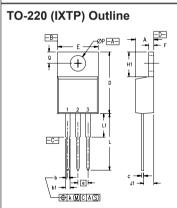
Source-Drain Diode

Characteristic Values (T, = 25°C, unless otherwise specified)

Symbol	Test Conditions	Min.	Тур.	Max.	,
Is	V _{GS} = 0 V			42	Α
I _{sm}	Repetitive			110	Α
V _{SD}	$I_F = I_S$, $V_{GS} = 0$ V, Pulse test, t ≤300 μs, duty cycle d≤ 2 %			1.5	V
t _{rr}	I _F = 25 A -di/dt = 100 A/μs		200		ns
\mathbf{Q}_{RM}	V _R = 100 V		2.0		μС







Pins:	1 - Gate	2 - Drair

MYZ	INCH	IES .	MILLIN	1ETERS
21M	MIN	MAX	MIN	MAX
Α	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
С	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
е	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
ØΡ	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

IXYS reserves the right to change limits, test conditions, and dimensions.



Fig. 1. Output Characteristics

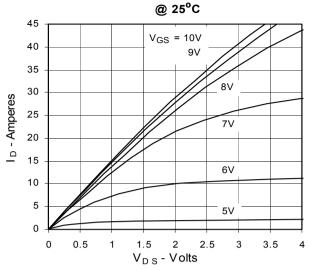


Fig. 2. Extended Output Characteristics
@ 25°C

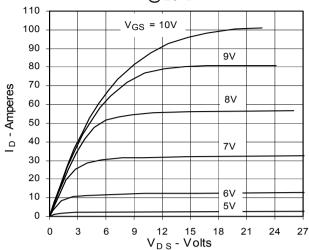


Fig. 3. Output Characteristics @ 125°C

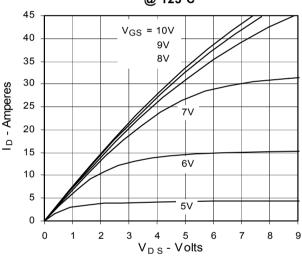


Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. Junction Temperature

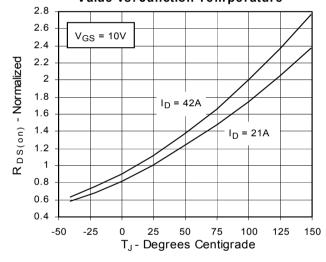


Fig. 5. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. I_{D}

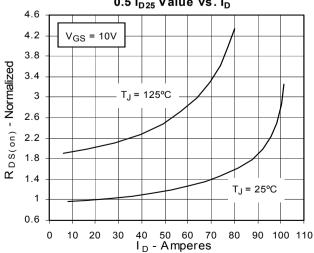


Fig. 6. Drain Current vs. Case
Temperature

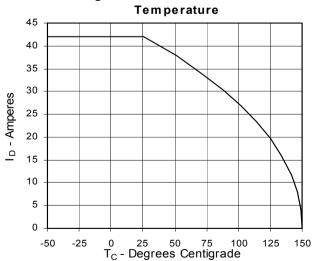




Fig. 7. Input Admittance

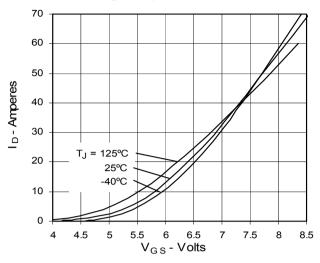


Fig. 8. Transconductance

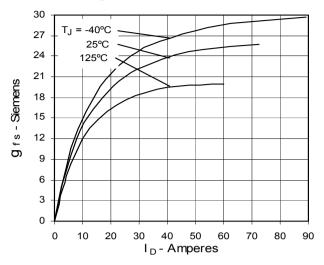


Fig. 9. Source Current vs. Source-To-Drain Voltage

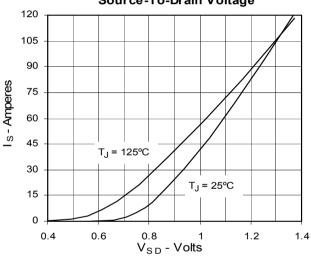


Fig. 10. Gate Charge

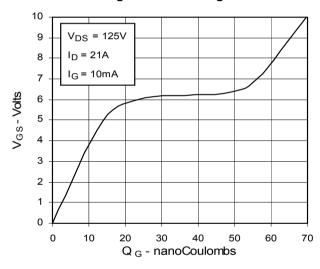


Fig. 11. Capacitance

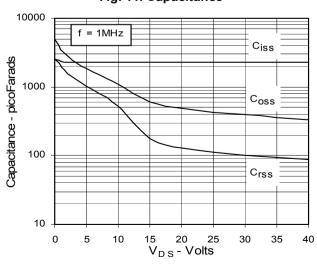
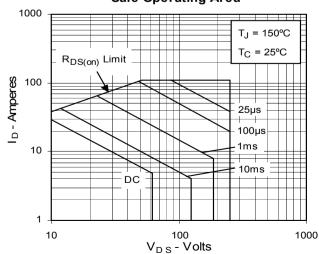


Fig. 12. Forward-Bias Safe Operating Area



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