

MOSFET

OptiMOS™ 6 Power-Transistor, 150 V

Features

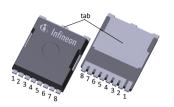
- N-channel, normal level
- Very low on-resistance R_{DS(on)}
- Superior thermal resistance
- 100% avalanche tested
- Pb-free lead plating; RoHS compliant
- Halogen-free according to IEC61249-2-21
- MSL 1 classified according to J-STD-020

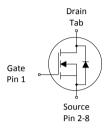
Product validation

Fully qualified according to JEDEC for Industrial Applications

Table 1 Key performance parameters

Parameter	Value	Unit
$V_{ m DS}$	150	V
$R_{\mathrm{DS(on),max}}$	4.7	mΩ
I_{D}	147	А
$Q_{\rm oss}$	153	nC
Q_{G}	51	nC
Q _{rr} (500A/μs)	145	nC







Type / Ordering code	Package	Marking	Related links
IPT047N15NM6	PG-HSOF-8	047N15N6	-

Public

OptiMOS™ 6 Power-Transistor, 150 V IPT047N15NM6



Table of contents

Description	1
Maximum ratings	3
Thermal characteristics	3
Electrical characteristics	4
Electrical characteristics diagrams	6
Package outlines	10
Revision history	13
Trademarks	13
Disclaimer	13



1 Maximum ratings

at T_A =25 °C, unless otherwise specified

Table 2 Maximum ratings

Parameter	Symbol	Values			Linit	Note / Test condition	
Parameter	Symbol	Min.	Тур.	Max.		Note / Test condition	
Continuous drain current ¹⁾	I _D	-	-	147 104 95 18.6	А	$V_{\rm GS}$ =10 V, $T_{\rm C}$ =25 °C $V_{\rm GS}$ =10 V, $T_{\rm C}$ =100 °C $V_{\rm GS}$ =8 V, $T_{\rm C}$ =100 °C $V_{\rm GS}$ =10 V, $T_{\rm A}$ =25 °C, $R_{\rm thJA}$ =40 °C/W ²⁾	
Pulsed drain current ³⁾	I _{D,pulse}	-	-	588	А	T -25 °C	
Avalanche current, single pulse 4)	I _{AS}	-	-	58	А	T _C =25 °C	
Avalanche energy, single pulse	E _{AS}	-	-	298	mJ	$I_{\rm D}$ =46 A, $R_{\rm GS}$ =25 Ω	
Gate source voltage	V_{GS}	-20	-	20	V	-	
Power dissipation	P_{tot}	-	-	234 3.8	w	$T_{\rm C}$ =25 °C $T_{\rm A}$ =25 °C, $R_{\rm thJA}$ =40 °C/W ²⁾	
Operating and storage temperature	$T_{\rm j}$, $T_{\rm stg}$	-55	-	175	°C	-	

¹⁾ Rating refers to the product only with datasheet specified absolute maximum values, maintaining case temperature as specified. For other case temperatures please refer to Diagram 2. De-rating will be required based on the actual environmental conditions.

2 Thermal characteristics

Table 3 Thermal characteristics

Parameter	Symbol	Values			Linit	Note / Test condition
	Symbol	Min.	Тур.	Max.	Oille	Note / Test condition
Thermal resistance, junction - case	R_{thJC}	-	-	0.64	°C/W	
Thermal resistance, junction - ambient, 6 cm² cooling area ⁵⁾	R_{thJA}	-	-	40	°C/W	-
Thermal resistance, junction - ambient, minimal footprint	R_{thJA}	-	-	62	°C/W	

Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm 2 (one layer, 70 μ m thick) copper area for drain connection. PCB is vertical in still air.

²⁾ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical in still air.

³⁾ See Diagram 3 for more detailed information

⁴⁾ See Diagram 13 for more detailed information



3 Electrical characteristics

at $T_{\rm j}$ =25 °C, unless otherwise specified

Table 4 Static characteristics

Parameter	Symbol		Values			Note / Test condition	
raiailletei	Syllibot	Min.	Тур.	Max.		Note / Test condition	
Drain-source breakdown voltage	V _{(BR)DSS}	150	-	-	V	$V_{\rm GS}$ =0 V, $I_{\rm D}$ =1 mA	
Gate threshold voltage	$V_{\rm GS(th)}$	3.0	3.5	4.0	V	$V_{\rm DS} = V_{\rm GS}, I_{\rm D} = 131 \mu{\rm A}$	
Zero gate voltage drain current	I_{DSS}	-	0.1 10	1 100	μΑ	$V_{\rm DS}$ =120 V, $V_{\rm GS}$ =0 V, $T_{\rm j}$ =25 °C $V_{\rm DS}$ =120 V, $V_{\rm GS}$ =0 V, $T_{\rm j}$ =125 °C	
Gate-source leakage current	I _{GSS}	-	10	100	nA	$V_{\rm GS}$ =20 V, $V_{\rm DS}$ =0 V	
Drain-source on-state resistance	$R_{\mathrm{DS(on)}}$	-	3.8 4.2 4.7	4.4 4.7 5.6	mΩ	V_{GS} =15 V, I_D =58 A V_{GS} =10 V, I_D =58 A V_{GS} =8 V, I_D =29 A	
Gate resistance	R_{G}	-	0.93	1.4	Ω	-	
Transconductance	g_{fs}	50	100	_	S	$ V_{\rm DS} \ge 2 I_{\rm D} R_{\rm DS(on)max}, I_{\rm D} = 58 \text{ A}$	

Table 5 Dynamic characteristics

Parameter	Symbol	Values			Linit	Note / Test condition
	Symbol	Min.	Тур.	Max.		Note / Test condition
Input capacitance ⁶⁾	C _{iss}	-	3600	4700	pF	
Output capacitance ⁶⁾	Coss	-	1100	1400	рF	V _{GS} =0 V, V _{DS} =75 V, <i>f</i> =1 MHz
Reverse transfer capacitance ⁶⁾	C _{rss}	-	16	28	pF	
Turn-on delay time	$t_{d(on)}$	_	15	-	ns	
Rise time	t _r	-	10	-	ns	$V_{\rm DD}$ =75 V, $V_{\rm GS}$ =10 V, $I_{\rm D}$ =29 A, $R_{\rm G,ext}$ =1.6 Ω
Turn-off delay time	$t_{\sf d(off)}$	_	23	-	ns	
Fall time	$t_{\rm f}$	-	11	-	ns	

 $^{^{6)}\;\;}$ Defined by design. Not subject to production test.



Table 6 Gate charge characteristics 7)

Parameter	Symbol		Values			Note / Test condition
	Symbol	Min.	Тур.	Max.	Ollic	Note / Test condition
Gate to source charge ⁸⁾	$Q_{ m gs}$	-	19.7	26	nC	
Gate charge at threshold	$Q_{\mathrm{g(th)}}$	-	12.8	-	nC	
Gate to drain charge ⁸⁾	$Q_{ m gd}$	-	11.8	17.7	nC	$V_{\rm DD}$ =75 V, $I_{\rm D}$ =29 A, $V_{\rm GS}$ =0 to 10 V
Switching charge	$Q_{\rm sw}$	-	18.7	-	nC	
Gate charge total ⁸⁾	$Q_{ m g}$	-	51	64	nC	
Gate plateau voltage	$V_{ m plateau}$	-	5.4	-	V	
Gate charge total, sync. FET	$Q_{\mathrm{g(sync)}}$	-	43	-	nC	V _{DS} =0.1 V, V _{GS} =0 to 10 V
Output charge ⁸⁾	$Q_{\rm oss}$	-	153	203	nC	V _{DS} =75 V, V _{GS} =0 V

⁷⁾ See "Gate charge waveforms" for parameter definition

Table 7 Reverse diode

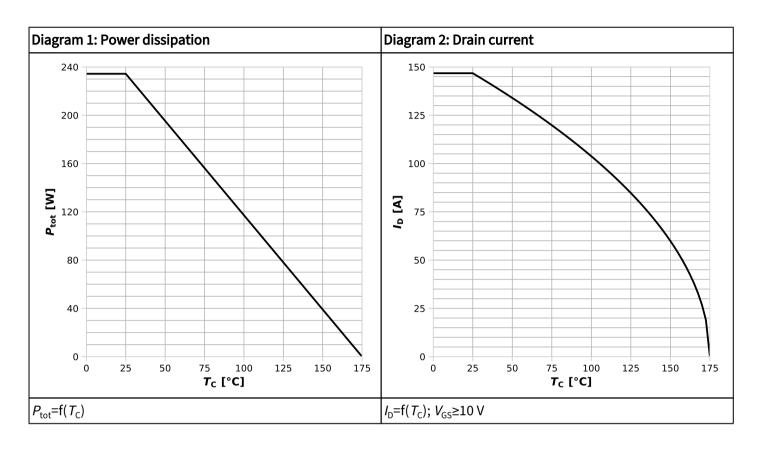
Parameter	Symbol	Values			Linit	Note / Test condition	
	Syllibol	Min.	Тур.	Max.		Note / Test condition	
Diode continuous forward current	I_{S}	-	-	147	Α	<i>T_c</i> =25 °C	
Diode pulse current	I _{S,pulse}	-	-	588	Α	7 _C -25 C	
Diode forward voltage	V_{SD}	-	0.85	1.0	V	$V_{\rm GS}$ =0 V, $I_{\rm F}$ =58 A, $T_{\rm j}$ =25 °C	
Reverse recovery time ⁹⁾	t _{rr}	-	35	70	ns	V _R =75 V, I _F =29 A, d <i>i</i> _F /d <i>t</i> =500 A/μs	
Reverse recovery charge ⁹⁾	$Q_{\rm rr}$	-	145	290	nC		
Reverse recovery time ⁹⁾	t _{rr}	-	30	60	ns	$V_{\rm R}$ =75 V, $I_{\rm F}$ =29 A, d $i_{\rm F}$ /d t =1000 A/ μ s	
Reverse recovery charge ⁹⁾	$Q_{\rm rr}$	-	242	484	nC		

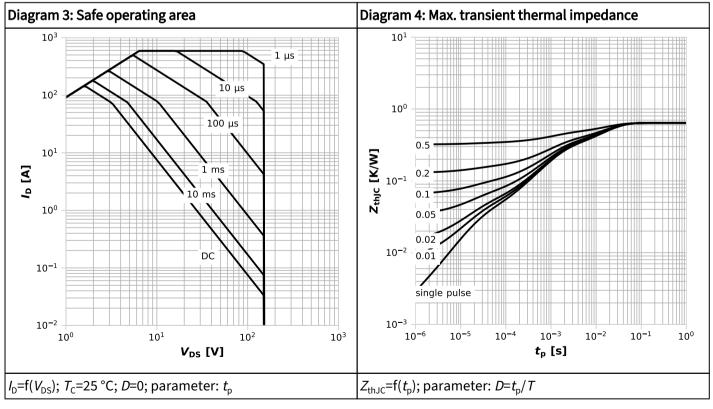
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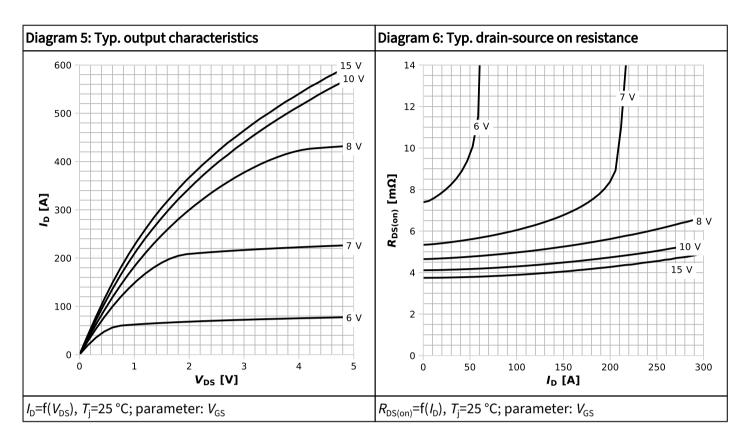


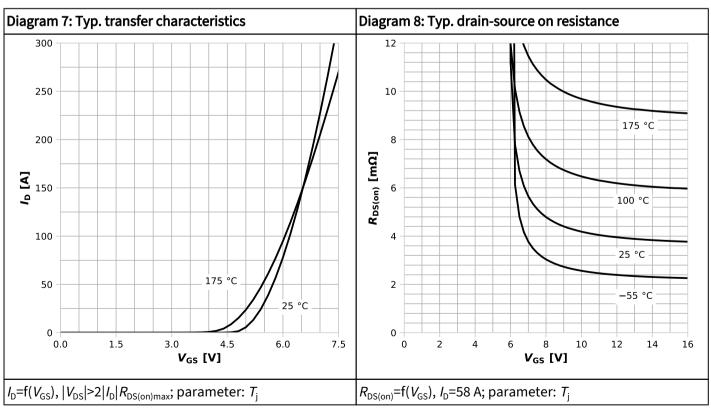
4 Electrical characteristics diagrams



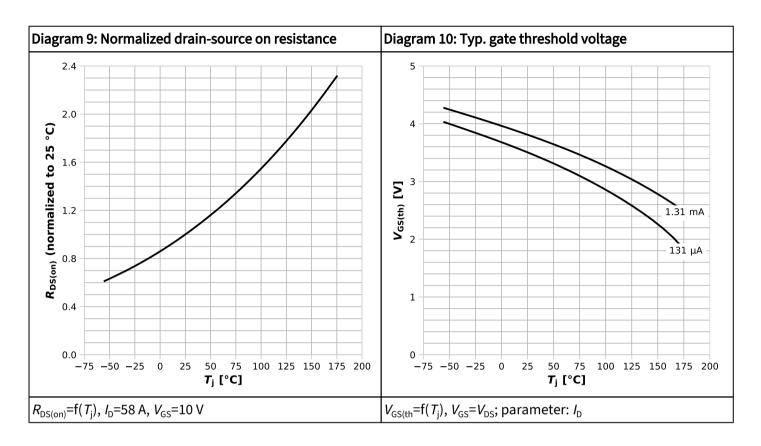


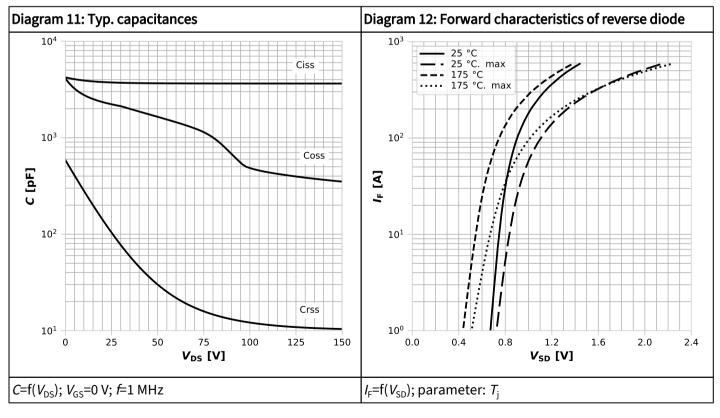




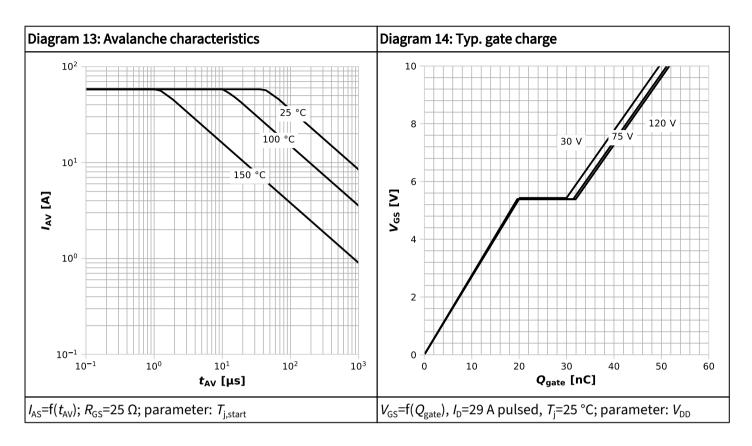


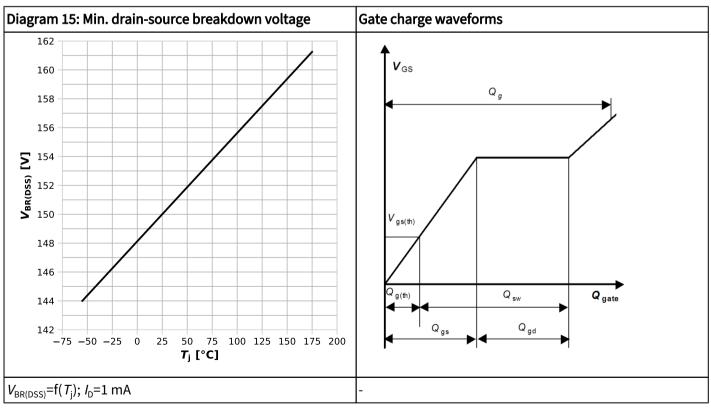






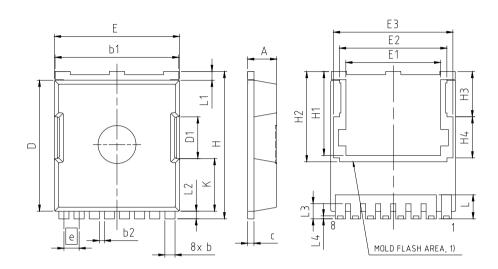




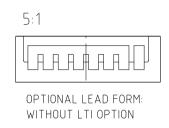




5 Package outlines



PACKAGE - GROUP NUMBER:	PG-HSOF-8-U01						
DIMENSIONS	MILLIM	ETERS					
DIMENSIONS	MIN.	MAX.					
Α	2.20	2.40					
b	0.70	0.90					
b1	9.70	9.90					
b2	0.42	0.50					
С	0.40	0.60					
D	10.28 10.58						
D1	3.	30					
E	9.70	10.10					
E1	7.50						
E2	8.50						
E3	9.46						
е	1.20 (BSC)					
Н	11.48 11.88						
H1	6.55 6.95						
H2	7.	15					
H3	3.	59					
H4	3.26						
N	8						
К	4.18						
L	1.60 2.10						
L1	0.50 0.90						
L2	0.50	0.70					
L3	1.00	1.30					
L4	0.13	0.33					



1) PATIALLY COVERED WITH MOLD FLASH

Figure 1 Outline PG-HSOF-8, dimensions in mm



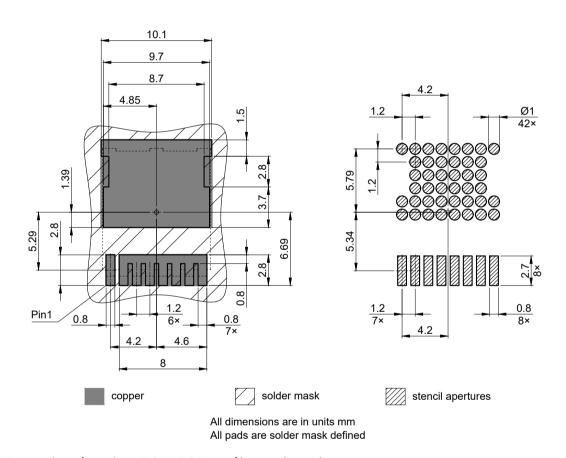


Figure 2 Footprint drawing PG-HSOF-8, dimensions in mm



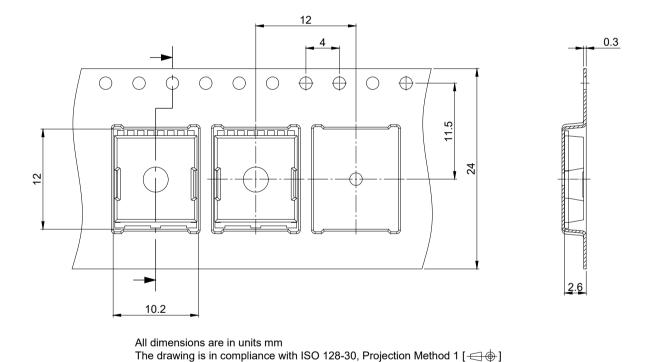


Figure 3 Packaging variant PG-HSOF-8, dimensions in mm



Revision history

IPT047N15NM6

Revision 2024-11-22, Rev. 1.0

Previous revisions

Revision	Date	Subjects (major changes since last revision)
1.0	2024-11-22	Release of final datasheet

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