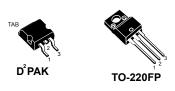


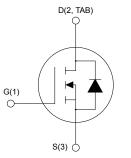


Datasheet

N-channel 650 V, 56 mΩ typ., 42 A, MDmesh M5 Power MOSFETs in D²PAK, TO-220FP and TO-220 packages







AM01475v1_noZen

Features

Order codes	V _{DS}	R _{DS(on)} max.	I _D
STB57N65M5			
STF57N65M5	650 V	63 mΩ	42 A
STP57N65M5			

- 100% avalanche tested
- · Excellent switching performance
- Extremely low R_{DS(on)}
- Low gate charge and input capacitance

Applications

Switching applications

Description

These devices are N-channel Power MOSFETs based on the MDmesh M5 innovative vertical process technology combined with the well-known PowerMESH horizontal layout. The resulting products offer extremely low on-resistance, making them particularly suitable for applications requiring high power and superior efficiency.



Product status links
STB57N65M5
STF57N65M5
STP57N65M5



Electrical ratings

Table 1. Absolute maximum ratings

Combal	Barrantar	Val	ue	Unit
Symbol	Parameter	D ² PAK, TO-220	TO-220FP	Unit
V_{GS}	Gate-source voltage	±2	25	V
I_	Drain current (continuous) at T _C = 25 °C	42	42 ⁽¹⁾	
I _D	Drain current (continuous) at T _C = 100 °C	26.5	26.5 (1)	Α
I _{DM} ⁽²⁾	Drain current (pulsed)	168		Α
P _{TOT}	Total power dissipation at T _C = 25 °C	5 °C 250 40		W
dv/dt ⁽³⁾	Peak diode recovery voltage slope	15		V/ns
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t = 1 s; T_C = 25 °C)	2.5		kV
T _J	Operating junction temperature range	-55 to 150		°C
T _{stg}	Storage temperature range			°C

- 1. Limited by maximum junction temperature.
- 2. Pulse width limited by safe operating area.
- 3. $I_{SD} \le 42$ A, $di/dt \le 400$ A/ μ s, $V_{DD} = 400$ V, V_{DS} (peak) $< V_{(BR)DSS}$.

Table 2. Thermal data

Symbol	Parameter		Unit		
Symbol	r al allietei	D ² PAK	TO-220	TO-220FP	Oilit
R _{thJC}	Thermal resistance, junction-to-case	0.5		3.1	°C/W
R _{thJA}	Thermal resistance, junction-to-ambient	30 ⁽¹⁾	62	2.5	°C/W

^{1.} When mounted on an 1 inch² FR-4, 2 Oz copper board.

Table 3. Avalanche characteristics

Symbol	Parameter	Value	Unit
I _{AR}	Avalanche current, repetitive or non-repetitive (pulse width limited by T _J max.)	7	А
E _{AS}	Single pulse avalanche energy (starting $T_J = 25$ °C, $I_D = I_{AR}$, $V_{DD} = 50$ V)	960	mJ

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2 Electrical characteristics

 T_C = 25 °C unless otherwise specified.

Table 4. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	I _D = 1 mA, V _{GS} = 0 V	650			V
		V _{GS} = 0 V, V _{DS} = 650 V			1	
I _{DSS}	Zero gate voltage drain current	$V_{GS} = 0 \text{ V}, V_{DS} = 650 \text{ V},$ $T_{C} = 125 ^{\circ}\text{C}^{(1)}$			100	μA
I _{GSS}	Gate body leakage current	V _{GS} = ±25 V, V _{DS} = 0 V			±100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3	4	5	V
R _{DS(on)}	Static drain-source on-resistance	V _{GS} = 10 V, I _D = 21 A		56	63	mΩ

^{1.} Specified by design, not tested in production.

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance	V _{DS} = 100 V, f = 1 MHz,	-	4200	-	pF
C _{oss}	Output capacitance	$V_{DS} = 100 \text{ V}, 1 = 1 \text{ IMHz},$ $V_{GS} = 0 \text{ V}$	-	115	-	pF
C _{rss}	Reverse transfer capacitance	VGS = 0 V	-	9	-	pF
C _{o(tr)} (1)	Equivalent capacitance time related		-	303	-	pF
C _{o(er)} (2)	Equivalent capacitance energy related	V _{DS} = 0 to 520 V, V _{GS} = 0 V	-	93	-	pF
R _g	Gate input resistance	f = 1 MHz, I _D = 0 A	-	1.3	-	Ω
Qg	Total gate charge	V _{DD} = 520 V, I _D = 21 A,	-	98	-	nC
Q _{gs}	Gate-source charge	V _{GS} = 0 to 10 V (see Figure 17. Test circuit for gate	-	23	-	nC
Q _{gd}	Gate-drain charge	charge behavior)	-	40	-	nC

^{1.} $C_{O(tr)}$ is an equivalent capacitance that provides the same charging time as C_{OSS} while V_{DS} is rising from 0 V to the stated

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(v)}	Voltage delay time	V _{DD} = 400 V, I _D = 28 A,	-	73	-	ns
t _{r(v)}	Voltage rise time	$R_G = 4.7 \Omega$, $V_{GS} = 10 V$	-	15	-	ns
t _{f(i)}	Current fall time	(see Figure 18. Test circuit for inductive load switching and diode	-	12	-	ns
t _{c(off)}	Crossing time	recovery times and Figure 21. Switching time waveform)	-	19	-	ns

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^{2.} $C_{O(er)}$ is an equivalent capacitance that provides the same stored energy as C_{OSS} while V_{DS} is rising from 0 V to the stated



Table 7. Source-drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current		-		42	Α
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)		-		168	Α
V _{SD} ⁽²⁾	Forward on voltage	I _{SD} = 42 A, V _{GS} = 0 V	-		1.5	V
t _{rr}	Reverse recovery time	I _{SD} = 42 A, di/dt = 100 A/μs,	-	418		ns
Q _{rr}	Reverse recovery charge	V _{DD} = 100 V	-	8		μC
I _{RRM}	Reverse recovery current	(see Figure 18. Test circuit for inductive load switching and diode recovery times)	-	40		Α
t _{rr}	Reverse recovery time	I _{SD} = 42 A, di/dt = 100 A/μs,	-	528		ns
Q _{rr}	Reverse recovery charge	V _{DD} = 100 V, T _J = 150 °C	-	12		μC
I _{RRM}	Reverse recovery current	(see Figure 18. Test circuit for inductive load switching and diode recovery times)	-	44		Α

^{1.} Pulse width limited by safe operating area.

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^{2.} Pulsed: pulse duration = 300 μs, duty cycle 1.5%.



2.1 Electrical characteristics (curves)

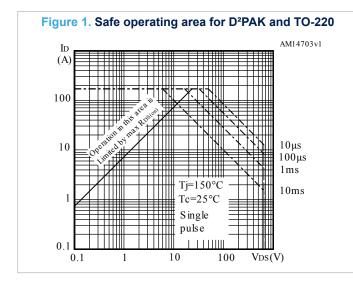
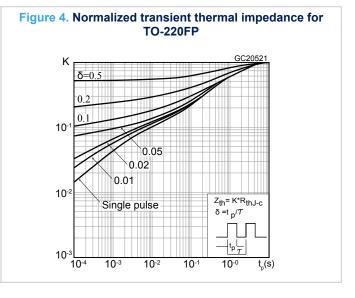
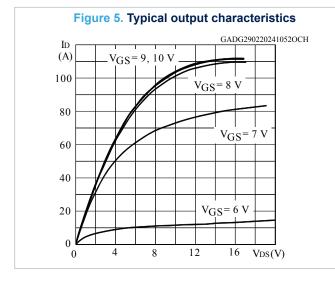
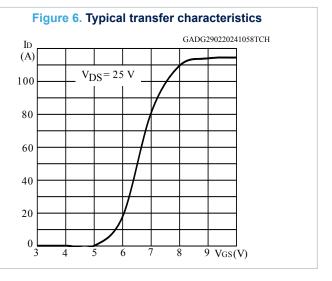


Figure 2. Normalized transient thermal impedance for D²PAK and TO-220 δ=0.5 δ=0.2 0.1 0.05 10-1 0.02 $Z_{th} = k R_{thJ-c}$ 0.01 $\delta = t_p / \tau$ Single pulse 10-2 10-5 10-4 10-3 10-2 10-1 $t_p(s)$







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Figure 7. Typical gate charge characteristics

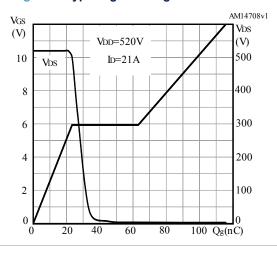


Figure 8. Typical drain-source on-resistance

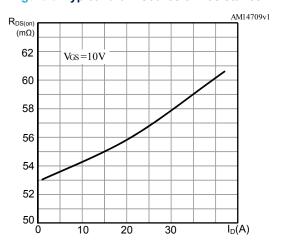


Figure 9. Typical capacitance characteristics

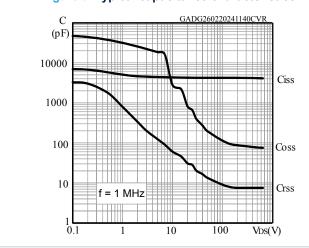


Figure 10. Typical output capacitance stored energy

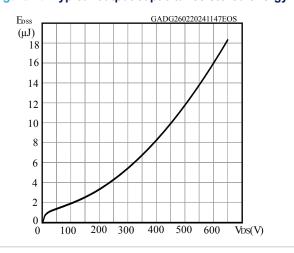


Figure 11. Normalized gate threshold vs temperature

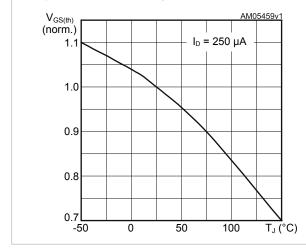
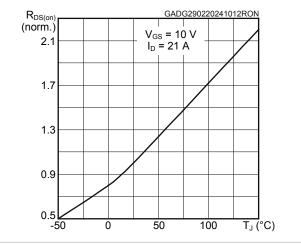


Figure 12. Normalized on-resistance vs temperature



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Figure 13. Typical reverse diode forward characteristics

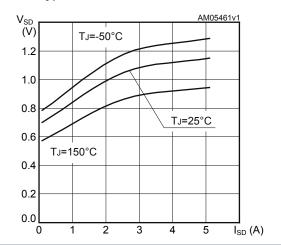
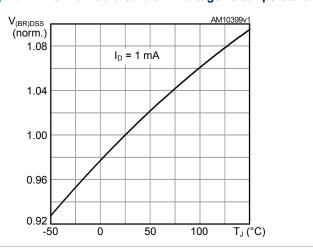
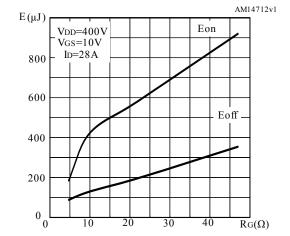


Figure 14. Normalized breakdown voltage vs temperature







Note: E_{on} including reverse recovery of a SiC diode.

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3 Test circuits

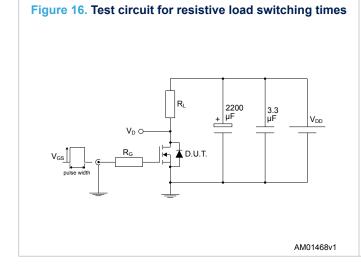


Figure 17. Test circuit for gate charge behavior

12 V 47 KΩ 1 KΩ

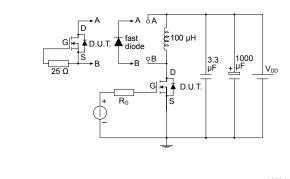
Vos 100 nF

12 V 47 KΩ

Vos 11 KΩ

AM01469v1

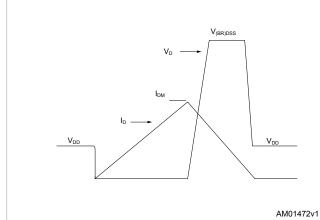
Figure 18. Test circuit for inductive load switching and diode recovery times

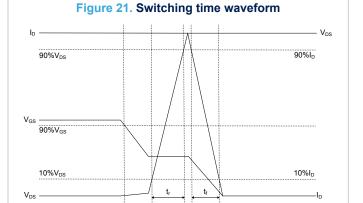


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Figure 19. Unclamped inductive load test circuit

Figure 20. Unclamped inductive waveform





AM05540v2

 $t_{\text{d}(\text{V})}$

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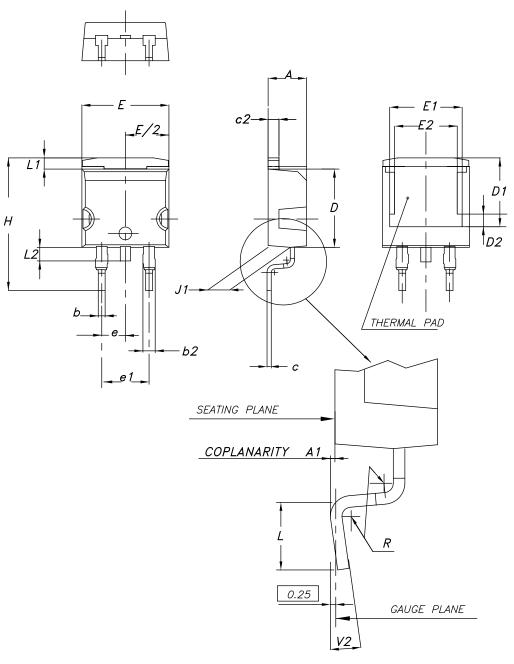


4 Package information

To meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions, and product status are available at: www.st.com. ECOPACK is an ST trademark.

4.1 D²PAK (TO-263) type A2 package information

Figure 22. D²PAK (TO-263) type A2 package outline



0079457_A2_27

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Table 8. D²PAK (TO-263) type A2 package mechanical data

Dim.	mm				
Dilli.	Min.	Тур.	Max.		
Α	4.40		4.60		
A1	0.03		0.23		
b	0.70		0.93		
b2	1.14		1.70		
С	0.45		0.60		
c2	1.23		1.36		
D	8.95		9.35		
D1	7.50	7.75	8.00		
D2	1.10	1.30	1.50		
E	10.00		10.40		
E1	8.70	8.90	9.10		
E2	7.30	7.50	7.70		
е		2.54			
e1	4.88		5.28		
Н	15.00		15.85		
J1	2.49		2.69		
L	2.29		2.79		
L1	1.27		1.40		
L2	1.30		1.75		
R		0.40			
V2	0°		8°		

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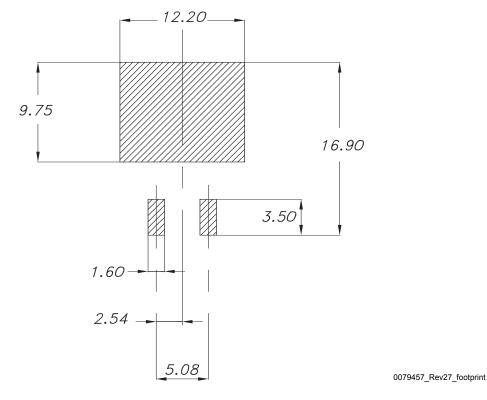
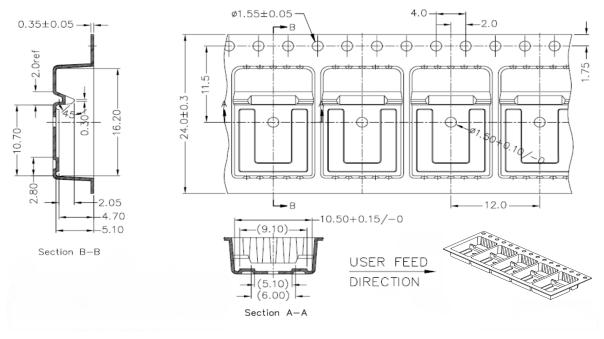


Figure 23. D²PAK (TO-263) recommended footprint (dimensions are in mm)

4.2 D²PAK packing information

Figure 24. D²PAK tape drawing (dimensions are in mm)



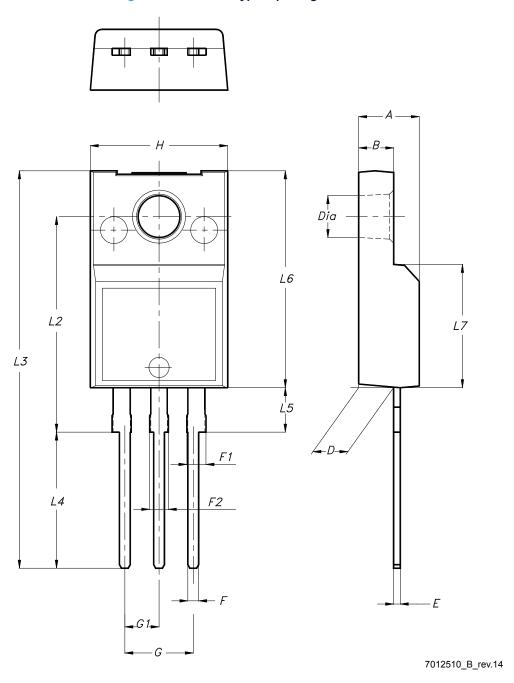
DM01095771_2

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4.3 TO-220FP package information

Figure 25. TO-220FP type B package outline



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Table 9. TO-220FP type B package mechanical data

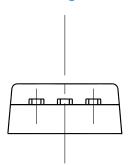
Dim.		mm	
Dilli.	Min.	Тур.	Max.
Α	4.40		4.60
В	2.50		2.70
D	2.50		2.75
E	0.45		0.70
F	0.75		1.00
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.20
G1	2.40		2.70
Н	10.00		10.40
L2		16.00	
L3	28.60		30.60
L4	9.80		10.60
L5	2.90		3.60
L6	15.90		16.40
L7	9.00		9.30
Dia	3.00		3.20

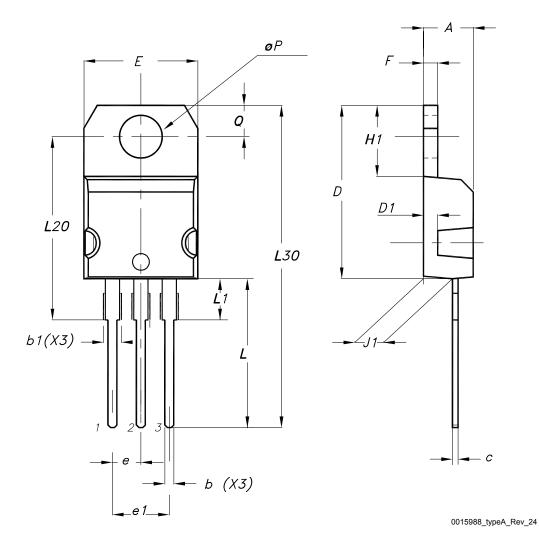
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4.4 TO-220 type A package information

Figure 26. TO-220 type A package outline





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Table 10. TO-220 type A package mechanical data

Dim.		mm	
DIM.	Min.	Тур.	Max.
Α	4.40		4.60
b	0.61		0.88
b1	1.14		1.55
С	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10.00		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13.00		14.00
L1	3.50		3.93
L20		16.40	
L30		28.90	
øΡ	3.75		3.85
Q	2.65		2.95
Slug flatness		0.03	0.10

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5 Ordering information

Table 11. Order codes

Order code	Marking	Package	Packing
STB57N65M5	57N65M5	D²PAK	Tape and reel
STF57N65M5		TO-220FP	Tube
STP57N65M5		TO-220	Tube

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Revision history

Table 12. Document revision history

Date	Version	Changes
06-Apr-2012	1	First release.
04-Jul-2012	2	Document status promoted from preliminary to production data. Added Section 2.1: Electrical characteristics (curves).
21-Aug-2012	3	Updated symbols and parameters in <i>Table 6: Switching times</i> . Minor text change on the cover page.
04-Dec-2012	4	The part number STW57N65M5 has been moved to a separate datasheet.
01-Mar-2024	5	The part number STI57N65M5 has been moved to a separate datasheet and the document has been updated accordingly. Modified I _{AR} value in <i>Table 3. Avalanche characteristics</i> . Updated <i>Section 4: Package information</i> .
20-Aug-2025	6	Minor text changes. Updated Section 4: Package information.

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4	Pac	kage information			
		D²PAK (TO-263) type A2 package information			
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	4.3	TO-220FP package information	12		
	4.4	TO-220 type A package information	14		
5	Ord	ering information	16		
Re	vision	history	17		

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