

#### N-Channel Enhancement Mode MOSFET

#### **Feature**

40V/290A

 $R_{DS(ON)} = 1.2 \text{ m}\Omega \text{ (typ.)} @V_{GS} = 10V$ 

 $R_{DS(ON)}$ = 1.6 m $\Omega$  (typ.) @V<sub>GS</sub> = 4.5V

- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free and Green Devices Available (RoHS Compliant)

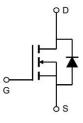
### **Pin Description**



TO-247A-3L

#### **Applications**

- Switching application
- Inverters
- Power Tool



N-Channel MOSFET

## **Ordering and Marking Information**



Package Code W:TO-247A-3L

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plateTermi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		175	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)	Source Current-Continuous(Body Diode) Tc=25°C		Α
Mounted on	Large Heat Sink			
<b>I</b> DМ	Pulsed Drain Current *	Tc=25°C	1150	А
	Outliness Paris Outline	Tc=25°C	290	Α
lσ	Continuous Drain Current	Tc=100°C	205	Α
		Tc=25°C	250	W
Pb	Maximum Power Dissipation Tc=100°C		125	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		0.6	°C/W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient **		40	°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	750***	mJ

Note:

- \* Repetitive rating; pulse width limited by max.junction temperature.
- \*\* Surface Mounted on FR4 Board.
- \*\*\* Limited by TJmax, starting TJ=25°C, L = 0.3mH, VD= 32V, VGs =10V.

## Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

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Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>DS</sub> =250μA	40	-		V
1	Drain to Source Leakage Current	V <sub>DS</sub> =40,V <sub>GS</sub> =0V	-	-	1	μΑ
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μΑ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	1	1.8	3	V
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
RDS(ON)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V,I <sub>DS</sub> =40A		1.2	1.5	mΩ
RDS(ON)	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V,I <sub>DS</sub> =40A		1.6	2.0	mΩ
Diode Cha	Diode Characteristics					
V <sub>SD</sub> *	Diode Forward Voltage	I <sub>SD</sub> =40A,V <sub>GS</sub> =0V	-	0.8	1.2	V
trr	Reverse Recovery Time	lan=404 dlan/dt=1004/up	-	44	-	ns
Qrr	Reverse Recovery Charge	IsD=40A,dIsD/dt=100A/µs	-	43	-	nC

# HYG016N04LS1W



## Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

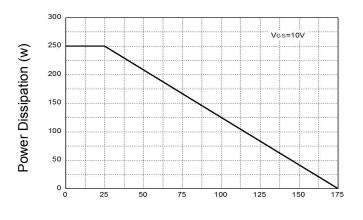
Cumbel	Barrarra et a r	Toot Conditions	HY	HYG016N04LS1		1114
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics					
<b>R</b> G	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	1.7	-	Ω
Ciss	Input Capacitance	V <sub>GS</sub> =0V,	-	5538	-	
Coss	Output Capacitance	V <sub>DS</sub> =25V,	-	1300	_	pF
Crss	Reverse Transfer Capacitance	Frequency=500KHz	-	80	-	
td(ON)	Turn-on Delay Time		-	16	-	
Tr	Turn-on Rise Time	$V_{DD}=20V,R_{G}=4\Omega,$	-	82	-	
<b>t</b> d(OFF)	Turn-off Delay Time	lps=40A,Vgs=10V	-	77	-	ns
Tf	Turn-off Fall Time		-	108	-	
Gate Cha	Gate Charge Characteristics					
Qg	Total Gate Charge (VGS=10V)		-	90	-	
Qg	Total Gate Charge (VGS=4.5V)	V <sub>DS</sub> =32V, V <sub>GS</sub> =10V,	-	43	-	
Qgs	Gate-Source Charge	I <sub>D</sub> =40A	-	20	-	nC
Qgd	Gate-Drain Charge		-	17	-	

Note: \*Pulse test, pulse width  $\leq 300$ us, duty cycle  $\leq 2\%$ 



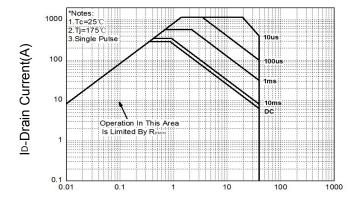
## **Typical Operating Characteristics**

**Figure 1: Power Dissipation** 



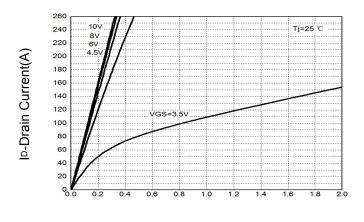
Tc-Case Temperature(°C)

Figure 3: Safe Operation Area



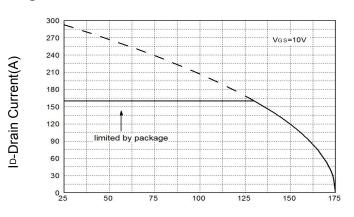
V<sub>DS</sub>-Drain-Source Voltage(V)

**Figure 5: Output Characteristics** 



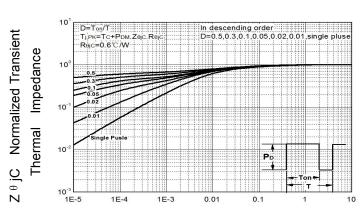
V<sub>DS</sub>-Drain-Source Voltage (V)

Figure 2: Drain Current



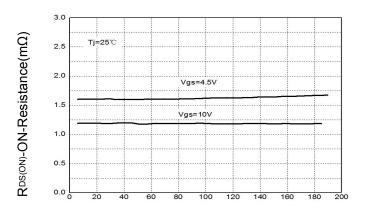
Tc-Case Temperature(°C)

Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 6: Drain-Source On Resistance

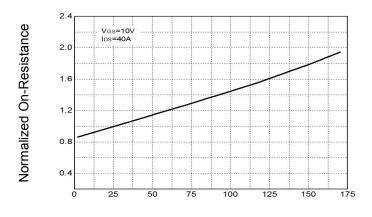


ID-Drain Current(A)



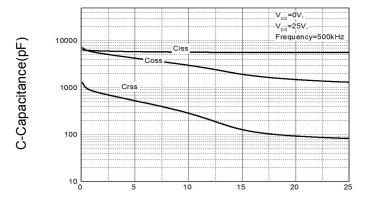
## **Typical Operating Characteristics(Cont.)**

Figure 9: On-Resistance vs. Temperature



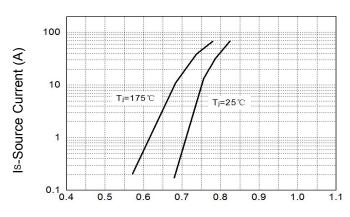
Tj-Junction Temperature (°C)

**Figure 11: Capacitance Characteristics** 



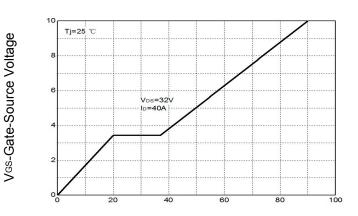
V<sub>DS</sub>-Drain-Source Voltage (V)

Figure 10: Source-Drain Diode Forward



Vsp-Source-Drain Voltage(V)

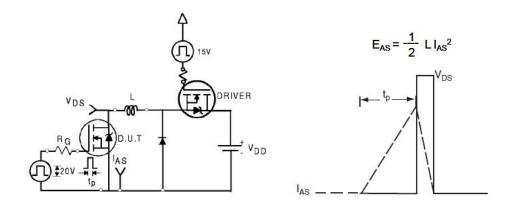
**Figure 12: Gate Charge Characteristics** 



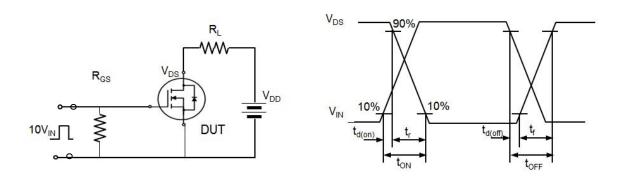
Qg-Gate Charge (nC)



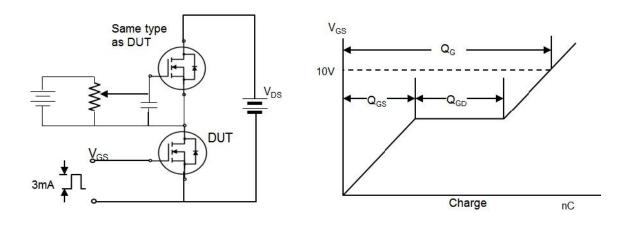
## **Avalanche Test Circuit and Waveforms**



# **Switching Time Test Circuit and Waveforms**



# **Gate Charge Test Circuit and Waveforms**



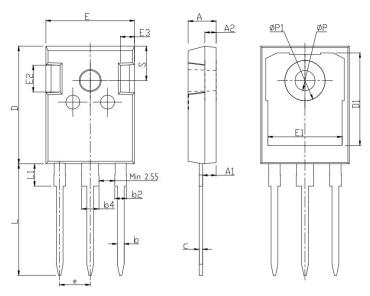


## **Device Per Unit**

Package Type	Unit	Quantity
TO-247A-3L	Tube	30

# Package Information

### TO-247A-3L

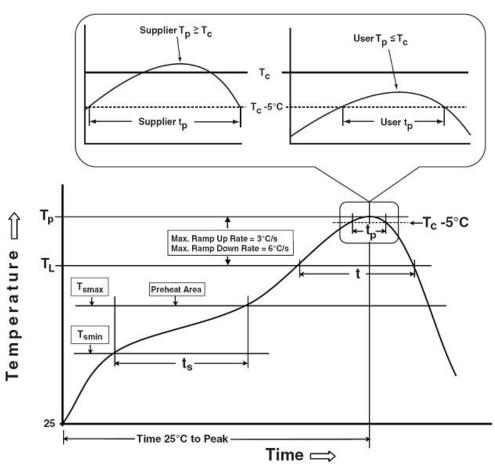


#### **COMMON DIMENSIONS**

0.0.5	mm mm			
SYMBOL	MIN	NOM	MAX	
Α	4.80	5.00	5.20	
A1	2.21	2.41	2.61	
A2	1.85	2.00	2.15	
b	1.11	1.21	1.36	
b2	1.91	2.01	2.21	
b4	2.91	3.01	3.21	
С	0.51	0.61	0.75	
D	20.70	21.00	21.30	
D1	16.25	16.55	16.85	
E	15.50	15.80	16.10	
E1	13.00	13.30	13.60	
E2	4.80	5.00	5.20	
E3	2.30	2.50	2.70	
е		5.44BSC		
L	19.62	19.92	20.22	
L1	-	-	4.30	
ФР	3.40	3.60	3.80	
ФР1	-	-	7.30	
S	6.15BSC			



### **Classification Profile**



### **Classification Reflow Profiles**

Sn-Pb Eutectic Assembly	Pb-Free Assembly
100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
3 °C/second max.	3°C/second max.
183 °C	217 °C
60-150 seconds	60-150 seconds
See Classification Temp in table 1	SeeClassification Tempin table 2
20** seconds	30** seconds
6 °C/second max.	6 °C/second max.
6 minutes max.	8 minutes max.
	100 °C 150 °C 60-120 seconds  3 °C/second max.  183 °C 60-150 seconds  See Classification Temp in table 1  20** seconds  6 °C/second max.

<sup>\*</sup>Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup> Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

#### Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## **Reliability Test Program**

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

#### **Customer Service**

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