

OptiMOSTM3 Power-Transistor

Features

- N-channel, normal level
- Excellent gate charge x R_{DS(on)} product (FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹⁾ for target application
- Ideal for high-frequency switching and synchronous rectification
- Halogen-free according to IEC61249-2-21

Туре	IPD180N10N3 G
	1 2 (tab)
Package	PG-TO252-3
Marking	180N10N

Maximum ratings, at T_i =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	ID	T _C =25 °C ²⁾	43	А
		T _C =100 °C	30	
Pulsed drain current ²⁾	I _{D,pulse}	T _C =25 °C	172	
Avalanche energy, single pulse	E _{AS}	I_{D} =33 A, R_{GS} =25 Ω	50	mJ
Gate source voltage	V_{GS}		±20	V
Power dissipation	P_{tot}	T _C =25 °C	71	W
Operating and storage temperature	$T_{\rm j},T_{\rm stg}$		-55 175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

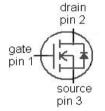
¹⁾J-STD20 and JESD22

Product Summary

V _{DS}	100	V
R _{DS(on),max TO-263}	18	mΩ
I_{D}	43	А







²⁾ See figure 3



Parameter	Symbol	Conditions	Values		Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	R_{thJC}		-	-	2.1	K/W
Thermal resistance,	R_{thJA}	minimal footprint	-	-	75	
junction - ambient		6 cm ² cooling area ³⁾	-	-	50	

Electrical characteristics, at T_j =25 °C, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =0 V, I _D =1 mA	100	-	-	V
Gate threshold voltage	$V_{\rm GS(th)}$	$V_{\rm DS}=V_{\rm GS},I_{\rm D}=33~\mu{\rm A}$	2	2.7	3.5	
Zero gate voltage drain current	I _{DSS}	$V_{\rm DS}$ =100 V, $V_{\rm GS}$ =0 V, $T_{\rm j}$ =25 °C	-	0.1	1	μΑ
		$V_{\rm DS}$ =100 V, $V_{\rm GS}$ =0 V, $T_{\rm j}$ =125 °C	-	10	100	
Gate-source leakage current	I _{GSS}	V _{GS} =20 V, V _{DS} =0 V	-	1	100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =33 A	1	14.7	18	mΩ
		V _{GS} =6 V, I _D =16 A	-	18.4	33	
Gate resistance	R_{G}		1	1.4		Ω
Transconductance	g_{fs}	$ V_{\rm DS} > 2 I_{\rm D} R_{\rm DS(on)max},$ $I_{\rm D} = 33~{\rm A}$	20	40	-	S

 $^{^{3)}}$ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm 2 (one layer, 70 μ m thick) copper area for drain connection. PCB is vertical in still air.



Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	Ciss		-	1350	1800	pF
Output capacitance	Coss	$V_{\rm GS}$ =0 V, $V_{\rm DS}$ =50 V, f =1 MHz	-	237	315	
Reverse transfer capacitance	C_{rss}		-	11	-	
Turn-on delay time	$t_{\rm d(on)}$		-	12	-	ns
Rise time	$t_{\rm r}$	V_{DD} =50 V, V_{GS} =10 V,	-	12	-]
Turn-off delay time	$t_{d(off)}$	$I_{\rm D}$ =33 A, $R_{\rm G,ext}$ =1.6 Ω	-	19	-	
Fall time	t_{f}		-	5	-	
Gate Charge Characteristics ⁶⁾	<u></u>	<u> </u>		7		l _n C
Gate to source charge	Q _{gs}		-	7	-	nC
Gate to drain charge	Q _{gd}	$V_{\rm DD}$ =50 V, $I_{\rm D}$ =33 A, $V_{\rm GS}$ =0 to 10 V	-	4	-	
Switching charge	Q_{sw}		-	6	-	
Gate charge total	Q_g		-	19	25	
Gate plateau voltage	$V_{ m plateau}$		-	4.9	-	V
Output charge	Q _{oss}	$V_{\rm DD}$ =50 V, $V_{\rm GS}$ =0 V	-	25	33	nC
Reverse Diode						
Diode continous forward current	Is	- 7 _C =25 °C	-	-	43	А
Diode pulse current	I _{S,pulse}		-	-	172	
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =33 A, T _j =25 °C	-	1	1.2	V
Reverse recovery time	t _{rr}	V _R =15 V, I _F =33 A,	-	55	-	ns
Reverse recovery charge	Q _{rr}	$di_F/dt=100 \text{ A/}\mu\text{s}$	-	92	_	nC

⁶⁾ See figure 16 for gate charge parameter definition

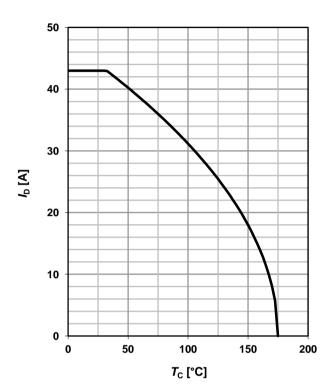


1 Power dissipation

$P_{\text{tot}} = f(T_{\text{C}})$

80 70 60 50 P_{tot} [W] 40 30 20 10 0 0 50 100 150 200 *T*_C [°C]

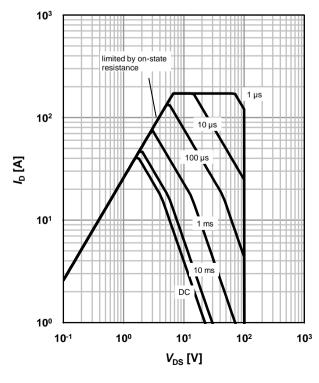
2 Drain current



3 Safe operating area

 $I_D=f(V_{DS}); T_C=25 \text{ °C}; D=0$

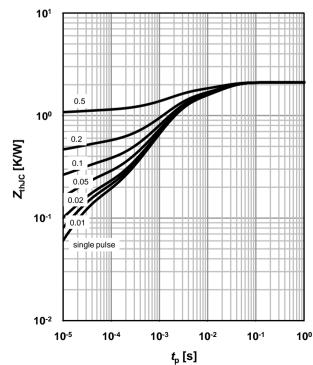
parameter: t_p



4 Max. transient thermal impedance

 Z_{thJC} =f(t_{p})

parameter: $D=t_p/T$

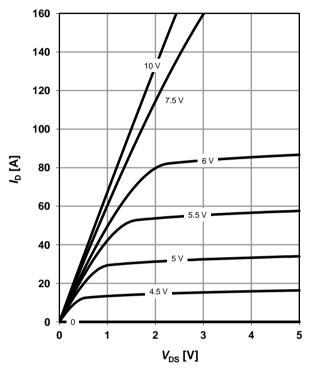




5 Typ. output characteristics

 $I_D=f(V_{DS}); T_j=25 °C$

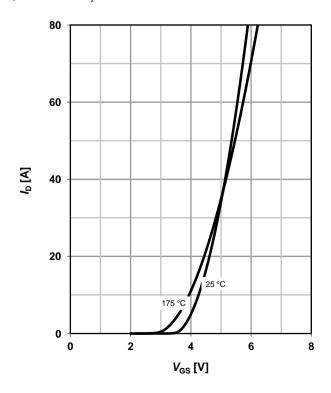
parameter: $V_{\rm GS}$



7 Typ. transfer characteristics

 $I_{D}=f(V_{GS}); |V_{DS}|>2|I_{D}|R_{DS(on)max}$

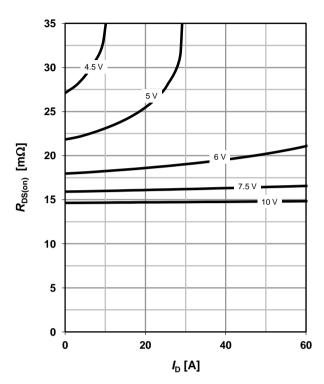
parameter: T_j



6 Typ. drain-source on resistance

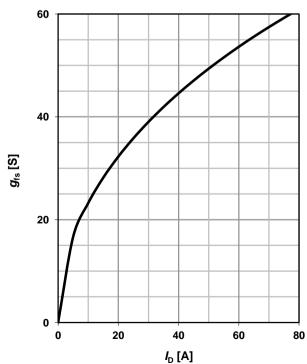
 $R_{DS(on)}=f(I_D); T_j=25 \text{ °C}$

parameter: V_{GS}



8 Typ. forward transconductance

 $g_{fs}=f(I_D); T_j=25 \text{ °C}$





9 Drain-source on-state resistance

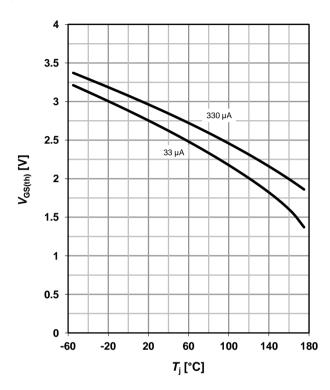
 $R_{DS(on)} = f(T_i); I_D = 33 \text{ A}; V_{GS} = 10 \text{ V}$

40 36 32 28 $R_{\mathrm{DS(on)}}$ [m Ω] 24 20 16 12 8 4 0 -60 -20 20 60 100 140 180 *T*_j [°C]

10 Typ. gate threshold voltage

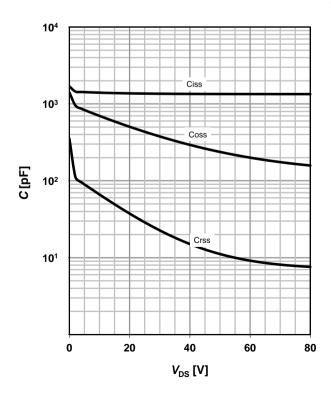
 $V_{GS(th)} = f(T_j); V_{GS} = V_{DS}$

parameter: I_D



11 Typ. capacitances

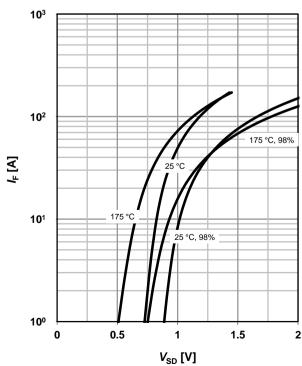
 $C=f(V_{DS}); V_{GS}=0 V; f=1 MHz$



12 Forward characteristics of reverse diode

 $I_{\mathsf{F}} = \mathsf{f}(V_{\mathsf{SD}})$

parameter: T_i

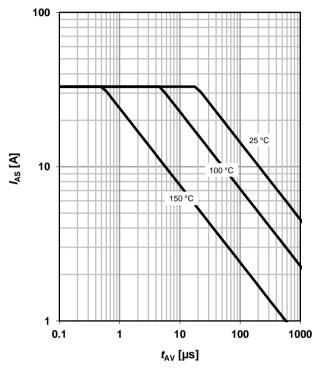




13 Avalanche characteristics

 $I_{AS}=f(t_{AV}); R_{GS}=25 \Omega$

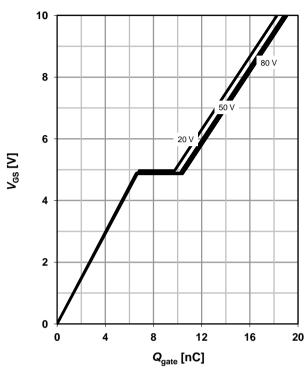
parameter: $T_{j(start)}$



14 Typ. gate charge

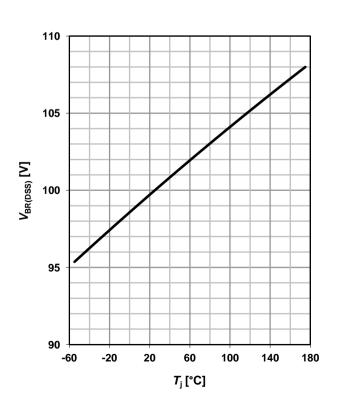
 V_{GS} =f(Q_{gate}); I_D =33 A pulsed

parameter: $V_{\rm DD}$

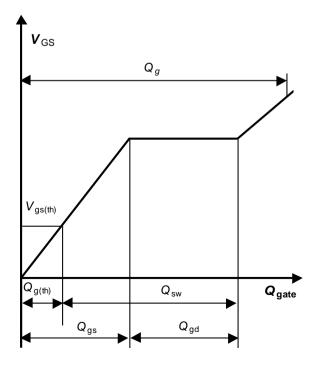


15 Drain-source breakdown voltage

 $V_{BR(DSS)}=f(T_j); I_D=1 \text{ mA}$

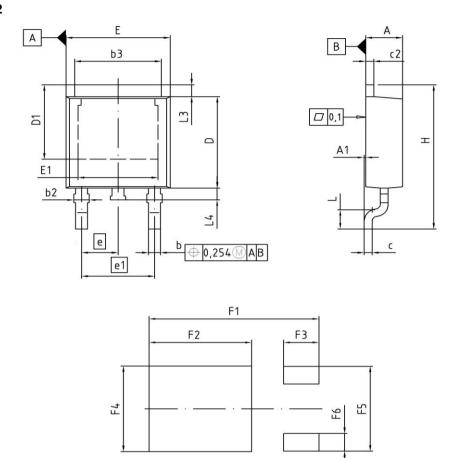


16 Gate charge waveforms





PG-TO-252



DIM	MILLIMETERS		INCH	HES	
DIM	MIN	MAX	MIN	MAX	
Α	2.16	2.41	0.085	0.095	
A1	0.00	0.15	0.000	0.006	
b	0.64	0.89	0.025	0.035	
ь2	0.65	1.15	0.026	0.045	
ь3	5.00	5.50	0.197	0.217	
С	0.46	0.60	0.018	0.024	
c2	0.46	0.98	0.018	0.039	
D	5.97	6.22	0.235	0.245	
D1	5.02	5.84	0.198	0.230	
Е	6.40	6.73	0.252	0.265	
E1	4.70	5.21	0.185	0.205	
е	2.	.29	0.090		
e1	4.57		0.180		
N		3	3		
Н	9.40	10.48	0.370	0.413	
L	1.18	1.70	0.046	0.067	
L3	0.90	1.25	0.035	0.049	
L4	0.51	1.00	0.020	0.039	
F1	10.50	10.70	0.413	0.421	
F2	6.30	6.50	0.248	0.256	
F3	2.10	2.30	0.083	0.091	
F4	5.70	5.90	0.224	0.232	
F5	5.66	5.86	0.223	0.231	
F6	1.10	1.30	0.043	0.051	

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