

MOSFETs Silicon N-channel MOS (U-MOSIX-H)

# TK3R3A06PL

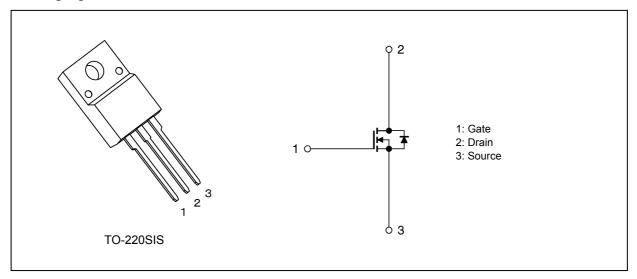
#### 1. Applications

- High-Efficiency DC-DC Converters
- · Switching Voltage Regulators
- · Motor Drivers

#### 2. Features

- (1) High-speed switching
- (2) Small gate charge:  $Q_{SW} = 21 \text{ nC (typ.)}$
- (3) Small output charge:  $Q_{oss} = 66 \text{ nC (typ.)}$
- (4) Low drain-source on-resistance:  $R_{DS(ON)} = 2.5 \text{ m}\Omega$  (typ.) ( $V_{GS} = 10 \text{ V}$ )
- (5) Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$
- (6) Enhancement mode:  $V_{th} = 1.5$  to 2.5 V ( $V_{DS} = 10$  V,  $I_D = 0.7$  mA)

#### 3. Packaging and Internal Circuit





# 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics				Rating	Unit	
Drain-source voltage			$V_{DSS}$	60	V	
Gate-source voltage			$V_{GSS}$	±20		
Drain current (DC)	(T <sub>c</sub> = 25 °C)	(Note 1)	I <sub>D</sub>	80	Α	
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	$I_D$	88	Α	
Drain current (pulsed)	(t = 100 μs)	(Note 1)	$I_{DP}$	400	Α	
Power dissipation	(T <sub>c</sub> = 25 °C)		$P_{D}$	42	W	
Single-pulse avalanche energy		(Note 3)	E <sub>AS</sub>	76	mJ	
Single-pulse avalanche current		(Note 3)	I <sub>AS</sub>	80	Α	
Channel temperature			$T_ch$	175	°C	
Storage temperature			$T_{stg}$	-55 to 175	°C	
Isolation voltage (RMS)	(t = 1.0 s)		V <sub>ISO(RMS)</sub>	2000	V	
Mounting torque			TOR	0.6	N · m	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics			Max	Unit
Channel-to-case thermal resistance	(T <sub>c</sub> = 25 °C)	R <sub>th(ch-c)</sub>	3.57	°C/W
Channel-to-ambient thermal resistance	(T <sub>a</sub> = 25 °C)	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 175 °C.

Note 2: Limited by silicon chip capability.

Note 3:  $V_{DD}$  = 48 V,  $T_{ch}$  = 25 °C (initial), L = 9.2  $\mu$ H,  $I_{AS}$  = 80 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



#### 6. Electrical Characteristics

# 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μΑ
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60	_		V
Drain-source breakdown voltage (Note 4)	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	45	_	_	
Gate threshold voltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.7 mA	1.5	_	2.5	
Drain-source on-resistance	R <sub>DS(ON)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 34 \text{ A}$	_	3.3	4.9	mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 40 A	_	2.5	3.3	

Note 4: If a reverse bias is applied between gate and source, this device enters  $V_{(BR)DSX}$  mode. Note that the drain-source breakdown voltage is lowered in this mode.

# 6.2. Dynamic Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	5000	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	55	_	
Output capacitance	C <sub>oss</sub>		_	950	_	
Gate resistance	r <sub>g</sub>	_	_	1.5	_	Ω
Switching time (rise time)	t <sub>r</sub>	See Fig. 6.2.1	_	7	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	25	_	
Switching time (fall time)	t <sub>f</sub>		_	16	_	
Switching time (turn-off time)	t <sub>off</sub>		_	66		

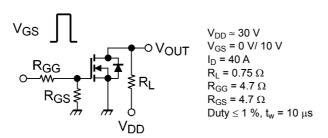


Fig. 6.2.1 Switching Time Test Circuit

# 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Qg	$V_{DD} \approx 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$	_	71	_	nC
gate-drain)		$V_{DD} \approx 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 40 \text{ A}$	_	35	_	
Gate-source charge 1	Q <sub>gs1</sub>	$V_{DD} \approx 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$	_	19	_	
Gate-drain charge	$Q_{gd}$		_	13	_	
Gate switch charge	Q <sub>SW</sub>		_	21	_	
Output charge	Q <sub>oss</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	66	_	



# 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (Note 5)	I <sub>DRP</sub> (t = 100 μs)	_	1	ı	400	Α
Diode forward voltage	$V_{DSF}$	I <sub>DR</sub> = 80 A, V <sub>GS</sub> = 0 V			-1.5	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V,	_	55	_	ns
Reverse recovery charge	Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs		72		nC

Note 5: Ensure that the channel temperature does not exceed 175 °C.

#### 7. Marking (Note)

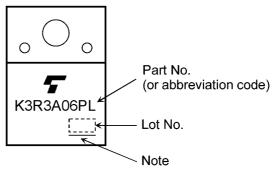


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



#### 8. Characteristics Curves (Note)

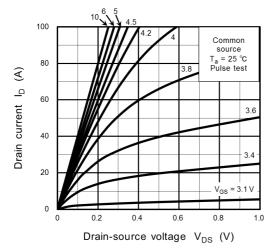


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>

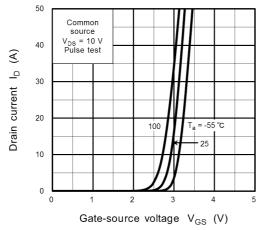


Fig. 8.3 I<sub>D</sub> - V<sub>GS</sub>

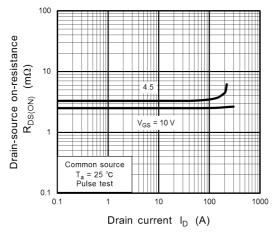


Fig. 8.5 R<sub>DS(ON)</sub> - I<sub>D</sub>

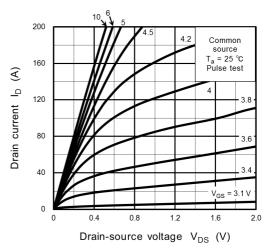


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>

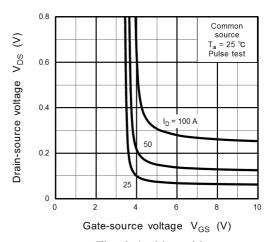


Fig. 8.4 VDS - VGS

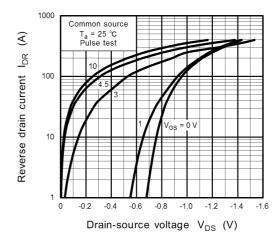


Fig. 8.6 IDR - VDS



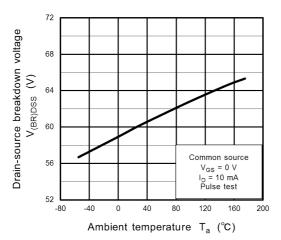


Fig. 8.7 V<sub>(BR)DSS</sub> - T<sub>a</sub>

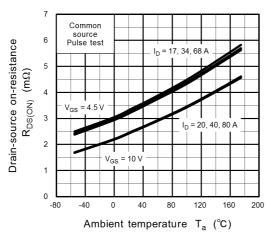


Fig. 8.9 RDS(ON) - Ta

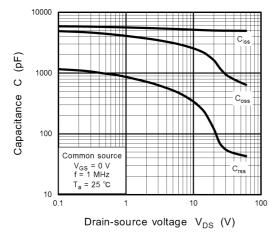


Fig. 8.11 Capacitance - V<sub>DS</sub>

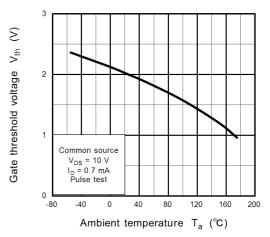


Fig. 8.8 V<sub>th</sub> - T<sub>a</sub>

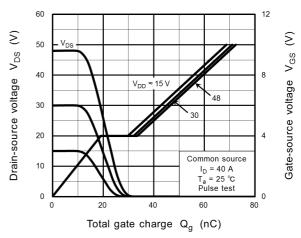


Fig. 8.10 Dynamic Input/Output Characteristics

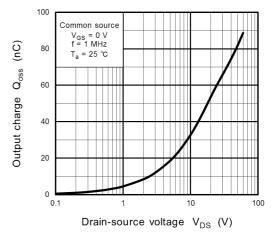


Fig. 8.12 Qoss - VDS



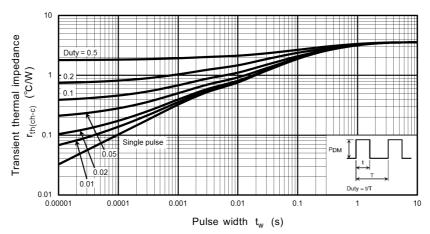
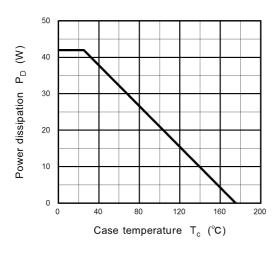


Fig. 8.13  $r_{th}$  -  $t_w$  (Guaranteed Maximum)



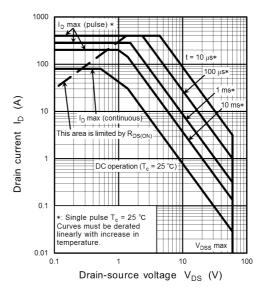


Fig. 8.14 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

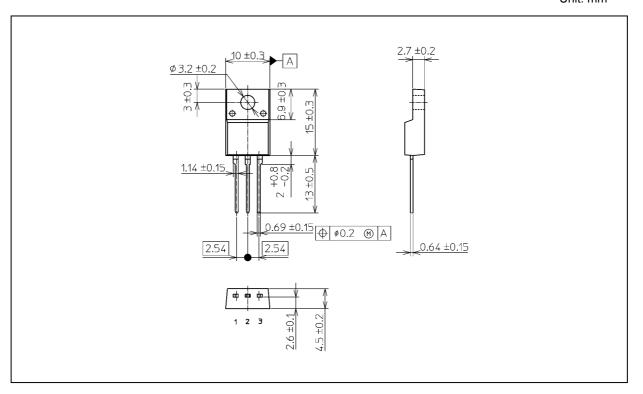
Fig. 8.15 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 1.56 g (typ.)

Package Name(s)	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	



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