Product Summary

V _{(BR)DSS}	R _{DS(on)TYP}	l _D
30V	15mΩ@10V	7.0
	20mΩ@4.5V	7A
-30V	22mΩ@-10V	-7A
	32mΩ@-4.5V	,,,,



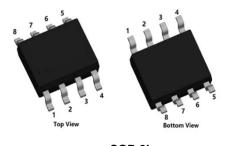
Feature

- High power and current handing capability
- Lead free product is acquired
- Surface mount package
- 100% Single Pluse avalanche energy Test

Applications

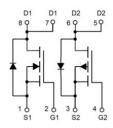
- Battery Protection
- Load Switch
- Power Management

Package

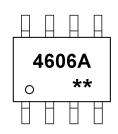


SOP-8L

Circuit diagram



Marking



4606A :Device Code ** :Week Code

Order Information

Device	Package	Unit/Tape
SP4606ACP8	SOP-8L	4000



Absolute maximum ratings (Ta=25°C,unless otherwise noted)

Parameter	Compleal	Val	l le:45		
Parameter	Symbol	N-Channel	P-Channel	Units	
Drain-Source Voltage	V _{DS}	30	-30	V	
Gate-Source Voltage	V _{GS}	±20	±20	V	
Continuous Drain Current	ID	7	-7	А	
Pulsed Drain Current	I _{DM}	28	-28	Α	
Single Pulse Avalanche Energy ¹	Eas	13	30	mJ	
Power Dissipation	P _D	2.5		W	
Thermal Resistance Junction-to-Ambient	R _{0JA}	50		°C/W	
Storage Temperature Range	T _{STG}	-55 to 150		$^{\circ}\!\mathbb{C}$	
Operating Junction Temperature Range	TJ	-55 to 150		$^{\circ}$ C	

N-Channel Electrical characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=250uA		-	_	V	
Drain-Source Leakage Current	I _{DSS}	VDS=24V , VGS=0V , TJ=25℃		-	1	uA	
Gate-Source Leakage Current	Igss	VGS=±20V, VDS=0V	-	-	±100	nA	
Gate Threshold Voltage	V _{GS(th)}	VGS=VDS , ID =250uA		1.5	2.2	V	
Otatia Dunin Carres On Daniston		VGS=10V , ID=5A	-	15	22	2	
Static Drain-Source On-Resistance	R _{DS(ON)}	VGS=4.5V , ID=3A	-	20	30	mΩ	
Dynamic characteristics							
Input Capacitance	C _{iss}		-	572	-		
Output Capacitance	Coss	VDS=15V , VGS=0V , f=1MHz	-	81	-	pF	
Reverse Transfer Capacitance	Crss			65	-		
Total Gate Charge	Qg	VDS=20V , VGS=4.5V , ID=12A		7.2	-		
Gate-Source Charge	Q _{gs}			1.4	-	nC	
Gate-Drain Charge	Q_{gd}			2.2	-		
Switching Characteristics							
Turn-On Delay Time	T _{d(on)}	VDD 40V VGC 40V DC 0.00 ID 54		4.1	-		
Rise Time	Tr			9.8	-	nS	
Turn-Off Delay Time	T _{d(off)}	VDD=12V, VGS=10V , RG=3.3Ω, ID=5A	-	15.5	-	113	
Fall Time	T _f]		6.0	-		
Diode Characteristics							
Diode Forward Voltage	V _{SD}	VGS=0V , IS=1A , TJ=25℃	-	-	1.2	V	
Maximum Body-Diode Continuous Current	Is		-	-	7	Α	
Reverse Recovery Time	T _{rr}	I _S =10A, di/dt=100A/us, TJ=25℃		15	-	nS	
Reverse Recovery Charge	Qrr			3	-	nC	

Note:

^{1.} The EAS test condition is VDD=15V,VGS=10V,L=0.5mH,RG=25 Ω



P-Channel Electrical characteristics (Ta=25℃, unless otherwise noted)

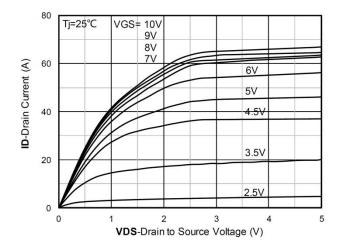
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=-250uA		_	-	V	
Drain-Source Leakage Current	I _{DSS}	VDS=-24V , VGS=0V , TJ=25℃	-	-	-1	uA	
Gate-Source Leakage Current	I _{GSS}	VGS=±20V , VDS=0V	-	-	±100	nA	
Gate Threshold Voltage	V _{GS(th)}	VGS=VDS , ID =-250uA		-1.5	-2.2	V	
0, 1, 5 , 0	_	VGS=-10V , ID=-5A	-	22	30	mΩ	
Static Drain-Source On-Resistance	R _{DS(ON)}	VGS=-4.5V , ID=-3A	-	32	50		
Dynamic characteristics							
Input Capacitance	C _{iss}		-	1070	-		
Output Capacitance	Coss	VDS=-15V , VGS=0V , f=1MHz	-	146	-	pF	
Reverse Transfer Capacitance	C _{rss}	1		142	-		
Total Gate Charge	Qg	VDS=-30V , VGS=-10V , ID=-6A		21	-		
Gate-Source Charge	Q _{gs}			2.1	-	nC	
Gate-Drain Charge	Q _{gd}			5.6	-		
Switching Characteristics							
Turn-On Delay Time	T _{d(on)}			7	-		
Rise Time	Tr	\\DD= 45\\\\CC= 40\\\\ DC=CO \\D= 44\\\	-	9	-	nS	
Turn-Off Delay Time	T _{d(off)}	VDD=-15V VGS=-10V , RG=6Ω, ID=-1A		30	-	113	
Fall Time	T _f			18	-		
Diode Characteristics							
Diode Forward Voltage	V _{SD}	VGS=0V , IS=-1A , TJ=25℃	-	-	-1.2	V	
Maximum Body-Diode Continuous Current	Is		-	-	-7	Α	
Reverse Recovery Time	T _{rr}	I _s =-20A, di/dt=-100A/us, Tj=25℃		50	-	nS	
Reverse Recovery Charge	Q _{rr}			31		nC	

Note:

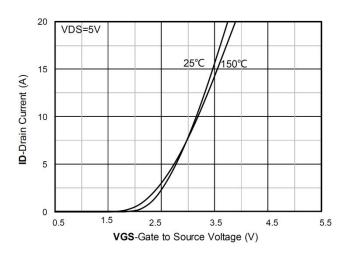
^{1.}The EAS test condition is VDD=-15V,VGS=-10V,L=0.5mH,RG=25 Ω



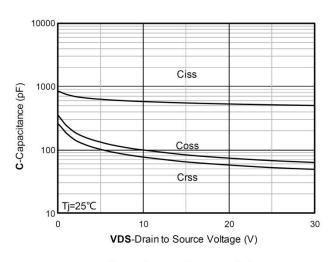
N-Channel Typical Characteristics



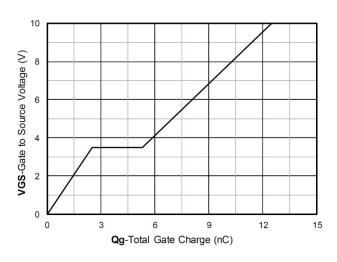
Output Characteristics



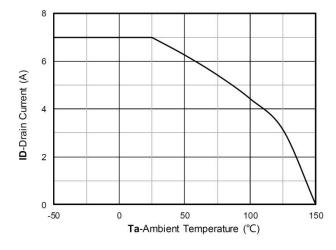
Transfer Characteristics



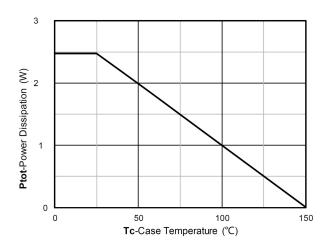
Capacitance Characteristics



Gate Charge

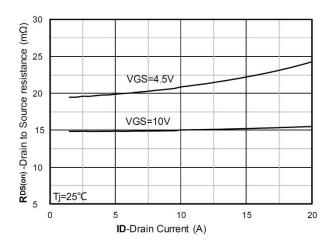


Current dissipation

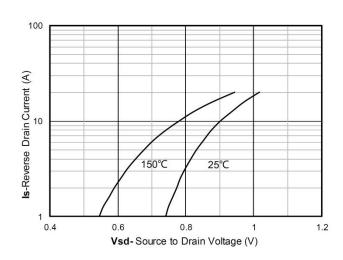


Power dissipation

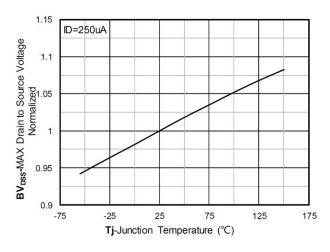




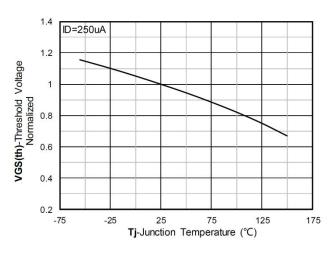
RDS(on) VS Drain Current



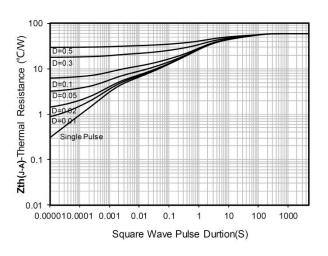
Forward characteristics of reverse diode



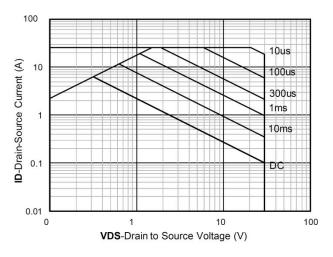
Normalized breakdown voltage



Normalized Threshold voltage



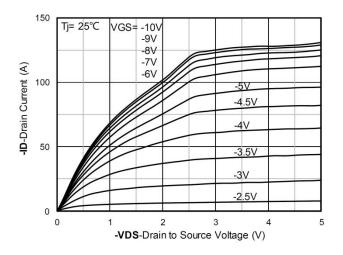
Maximum Transient Thermal Impedance

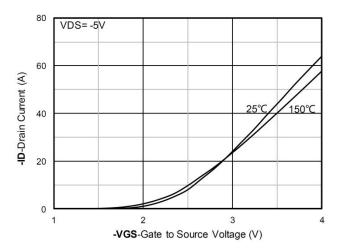


Safe Operation Area



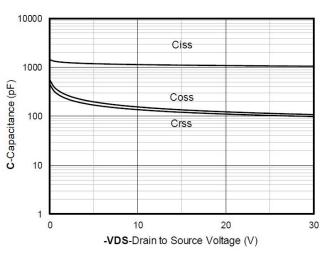
P-Channel Typical Characteristics

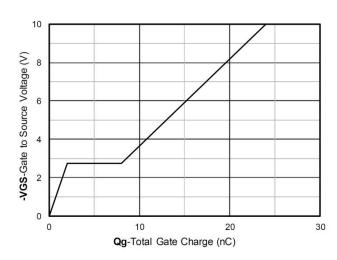




Output Characteristics

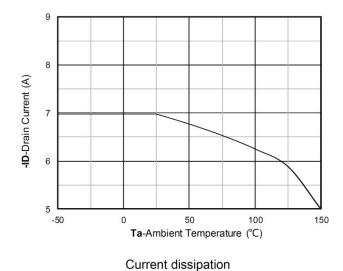
Transfer Characteristics

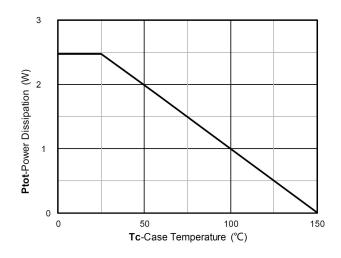




Capacitance Characteristics

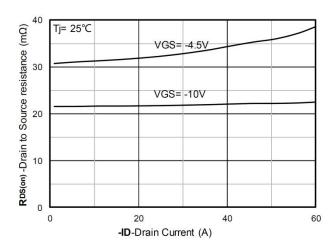
Gate Charge



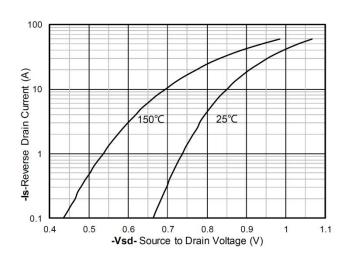


Power dissipation

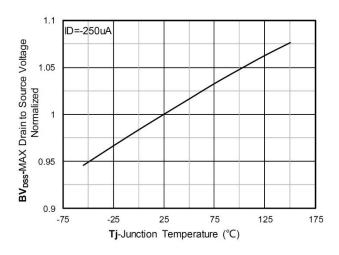




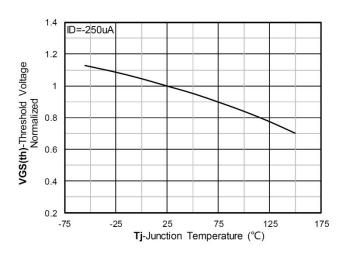
RDS(on) VS Drain Current



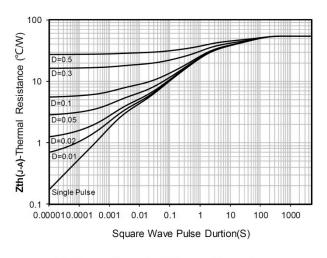
Forward characteristics of reverse diode



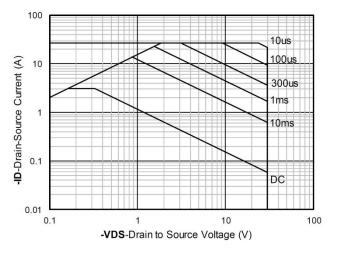
Normalized breakdown voltage



Normalized Threshold voltage



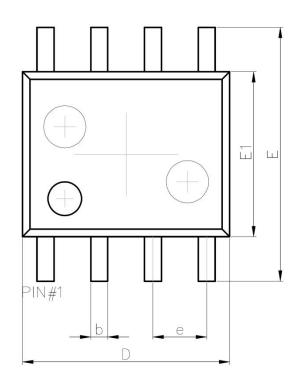
Maximum Transient Thermal Impedance

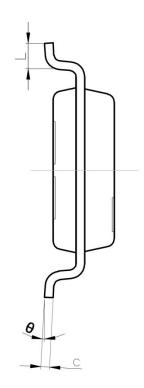


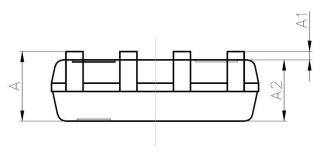
Safe Operation Area



SOP-8L Package Information







Symbol	Dimensions In Millimeters			
Symbol	Min.	Max.		
Α	1.35	1.75		
A1	0.10	0.25		
A2	1.35	1.55		
b	0.33	0.51		
С	0.17	0.25		
D	4.80	5.00		
е	1.27 REF.			
E	5.80	6.20		
E1	3.80	4.00		
L	0.40	1.27		
θ	0°	8°		