

Product Summary

V _{(BR)DSS}	R _{DS(on)TYP}	l _D
40V	17mΩ@10V	23A
400	24mΩ@4.5V	23A
40)/	38mΩ@-10V	-13A
-40V	50mΩ@-4.5V	10/1



Feature

- High power and current handing capability
- Lead free product is acquired
- Surface mount package
- 100% Single Pluse avalanche energy Test

Applications

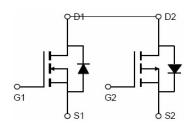
- Battery Protection
- Load Switch
- Power Management

Package

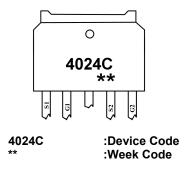


TO-252-4L

Circuit diagram



Marking



Order Information

Device	Package	Unit/Tape
SP4024CTM	TO-252-4L	2500



Absolute maximum ratings (Ta=25°C,unless otherwise noted)

Davanatav	Coursels al	Va	Unita	
Parameter	Symbol	N-Channel	P-Channel	Units
Drain-Source Voltage	V _{DS}	40	-40	V
Gate-Source Voltage	V _{GS}	±20	±20	V
Continuous Drain Current (T _C =25°C)	I _D	23	-13	А
Continuous Drain Current (T _C =100°C)	I _D	15	-9	А
Pulsed Drain Current	I _{DM}	92	-52	А
Single Pulse Avalanche Energy ¹	E _{AS}	25	20	mJ
Power Dissipation (T _C =25°C)	P _D	30		W
Thermal Resistance Junction-to-Case	Rejc	4.2		°C/W
Storage Temperature Range	T _{STG}	-55 to 150		$^{\circ}\!\mathbb{C}$
Operating Junction Temperature Range	TJ	-55 to 150		$^{\circ}$ C

N-Channel Electrical characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=250uA	40	-	_	V	
Drain-Source Leakage Current	I _{DSS}	VDS=32V , VGS=0V , TJ=25℃	-	-	1	uA	
Gate-Source Leakage Current	I _{GSS}	VGS=±20V , VDS=0V	-	-	±100	nA	
Gate Threshold Voltage	V _{GS(th)}	VGS=VDS , ID=250uA	1	1.7	2.5	V	
Chatia Dania Carras On Daniatana		VGS=10V, ID=8A	-	17 22	22		
Static Drain-Source On-Resistance	R _{DS(ON)}	VGS=4.5V, ID=6A	-	24	32	mΩ	
Dynamic characteristics							
Input Capacitance	C _{iss}		-	834	-		
Output Capacitance	Coss	VDS=15V , VGS=0V , f=1MHz	-	51	-	pF	
Reverse Transfer Capacitance	C _{rss}			45	-	1	
Total Gate Charge	Qg		-	25	-		
Gate-Source Charge	Q _{gs}	VDS=20V , VGS=4.5V , ID=5A	-	6	-	nC	
Gate-Drain Charge	Q _{gd}			5	-		
Switching Characteristics							
Turn-On Delay Time	T _{d(on)}		-	8	-		
Rise Time	Tr	VDD-20V VCS-10V BC-20 ID-5A	-	7	-	nS	
Turn-Off Delay Time	T _{d(off)}	VDD=20V, VGS=10V , RG=3Ω, ID=5A	-	26	-	113	
Fall Time	T _f			4	-	1	
Diode Characteristics							
Diode Forward Voltage	V _{SD}	VGS=0V , IS=1A , TJ=25℃	-	-	1.2	V	
Maximum Body-Diode Continuous Current	Is		-	-	23	Α	
Reverse Recovery Time	Trr	1 -204 di/d+-1004/up TI-25°C	-	10	-	nS	
Reverse Recovery Charge	Q _{rr}	− I _s =20A, di/dt=100A/us, TJ=25°C		5	-	nC	

Note:

^{1.}The EAS test condition is VDD=20V,VGS=10V,L=0.5mH,RG=25 Ω



P-Channel Electrical characteristics (Ta=25℃, unless otherwise noted)

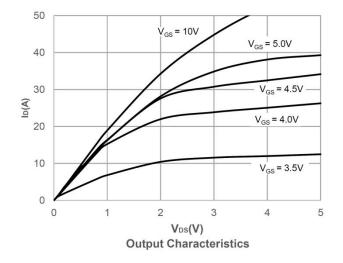
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=-250uA	-40	_	-	V
Drain-Source Leakage Current	I _{DSS}	VDS=-32V , VGS=0V , TJ=25℃	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	VGS=±20V, VDS=0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	VGS=VDS , ID =-250uA	-1	-1.6	-2.5	V
0		VGS=-10V , ID=-5A	-	38	45	mΩ
Static Drain-Source On-Resistance	R _{DS(ON)}	VGS=-4.5V , ID=-4A	-	50	60	
Dynamic characteristics						
Input Capacitance	C _{iss}		-	915	-	
Output Capacitance	Coss	VDS=-15V , VGS=0V , f=1MHz	-	104	-	pF
Reverse Transfer Capacitance	Crss			92	-	1
Total Gate Charge	Qg			24	-	
Gate-Source Charge	Q _{gs}	VDS=-15V , VGS=-4.5V , ID=-1A	-	3	-	nC
Gate-Drain Charge	Q _{gd}			4	-	
Switching Characteristics						
Turn-On Delay Time	T _{d(on)}			11	-	
Rise Time	Tr	VDD 45V VOC 40V DC 20 ID 44	-	17	-	nS
Turn-Off Delay Time	T _{d(off)}	- VDD=-15V, VGS=-10V , RG=3Ω, ID=-1A		55	-	113
Fall Time	Tf			19	-	
Diode Characteristics						
Diode Forward Voltage	V _{SD}	VGS=0V , IS=-1A , TJ=25℃	-	-	-1.2	V
Maximum Body-Diode Continuous Current	Is		-	-	-13	Α
Reverse Recovery Time	T _{rr}	L = 5A di/dt=100A/up TI=25°C	-	13	-	nS
Reverse Recovery Charge	Qrr	l _s =-5A, di/dt=100A/us, TJ=25℃		7	-	nC

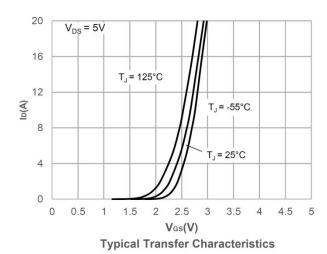
Note:

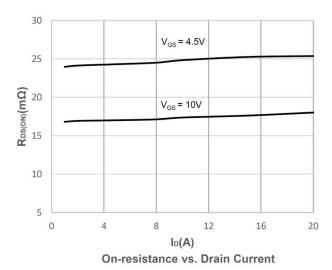
^{1.}The EAS test condition is VDD=-20V,VGS=-10V,L=0.5mH,RG=25 Ω

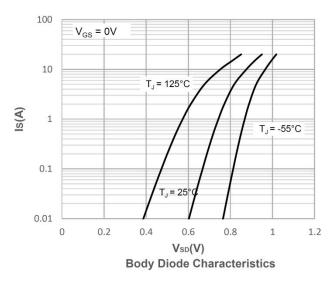


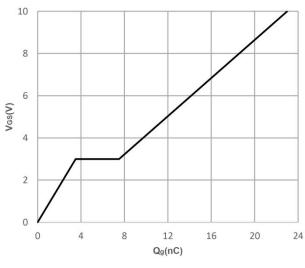
N-Channel Typical Characteristics

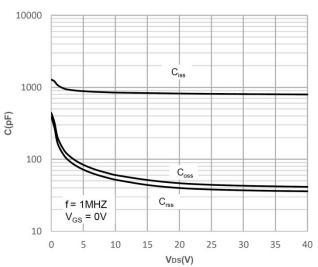






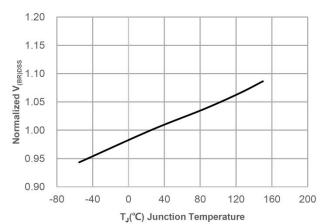




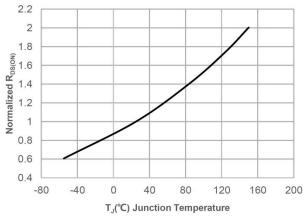


Gate Charge Characteristics

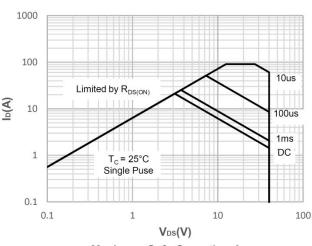




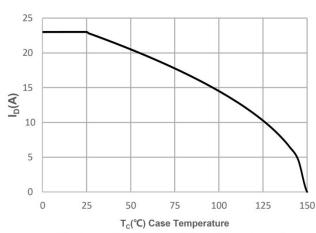
Normalized Breakdown voltage vs. Junction Temperature



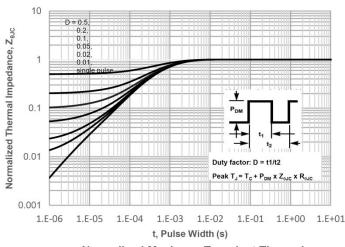
Normalized on Resistance vs. Junction Temperature



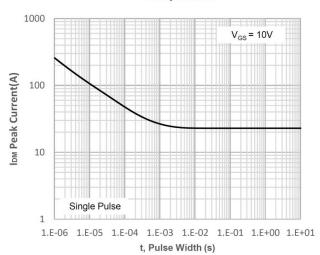
Maximum Safe Operating Area



Maximum Continuous Drian Current vs. Case Temperature



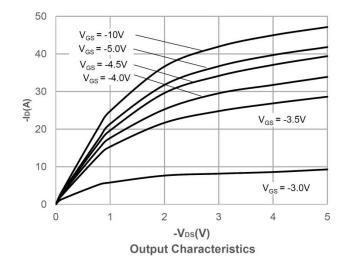
Normalized Maximum Transient Thermal Impedance

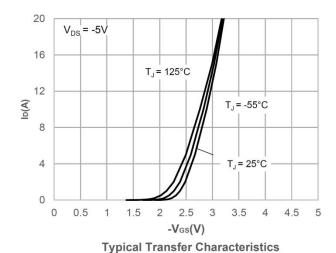


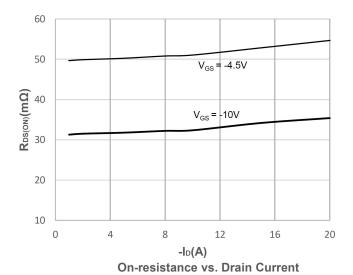
Peak Current Capacity

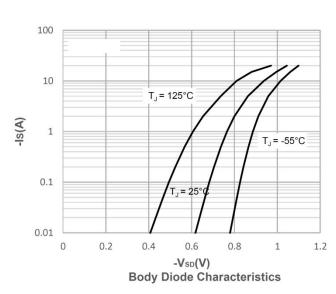


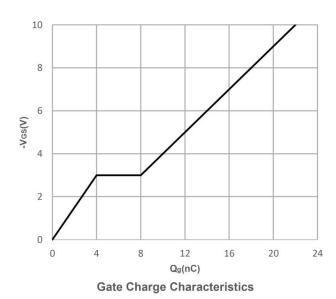
P-Channel Typical Characteristics

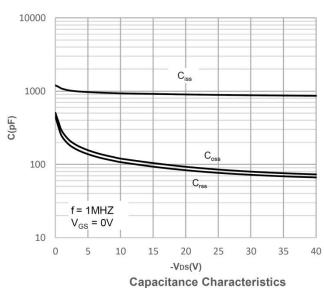




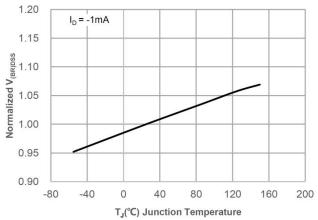




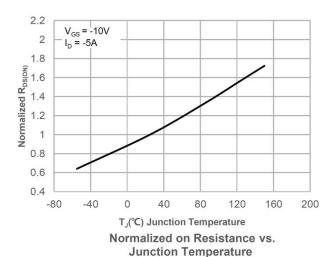


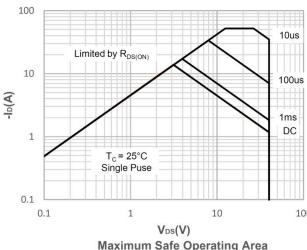




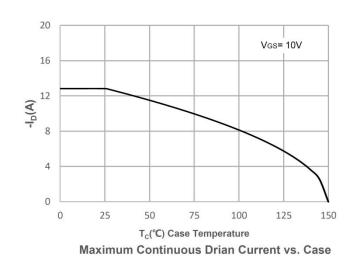


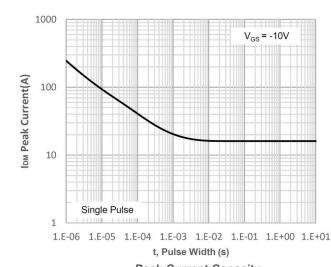
Normalized Breakdown voltage vs. Junction **Temperature**

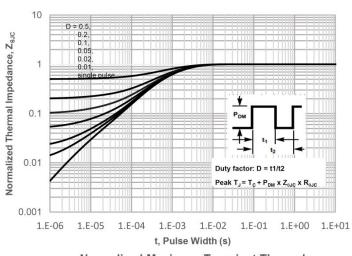










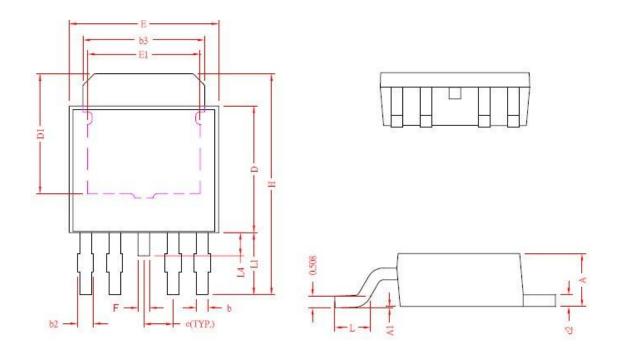


Normalized Maximum Transient Thermal Impedance

V_{GS} = -10V



TO-252-4L Package Information



Symbol	Dimensions In Millimeters			
	Min.	Max.		
А	2.20	2.40		
A1	0	0.15		
b	0.40	0.60		
b2	0.50	0.80		
b3	5.20	5.50		
c2	0.45	0.55		
D	5.40	5.80		
D1	4.57	-		
E	6.40	6.80		
E1	3.81	-		
е	1.27REF.			
F	0.40	0.60		
Н	9.40	10.20		
L	1.40	1.77		
L1	2.40 3.00			
L4	0.80	1.20		