

• General Description

The AGM16N65F combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

Application

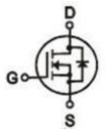
- Electronic Ballast
- Electronic Transformer
- Switch Mode Power Supply

Product Summary

BVDSS	RDSON	ID
650V	0.58Ω	16A

TO-220F Pin Configuration





Package Marking and Ordering Information

Device Marking	Device	Device Package Reel Size		Tape width	Quantity	
AGM16N65F	AGM16N65F	TO-220F			1000	

Table 1. Absolute Maximum Ratings (Tc=25℃)

Symbol	Parameter	Value	Unit	
VDS	Drain-Source Voltage (VGS=0V)	650	V	
VGS	Gate-Source Voltage (VDS=0V) ±30			
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	16	А	
_	Drain Current-Continuous(Tc=100℃)	6.4	Α	
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	64	А	
PD	Maximum Power Dissipation(Tc=25℃)	53	W	
	Maximum Power Dissipation(Tc=100℃)	21	w	
EAS	Avalanche energy (Note 3)	460	mJ	
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}\!\mathbb{C}$	

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹		62.5	°C/W
RθJC	Thermal Resistance Junction-Case ¹		2.36	°C/W



Table 3. Electrical Characteristics (TC=25 ℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off Sta	ates					
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	650			V
IDSS	Zero Gate Voltage Drain Current	VDS=650V,VGS=0V			1	μΑ
IGSS	Gate-Body Leakage Current	VGS=±30V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250µA	3.0		4.0	V
gFS	Forward Transconductance	VDS=15V,ID=8A				S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=8A		0.58	0.65	Ω
1120(011)		VGS=4.5V, ID=8A				Ω
Dynamic (Characteristics					
Ciss	Input Capacitance	VDS=25V,VGS=0V,		2063		pF
Coss	Output Capacitance	F=1MHZ		204		pF
Crss	Reverse Transfer Capacitance			29		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz				Ω
Switching	Times					
td(on)	Turn-on Delay Time			54		nS
tr	Turn-on Rise Time	VDD=325V,		40		nS
td(off)	Turn-Off Delay Time	ID=16A,RGEN=25Ω		312		nS
tf	Turn-Off Fall Time			66		nS
Qg	Total Gate Charge			74		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=520V, ID=16A		10		nC
Qgd	Gate-Drain Charge	- 100 0201, 10 10/1		40		nC
Source-Dr	rain Diode Characteristics					
ISD	Source-Drain Current(Body Diode)	TC=25℃			16	Α
VSD	Forward on Voltage	VGS=0V,ISD=8A, TJ=25℃			1.4	V
trr	Reverse Recovery Time	Vgs=0V,IF=16A ,		682		ns
Qrr	Reverse Recovery Charge	dI/dt=100A/µs		4.5		nc

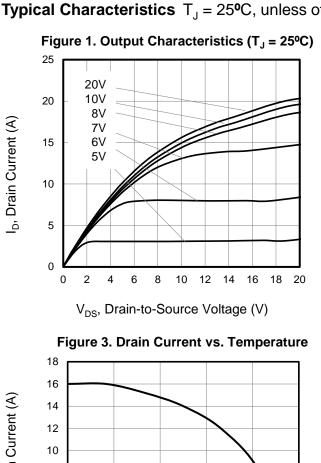
Notes 1. The maximum current rating is package limited.

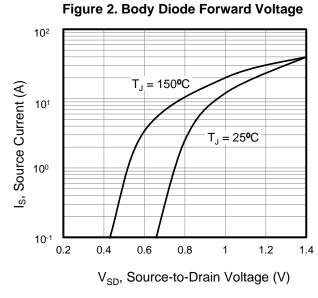
Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

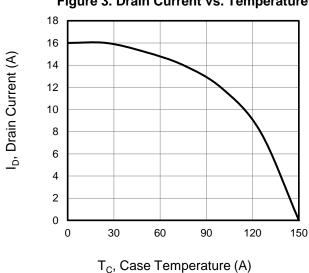
Notes 3.EAS condition: TJ=25℃

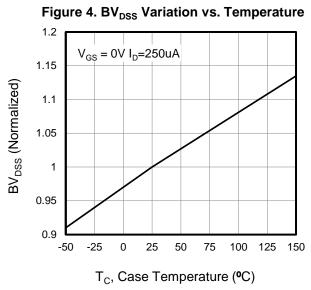


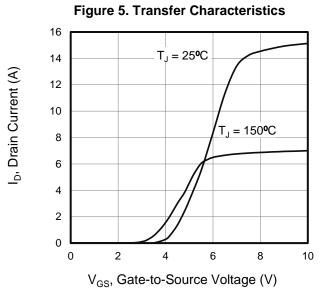
Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

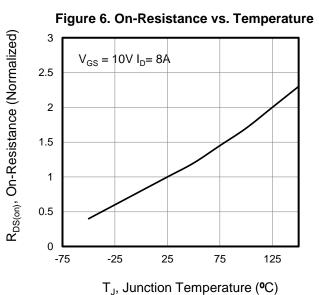






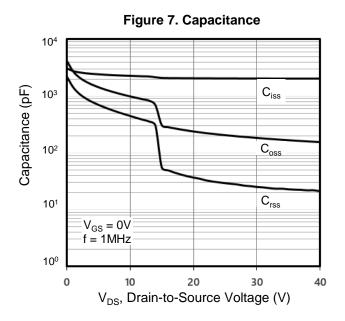








Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted



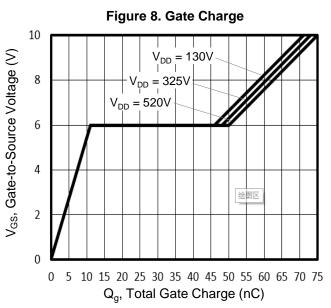


Figure 9. Transient Thermal Impedance

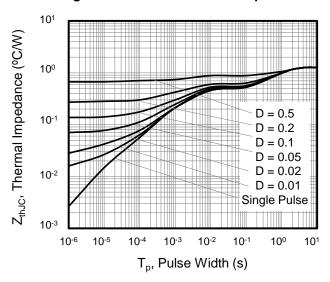




Figure A: Gate Charge Test Circuit and Waveform

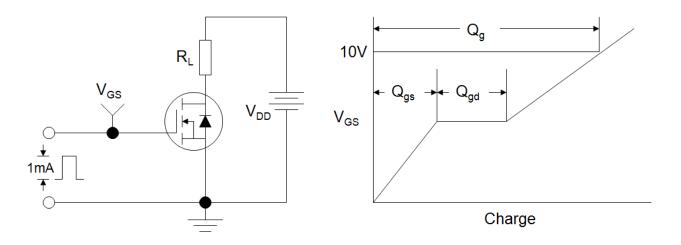


Figure B: Resistive Switching Test Circuit and Waveform

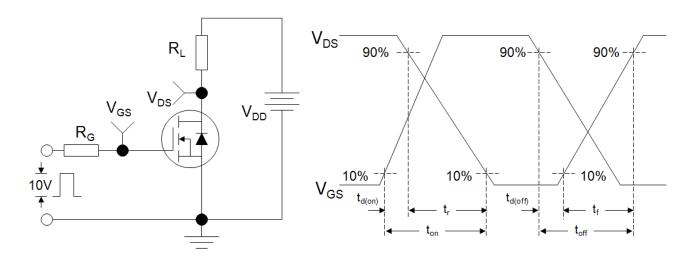
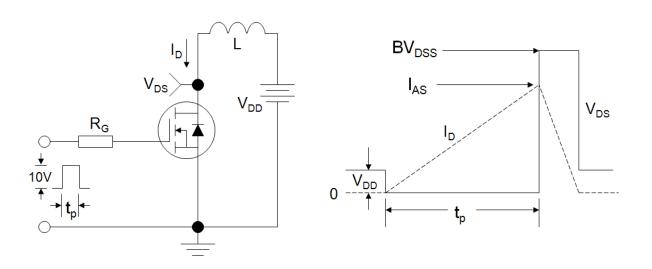
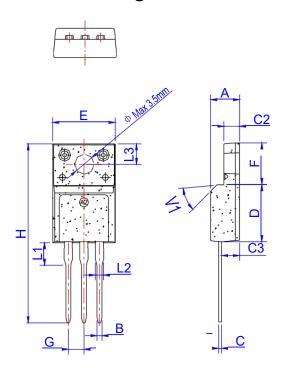


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F Package Mechanical Data



	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	4.50		4.90	0.177		0.193	
В	0.74	0.80	0.83	0.029	0.031	0.033	
С	0.47		0.65	0.019		0.026	
C2	2.45		2.75	0.096		0.108	
СЗ	2.60		3.00	0.102		0.118	
D	8.80		9.30	0.346		0.366	
Е	9.80		10.4	0.386		0.410	
F	6.40		6.80	0.252		0.268	
G		2.54			0.1		
Н	28.0		29.8	1.102		1.173	
L1		3.63			0.143		
L2	1.14		1.70	0.045		0.067	
L3		3.30			0.130		
V1		45°			45°		



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