

62 mm C-Series module with CoolSiC[™] Trench MOSFET and pre-applied thermal interface material

Features

- · Electrical features
 - V_{DSS} = 2000 V
 - $I_{DN} = 300 \text{ A} / I_{DRM} = 600 \text{ A}$
 - Low switching losses
 - High current density
- Mechanical features
 - 4 kV AC 1 min insulation
 - Pre-applied thermal interface material

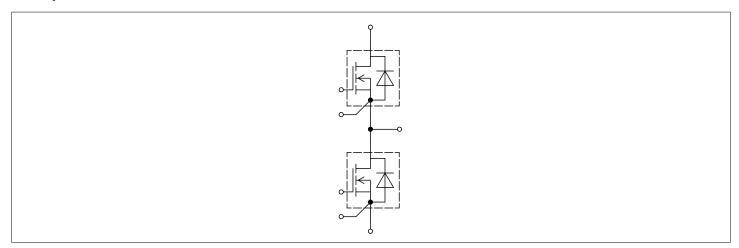
Potential applications

- UPS systems
- DC/DC converter
- High-frequency switching application
- Solar applications

Product validation

• Qualified for industrial applications according to the relevant tests of IEC 60747, 60749 and 60068

Description





62 mm C-Series module



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62 mm C-Series module

1 Package



1 Package

Table 1 Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS, f = 50 Hz, t = 1 min	4.0	kV
Material of module baseplate			Cu	
Internal isolation		basic insulation (class 1, IEC 61140)	Al ₂ O ₃	
Creepage distance	d_{Creep}	terminal to heatsink	29.0	mm
Creepage distance	d_{Creep}	terminal to terminal	23.0	mm
Clearance	d_{Clear}	terminal to heatsink	23.0	mm
Clearance	d_{Clear}	terminal to terminal	11.0	mm
Comparative tracking index	CTI		> 400	
Relative thermal index (electrical)	RTI	housing	140	°C

Table 2 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур.	Max.	
Stray inductance module	L _{sCE}				20		nH
Module lead resistance, terminals - chip	R _{CC'+EE'}	T _C = 25 °C, per switch			0.475		mΩ
Storage temperature	$T_{\rm stg}$			-40		125	°C
Maximum baseplate operation temperature	T_{BPmax}					125	°C
Mounting torque for module mounting	М	- Mounting according to valid application note	M6, Screw	3		6	Nm
Terminal connection torque	М	- Mounting according to valid application note	M6, Screw	2.5		5	Nm
Weight	G				340		g

Note: Storage and shipment of modules with TIM => see AN2012-07

2 MOSFET, T1 / T2

Table 3 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Drain-source voltage	$V_{\rm DSS}$		T _{vj} = 25 °C	2000	V
Implemented drain current	I _{DN}			300	Α

(table continues...)

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2 MOSFET, T1 / T2



Table 3 (continued) Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Continuous DC drain current	I _{DDC}	$T_{\rm vj}$ = 175 °C, $V_{\rm GS}$ = 18 V	T _H = 25 °C	245	A
Repetitive peak drain current	I _{DRM}	verified by design, t _p limited by T _{vjmax}		600	А
Gate-source voltage, max. transient voltage	V_{GS}	D < 0.01		-10/23	V
Gate-source voltage, max. static voltage	V_{GS}			-7/20	V

Table 4 Recommended values

Parameter	Symbol	Note or test condition	Values	Unit
On-state gate voltage	V _{GS(on)}		18	V
Off-state gate voltage	V _{GS(off)}		-3	V

Table 5 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min. Typ. M		Max.	
Drain-source on-resistance	R _{DS(on)}	I _D = 300 A	$V_{\rm GS} = 18 \text{V},$ $T_{\rm vj} = 25 ^{\circ}\text{C}$		3.5	5.3	mΩ
			$V_{\rm GS} = 18 \text{ V},$ $T_{\rm vj} = 125 ^{\circ}\text{C}$		7.3		
			V _{GS} = 18 V, T _{vj} = 175 °C		10.4		
Gate threshold voltage	V _{GS(th)}		I_D = 168 mA, V_{DS} = V_{GS} , T_{vj} = 25 °C, (tested after 1ms pulse at V_{GS} = +20 V)		4.3	5.15	V
Total gate charge	Q _G	$V_{\rm DD}$ = 1200 V, $V_{\rm GS}$ = -3/18 V	V _{DD} = 1200 V, V _{GS} = -3/18 V		1.17		μC
Internal gate resistor	R _{Gint}	T _{vj} = 25 °C			1.2		Ω
Input capacitance	C _{ISS}	$f = 100 \text{ kHz}, V_{DS} = 1200 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		36.1		nF
Output capacitance	C _{OSS}	$f = 100 \text{ kHz}, V_{DS} = 1200 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		0.845		nF
Reverse transfer capacitance	C _{rss}	$f = 100 \text{ kHz}, V_{DS} = 1200 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		0.061		nF
C _{OSS} stored energy	E _{OSS}	$V_{\rm DS}$ = 1200 V, $V_{\rm GS}$ = -3/18 V	/, T _{vj} = 25 °C		1520		μJ
Drain-source leakage current	I _{DSS}	$V_{\rm DS}$ = 2000 V, $V_{\rm GS}$ = -3 V	T _{vj} = 25 °C		0.06	527	μΑ
Gate-source leakage current	I _{GSS}	$V_{\rm DS}$ = 0 V, $T_{\rm vj}$ = 25 °C	V _{GS} = 20 V			400	nA

(table continues...)

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3 Body diode (MOSFET, T1 / T2)



Table 5 (continued) Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур. М	Max.	
Turn-on delay time	t _{d on}	$I_D = 300 \text{ A}, R_{Gon} = 7.1 \Omega,$ $V_{DD} = 1200 \text{ V},$ $V_{GS} = -3/18 \text{ V}$	T _{vj} = 25 °C		204		ns
(inductive load)			T _{vj} = 125 °C		187		
		VGS = -3/10 V	T _{vj} = 175 °C		181		
Rise time (inductive load)	t _r	$I_{\rm D} = 300 \text{ A}, R_{\rm Gon} = 7.1 \Omega,$	T _{vj} = 25 °C		219		ns
		$V_{DD} = 1200 \text{ V},$ $V_{GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		195		
		VGS 3/10 V	T _{vj} = 175 °C		194		
Turn-off delay time	$t_{ m d\ off}$	$I_{\rm D} = 300 \text{ A}, R_{\rm Goff} = 4.3 \Omega,$	T _{vj} = 25 °C		256		ns
(inductive load)	$V_{GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		282			
		VGS 3/10 V	T _{vj} = 175 °C		296		
Fall time (inductive load)	$V_{\rm DD} = 1200 \rm V,$	T _{vj} = 25 °C		80.3		ns	
		$V_{DD} = 1200 \text{ V},$ $V_{GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		82.1		
			T _{vj} = 175 °C		84.2		
Turn-on energy loss per	E _{on}	$I_D = 300 \text{ A}, V_{DD} = 1200 \text{ V},$ $L_{\sigma} = 25 \text{ nH}, V_{GS} = -3/18 \text{ V},$ $R_{GOD} = 7.1 \Omega, \text{ di/dt} = 3.4$	T _{vj} = 25 °C		45.5		mJ
pulse			T _{vj} = 125 °C		46.5		
		$kA/\mu s (T_{vj} = 175 °C)$	T _{vj} = 175 °C		50.5		
Turn-off energy loss per	E _{off}	$I_{\rm D} = 300 \text{ A}, V_{\rm DD} = 1200 \text{ V},$	T _{vj} = 25 °C		23.7		mJ
pulse		$L_{\sigma} = 25 \text{ nH}, V_{GS} = -3/18 \text{ V},$ $R_{Goff} = 4.3 \Omega, \text{ dv/dt} = 11.4$	T _{vj} = 125 °C		24.5		
		$kV/\mu s (T_{vj} = 175 °C)$	T _{vj} = 175 °C		25.2		
Thermal resistance, junction to heat sink	R _{thJH}	per MOSFET, Valid with IFX pre-applied Thermal Interface Material				0.153	K/W
Temperature under switching conditions	T _{vj op}			-40		175	°C

Note:

The selection of positive and negative gate-source voltages impacts losses and the long-term behavior of the MOSFET and body diode. The design guidelines described in Application Notes AN 2018-09 and AN 2021-13 must be considered to ensure sound operation of the device over the planned lifetime.

 $T_{\rm vj,op}$ > 150 °C is allowed for operation at overload conditions for MOSFET and body diode. For detailed specifications, please refer to AN 2021-13.

3 Body diode (MOSFET, T1 / T2)

Table 6 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
DC body diode forward current	I _{SD}	$T_{\rm vj}$ = 175 °C, $V_{\rm GS}$ = -3 V	T _H = 25 °C	180	A

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3 Body diode (MOSFET, T1 / T2)

Table 7Characteristic values

Parameter	Symbol	ol Note or test condition			Values		
				Min.	Тур.	Max.	
Forward voltage	V_{SD}	$I_{SD} = 300 \text{ A}, V_{GS} = -3 \text{ V}$	T _{vj} = 25 °C		4.6	6.15	V
			T _{vj} = 125 °C		4.15		
			T _{vj} = 175 °C		4		1

4 Characteristics diagrams

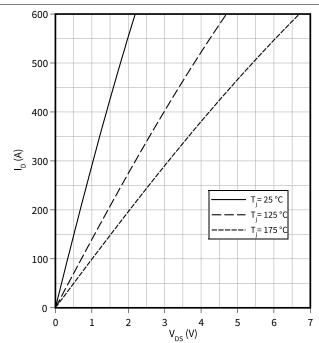


4 Characteristics diagrams

Output characteristic (typical), MOSFET, T1 / T2

 $I_D = f(V_{DS})$

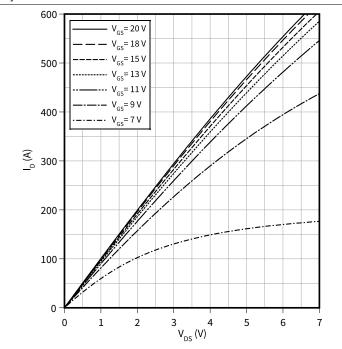
 $V_{GS} = 18 V$



Output characteristic field (typical), MOSFET, T1 / T2

 $I_D = f(V_{DS})$

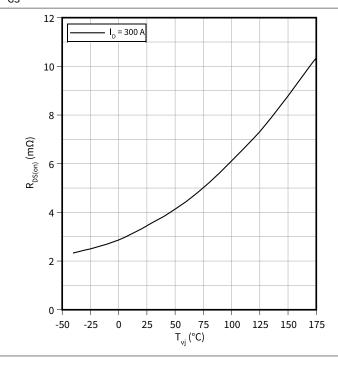
T_{vj} = 175 °C



Drain source on-resistance (typical), MOSFET, T1 / T2

 $R_{DS(on)} = f(T_{vj})$

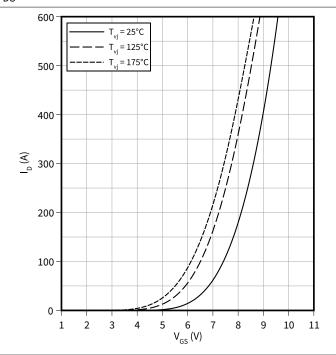
 $V_{GS} = 18 V$



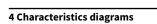
Transfer characteristic (typical), MOSFET, T1 / T2

 $I_D = f(V_{GS})$

 $V_{DS} = 20 \text{ V}$



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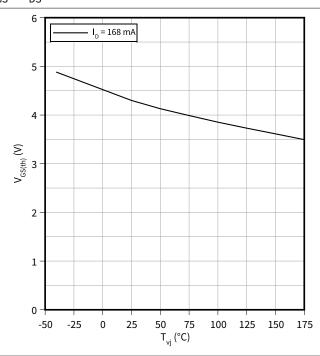




Gate-source threshold voltage (typical), MOSFET, T1/

 $V_{GS(th)} = f(T_{vi})$

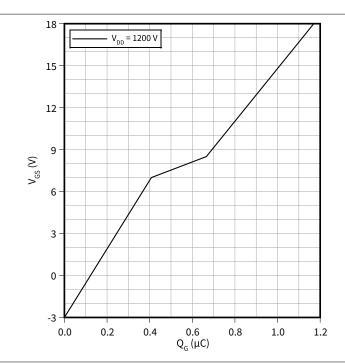




Gate charge characteristic (typical), MOSFET, T1 / T2

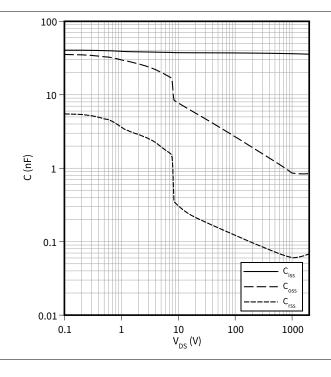
 $V_{GS} = f(Q_G)$

$$I_D = 300 \text{ A}, T_{vj} = 25 \,^{\circ}\text{C}$$



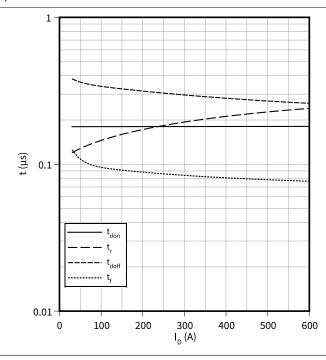
Capacity characteristic (typical), MOSFET, T1 / T2

 $C = f(V_{DS})$ $f = 100 \text{ kHz}, T_{vj} = 25 \text{ °C}, V_{GS} = 0 \text{ V}$



Switching times (typical), MOSFET, T1 / T2

 R_{Goff} = 4.3 Ω , R_{Gon} = 7.1 Ω , V_{DD} = 1200 V, T_{vj} = 175 °C, V_{GS} = -3/18 V



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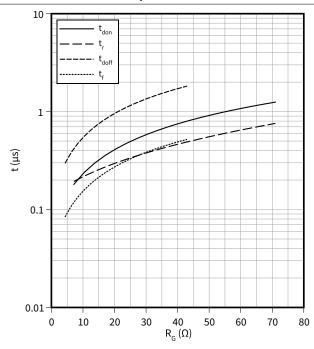


4 Characteristics diagrams

Switching times (typical), MOSFET, T1 / T2

 $t = f(R_G)$

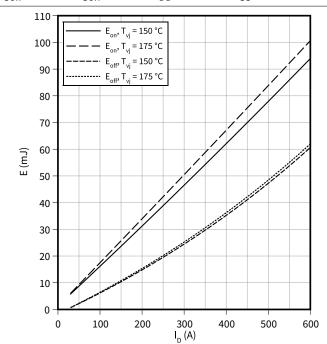
 V_{DD} = 1200 V, I_{D} = 300 A, T_{vj} = 175 °C, V_{GS} = -3/18 V



Switching losses (typical), MOSFET, T1 / T2

 $E = f(I_D)$

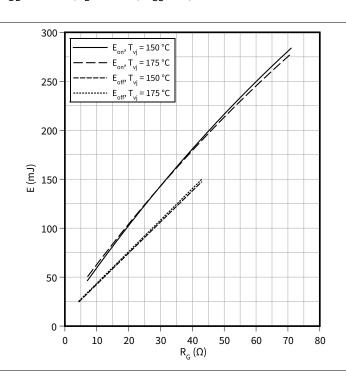
 R_{Goff} = 4.3 Ω , R_{Gon} = 7.1 Ω , V_{DD} = 1200 V, V_{GS} = -3/18 V



Switching losses (typical), MOSFET, T1 $\!\!\!/$ T2

 $E = f(R_G)$

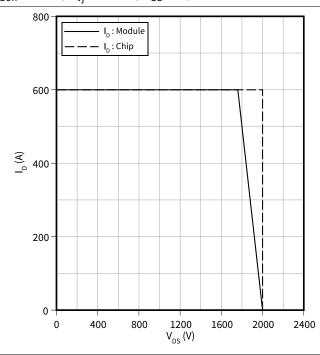
 $V_{DD} = 1200 \text{ V}, I_D = 300 \text{ A}, V_{GS} = -3/18 \text{ V}$



Reverse bias safe operating area (RBSOA), MOSFET, T1 / T2 $\,$

 $I_D = f(V_{DS})$

 $R_{Goff} = 4.3 \Omega, T_{vi} = 175 \,^{\circ}\text{C}, V_{GS} = -3/18 \,^{\circ}\text{V}$



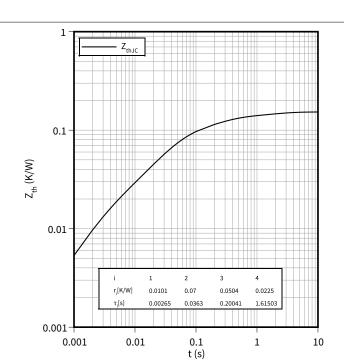
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4 Characteristics diagrams

Transient thermal impedance, MOSFET, T1 / T2

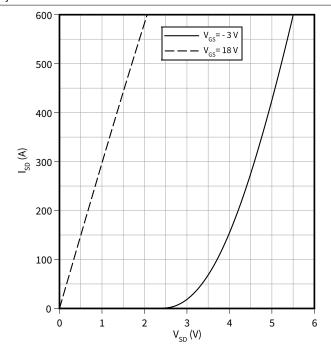
 $Z_{th} = f(t)$



Forward characteristic body diode (typical), MOSFET, T1 / T2 $\,$

 $I_{SD} = f(V_{SD})$





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5 Circuit diagram

5 Circuit diagram

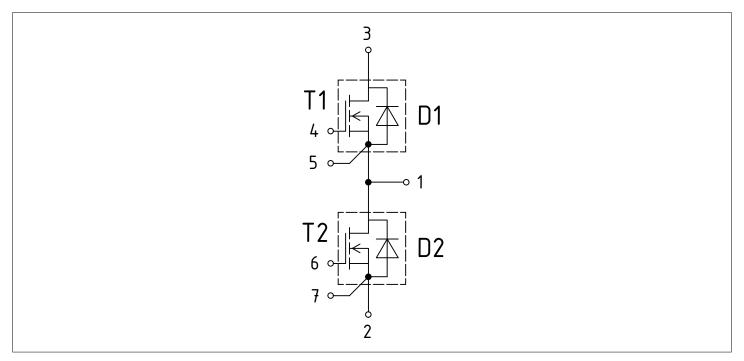


Figure 1

6 Package outlines



6 Package outlines

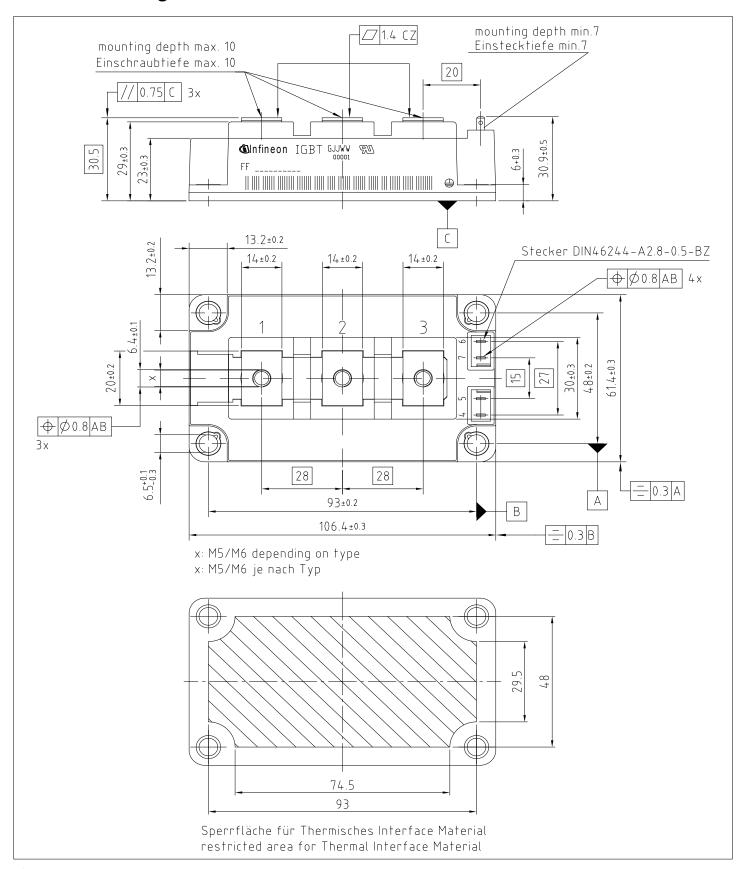
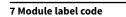


Figure 2

62 mm C-Series module





7 Module label code

Code format	Data Matrix		Barcode 0	Code128
Encoding	ASCII text		Code Set	A
Symbol size	16x16		23 digits	
Standard	IEC24720 and IEC16022		IEC8859-1	
Code content	Content Module serial number Module material number Production order number Date code (production year) Date code (production week) Digit 1 - 5 6 - 11 12 - 19 20 - 21 22 - 23			Example 71549 142846 55054991 15 30
Example	71549142846550549911530			#6550549911530

Figure 3

62 mm C-Series module



Revision history

Revision history

Document revision	Date of release	Description of changes
1.00	2023-05-05	Initial version

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