

MOSFET – N-Channel UniFET™

250 V, 69 A, 41 m Ω

FDA69N25

Description

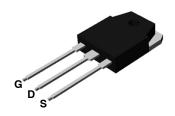
UniFET MOSFET is **onsemi**'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on–state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

Features

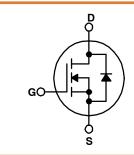
- $R_{DS(on)} = 34 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 34.5 \text{ A}$
- Low Gate Charge (Typ. 77 nC)
- Low C_{rss} (Typ. 84 pF)

Applications

- PDP TV
- Uninterruptible Power Supply
- AC-DC Power Supply



TO-3P-3LD CASE 340BZ



MARKING DIAGRAM



FDA69N25 = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)

ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping [†]
FDA69N25	TO-3P-3LD (Pb-Free)	450 Units / Tube

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <u>BRD8011/D</u>.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter		Rating	Unit
V _{DSS}	Drain-Source Voltage		250	V
V _{DS(Avalanche)}	Repetitive Avalanche Voltage (Notes 1, 2)		300	V
I _D	Drain Current	- Continuous (T _C = 25°C) - Continuous (T _C = 100°C)	69 44.2	Α
I _{DM}	Drain Current	- Pulsed (Note 1)	276	Α
V _{GSS}	Gate-Source Voltage		±30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		1894	mJ
I _{AR}	Avalanche Current (Note 1)		69	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		48	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P _D	Power Dissipation	(T _C = 25°C) – Derate Above 25°C	480 3.84	W W/°C
T _{J,} T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. L = 0.64 mH, I_{AS} = 69 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C.

3. $I_{SD} \le 69$ A, $I_{SD} \le 69$ A, $I_{SD} \le I_{SD} \le I_{SD}$, starting $I_{SD} \le I_{SD} \le I_{SD}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.26	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Charac	cteristics	•			•	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	250	_	_	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.25	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 250 V, V _{GS} = 0 V	-	_	1	μΑ
		V _{DS} = 200 V, T _C = 125°C	-	_	10	μΑ
I _{GSSF}	Gate to Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	-	_	100	nA
I _{GSSR}	Gate to Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	-100	nA
On Charac	cteristics	•	•		•	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0	-	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 34.5 A	_	0.034	0.041	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D =34.5 A	-	25	-	S
Dynamic C	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	3570	4640	pF
Coss	Output Capacitance		_	750	980	pF
C_{rss}	Reverse Transfer Capacitance		_	84	130	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 125 \text{ V}, I_D = 69 \text{ A}, V_{GS} = 10 \text{ V},$	_	95	200	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$ (Note 4)	_	855	1720	ns
$t_{d(off)}$	Turn-Off Delay Time		_	130	270	ns
t _f	Turn-Off Fall Time		-	220	450	ns
Qg	Total Gate Charge	$V_{DS} = 200 \text{ V}, I_D = 69 \text{ A}, V_{GS} = 10 \text{ V}$	_	77	100	nC
Qgs	Gate-Source Charge	(Note 4)	_	24	_	nC
Qgd	Gate-Drain Charge		_	37	_	nC
Drain-Sou	urce Diode Characteristics and Maximum	Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current		_	-	34	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		_	_	136	Α
V_{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 69 A	_	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 69 A, dI _F /dt = 100 A/μs	_	210		ns
Q _{rr}	Reverse Recovery Charge		_	5.7	_	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

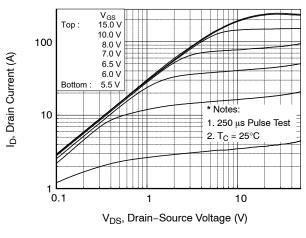


Figure 1. On-Region Characteristics

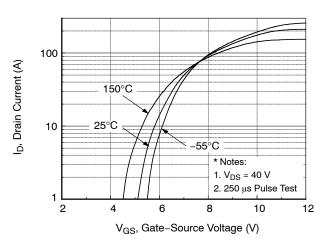


Figure 2. Transfer Characteristics

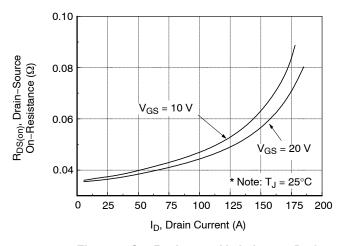


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

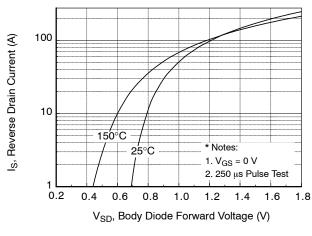


Figure 4. Body Diode Forward Voltage Variation vs. Source Current And Temperature

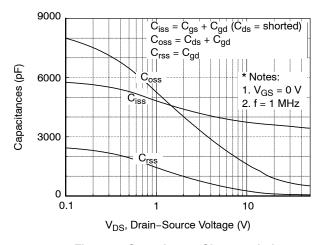


Figure 5. Capacitance Characteristics

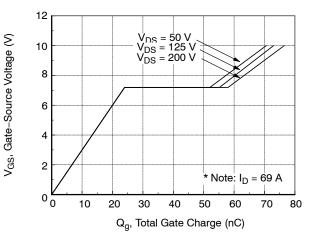


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

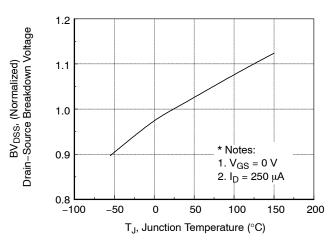


Figure 7. Breakdown Voltage Variation vs. Temperature

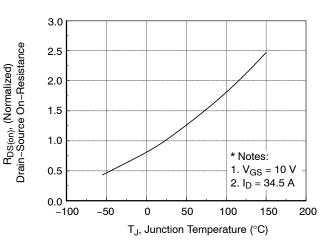


Figure 8. On–Resistance Variation vs. Temperature

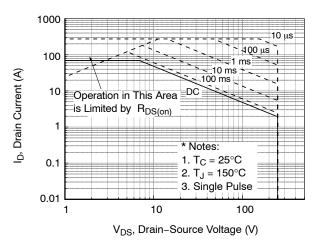


Figure 9. Maximum Safe Operating Area

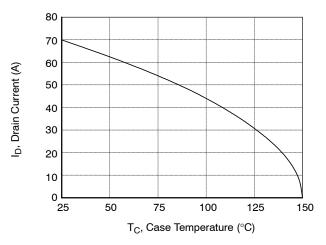


Figure 10. Maximum Drain Current vs.

Case Temperature

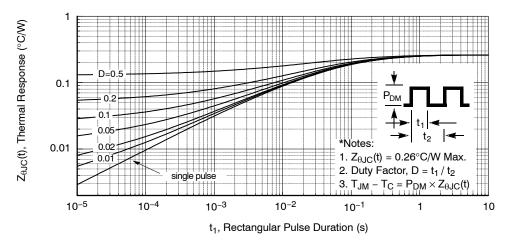


Figure 11. Transient Thermal Response Curve

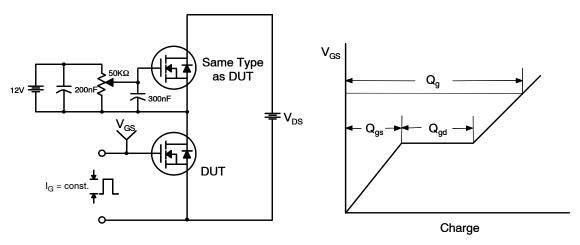


Figure 12. Gate Charge Test Circuit & Waveform

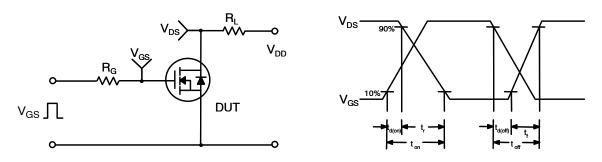


Figure 13. Resistive Switching Test Circuit & Waveforms

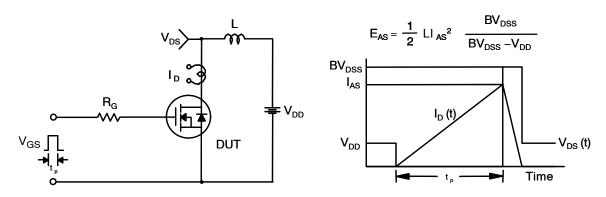
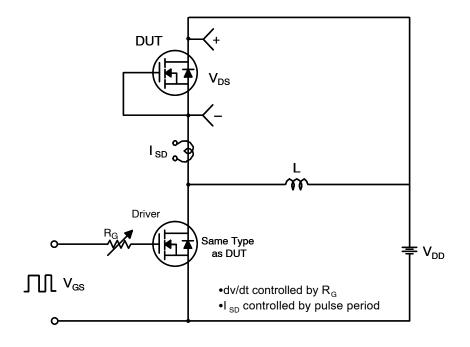


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



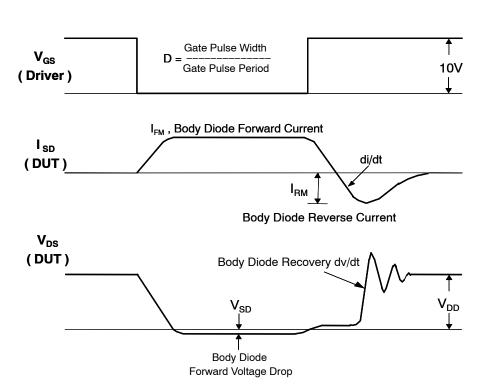


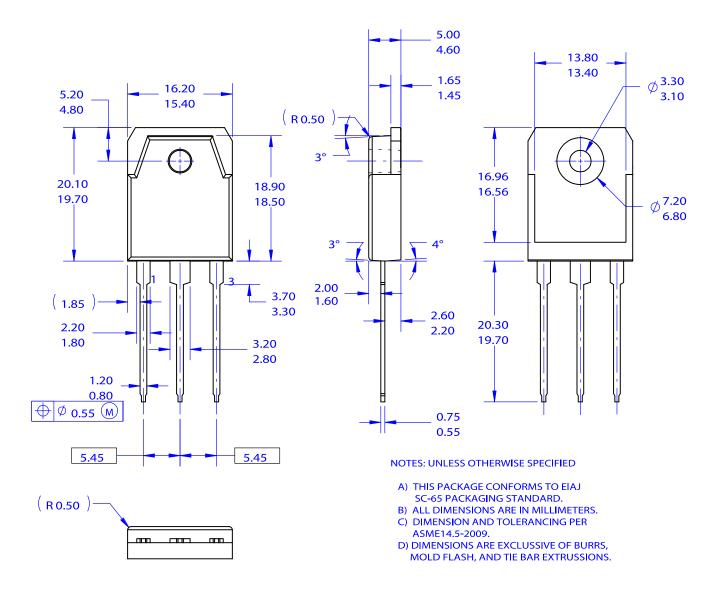
Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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TO-3P-3LD / EIAJ SC-65, ISOLATED CASE 340BZ ISSUE O

DATE 31 OCT 2016



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