

N-Channel Enhancement Mode MOSFET

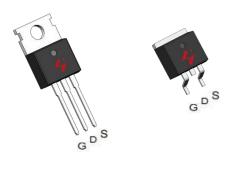
Feature

- 40V/240A
 RDS(ON)= 1.4 mΩ (typ.) @VGS = 10V
 RDS(ON)= 1.8 mΩ (typ.) @VGS = 4.5V
- 100% Avalanche Tested
- Reliable and Rugged
- Lead-Free and Green Devices Available (RoHS Compliant)

Applications

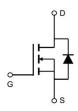
- Power Management for DC/DC
- Battery management
- Power management in Inverter System

Pin Description



TO-220FB-3L

TO-263-2L



N-Channel MOSFET

Ordering and Marking Information





Package Code

P:TO-220FB-3L

B: TO-263-2L

Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Rat	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range		-55 to 175	°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode)		240	А
Mounted on	Large Heat Sink		'	
I DM	Pulsed Drain Current *	Tc=25°C	1000	А
	Continuous Davis Comment	Tc=25°C	240	А
lь	Continuous Drain Current Tc=100°C		170	А
	Maria de Branco Branco de Carlos	Tc=25°C	200	W
Po	Maximum Power Dissipation Tc=100°C		100	W
R₀Jc	Thermal Resistance, Junction-to-Case		0.75	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		62.5	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		758**	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

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Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} = 250μA	40	-	-	V
		V _{DS} =40V,V _{GS} =0V	-	-	1	μA
IDSS	Drain-to-Source Leakage Current	TJ=125°C	; -	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} = 250µA	1	1.9	3	V
Igss	Gate-Source Leakage Current	Vgs=±20V,Vps=0V	-	-	±100	nA
D	Drain-Source On-State Resistance	V _{GS} = 10V,I _{DS} = 40A		1.4	1.7	mΩ
Rds(on)		V _{GS} = 4.5V,I _{DS} = 40A	-	1.8	2.2	mΩ
Diode Cha	Diode Characteristics					
VsD	Diode Forward Voltage	IsD=40A,VGS=0V	-	0.8	1.2	V
trr	Reverse Recovery Time	140A dl/d4-400A/	-	42.2	-	ns
Qrr	Reverse Recovery Charge	IsD=40A,dIsD/dt=100A/µs	-	42.1	-	nC

^{**} Surface mounted on 1in2 FR-4 board.

^{***} Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25Ω , Vgs =10V.

HYG016N04LS1P/B



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Comple al	Downwotor	Took Conditions	HY	HYG016N04LS1		I I to it
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic (Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	1.8	-	Ω
Ciss	Input Capacitance	V _{GS} =0V,	-	5894	-	
Coss	Output Capacitance	V _{DS} = 25V,	-	1276	-	pF
Crss	Reverse Transfer Capacitance	Frequency=500kHz	-	53	-	
td(ON)	Turn-on Delay Time		-	17	-	
Tr	Turn-on Rise Time	V_{DD} = 20 V , R_{G} =4 Ω ,	-	76	-	
t d(OFF)	Turn-off Delay Time	lps=40A,Vgs= 10V	-	78	-	ns
Tf	Turn-off Fall Time		-	101	-	
Gate Cha	Gate Charge Characteristics					
Qg	Total Gate Charge(V _{GS} =10V)		-	92.5	-	
Qg	Total Gate Charge (V _{GS} =4.5V)	$V_{DS} = 32V, V_{GS} = 10V,$	-	43.6	-	0
Qgs	Gate-Source Charge	I _{Ds} = 40A	-	22.7	-	nC
Qgd	Gate-Drain Charge		-	15.4	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

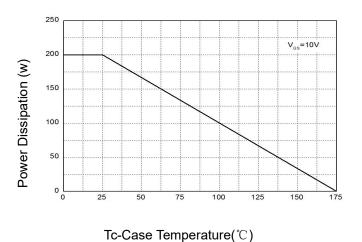
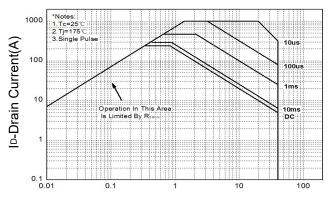


Figure 3: Safe Operation Area



Vps-Drain-Source Voltage(V)

Figure 5: Output Characteristics

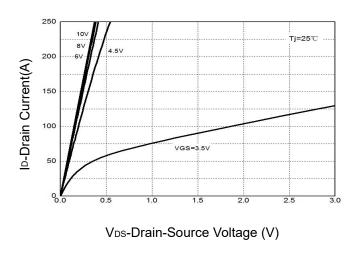
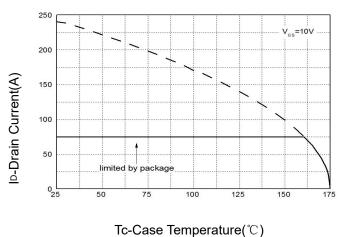
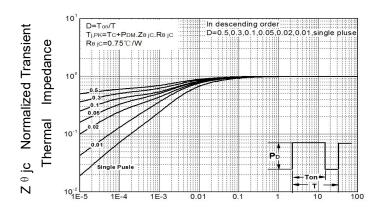


Figure 2: Drain Current



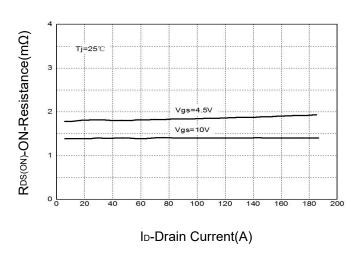
10-0d3c Temperature(c)

Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

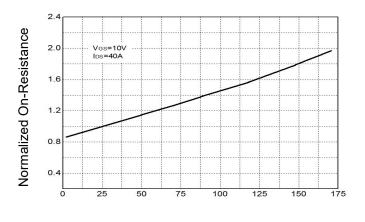
Figure 6: Drain-Source On Resistance





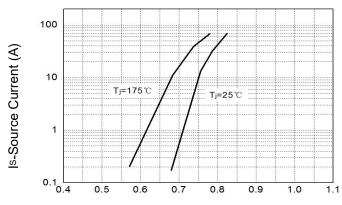
Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature



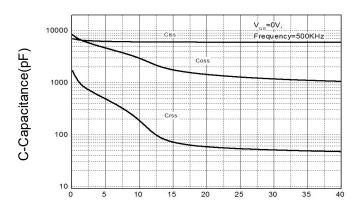
Tj-Junction Temperature (°C)

Figure 8: Source-Drain Diode Forward



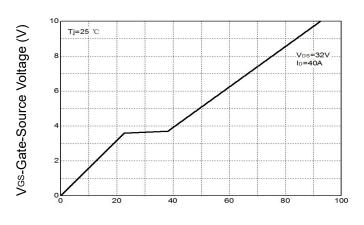
Vsp-Source-Drain Voltage(V)

Figure 9: Capacitance Characteristics



V_{DS}-Drain-Source Voltage (V)

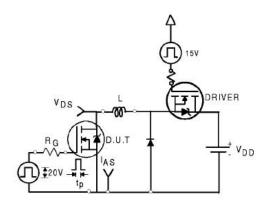
Figure 10: Gate Charge Characteristics

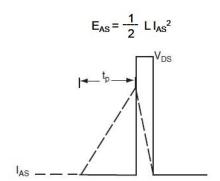


QG-Gate Charge (nC)

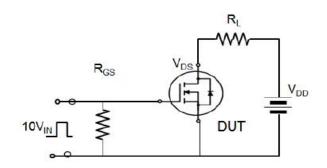


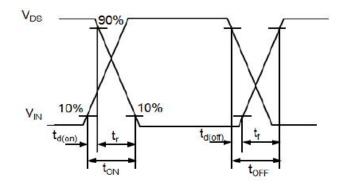
Avalanche Test Circuit



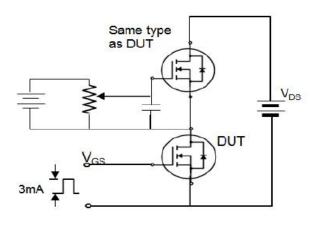


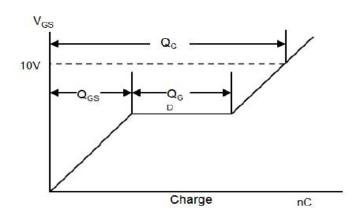
Switching Time Test Circuit





Gate Charge Test Circuit





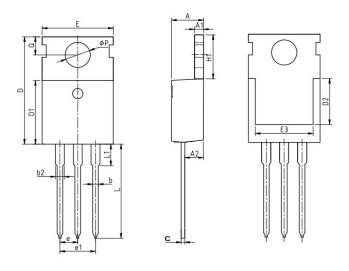


Device Per Unit

Package Type	Unit	Quantity
TO-220FB-3L	Tube	50
TO-263-2L	Reel	800

Package Information

TO-220FB-3L



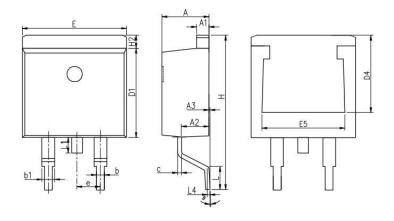
COMMON DIMENSIONS

SYMBOL	mm			
STIVIBUL	MIN	NOM	MAX	
Α	4.37	4.57	4.77	
A1	1.25	1.30	1.45	
A2	2.20	2.40	2.60	
b	0.70	0.80	0.95	
b2	1.17	1.27	1.47	
С	0.40	0.50	0.65	
D	15.10	15.60	16.10	
D1	8.80	9.10	9.40	
D2	5.50	-	ı	
E	9.70	10.00	10.30	
E3	7.00	-	ı	
е	2.54 BSC			
e1		5.08 BSC		
H1	6.25 6.50 6.85		6.85	
L	12.75	13.50	13.80	
L1	_	3.10	3.40	
ФР	3.40	3.60	3.80	
Q	2.60	2.80	3.00	



Package Information

TO-263-2L

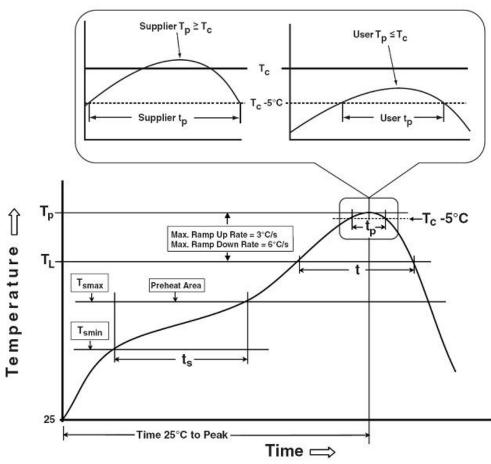


COMMON DIMENSIONS

	mm			
SYMBOL	MIN	NOM	MAX	
Α	4.37	4.57	4.77	
A1	1.22	1.27	1.42	
A2	2.49	2.69	2.89	
A3	0	0.13	0.25	
b	0.7	0.81	0.96	
b1	1.17	1.27	1.47	
С	0.3	0.38	0.53	
D1	8.5	8.7	8.9	
D4	6.6	-	-	
Е	9.86	10.16	10.36	
E5	7.06	-	-	
е		2.54 BSC		
Н	14.7	15.1	15.5	
H2	1.07	1.27	1.47	
L	2	2.3	2.6	
L1	1.4	1.55	1.7	
L4	0.25 BSC			
θ	0°	5°	9°	



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly		
Preheat & Soak	400 °C	450 °C		
Temperature min (T _{smin})	100 °C	150 °C		
Temperature max (T _{smax})	150 °C	200 °C		
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds		
Average ramp-up rate	2 °C/	200/		
(T _{smax} to T _P)	3 °C/second max.	3°C/second max.		
Liquidous temperature (T₋)	183 °C	217 °C		
Time at liquidous (t∟)	60-150 seconds	60-150 seconds		
Peak package body Temperature	Con Classification Towns in table 4	CacClassification Tampin table 2		
(T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2		
Time (t _P)** within 5°C of the specified	00**	0.0**		
classification temperature (T₀)	20** seconds	30** seconds		
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.		
Time 25°C to peak temperature	6 minutes max.	8 minutes max.		
*Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.				

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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