62 mm C-Series module



Preliminary datasheet 62 mm C-Series module with CoolSiC[™] Trench MOSFET and pre-applied thermal interface material

Features

- · Electrical features
 - V_{DSS} = 1200 V
 - $I_{DN} = 420 \text{ A} / I_{DRM} = 840 \text{ A}$
 - High current density
 - Low switching losses
- Mechanical features
 - 4 kV AC 1 min insulation
 - Pre-applied thermal interface material

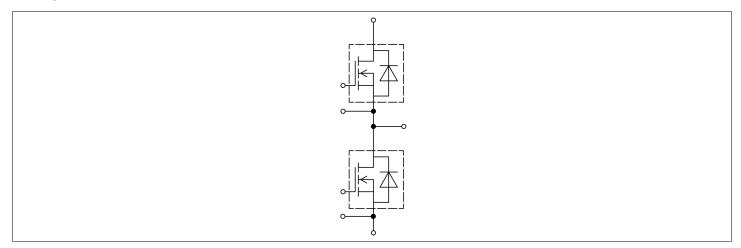
Potential applications

- UPS systems
- Solar applications
- DC/DC converter
- High-frequency switching application
- Energy storage systems
- · DC charger for EV

Product validation

• Qualified for industrial applications according to the relevant tests of IEC 60747, 60749 and 60068

Description





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1 Package



1 Package

Table 1 Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS, f = 50 Hz, t = 60 s	4.0	kV
Material of module baseplate			Cu	
Internal isolation		basic insulation (class 1, IEC 61140)	Al ₂ O ₃	
Creepage distance	d_{Creep}	terminal to heatsink	29.0	mm
Creepage distance	d_{Creep}	terminal to terminal	23.0	mm
Clearance	d_{Clear}	terminal to heatsink	23.0	mm
Clearance	d_{Clear}	terminal to terminal	11.0	mm
Comparative tracking index	CTI		> 400	
Relative thermal index (electrical)	RTI	housing	140	°C

Table 2 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур.	Max.	
Stray inductance module	L _{sCE}				20		nH
Module lead resistance, terminals - chip	R _{CC'+EE'}	T _H = 25 °C, per switch			0.475		mΩ
Storage temperature	$T_{\rm stg}$			-40		125	°C
Maximum baseplate operation temperature	T_{BPmax}					125	°C
Mounting torque for module mounting	М	- Mounting according to valid application note	M6, Screw	3		6	Nm
Terminal connection torque	М	- Mounting according to valid application note	M6, Screw	2.5		5	Nm
Weight	G				340		g

Note: Storage and shipment of modules with TIM => see AN2012-07.

2 MOSFET

Table 3 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Drain-source voltage	$V_{\rm DSS}$		T _{vj} = 25 °C	1200	V
Implemented drain current	I _{DN}			420	Α

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2 MOSFET



Table 3 (continued) Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Continuous DC drain current	I _{DDC}	$T_{\rm vj}$ = 175 °C, $V_{\rm GS}$ = 18 V	T _H = 65 °C	350	А
Repetitive peak drain current	/ _{DRM}	verified by design, t _p limited by T _{vjmax}		840	А
Gate-source voltage, max. transient voltage	V _{GS}	D < 0.01		-10/23	V
Gate-source voltage, max. static voltage	V _{GS}			-7/20	V

Table 4 Recommended values

Parameter	Symbol	Note or test condition	Values	Unit
On-state gate voltage	V _{GS(on)}		1518	V
Off-state gate voltage	V _{GS(off)}		-50	V

Table 5 Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
				Min.	Тур.	Max.	-
Drain-source on-resistance	R _{DS(on)}	I _D = 420 A	$V_{\rm GS} = 18 \text{ V},$ $T_{\rm vj} = 25 ^{\circ}\text{C}$		1.96		mΩ
			V _{GS} = 18 V, T _{vj} = 125 °C		3.17		
			V _{GS} = 18 V, T _{vj} = 175 °C		4.21		
			$V_{\rm GS} = 15 \text{ V},$ $T_{\rm vj} = 25 ^{\circ}\text{C}$		2.36		
Gate threshold voltage	V _{GS(th)}	I_D = 168 mA, V_{DS} = V_{GS} , T_{vj} = 25 °C, (tested after 1ms pulse at V_{GS} = +20 V)		3.45	4.3	5.15	V
Total gate charge	Q _G	$V_{\rm DD}$ = 800 V, $V_{\rm GS}$ = -3/18 V			1.2		μC
Internal gate resistor	R _{Gint}	T _{vj} = 25 °C			1.3		Ω
Input capacitance	C _{ISS}	$f = 100 \text{ kHz}, V_{DS} = 800 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		36.3		nF
Output capacitance	C _{OSS}	$f = 100 \text{ kHz}, V_{DS} = 800 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		1.8		nF
Reverse transfer capacitance	C _{rss}	$f = 100 \text{ kHz}, V_{DS} = 800 \text{ V},$ $V_{GS} = 0 \text{ V}$	T _{vj} = 25 °C		0.118		nF
C _{OSS} stored energy	E _{OSS}	$V_{\rm DS}$ = 800 V, $V_{\rm GS}$ = -3/18 V,	T _{vj} = 25 °C		709		μJ
Drain-source leakage current	I _{DSS}	$V_{\rm DS}$ = 1200 V, $V_{\rm GS}$ = -3 V	T _{vj} = 25 °C		0.24	527	μА

(table continues...)

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2 MOSFET



Table 5 (continued) Characteristic values

Parameter	Symbol	Note or test condition			Values		Unit
			Min.		Тур. Мах.		
Gate-source leakage current	I _{GSS}	$V_{\rm DS} = 0 \text{ V}, T_{\rm vj} = 25 ^{\circ}\text{C}$	V _{GS} = 20 V			400	nA
Turn-on delay time		T _{vj} = 25 °C		143		ns	
(inductive load)		$V_{\rm DD} = 600 \text{ V}, V_{\rm GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		132		
			T _{vj} = 175 °C		130		
Rise time (inductive load)	t _r	$I_{\rm D} = 420 \text{ A}, R_{\rm Gon} = 4.7 \Omega,$	T _{vj} = 25 °C		153		ns
		$V_{\rm DD} = 600 \text{ V}, V_{\rm GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		142		
			T _{vj} = 175 °C		127		
Turn-off delay time (inductive load)	t _{d off}		T _{vj} = 25 °C		150		ns
		$V_{\rm DD} = 600 \text{ V}, V_{\rm GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		162		1
			T _{vj} = 175 °C		169		
Fall time (inductive load)	V = 600 V V = 2/10 V	T _{vj} = 25 °C		33		ns	
		$V_{\rm DD} = 600 \text{ V}, V_{\rm GS} = -3/18 \text{ V}$	T _{vj} = 125 °C		33		
			T _{vj} = 175 °C		34		
Turn-on energy loss per	E _{on}	$I_{\rm D}$ = 420 A, $V_{\rm DD}$ = 600 V,	T _{vj} = 25 °C		17.2		mJ
pulse		$L_{\sigma} = 10 \text{ nH}, V_{GS} = -3/18 \text{ V},$ $R_{Gon} = 4.7 \Omega, \text{ di/dt} = 5.2$	T _{vj} = 125 °C		16.8		
		$kA/\mu s (T_{vi} = 175 °C)$	T _{vj} = 175 °C		17.2		
Turn-off energy loss per	E _{off}	$I_{\rm D}$ = 420 A, $V_{\rm DD}$ = 600 V,	T _{vi} = 25 °C		7.5		mJ
pulse		$L_{\sigma} = 10 \text{ nH}, V_{GS} = -3/18 \text{ V},$	T _{vj} = 125 °C		8		1
	$\kappa_{\text{Goff}} = 1.5 \Omega$, $\alpha v/\alpha t = 14.2 \vdash$	T _{vj} = 175 °C		8.4			
Thermal resistance, junction to heat sink	R _{thJH}	per MOSFET, Valid with IF Thermal Interface Materi				0.14	K/W
Temperature under switching conditions	T _{vj op}			-40		175	°C

Note:

The selection of positive and negative gate-source voltages impacts losses and the long-term behavior of the MOSFET and body diode. The design guidelines described in Application Notes AN 2018-09 and AN 2021-13 must be considered to ensure sound operation of the device over the planned lifetime.

Tvj,op > 150°C is allowed for operation at overload conditions for MOSFET and body diode. For detailed specifications, please refer to AN 2021-13.

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3 Body diode (MOSFET)

3 Body diode (MOSFET)

Table 6 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
DC body diode forward	I _{SD}	$T_{\rm vj}$ = 175 °C, $V_{\rm GS}$ = -3 V	T _H = 65 °C	190	Α
current					

Table 7 Characteristic values

Parameter	Symbol Note or test condition			Values			Unit
				Min.	Тур.	Max.	
Forward voltage	V_{SD}	$I_{SD} = 420 \text{ A}, V_{GS} = -3 \text{ V}$	T _{vj} = 25 °C		4.22	5.59	V
			T _{vj} = 125 °C		3.95		
			T _{vj} = 175 °C		3.85		

4 Characteristics diagrams

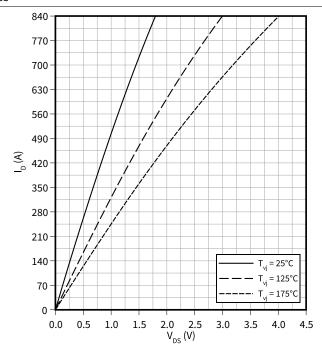


4 Characteristics diagrams

Output characteristic (typical), MOSFET

 $I_D = f(V_{DS})$

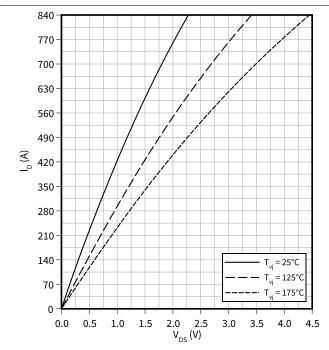
 $V_{GS} = 18 V$



Output characteristic (typical), MOSFET

 $I_D = f(V_{DS})$

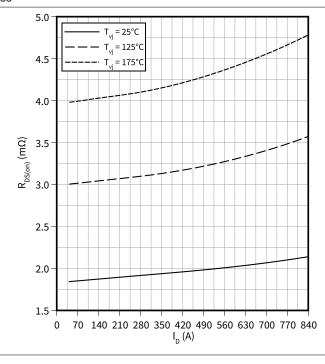
 $V_{GS} = 15 V$



Drain source on-resistance (typical), MOSFET

 $R_{DS(on)} = f(I_D)$

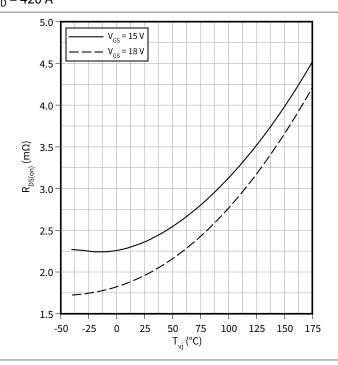
 $V_{GS} = 18 V$



Drain source on-resistance (typical), MOSFET

 $R_{DS(on)} = f(T_{vi})$

 $I_D = 420 A$



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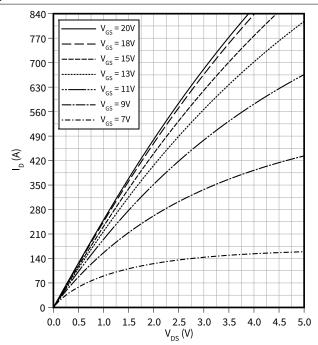


4 Characteristics diagrams



 $I_D = f(V_{DS})$

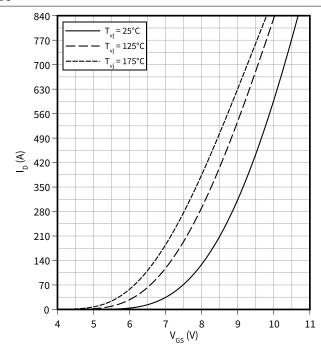
 $T_{vj} = 175$ °C



Transfer characteristic (typical), MOSFET

 $I_D = f(V_{GS})$

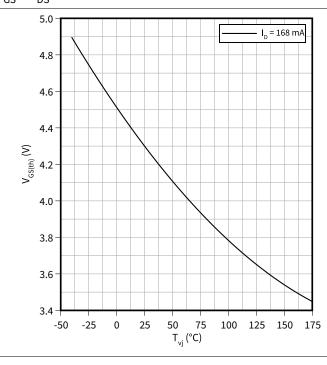
 $V_{DS} = 20 V$



Gate-source threshold voltage (typical), MOSFET

 $V_{GS(th)} = f(T_{vj})$

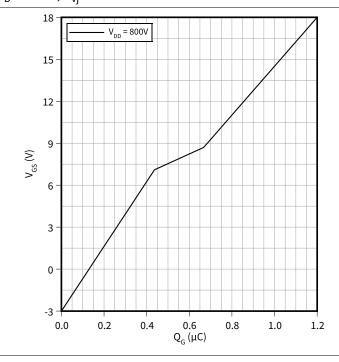
 $V_{GS} = V_{DS}$



Gate charge characteristic (typical), MOSFET

 $V_{GS} = f(Q_G)$

 $I_D = 420 A$, $T_{vj} = 25 °C$



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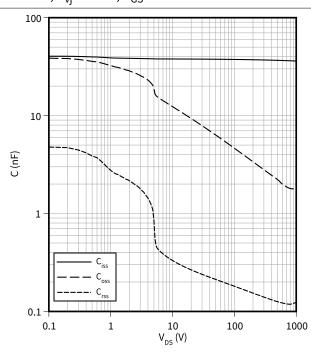


4 Characteristics diagrams

Capacity characteristic (typical), MOSFET

 $C = f(V_{DS})$

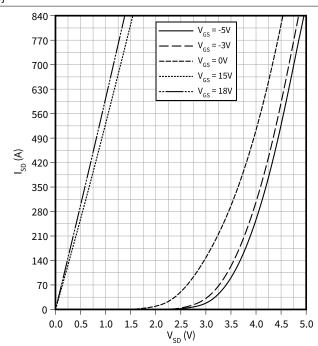
f = 100 kHz, $T_{vj} = 25 \,^{\circ}\text{C}$, $V_{GS} = 0 \,^{\circ}\text{V}$



Forward characteristic body diode (typical), MOSFET

 $I_{SD} = f(V_{SD})$

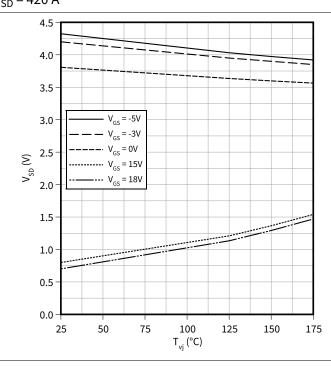
 T_{vj} = 25 °C



Forward voltage of body diode (typical), MOSFET

 $V_{SD} = f(T_{vi})$

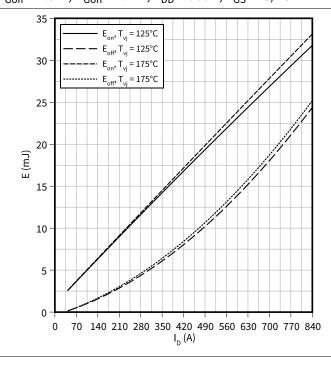
I_{SD} = 420 A



Switching losses (typical), MOSFET

 $E = f(I_D)$

 $R_{Goff} = 1.5 \Omega$, $R_{Gon} = 4.7 \Omega$, $V_{DD} = 600 V$, $V_{GS} = -3/18 V$



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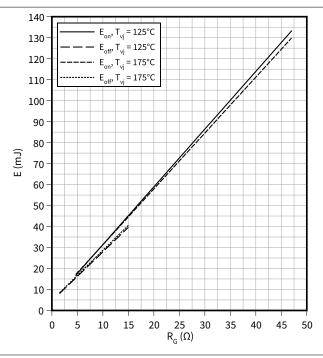


4 Characteristics diagrams

Switching losses (typical), MOSFET

 $E = f(R_G)$

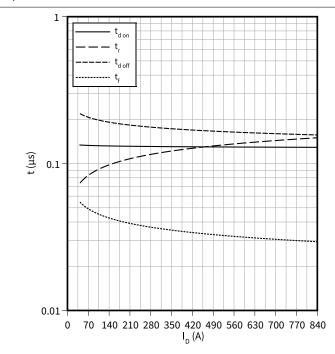
 V_{DD} = 600 V, I_{D} = 420 A, V_{GS} = -3/18 V



Switching times (typical), MOSFET

 $t = f(I_D)$

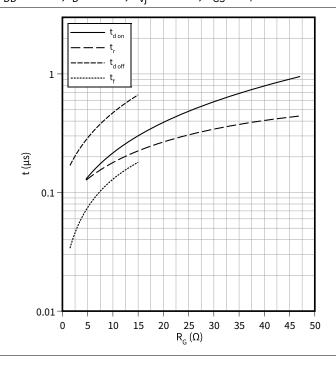
 R_{Goff} = 1.5 Ω , R_{Gon} = 4.7 Ω , V_{DD} = 600 V, T_{vj} = 175 °C, V_{GS} = -3/18 V



Switching times (typical), MOSFET

 $t = f(R_G)$

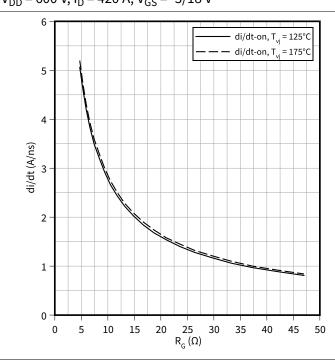
 V_{DD} = 600 V, I_{D} = 420 A, T_{vj} = 175 °C, V_{GS} = -3/18 V



Current slope (typical), MOSFET

 $di/dt = f(R_G)$

 V_{DD} = 600 V, I_{D} = 420 A, V_{GS} = -3/18 V



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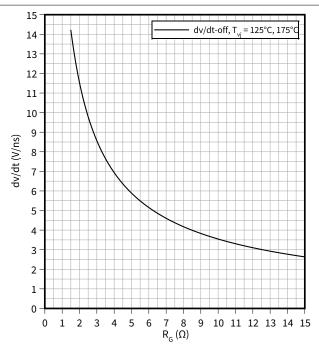
Revision 0.20 2023-03-03

4 Characteristics diagrams

Voltage slope (typical), MOSFET

 $dv/dt = f(R_G)$

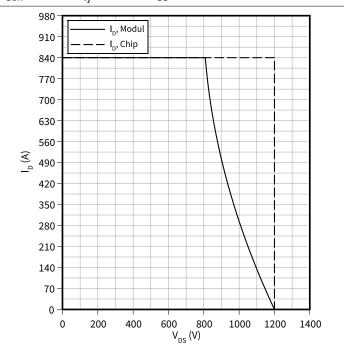
$$V_{DD} = 600 \text{ V}, I_D = 420 \text{ A}, V_{GS} = -3/18 \text{ V}$$



Reverse bias safe operating area (RBSOA), MOSFET

 $I_D = f(V_{DS})$

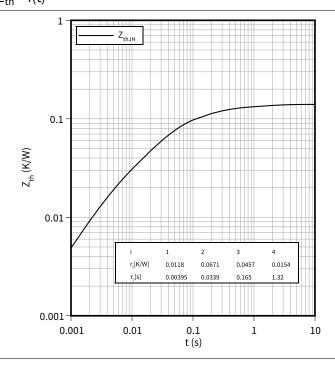
$$R_{Goff} = 1.5 \Omega$$
, $T_{vj} = 175 \,^{\circ}\text{C}$, $V_{GS} = -3/18 \,^{\circ}\text{V}$



Transient thermal impedance, MOSFET

 $Z_{th} = f(t)$

Datasheet



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5 Circuit diagram

5 Circuit diagram

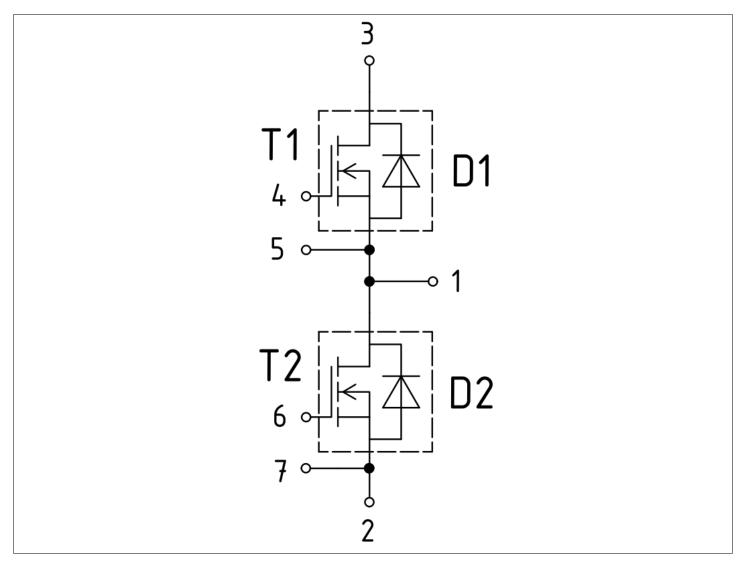


Figure 1

6 Package outlines



6 Package outlines

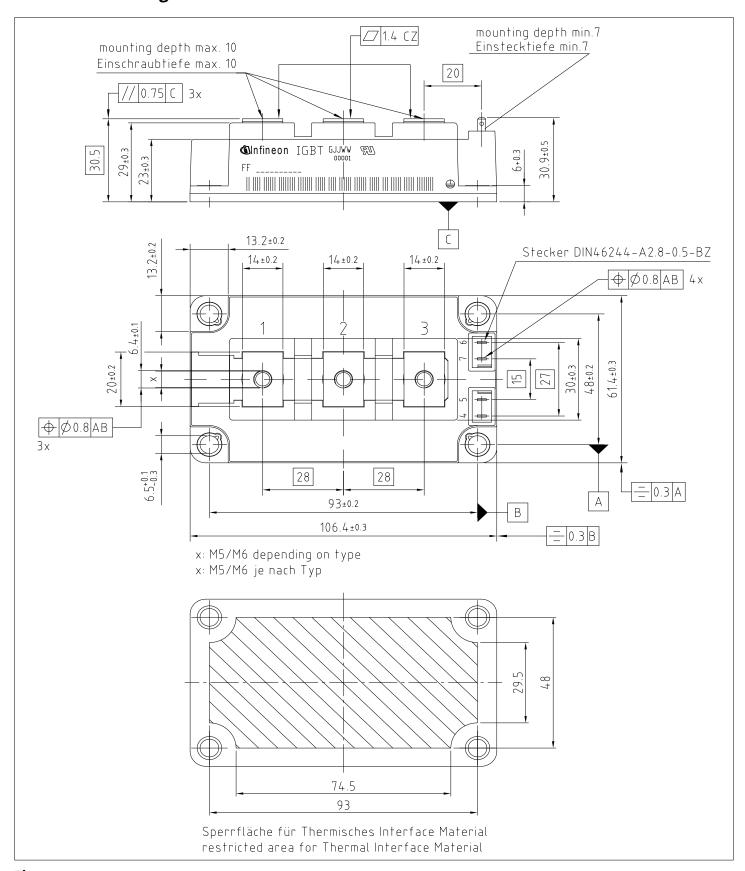
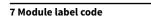


Figure 2

62 mm C-Series module





7 Module label code

Code format	Data Matrix		Barcode (Code128		
Encoding	ASCII text		Code Set			
Symbol size	16x16		23 digits			
Standard	IEC24720 and IEC16022		IEC8859-1			
Code content	Content Module serial number Module material number Production order number Date code (production year) Date code (production week)	Module serial number 1 - 5 Module material number 6 - 11 Production order number 12 - 19 Date code (production year) 20 - 21		Example 71549 142846 55054991 15 30		
Example 71549142846550549911530 71549142846550549911530						

Figure 3

62 mm C-Series module



Revision history

Revision history

Document revision	Date of release	Description of changes
0.10	2023-01-20	Initial version
0.20	2023-03-03	Preliminary datasheet

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