

#### • General Description

The AGM1010A-E combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

#### Features

- Advance high cell density Trench technology
- Low R<sub>DS(ON)</sub> to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

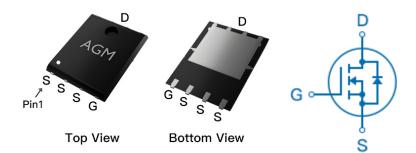
#### Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

## **Product Summary**

BVDSS	RDSON	ID
100V	5.5mΩ	90A

### PDFN5\*6 Pin Configuration



#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM1010A	AGM1010A-E	PDFN5*6	330mm	12mm	3000

#### Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	90	А
.5	Drain Current-Continuous(Tc=100℃)	70	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	360	А
PD	Maximum Power Dissipation(Tc=25℃)	125	W
	Maximum Power Dissipation(Tc=100℃)	50	W
EAS	Avalanche energy (Note 3)	380	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}\!\mathbb{C}$

#### Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>		20	°C/W
RøJC	Thermal Resistance Junction-Case <sup>1</sup>		1.0	°C/W



Table 3. Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off Sta	tes					
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	100			V
IDSS	Zero Gate Voltage Drain Current	VDS=100V,VGS=0V			1	μΑ
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	1.6	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=8A		20		S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=15A		5.5	7.6	mΩ
1.20(0)		VGS=4.5V, ID=8A		8.0	10.4	mΩ
Dynamic C	Characteristics					
Ciss	Input Capacitance	VDS=40V,VGS=0V,		3453		pF
Coss	Output Capacitance	F=1MHZ		972		pF
Crss	Reverse Transfer Capacitance			33		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz		1.4		Ω
Switching	Times					
td(on)	Turn-on Delay Time			14.5		nS
tr	Turn-on Rise Time	VGS=10V,VDS=50V,		21.5		nS
td(off)	Turn-Off Delay Time	ID=1A,RGEN=6Ω		54		nS
tf	Turn-Off Fall Time			84		nS
Qg	Total Gate Charge			39.5		nC
Qgs	Gate-Source Charge	VGS=10V, VDS=50V, ID=8.5A		4.4		nC
Qgd	Gate-Drain Charge	_ ID-0.5A		12.3		nC
Source-Dr	ain Diode Characteristics			. '		
ISD	Source-Drain Current(Body Diode)				90	А
VSD	Forward on Voltage	VGS=0V,IS=15A		0.7	1.2	V
trr	Reverse Recovery Time	IF=15A , dI/dt=100A/μs ,		19		ns
Qrr	Reverse Recovery Charge	TJ=25℃				nc

Notes 1. The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25  $^{\circ}$ C,VDD=50V,Vgs=10V,ID=39A,L=0.5mH,RG=25ohm



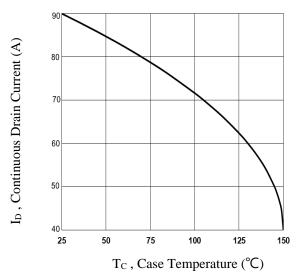


Fig.1 Continuous Drain Current vs. Tc

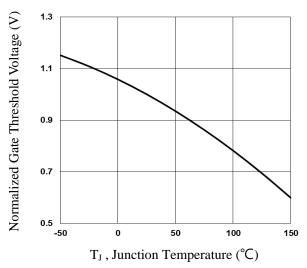


Fig.3 Normalized Vth vs. T<sub>J</sub>

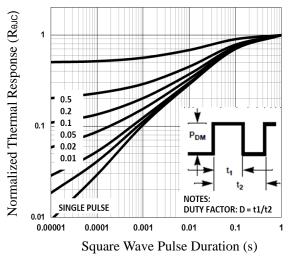


Fig.5 Normalized Transient Impedance

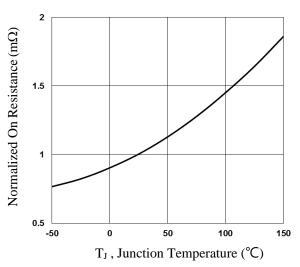


Fig.2 Normalized RDSON vs. TJ

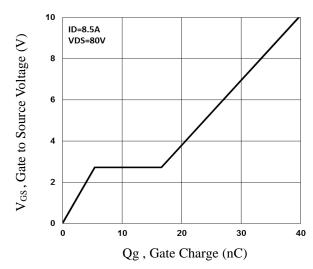
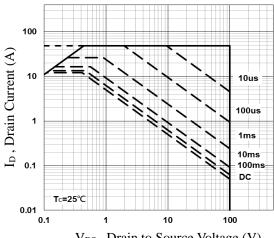


Fig.4 Gate Charge Characteristics

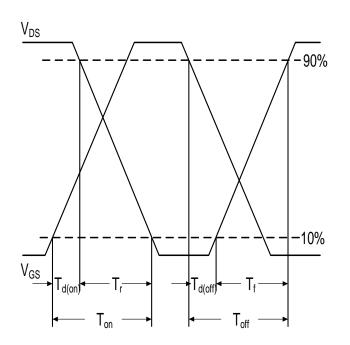


V<sub>DS</sub> , Drain to Source Voltage (V)

Fig.6 Maximum Safe Operation Area



## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



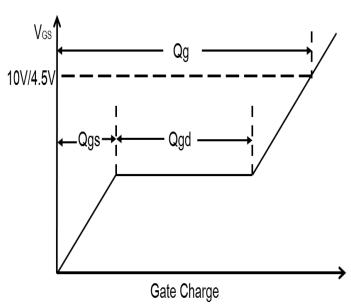
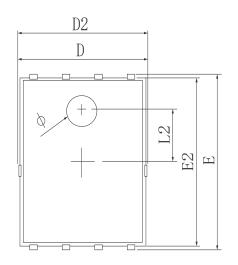


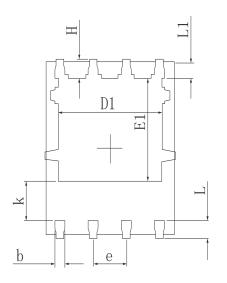
Fig.7 Switching Time Waveform

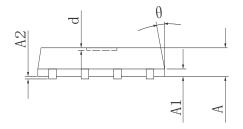
Fig.8 Gate Charge Waveform



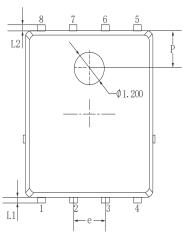
## •Dimensions (PDFN5\*6)

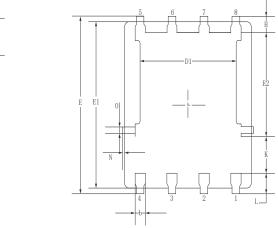






oramor.		MILLIMETER	
SYMBOL	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1		0.254 REF.	
A2		0~0.05	
D	4. 824	4.900	4.976
D1	3.910	4.010	4. 110
D2	4. 924	5.000	5. 076
Е	5. 924	6.000	6.076
E1	3. 375	3. 475	3. 575
E2	5. 674	5. 750	5. 826
b	0.350	0.400	0.450
е		1.270 TYP.	
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1. 390
Н	0.549	0.625	0.701
θ	8°	10°	12°
Ф	1.100	1.200	1.300
d			0.100





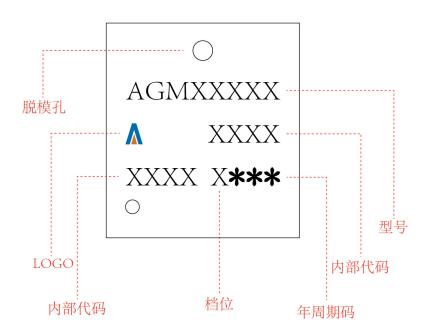
M	θ
	Ā
C	
D—————————————————————————————————————	

Millimeters		
MIN.	NOM.	MAX.
0.90	1.05	1. 20
0.35	0.40	0.50
0.20	0. 25	0.35
4.90	5. 05	5. 20
3. 72	3. 82	3. 92
6.00	6. 15	6. 30
5. 60	5. 75	5. 90
3. 47	3. 57	3. 67
	1.27 BSC.	
0.48	0.58	0.68
1. 17	1. 27	1. 37
0.64	0.74	0.84
0.20 REF.		
8°	10°	12°
0.08 REF.		
0	-	0. 15
0.25 REF.		
	1.28 REF.	
	MIN. 0. 90 0. 35 0. 20 4. 90 3. 72 6. 00 5. 60 3. 47 0. 48 1. 17 0. 64	MIN. NOM. 0. 90 1. 05 0. 35 0. 40 0. 20 0. 25 4. 90 5. 05 3. 72 3. 82 6. 00 6. 15 5. 60 5. 75 3. 47 3. 57 1. 27 BSC. 0. 48 0. 58 1. 17 1. 27 0. 64 0. 74 0. 20 REF. 8° 10° 0. 08 REF. 0 - 0. 25 REF.

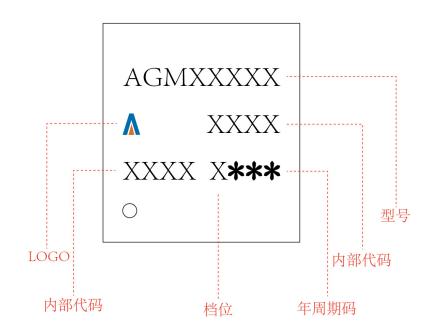


# PDFN5\*6 Marking Instructions:

## Model1:



## Model2:





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