TrenchHV[™] Power MOSFET

IXTA56N15T IXTP56N15T

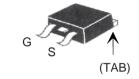
 $V_{DSS} = 150 V$ $I_{D25} = 56 A$ $R_{DS(on)} \le 36 m\Omega$

N-Channel Enhancement Mode Avalanche Rated

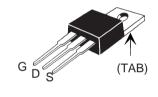


Symbol	Test Conditions	Maximum	Maximum Ratings			
V _{DSS} V _{DGR}	$T_J = 25^{\circ}\text{C to } 175^{\circ}\text{C}$ $T_J = 25^{\circ}\text{C to } 175^{\circ}\text{C}; R_{GS} = 1 \text{ M}\Omega$	150 150	V			
V _{GSM}	Transient	± 30	V			
 _{D25} _{DM}	$T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$, pulse width limited by T_{JM}	56 140	A A			
I _{AR} E _{AS}	$T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$	5 500	A mJ			
dv/dt	$I_{_{S}} \leq I_{_{DM}}$, di/dt \leq 100 A/ μ s, $V_{_{DD}} \leq V_{_{DSS}}$ $T_{_{J}} \leq$ 175°C, $R_{_{G}} = 5 \Omega$	3	V/ns			
P _D	T _C = 25°C	300	W			
T _J T _{JM} T _{stg}		-55 +175 175 -55 +175	°C °C °C			
T _L	1.6 mm (0.062 in.) from case for 10 s Plastic body for 10 seconds	300 260	°C			
M _d	Mounting torque (TO-220)	1.13 / 10	Nm/lb.in.			
Weight	TO-220 TO-263	3 2.5	g g			

TO-263 (IXTA)



TO-220 (IXTP)



G = Gate D = Drain S = Source TAB = Drain

Features

- Unclamped Inductive Switching (UIS) rated
- Low package inductance
- easy to drive and to protect
- 175 °C Operating Temperature

Advantages

- Easy to mount
- Space savings
- High power density

Symbol (T _J = 25°C u	Test Conditions nless otherwise specified)	Cha Min.	aracteri Typ.	stic Va		
BV _{DSS}	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		150			V
$V_{\rm GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		2.5		4.5	V
I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$				± 100	nA
I _{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$	T _J = 150°C			5 200	μ Α μ Α
R _{DS(on)}	V _{GS} = 10 V, I _D = 28 A, Notes	s 1, 2			36	$m\Omega$



Symbol	Test Conditions	Characteristic Values					
(T _J = 25°C	unless otherwise specified)	Min.	Тур.	Max.			
g_{fs}	$V_{DS} = 10 \text{ V}; I_{D} = 28 \text{ A}, \text{ Note 1}$	23	39	S			
C _{iss}			2250	pF			
C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		330	pF			
\mathbf{C}_{rss}			50	pF			
t _{d(on)}	Resistive Switching Times		16	ns			
t,	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_{D} = 28 \text{ A}$		17	ns			
$\mathbf{t}_{d(off)}$	$R_{\rm G} = 5 \Omega $ (External)		43	ns			
t _f			18	ns			
Q _{g(on)}			34	nC			
\mathbf{Q}_{gs}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \text{ V}_{DSS}, I_{D} = 25 \text{ A}$		10	nC			
\mathbf{Q}_{gd}			11	nC			
R _{thJC}				0.50 °C/W			
R _{thCH}	TO-220		0.50	°C/W			

Source-Drain Diode

Symbol T _J = 25°C u	Characte n. Typ.	ristic Va Max.	lues	
I _s	$V_{GS} = 0 V$		56	Α
I _{SM}	Pulse width limited by $T_{_{JM}}$		160	Α
V _{SD}	$I_{\rm F}$ =25 A, $V_{\rm GS}$ = 0 V, Note 1		1.1	V
t _{rr}	$I_F = 25 \text{ A}, -di/dt = 100 \text{ A/}\mu\text{s}$	100		ns
	$V_{R} = 50 \text{ V}, V_{GS} = 0 \text{ V}$			

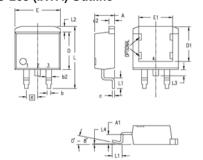
Notes: 1. Pulse test, $t \le 300 \mu s$, duty cycle, $d \le 2 \%$;

2. On through-hole packages, $R_{\rm DS(on)}$ Kelvin test contact location must be 5 mm or less from the package body.

PRELIMINARYTECHNICALINFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a preproduction design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

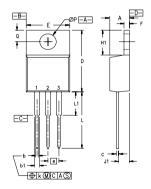
TO-263 (IXTA) Outline



Pins: 1 - Gate 2 - Drain 3 - Source 4, TAB - Drain

Dim.	Mill	imeter	Inches			
	Min.	Min. Max.		Max.		
Α	4.06	4.83	.160	.190		
A1	2.03	2.79	.080	.110		
b	0.51	0.99	.020	.039		
b2	1.14	1.40	.045	.055		
С	0.46	0.74	.018	.029		
c2	1.14	1.40	.045	.055		
D	8.64	9.65	.340	.380		
D1	7.11	8.13	.280	.320		
E	9.65	10.29	.380	.405		
E1	6.86	8.13	.270	.320		
е	2.54	BSC	.100	BSC		
L	14.61	15.88	.575	.625		
L1	2.29	2.79	.090	.110		
L2	1.02	1.40	.040	.055		
L3	1.27	1.78	.050	.070		
L4	0	0.38	0	.015		
R	0.46	0.74	.018	.029		

TO-220 (IXTP) Outline



Pins: 1 - Gate 2 - Drain 3 - Source 4, TAB - Drain

0)///	INCH	IES	MILLIMETERS			
MYZ	MIN	MAX	MIN	MAX		
Α	.170	.190	4.32	4.83		
b	.025	.040	0.64	1.02		
b1	.045	.065	1.15	1.65		
С	.014	.022	0.35	0.56		
D	.580	.630	14.73	16.00		
E	.390	.420	9.91	10.66		
е	.100	BSC	2.54 BSC			
F	.045 .055		1.14	1.40		
H1	.230	.270	5.85	6.85		
J1	.090	.110	2.29	2.79		
k	0 .015		0	0.38		
L	.500 .550		12.70	13.97		
L1	.110	.230	2.79	5.84		
ØΡ	.139	.161	3.53	4.08		
Q	.100 .125		2.54	3.18		

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IXYS MOSFETs and IGBTs are covered	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065B1	6,683,344	6,727,585	7,005,734B2 7,157,338B2
by one or more of the following U.S. patents:	4,850,072	5,017,508	5,063,307	5,381,025	6,259,123B1	6,534,343	6,710,405B2	6,759,692	7,063,975 B2
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728B1	6,583,505	6,710,463	6,771,478B2	7,071,537



0

0 0.2 0.4 0.6

@ 25°C 60 $V_{GS} = 10V$ 55 9V 50 8V 45 40 ID - Amperes 35 30 25 20 6V 15 10 5 5V

8.0

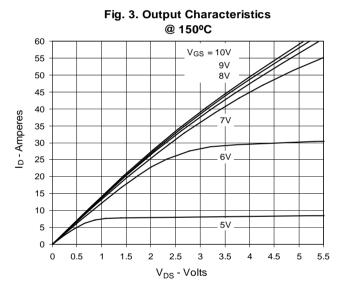
1.2 1.4 1.6 1.8 2 2.2

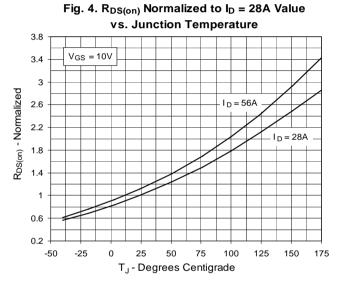
V_{DS} - Volts

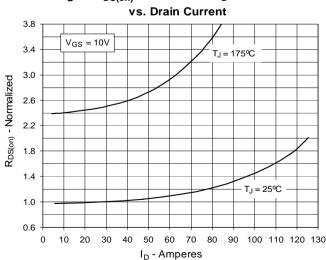
Fig. 1. Output Characteristics

@ 25°C 150 V_{GS} = 10V 135 120 105 ID - Amperes 90 75 60 7V 45 30 6V 15 0 0 2 4 6 12 14 16 18 20 22 24 26 V_{DS} - Volts

Fig. 2. Extended Output Characteristics







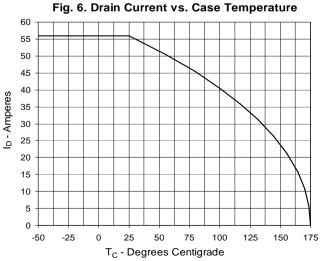
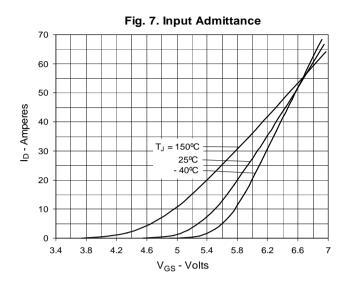


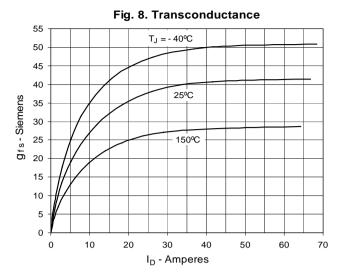
Fig. 5. R_{DS(on)} Normalized to I_D = 28A Value

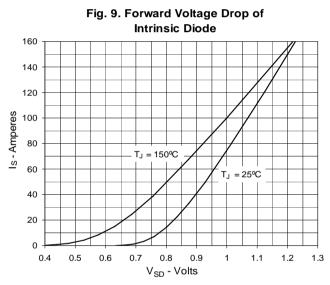
Vs. Drain Current

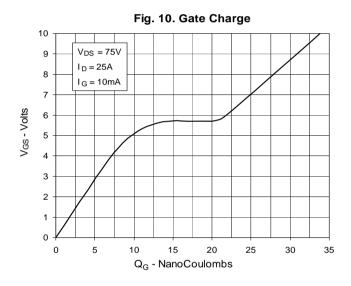
Fig. 6. Drain Current vs.

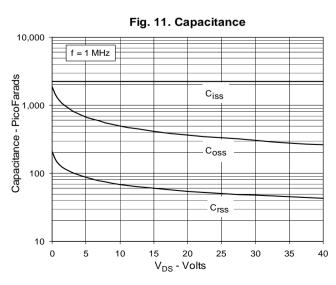


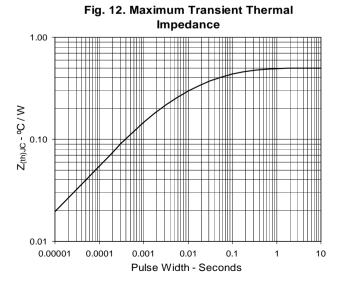






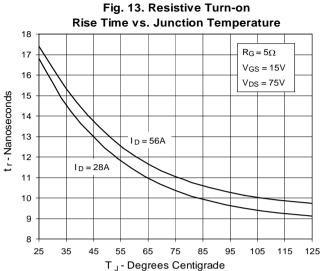


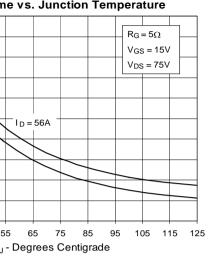


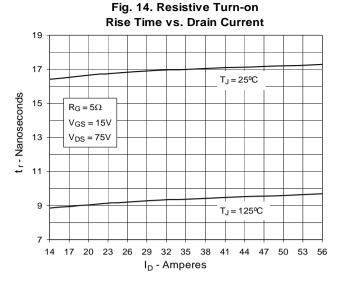


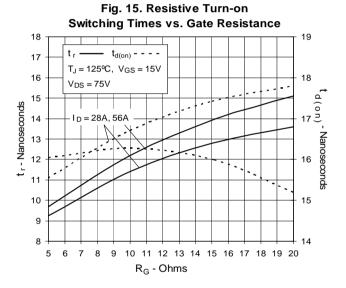
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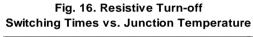


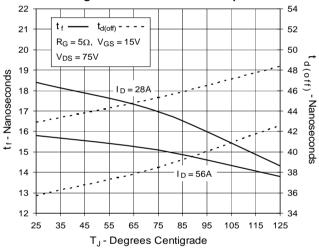


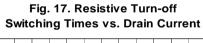












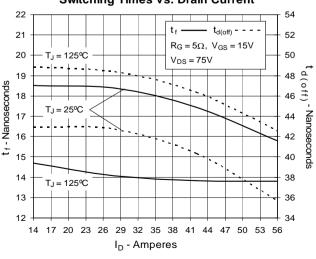


Fig. 18. Resistive Turn-off Switching Times vs. Gate Resistance

