

MOSFET – Power, Single N-Channel

40 V, 7.3 mΩ, 51 A

NTTFS5C466NL

Features

- Small Footprint (3.3 x 3.3 mm) for Compact Design
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parar	Symbol	Value	Unit		
Drain-to-Source Voltag	V_{DSS}	40	V		
Gate-to-Source Voltage	9		V _{GS}	±20	V
Continuous Drain		T _C = 25°C	I _D	51	Α
Current R _{θJC} (Notes 1, 2, 3, 4)	Steady	T _C = 100°C		36	
Power Dissipation	State	T _C = 25°C	P_{D}	38	W
R _{θJC} (Notes 1, 2, 3)		T _C = 100°C		19	
Continuous Drain		T _A = 25°C	I _D	14	Α
Current R _{0JA} (Notes 1, 3, 4)	Steady State	T _A = 100°C		12	
Power Dissipation		T _A = 25°C	P_{D}	3.1	W
R _{θJA} (Notes 1, 3)		T _A = 100°C		2.1	
Pulsed Drain Current	$T_A = 25$	°C, t _p = 10 μs	I _{DM}	200	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			IS	10	Α
Single Pulse Drain-to-S Energy (I _{L(pk)} = 3 A)	E _{AS}	72	mJ		
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

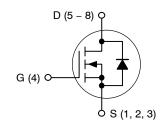
THERMAL RESISTANCE MAXIMUM RATINGS (Note 1)

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 3)	$R_{\theta JC}$	3.5	°C/W
Junction-to-Ambient - Steady State (Note 3)	$R_{\theta JA}$	48	

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Psi (Ψ) is used as required per JESD51-12 for packages in which substantially less than 100% of the heat flows to single case surface.
- 3. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.
- Continuous DC current rating. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX	
40 V	7.3 mΩ @ 10 V	51 A	
	12 mΩ @ 4.5 V	31A	

N-Channel





WDFN8 (μ8FL) CASE 511AB

MARKING DIAGRAM



466L = Specific Device Code
A = Assembly Location
Y = Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

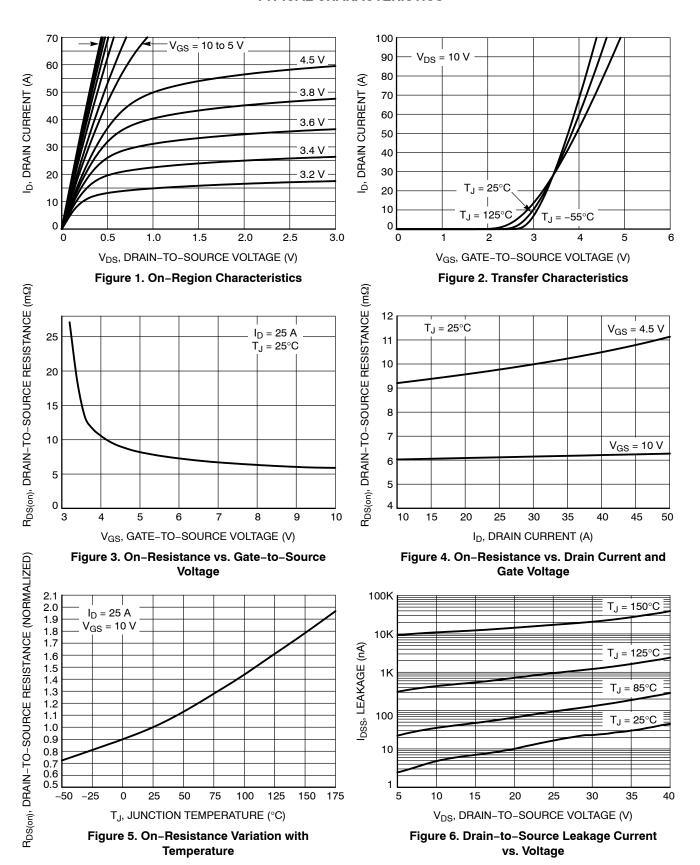
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				•	1	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /				29		mV/° C
Zero Gate Voltage Drain Current	I _{DSS}	Vcs = 0 V.	T _J = 25°C			10	μΑ
		V _{GS} = 0 V, V _{DS} = 40 V	T _J = 125°C			250	Ī
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{G}$	_S = 20 V			100	nA
ON CHARACTERISTICS (Note 5)					•	•	•
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D}$	= 30 μΑ	1.2		2.2	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _[_O = 10 A		6.1	7.3	mΩ
		V _{GS} = 4.5 V, I	_D = 10 A		9.7	12	Ī
Forward Transconductance	9FS	V _{DS} = 15 V, I _E	₀ = 25 A		33		S
CHARGES, CAPACITANCES AND GA	TE RESISTANO	E			•	•	•
Input Capacitance	C _{iss}	V _{GS} = 0 V, f =		880		pF	
Output Capacitance	C _{oss}	V _{DS} = 25		340		_	
Reverse Transfer Capacitance	C _{rss}	1		16			
Total Gate Charge	Q _{G(TOT)}				7.0		nC
Threshold Gate Charge	Q _{G(TH)}	1.,		1.8		nC	
Gate-to-Source Charge	Q _{GS}	$V_{GS} = 4.5 \text{ V}, V_{DS} = 2$		3.3			
Gate-to-Drain Charge	Q _{GD}	1			2.5		Ī
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 2	20 V, I _D = 25 A		16		nC
Gate Resistance	R_{G}	T _A = 25°)C			3.0	Ω
SWITCHING CHARACTERISTICS (No	te 6)					•	
Turn-On Delay Time	t _{d(on)}				10		ns
Rise Time	t _r	V _{GS} = 4.5 V. V _C	s = 20 V.		67		Ī
Turn-Off Delay Time	t _{d(off)}	$V_{GS} = 4.5 \text{ V}, V_{D}$ $I_{D} = 25 \text{ A}, R_{G}$	= 2.5 Ω		26		
Fall Time	t _f	1		32		Ī	
DRAIN-SOURCE DIODE CHARACTER	RISTICS				-	•	
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, I _S = 20 A	T _J = 25°C		0.9	1.2	V
			T _J = 125°C		0.8		
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, } dI_S/dt = 100 \text{ A/}\mu\text{s,}$ $I_S = 25 \text{ A}$			22		ns
Charge Time	ta				10		1
Discharge Time	t _b				12		1
Reverse Recovery Charge	Q _{RR}				6.0		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%.

6. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

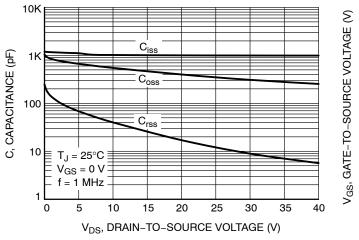


Figure 7. Capacitance Variation

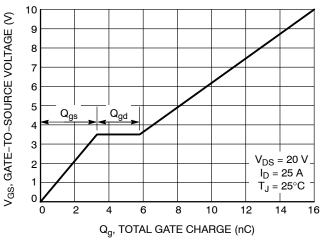


Figure 8. Gate-to-Source vs. Total Charge

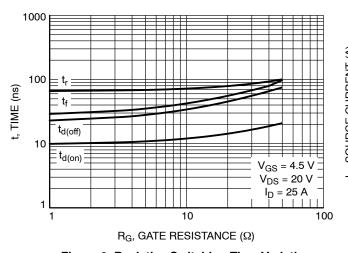


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

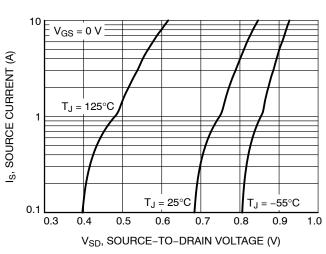


Figure 10. Diode Forward Voltage vs. Current

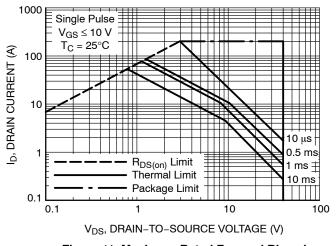


Figure 11. Maximum Rated Forward Biased Safe Operating Area

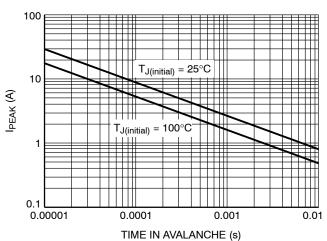


Figure 12. Maximum Drain Current vs. Time in Avalanche

TYPICAL CHARACTERISTICS

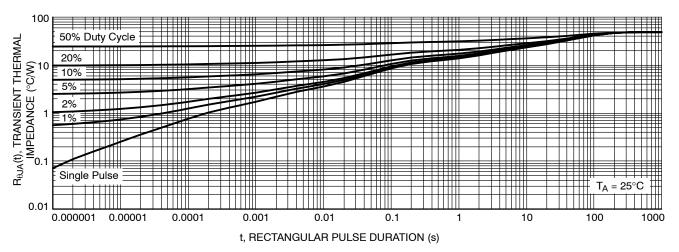


Figure 13. Thermal Response

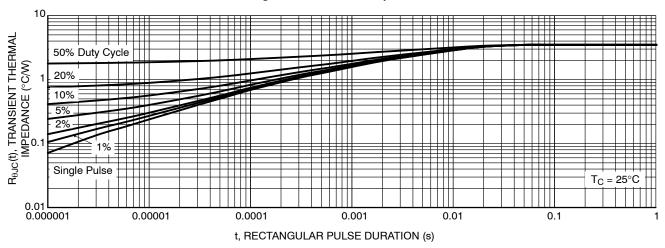


Figure 14. Thermal Response

DEVICE ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
NTTFS5C466NLTAG	466L	WDFN8 (Pb-Free)	1500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.







SCALE 2:1

WDFN8 3.3x3.3, 0.65P CASE 511AB ISSUE D

DATE 23 APR 2012



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION D1 AND E1 DO NOT INCLUDE MOLD FLASH
 PROTRUSIONS OR GATE BURRS.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.70	0.75	0.80	0.028	0.030	0.031	
A1	0.00		0.05	0.000		0.002	
b	0.23	0.30	0.40	0.009	0.012	0.016	
С	0.15	0.20	0.25	0.006	0.008	0.010	
D		3.30 BSC		0	.130 BSC	;	
D1	2.95	3.05	3.15	0.116	0.120	0.124	
D2	1.98	2.11	2.24	0.078	0.083	0.088	
E	3.30 BSC			0.130 BSC			
E1	2.95	3.05	3.15	0.116	0.120	0.124	
E2	1.47	1.60	1.73	0.058	0.063	0.068	
E3	0.23	0.30	0.40	0.009	0.012	0.016	
е		0.65 BSC			0.026 BSC		
G	0.30	0.41	0.51	0.012	0.016	0.020	
K	0.65	0.80	0.95	0.026	0.032	0.037	
L	0.30	0.43	0.56	0.012	0.017	0.022	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
М	1.40	1.50	1.60	0.055	0.059	0.063	
θ	0 °		12 °	0 °		12 °	



GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code Α = Assembly Location

= Year WW = Work Week = Pb-Free Package



DIMENSION: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	WDFN8 3.3X3.3, 0.65P		PAGE 1 OF 1	

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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