

# **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)TYP</sub>	l <sub>D</sub>
100V	20mΩ@10V	30A
	25mΩ@4.5V	30A



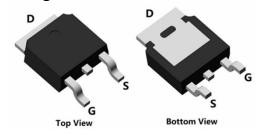
#### **Feature**

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

# **Applications**

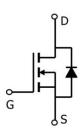
- Power switching application
- Battery management
- Uninterruptible power supply

#### **Package**



TO-252(1:G 2:D 3:S)

### Circuit diagram



# Marking



SP010N20GTH : Product code \*\* : Week code

#### **Order Information**

Device	Package	Unit/Tube	
SP010N20GTH	TO-252	2500	



# Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current (Tc=25°C)	I <sub>D</sub>	30	Α
Continuous Drain Current (Tc=100°C)	I <sub>D</sub>	20	Α
Pulsed Drain Current	I <sub>DM</sub>	120	А
Single Pulse Avalanche Energy <sup>1</sup>	Eas	49	mJ
Power Dissipation (Tc=25°C)	P <sub>D</sub>	50	W
Thermal Resistance Junction-to-Case	R <sub>eJC</sub>	2.5	°C/W
Storage Temperature Range	T <sub>STG</sub>	55 to 150	$^{\circ}$ C
Operating Junction Temperature Range	TJ	55 to 150	$^{\circ}$

# Electrical characteristics (Ta=25°C, unless otherwise noted)

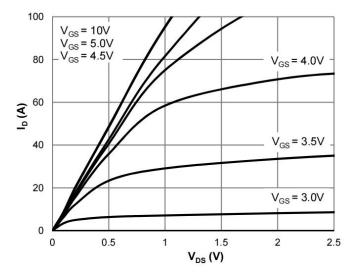
Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 80V, V <sub>GS</sub> = 0V	-	-	1	uA	
Gate Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±0.1	nA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.8	2.5	V	
D : 0 OND : 1		V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	20	25	mΩ	
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	$V_{GS} = 4.5V, I_D = 10A$	-	25	33		
Dynamic Characteristics							
Input Capacitance	Ciss		-	695	-		
Output Capacitance	Coss	$V_{DS}$ =50V, $V_{GS}$ = 0V, f = 1.0MHz	-	151	-	pF	
Reverse Transfer Capacitance	Crss		-	9	-		
Total Gate Charge	Qg		-	14.6	-		
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =50V , VGS=10V , ID=10A	-	2.3	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>		-	4.2	-		
Switching Characteristics							
Turn-On Delay Time	t <sub>d(on)</sub>		-	6.6	-		
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> =50V, ID=10A	-	2	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G = 2.2\Omega$	-	19	-		
Fall Time	t <sub>f</sub>		-	2.3	-		
Drain-Source Body Diode Characteri	stics				•	•	
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃	-	-	1.2	V	
Maximum Body-Diode Continuous Current	Is		-	-	30	Α	
Reverse Recovery Time	Trr	L = 20A di/dt=100A/up T = 25 °C	-	40	-	nS	
Reverse Recovery Charge	Qrr	I <sub>S</sub> =20A, di/dt=100A/us, T <sub>J</sub> =25℃	-	68	-	nC	

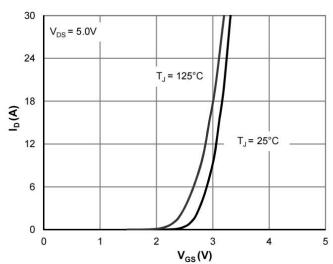
#### Note:

1. The EAS test condition is VDD=50V,VGS=10V,L=0.5mH,RG=25 $\Omega$ 



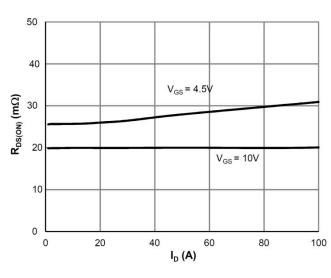
# **Typical Characteristics**

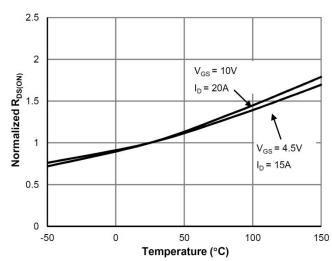




**Typical Output Characteristics** 

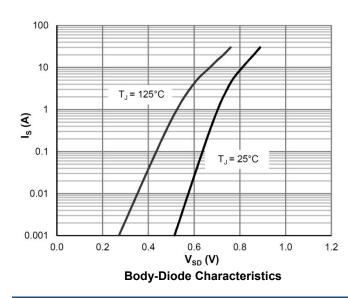


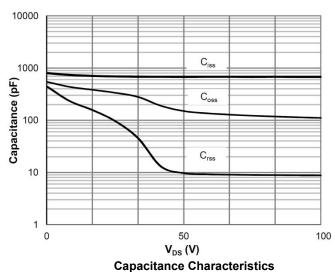




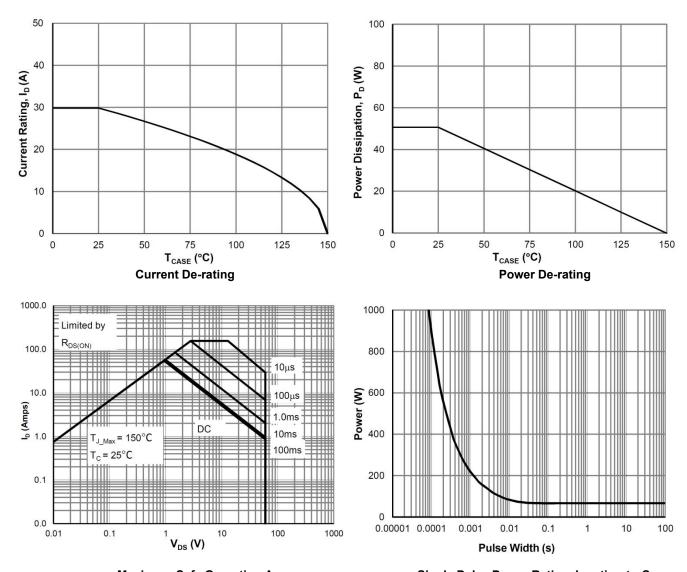
**On-Resistance vs.Drain Current** 

**On-Resistance vs. Junction Temperature** 



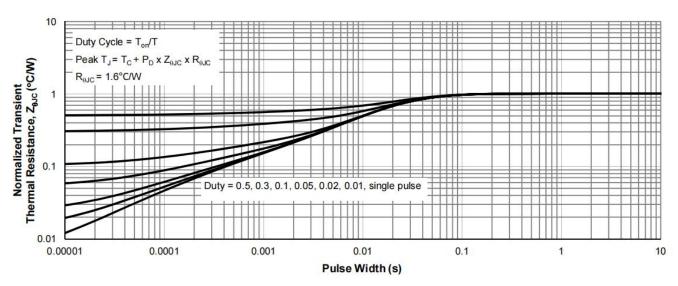






**Maximum Safe Operating Area** 

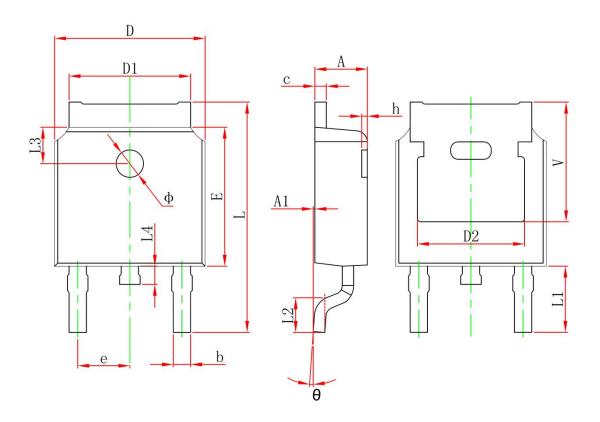
Single Pulse Power Rating, Junction-to-Case



**Normalized Maximum Transient Thermal Impedance** 



# TO-252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	4.830 REF.		REF.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 REF.		0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 REF.		0.211 R	REF.	