

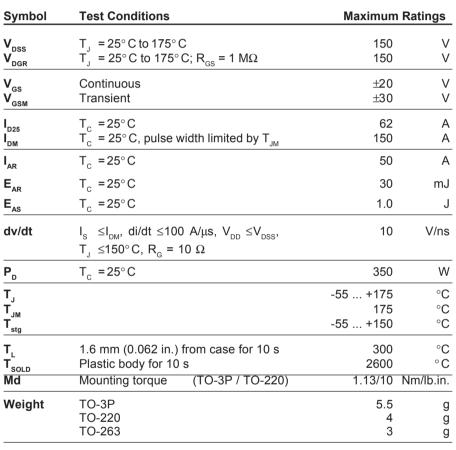
# PolarHT<sup>™</sup> Power MOSFET

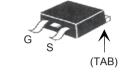
IXTA 62N15P IXTP 62N15P IXTQ 62N15P  $V_{DSS} = 150 V \ I_{D25} = 62 A \ R_{DS(on)} \le 40 m\Omega$ 

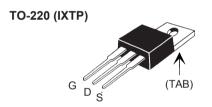
N-Channel Enhancement Mode Avalanche Rated

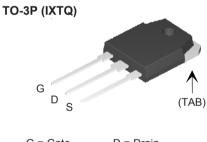


TO-263 (IXTA)









G = Gate	D = Drain
S = Source	TAB = Drain

## **Features**

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
- easy to drive and to protect

# Advantages

- Easy to mount
- Space savings
- High power density

<b>Symbol</b> (T <sub>J</sub> = 25° C, t	Test Conditions unless otherwise specified)		Ch Min.	_	istic Va Max	
BV <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		150			V
$V_{\rm GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.0		5.5	V
GSS	$V_{GS} = \pm 20 V_{DC}, V_{DS} = 0$				±100	nA
I <sub>DSS</sub>	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$	T <sub>J</sub> = 150° C			25 250	μA μA
R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_{D} = 0.5 I_{D25}$ Pulse test, t ≤300 µs, duty	cycle d ≤ 2 %		33	40	mΩ



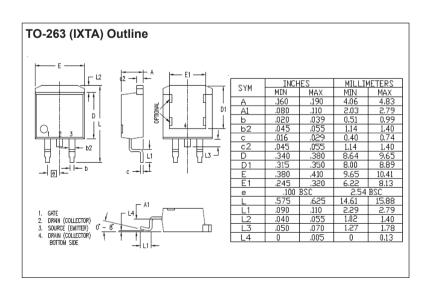
#### Symbol **Test Conditions Characteristic Values** (T<sub>1</sub> = 25° C, unless otherwise specified) Min. Typ. Max. $V_{DS}$ = 10 V; $I_{D}$ = 0.5 $I_{D25}$ , pulse test 24 S $\boldsymbol{g}_{\mathsf{fs}}$ Ciss 2250 рF $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$ 660 рF 185 pF Crss 27 t<sub>d(on)</sub> ns $V_{GS}$ = 10 V, $V_{DS}$ = 0.5 $V_{DSS}$ , $I_{D}$ = 0.5 $I_{D25}$ 38 t, ns $R_c = 10 \Omega (External)$ 76 ns $\mathbf{t}_{\mathsf{d(off)}}$ 35 t, ns $\boldsymbol{\mathsf{Q}_{\mathsf{g(on)}}}$ 70 nC $\mathbf{Q}_{\mathrm{gs}}$ $V_{GS}$ = 10 V, $V_{DS}$ = 0.5 $V_{DSS}$ , $I_{D}$ = 0.5 $I_{D25}$ 20 nC $\mathbf{Q}_{\underline{g}\underline{d}}$ 38 nC $\mathbf{R}_{\mathrm{thJC}}$ 0.42°C/W $\mathbf{R}_{\text{thCS}}$ °C/W (TO-3P) 0.21 (TO-220) 0.25 $^{\circ}$ C/W

### Source-Drain Diode

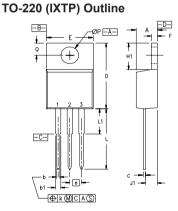
Characteristic Values

(T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Test Conditions	Min.	Тур.	Max.	
I <sub>s</sub>	V <sub>GS</sub> = 0 V			62	Α
I <sub>sm</sub>	Repetitive			150	Α
V <sub>SD</sub>	$I_F = I_S, V_{GS} = 0 \text{ V},$ Pulse test, t ≤300 µs, duty cycle d≤ 2 %			1.5	V
t <sub>rr</sub> Q <sub>RM</sub>	$I_F = 25 \text{ A, -di/dt} = 100 \text{ A/}\mu\text{s}$ $V_R = 100 \text{ V, V}_{GS} = 0 \text{ V}$		150 2.0		ns μC



# 



Pins:	1 - Gate	2 - Drain
	3 - Source	Tab - Drain

MYZ	INCHES		MILLIMETERS		
2114	MIN MAX		MIN	MAX	
Α	.170	.190	4.32	4.83	
b	.025	.040	0.64	1.02	
b1	.045	.065	1.15	1.65	
С	.014	.022	0.35	0.56	
D	.580	.630	14.73	16.00	
Ε	.390	.420	9.91	10.66	
е	.100 BSC		2.54 BSC		
F	.045	.055	1.14	1.40	
H1	.230	.270	5.85	6.85	
J1	.090	.110	2.29	2.79	
k	0	.015	0	0.38	
L	.500	.550	12.70	13.97	
L1	.110	.230	2.79	5.84	
ØΡ	.139	.161	3.53	4.08	
Q	.100	.125	2.54	3.18	

IXYS reserves the right to change limits, test conditions, and dimensions.



Fig. 1. Output Characteristics

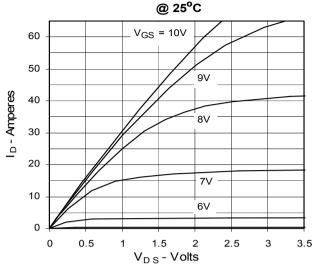


Fig. 3. Output Characteristics @ 150°C

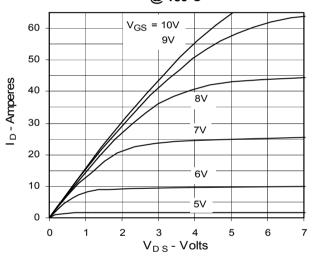


Fig. 5. R<sub>DS(on)</sub> Normalized to

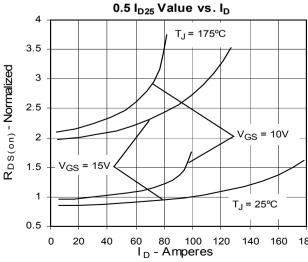


Fig. 2. Extended Output Characteristics
@ 25°C

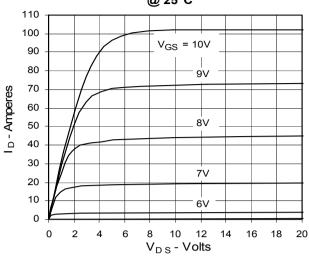


Fig. 4.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Junction Temperature

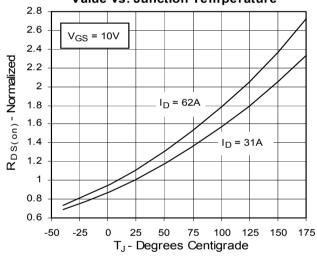


Fig. 6. Drain Current vs. Case

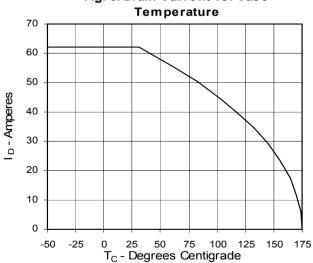




Fig. 7. Input Admittance

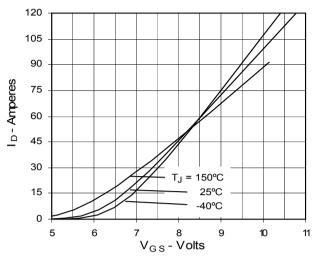


Fig. 8. Transconductance

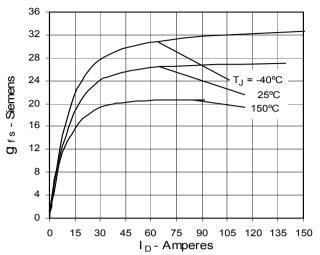


Fig. 9. Source Current vs. Source-To-Drain Voltage

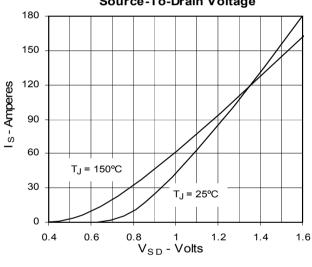


Fig. 10. Gate Charge

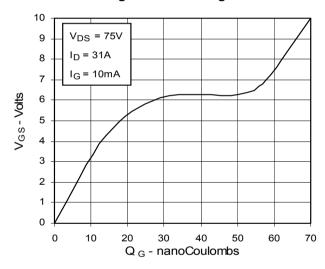


Fig. 11. Capacitance

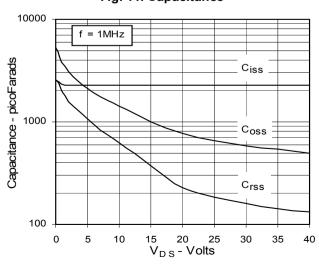
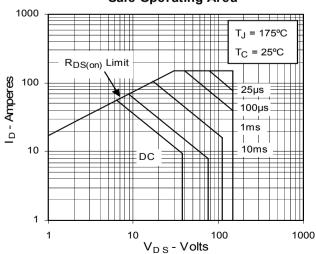


Fig. 12. Forward-Bias Safe Operating Area



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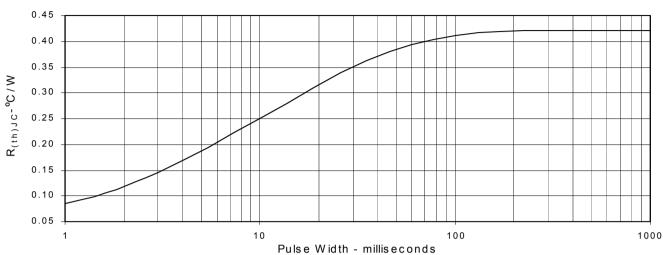


Fig. 13. Maximum Transient Thermal Resistance

