

MOSFET – Power, N-Channel, Shielded Gate

60 V, 5.2 mΩ, 78 A

NTTFS5D1N06HL

General Description

This N-Channel MOSFET is produced using **onsemi**'s advanced MOSFET process that incorporates Shielded Gate technology. This process has been optimized to minimize on-state resistance and yet maintain superior switching performance with best in class soft body diode.

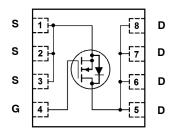
Features

- Shielded Gate MOSFET Technology
- Max $r_{DS(on)} = 5.2 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 16 \text{ A}$
- Max $r_{DS(on)} = 7.1 \text{ m}\Omega$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 13 \text{ A}$
- Lowers Switching Noise/EMI
- MSL1 Robust Package Design
- 100% UIL Tested
- RoHS Compliant

Applications

- Primary DC-DC MOSFET
- Synchronous Rectifier in DC–DC and AC–DC
- Motor Drive

ELECTRICAL CONNECTION



N-Channel MOSFET



WDFN8 (3.3x3.3, 0.65 P) CASE 511DY

MARKING DIAGRAM



 1N06
 = Device Code

 A
 = Assembly Location

 Y
 = Year Code

 WW
 = Work Week Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

MOSFET MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)

Symbol		Para	meter		Ratings	Unit
V_{DS}	Drain to Source Voltage				60	V
V _{GS}	Gate to Source \	/oltage			±20	V
I _D	Drain Current	-Continuous	T _C = 25°C	(Note 5)	78	А
		-Continuous	T _C = 100°C	(Note 5)	49	1
		-Continuous	T _A = 25°C	(Note 1a)	18	1
		-Pulsed		(Note 4)	216	1
E _{AS}	Single Pulse Ava	lanche Energy		(Note 3)	72	mJ
P _D	Power Dissipation	n	T _C = 25°C		63	W
	Power Dissipation	n	T _A = 25°C	(Note 1a)	3.2	1
T _J , T _{STG}	Operating and St	torage Junction Tempe	rature Range		-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case	2	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	39	

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
FF CHARACT	ERISTICS			•		•
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$, referenced to 25°C		37		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 48 V, V _{GS} = 0 V			10	μΑ
I _{GSS}	Gate to Source Leakage Current	V _{GS} = +20 V, V _{DS} = 0 V			100	nA
N CHARACTE	ERISTICS					
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 80 \mu A$	1.2	1.6	2.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 80 μA, referenced to 25°C		-5.2		mV/°C
r _{DS(on)}	Static Drain to Source On	V _{GS} = 10 V, I _D = 16 A		4.4	5.2	mΩ
	Resistance	V _{GS} = 4.5 V, I _D = 13 A		5.6	7.1	7
YNAMIC CHA	RACTERISTICS					
C _{ISS}	Input Capacitance	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V},$		1610		pF
C _{OSS}	Output Capacitance	f = 1 MHz		313		7
C _{RSS}	Reverse Transfer Capacitance]		12.2		1
R_{G}	Gate Resistance			0.9		Ω
SWITCHING CH	HARACTERISTICS					
t _{d(ON)}	Turn – On Delay Time	V _{DD} = 30 V, I _D = 16 A,		14		ns
t _{rd(ON)}	Rise Time	$V_{GS} = 4.5 \text{ V}, R_{GEN} = 2.5 \Omega$		24		7
t _{d(OFF)}	Turn – Off Delay Time]		41.3		7
t _f	Fall Time	1		12.2		

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted) (continued)

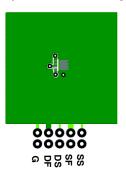
Symbol	Parameter	Test Condit	Test Conditions		Тур	Max	Units
WITCHING CHARACTERISTICS							
Qg	Total Gate Charge	V _{GS} = 0V to 10 V			22.5		nC
Qg	Total Gate Charge	V _{GS} = 0V to 4.5 V			10.3		
Q _{gs}	Gate to Source Charge		V _{DD} = 30 V		5		
Q_{gd}	Gate to Drain "Miller" Charge	7	I _D = 16 A		3		
RAIN-SOURC	E DIODE CHARACTERISTICS	•	-			<u> </u>	-

	V_{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _S = 16 A (Note 2)	8.0	1.2	V
			V _{GS} = 0 V, I _S = 16 A (Note 2)	0.66		
ĺ	t _{rr}	Reverse Recovery Time	I _F = 16 A, di/dt = 100 A/μs	35.1		ns
ĺ	Q_{rr}	Reverse Recovery Charge		37		nC

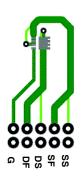
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

NOTES:

1. $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 \times 1.5 in. board of FR-4 material. $R_{\theta CA}$ is determined by the user's board design.



a) 53°C/W when mounted on a 1 in² pad of 2 oz copper.



b) 125°C/W when mounted on a minimum pad of 2 oz copper.

- Pulse Test: Pulse Width < 300 μs, Duty cycle < 2.0%.
 E_{AS} of 72 mJ is based on starting T_J = 25°C; L = 1 mH, I_{AS} = 12 A, V_{DD} = 48 V, V_{GS} = 10 V. 100% test at L = 1 mH, I_{AS} = 12 A.
 Pulsed I_D please refer to SOA graph for more details.
 Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design.

TYPICAL CHARACTERISTICS

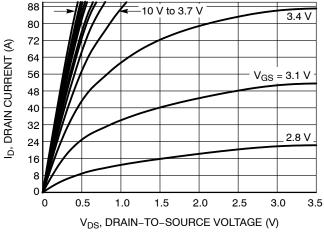
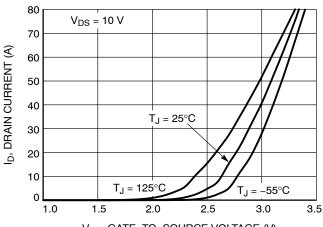


Figure 1. On-Region Characteristics



V_{GS}, GATE-TO-SOURCE VOLTAGE (V) Figure 2. Transfer Characteristics

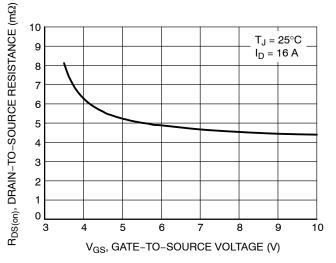


Figure 3. On-Resistance vs. Gate-to-Source Voltage

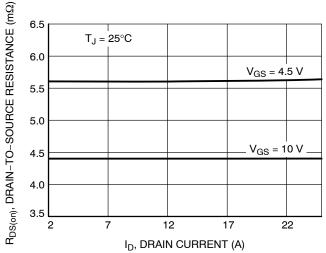


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

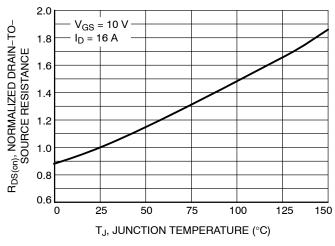


Figure 5. On–Resistance Variation with Temperature

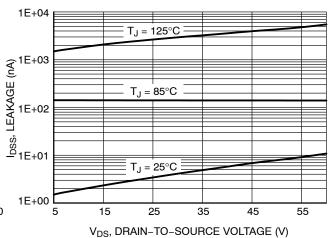


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS (continue)

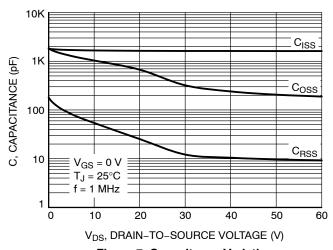


Figure 7. Capacitance Variation

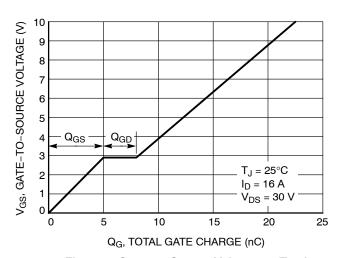


Figure 8. Gate-to-Source Voltage vs. Total Charge

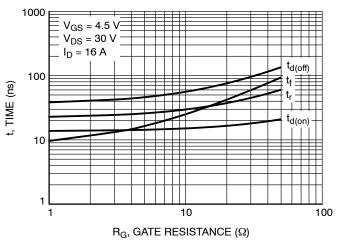


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

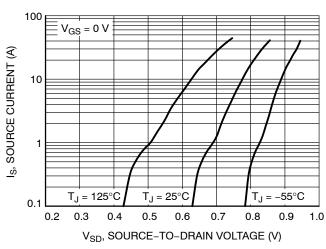


Figure 10. Diode Forward Voltage vs. Current

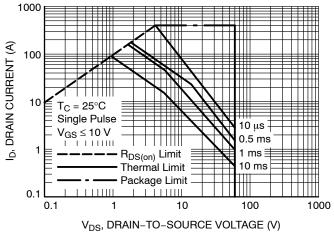


Figure 11. Maximum Rated Forward Biased Safe Operating Area

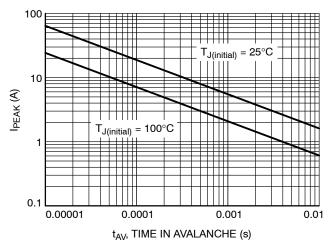


Figure 12. Maximum Drain Current vs. Time in Avalanche

TYPICAL CHARACTERISTICS (continue)

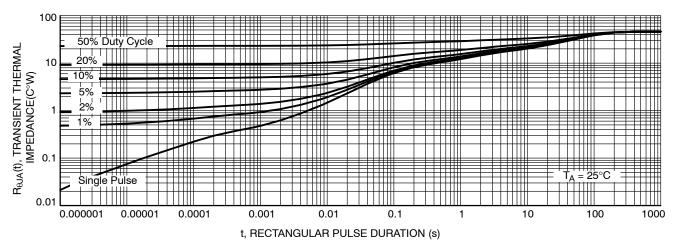


Figure 13. Thermal Response

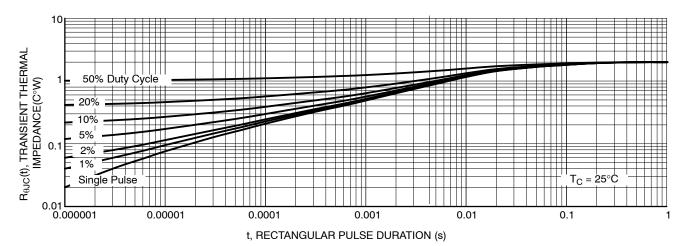


Figure 14. Thermal Response

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
1N06	NTTFS5D1N06HLTAG	WDFN8 (3.3x3.3)	7"	12 mm	1500 Tape & Reel

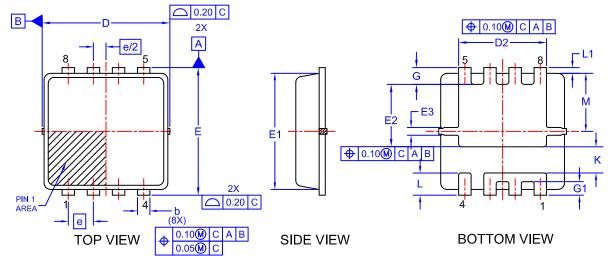
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.





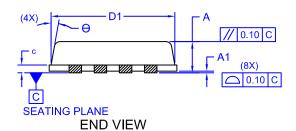
WDFN8 3.3x3.3, 0.65P CASE 511DY ISSUE A

DATE 21 AUG 2018



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETERS
- 2. DIMENSIONS D1 & E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS NOR GATE BURRS.



	3.46
<u> </u>	2.38
0.78 (4X)	
0.75	2.51
0.57	4.10
0.60 (3)	1.00 1.00
_	DMMENDED LAND PATTERN

GENERIC MARKING DIAGRAM*

O XXXX AYWW

 $\begin{array}{ll} \text{XXXX} = \text{Specific Device Code} \\ \text{A} &= \text{Assembly Location} \\ \text{Y} &= \text{Year Code} \end{array}$

WW = Work Week Code

MILLIMETERS					
DIM	MIN	NOM	MAX		
Α	0.70	0.75	0.80		
A1	0.00	-	0.05		
b	0.23	0.33	0.43		
С	0.15	0.20	0.25		
D	3.20	3.30	3.40		
D1	2.95	3.13	3.30		
D2	1.98	2.20	2.40		
Е	3.20	3.30	3.40		
E1	2.80	3.00	3.15		
E2	1.40	1.60	1.80		
E3	0.15	0.25	0.40		
е	0	.65 BS	С		
G	0.30	0.43	0.55		
G1	0.25	0.35	0.45		
K	0.55	0.75	0.95		
L	0.35	0.52	0.65		
L1	0.06	0.15	0.30		
М	1.35	1.50	1.60		
Φ	0	-	12		

*This information is generic. Please refer to
device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot " ■",
may or may not be present. Some products
may not follow the Generic Marking.

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DESCRIPTION:	WDFN8 3.3x3.3, 0.65P		PAGE 1 OF 1		

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