



OptiMOS[™]3 Power-Transistor

Features

- N-channel, normal level
- Excellent gate charge x R DS(on) product (FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹⁾ for target application
- Ideal for high-frequency switching and synchronous rectification
- Halogen-free according to IEC61249-2-21

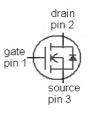
Product Summary

V _{DS}	120	٧
R _{DS(on),max}	14.7	mΩ
ID	56	Α





Туре	IPB144N12N3 G	IPI147N12N3 G	IPP147N12N3 G
	1 3 2 (tab)	123	123
Package	PG-TO263-3	PG-TO262-3	PG-TO220-3
Marking	144N12N	147N12N	147N12N



Maximum ratings, at T_j =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I _D	T _A =25 °C, R _{thJA} =45 K/W	56	А
		T _C =100 °C	41	
Pulsed drain current ²⁾	/ _{D,pulse}	$I_{\rm D}$ =56 A, $V_{\rm DS}$ =80 V, d <i>i</i> /d <i>t</i> =100 A/ μ s, $T_{\rm j,max}$ =175 °C	224	
Avalanche energy, single pulse	E _{AS}	$I_{\rm D}$ =56 A, $R_{\rm GS}$ =25 Ω	90	mJ
Gate source voltage ³⁾	V_{GS}		±20	V
Power dissipation	P_{tot}	T _C =25 °C	107	W
Operating and storage temperature	$T_{\rm j},T_{\rm stg}$		-55 175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

¹⁾J-STD20 and JESD22

²⁾ see figure 3

 $^{^{3)}\,}T_{jmax}\!\!=\!\!150^{\circ}\text{C}$ and duty cycle D=0.01 for Vgs<-5V



IPB144N12N3 G

IPI147N12N3 G IPP147N12N3 G

Parameter	Symbol	Conditions	Values		Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	R_{thJC}		-	-	1.4	K/W
Thermal resistance, junction -	R_{thJA}	minimal footprint	-	-	62	
ambient		6 cm2 cooling area ⁴⁾	-	-	40	

Electrical characteristics, at T_j =25 °C, unless otherwise specified

Static characteristics

Drain aguras breakdawn valtaga	l _V	\/ -0\/ / -1 mA	120			T _V
Drain-source breakdown voltage	V _{(BR)DSS}	$V_{\rm GS}$ =0 V, $I_{\rm D}$ =1 mA	120	-	-	
Gate threshold voltage	$V_{\rm GS(th)}$	$V_{\rm DS} = V_{\rm GS}$, $I_{\rm D} = 61~\mu{\rm A}$	2	3	4	
Zero gate voltage drain current	I _{DSS}	V _{DS} =100 V, V _{GS} =0 V, T _j =25 °C	ı	0.1	1	μΑ
		V _{DS} =100 V, V _{GS} =0 V, T _j =125 °C	ı	10	100	
Gate-source leakage current	I _{GSS}	V _{GS} =20 V, V _{DS} =0 V	1	1	100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =56 A, (TO263)	1	12.3	14.4	mΩ
		V _{GS} =10 V, I _D =56 A, (TO220, TO262)	1	12.6	14.7	
Gate resistance	R _G		1	1.2	1	Ω
Transconductance	g _{fs}	V _{DS} >2 I _D R _{DS(on)max} , I _D =56 A	31	62	-	s

 $^{^{4)}}$ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical in still air.



IPB144N12N3 G

IPI147N12N3 G IPP147N12N3 G

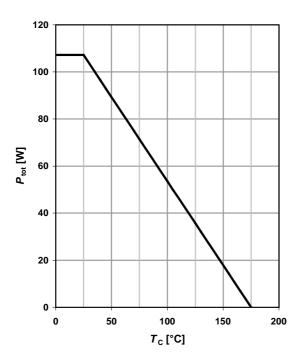
Parameter	Symbol	Conditions		Values		Unit
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	Ciss		-	2420	3220	pF
Output capacitance	Coss	V _{GS} =0 V, V _{DS} =60 V, f=1 MHz	-	304	404	1
Reverse transfer capacitance	Crss		-	17	-	
Turn-on delay time	$t_{d(on)}$		-	16	-	ns
Rise time	t _r	V _{DD} =60 V, V _{GS} =10 V,	-	9	-	
Turn-off delay time	$t_{d(off)}$	$I_{\rm D}$ =56 A, $R_{\rm G}$ =1.6 Ω	-	24	-	
Fall time	t_{f}		-	4	-	
Gate Charge Characteristics ⁵⁾						
Gate to source charge	Q _{gs}		-	13	-	nC
Gate to drain charge	Q_{gd}	.,,	-	9	-	
Switching charge	Q _{sw}	V _{DD} =60 V, I _D =56 A, V _{GS} =0 to 10 V	-	15	-	
Gate charge total	Qg		-	37	49	
Gate plateau voltage	V _{plateau}		-	5.5	ı	٧
Output charge	Q _{oss}	V _{DD} =60 V, V _{GS} =0 V	ı	42	55	nC
Reverse Diode						
Diode continous forward current	Is	T =25 °C	-	-	56	А
Diode pulse current	I _{S,pulse}	T _C =25 °C	-	-	224	1
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =56 A, T _j =25 °C	-	1	1.2	V
Reverse recovery time	t _{rr}	V _R =60 V, I _F =I _S ,	-	91		ns
Reverse recovery charge	Q _{rr}	d <i>i_F</i> /d <i>t</i> =100 A/μs	-	259	-	nC

⁵⁾ See figure 16 for gate charge parameter definition



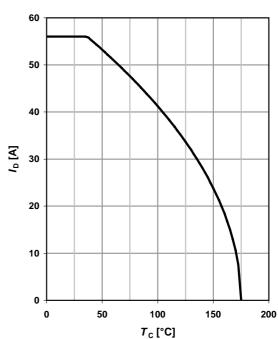
1 Power dissipation

P_{tot} =f(T_{C})



2 Drain current

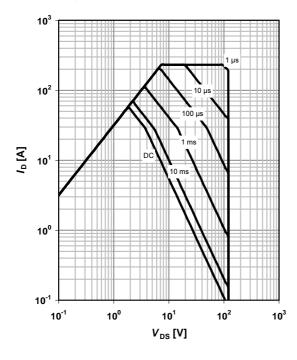
$$I_D = f(T_C); V_{GS} \ge 10 \text{ V}$$



3 Safe operating area

$$I_D$$
=f(V_{DS}); T_C =25 °C; D =0

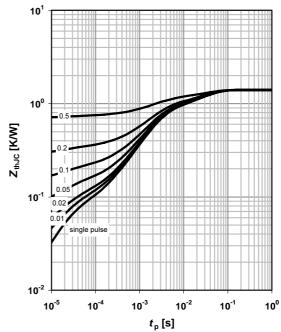
parameter: t_p



4 Max. transient thermal impedance

$$Z_{\rm thJC}$$
=f($t_{\rm p}$)

parameter: $D=t_p/T$

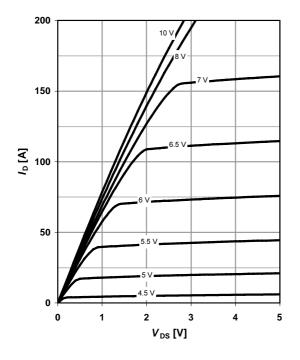




5 Typ. output characteristics

 $I_D=f(V_{DS}); T_j=25 \text{ °C}$

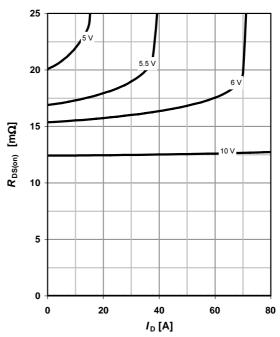
parameter: $V_{\rm GS}$



6 Typ. drain-source on resistance

 $R_{DS(on)}$ =f(I_D); T_j =25 °C

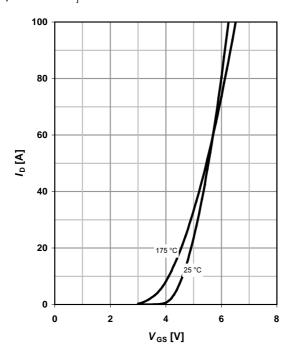
parameter: $V_{\rm GS}$



7 Typ. transfer characteristics

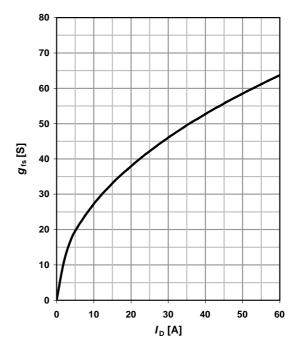
 I_{D} =f(V_{GS}); $|V_{DS}|$ >2 $|I_{D}|R_{DS(on)max}$

parameter: T_j



8 Typ. forward transconductance

 g_{fs} =f(I_D); T_j =25 °C





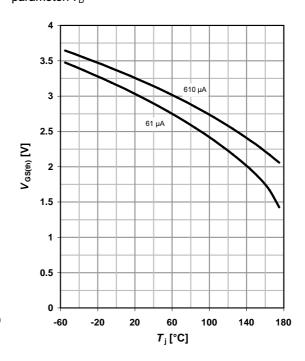
9 Drain-source on-state resistance

 $R_{DS(on)}$ =f(T_j); I_D =56 A; V_{GS} =10 V

35 30 25 R_{DS(on)} [m Ω] 20 15 10 5 0 -60 -20 20 60 140 180 *T*_j [°C]

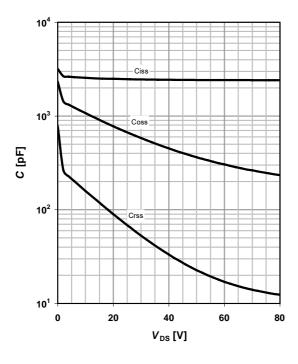
10 Typ. gate threshold voltage

 $V_{\text{GS(th)}}$ =f(T_{j}); V_{GS} = V_{DS} parameter: I_{D}



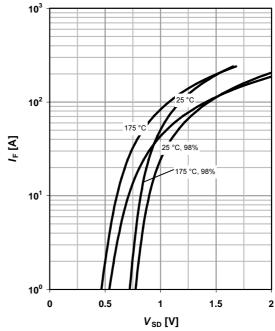
11 Typ. capacitances

 $C=f(V_{DS}); V_{GS}=0 V; f=1 MHz$



12 Forward characteristics of reverse diode

 $I_{\text{F}} = f(V_{\text{SD}})$ parameter: T_{j}

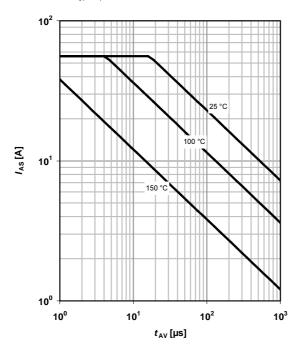




13 Avalanche characteristics

 I_{AS} =f(t_{AV}); R_{GS} =25 Ω

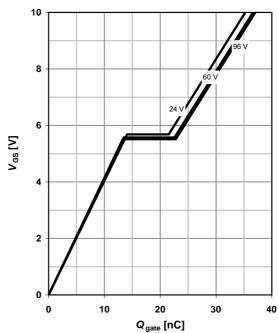
parameter: $T_{\rm j(start)}$



14 Typ. gate charge

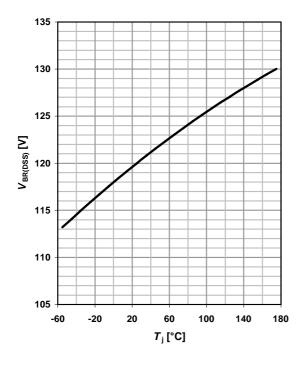
 $V_{\rm GS}$ =f(Q_{gate}); $I_{\rm D}$ =56 A pulsed

parameter: $V_{\rm DD}$

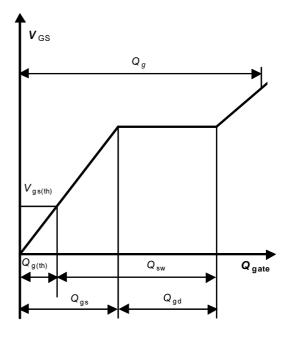


15 Drain-source breakdown voltage

 $V_{BR(DSS)}$ =f(T_j); I_D =1 mA

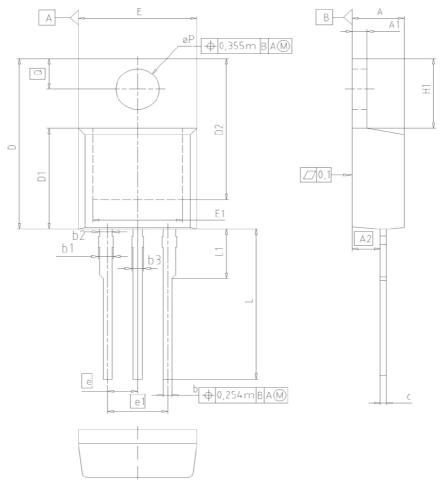


16 Gate charge waveforms

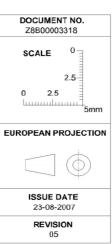




PG-TO220-3: Outline

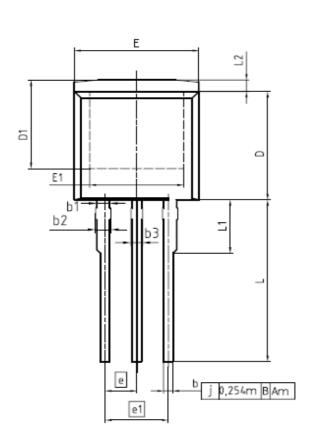


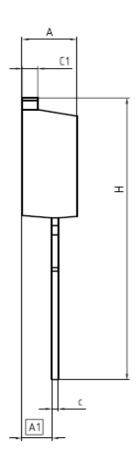
DIM	MILLIN	METERS	INC	HES	
DIIVI	MIN	MAX	MIN	MAX	
Α	4.30	4.57	0.169	0.180	
A1	1.17	1.40	0.046	0.055	
A2	2.15	2.72	0.085	0.107	
b	0.65	0.86	0.026	0.034	
b1	0.95	1.40	0.037	0.055	
b2	0.95	1.15	0.037	0.045	
b3	0.65	1.15	0.026	0.045	
С	0.33	0.60	0.013	0.024	
D	14.81	15.95	0.583	0.628	
D1	8.51	9.45	0.335	0.372	
D2	12.19	13.10	0.480	0.516	
E	9.70	10.36	0.382	0.408	
E1	6.50	8.60	0.256	0.339	
e	2.	54	0.100		
e1	5.0	08	0.200		
N		3	3		
H1	5.90	6.90	0.232	0.272	
L	13.00	14.00	0.512	0.551	
L1	-	4.80	-	0.189	
øΡ	3.60	3.89	0.142	0.153	
Q	2.60	3.00	0.102	0.118	





PG-TO262-3-1 (I²PAK)



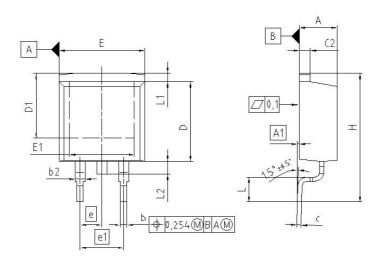


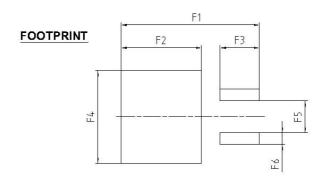
DIM	MILLIME	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
A	4,300	4,572	0,169	0,180
A1	2.150	2.718	0.085	0.107
Ь	0.650	0.864	0.026	0.034
b1	0,950	1,093	0,037	0,043
b2	0.950	1,400	0.037	0.055
ь3	0.650	1.118	0.026	0.044
С	0,330	0,600	0,013	0,024
c1	1.170	1,400	0.046	0.055
D	8,509	9.450	0.335	0,372
D1	6,900	-	0,272	•
E	9.700	10.363	0.382	0.408
E1	6,500	8,600	0,256	0,339
e	2,5	40	0,100	
e1	5.0	80	0.2	100
N	3			3
L	13,000	14,000	0,512	0,551
L1	-	4.800	-	0.189
L2	-	1,727	•	0,068

REFERENCE
JEDEC TO262
SCALE 0
2.5 0 2,5 5mm
EUROPEAN PROJECTION
ISSUE DATE 05-05-2006
FILE TO262_1

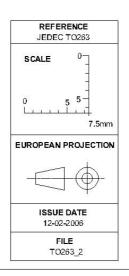


PG-TO-263 (D2-Pak)





DIM	MILLIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	4.300	4.572	0.169	0.180
A1	0.000	0.254	0.000	0.010
b	0.650	0.850	0.026	0.033
b2	0.950	1.321	0.037	0.052
C	0.330	0.650	0.013	0.026
c2	0.170	1.400	0.046	0.055
D	8.509	9.450	0.335	0.372
D1	7.100	-	0.280	-
E	9.800	10.312	0.386	0.406
E1	6.500		0.256	
e	2.5	40	0.100	
e1	5.0	80	0.200	
N	2	2	2	
Н	14.605	15.875	0.575	0.625
L	2.200	3.000	0.087	0.118
L1	-	1.600	-	0.063
L2	1.000	1.778	0.039	0.070
F1	16.050	16.250	0.632	0.640
F2	9.300	9.500	0.366	0.374
F3	4.500	4.700	0.177	0.185
F4	10.700	10.900	0.421	0.429
F5	3.630	3.830	0.143	0.151
F6	1.100	1.300	0.043	0.051





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