

## **MOSFET**

## **Small-Signal Transistor**

#### **Features**

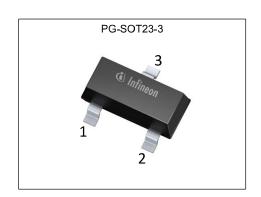
- N-channel
- Depletion mode
- dv/dt rated
- Pb-free lead-plating; RoHS compliantHalogen-free according to AEC61249-2-21

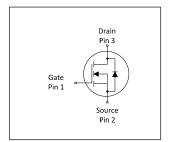
### **Product validation**

Fully qualified according to JEDEC for Industrial Applications

Table 1 **Key Performance Parameters** 

Tubic : Itoy i oi		
Parameter	Value	Unit
$V_{ extsf{DS}}$	100	V
R <sub>DS(on),max</sub>	12	Ω
I <sub>DSS,min</sub>	0.09	A
ESD Sensitivity, JESD22-A114 (HBM)	Class 0 (<250V)	











Type / Ordering Code	Package	Marking	Related Links
BSS169I	PG-SOT23	Fls	-



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## 1 Maximum ratings at $T_A$ =25 °C, unless otherwise specified

Table 2 **Maximum ratings** 

Devemeter	0	Values			ļ., .,	N
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Continuous drain current	I <sub>D</sub>	-	-	0.19 0.15	А	T <sub>A</sub> =25 °C T <sub>A</sub> =70 °C
Pulsed drain current	I <sub>D,pulse</sub>	-	-	0.76	Α	T <sub>A</sub> =25 °C
Reverse diode dv/dt	d <i>v</i> /d <i>t</i>	-	-	6	kV/µs	$I_D$ =0.19 A, $V_{DS}$ =20 V, d <i>i</i> /d <i>t</i> =200 A/ $\mu$ s, $T_{j,max}$ =150 °C
Gate source voltage	V <sub>GS</sub>	-20	-	20	V	-
Power dissipation	P <sub>tot</sub>	-	-	0.36	W	T <sub>A</sub> =25 °C
Operating and storage temperature	T <sub>j</sub> , T <sub>stg</sub>	-55	-	150	°C	IEC climatic category; DIN IEC 68-1: 55/150/56

#### 2 **Thermal characteristics**

#### Table 3 Thermal characteristics

Parameter	Symbol	Values			l Init	Note / Took Condition
	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Thermal resistance, junction - ambient, minimal footprint	$R_{thJA}$	-	-	250	K/W	-

## **Electrical characteristics**

at  $T_j$ =25 °C, unless otherwise specified

Table 4 **Static characteristics** 

Parameter	Constant	Values				
	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	100	-	-	V	V <sub>GS</sub> =-10 V, I <sub>D</sub> =250 μA
Gate threshold voltage	$V_{\rm GS(th)}$	-2.9	-2.2	-1.8	V	V <sub>DS</sub> =3 V, I <sub>D</sub> =50 μA
Drain-source cutoff current	$I_{D(off)}$	-	-	0.1 10	μA	V <sub>DS</sub> =100 V, V <sub>GS</sub> =-10 V, T <sub>j</sub> =25 °C V <sub>DS</sub> =100 V, V <sub>GS</sub> =-10 V, T <sub>j</sub> =125 °C
Gate-source leakage current	I <sub>GSS</sub>	-	-	10	nA	V <sub>GS</sub> =20 V, V <sub>DS</sub> =0 V
On-state drain current	I <sub>DSS</sub>	90	-	-	mA	V <sub>GS</sub> =0 V, V <sub>DS</sub> =10 V
Drain-source on-state resistance	R <sub>DS(on)</sub>	-	5.3 2.9	12 -	Ω	V <sub>GS</sub> =0 V, I <sub>D</sub> =0.05 A V <sub>GS</sub> =10 V, I <sub>D</sub> =0.19 A
Transconductance	<b>g</b> fs	-	0.20	-	S	V <sub>DS</sub>  >2 I <sub>D</sub>  R <sub>DS(on)max</sub> , I <sub>D</sub> =0.15 A



Table 5 Dynamic characteristics

Parameter	C	Values			11:4	Note / Tool One life
	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Input capacitance	C <sub>iss</sub>	-	51	-	pF	V <sub>GS</sub> =-10 V, V <sub>DS</sub> =25 V, f=1 MHz
Output capacitance	Coss	-	9	-	pF	V <sub>GS</sub> =-10 V, V <sub>DS</sub> =25 V, f=1 MHz
Reverse transfer capacitance	C <sub>rss</sub>	-	4	-	pF	V <sub>GS</sub> =-10 V, V <sub>DS</sub> =25 V, f=1 MHz
Turn-on delay time	$t_{\sf d(on)}$	-	2.9	-	ns	$V_{\rm DD}$ =50 V, $V_{\rm GS}$ =-3 to 7 V, $I_{\rm D}$ =0.12 A, $R_{\rm G}$ =6 $\Omega$
Rise time	t <sub>r</sub>	-	2.7	-	ns	$V_{\rm DD}$ =50 V, $V_{\rm GS}$ =-3 to 7 V, $I_{\rm D}$ =0.12 A, $R_{\rm G}$ =6 $\Omega$
Turn-off delay time	$t_{\sf d(off)}$	-	11	-	ns	$V_{\rm DD}$ =50 V, $V_{\rm GS}$ =-3 to 7 V, $I_{\rm D}$ =0.12 A, $R_{\rm G}$ =6 $\Omega$
Fall time	$t_{\mathrm{f}}$	-	27	-	ns	$V_{\rm DD}$ =50 V, $V_{\rm GS}$ =-3 to 7 V, $I_{\rm D}$ =0.12 A, $R_{\rm G}$ =6 $\Omega$

Table 6 Gate charge characteristics

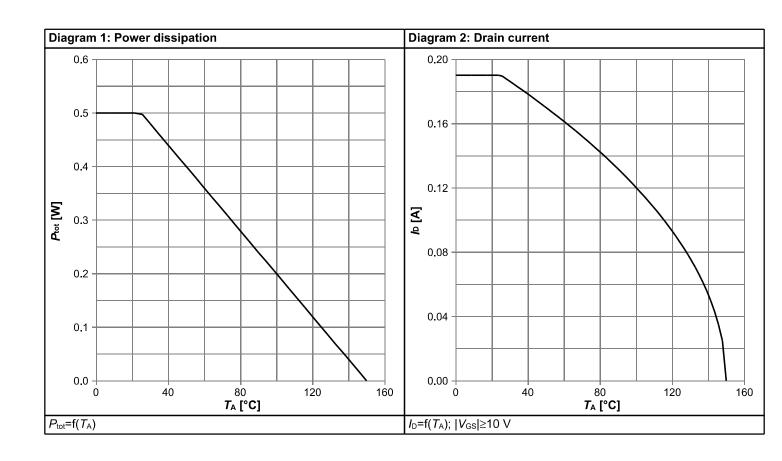
Davamatav	Cymphol	Values			Unit	Note / Test Condition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Gate to source charge	Q <sub>gs</sub>	-	0.12	-	nC	$V_{\rm DD}$ =80 V, $I_{\rm D}$ =0.12 A, $V_{\rm GS}$ =-3 to 7 V
Gate to drain charge	$Q_{ m gd}$	-	0.9	-	nC	$V_{\rm DD}$ =80 V, $I_{\rm D}$ =0.12 A, $V_{\rm GS}$ =-3 to 7 V
Gate charge total	Qg	-	2.1	-	nC	$V_{\rm DD}$ =80 V, $I_{\rm D}$ =0.12 A, $V_{\rm GS}$ =-3 to 7 V
Gate plateau voltage	V <sub>plateau</sub>	-	-0.43	-	V	$V_{DD}$ =80 V, $I_{D}$ =0.12 A, $V_{GS}$ =-3 to 7 V

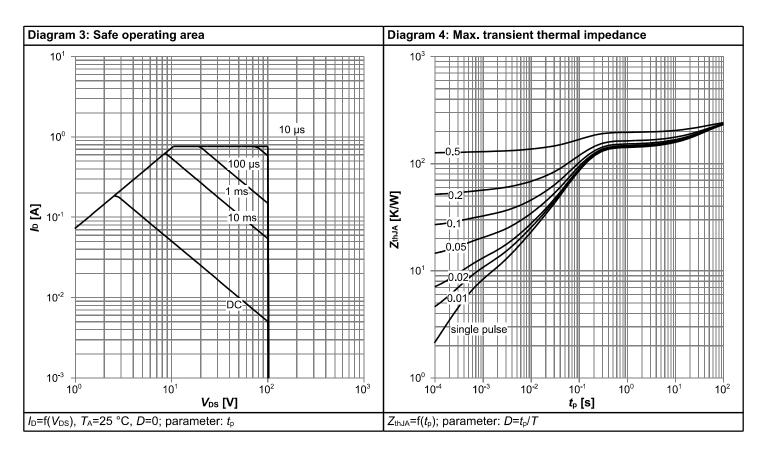
### Table 7 Reverse diode

Parameter	Comple at		Values			N / / T / A D   1711
	Symbol	Min.	Typ.	Max.	Unit	Note / Test Condition
Diode continous forward current	Is	-	-	0.19	Α	T <sub>A</sub> =25 °C
Diode pulse current	I <sub>S,pulse</sub>	-	-	0.76	Α	T <sub>A</sub> =25 °C
Diode forward voltage	<b>V</b> <sub>SD</sub>	-	0.82	1.2	V	V <sub>GS</sub> =-10 V, I <sub>F</sub> =0.19 A, T <sub>j</sub> =25 °C
Reverse recovery time	t <sub>rr</sub>	-	20.5	25.6	ns	V <sub>R</sub> =50 V, I <sub>F</sub> =0.12 A, d <i>i</i> <sub>F</sub> /d <i>t</i> =100 A/μs
Reverse recovery charge	Q <sub>rr</sub>	-	9.7	12.1	nC	V <sub>R</sub> =50 V, I <sub>F</sub> =0.12 A, di <sub>F</sub> /dt=100 A/μs

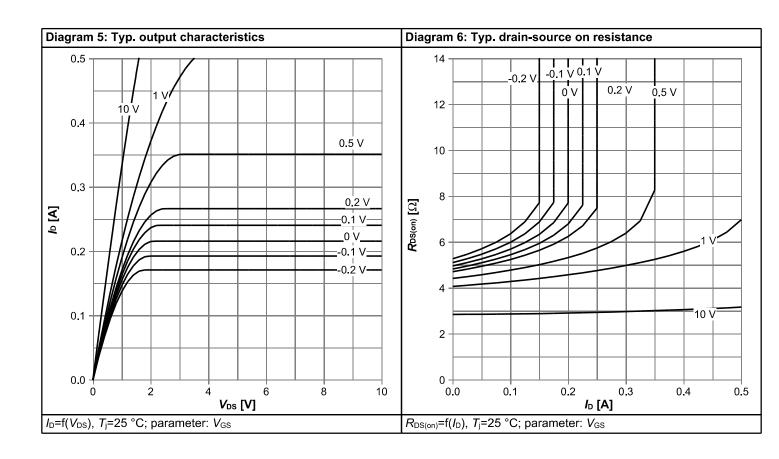


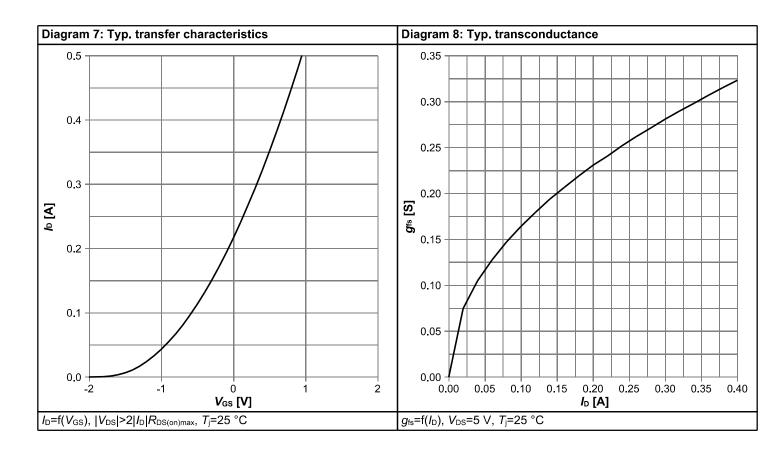
## 4 Electrical characteristics diagrams



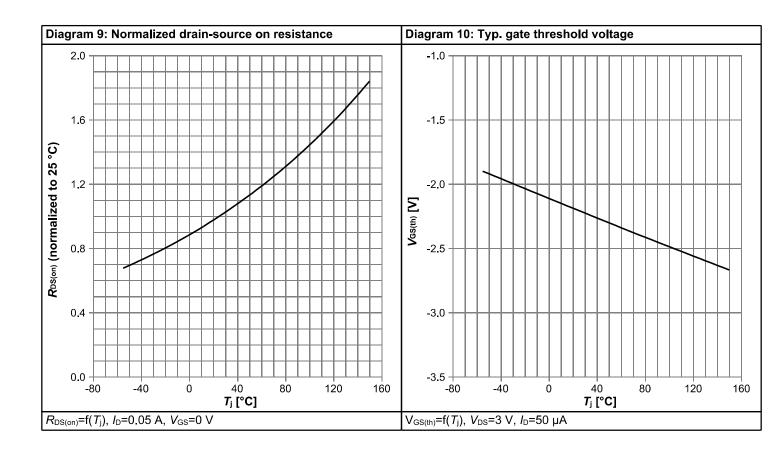


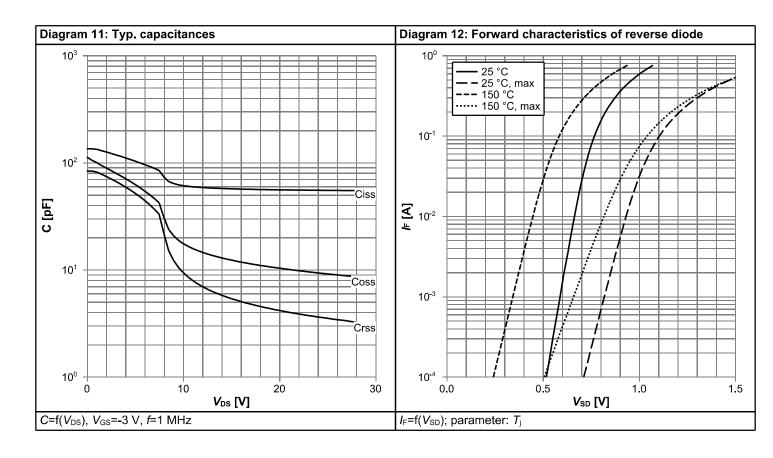




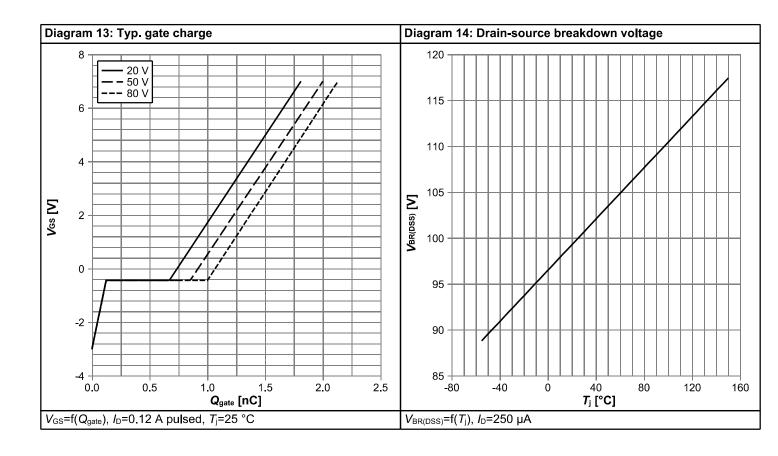


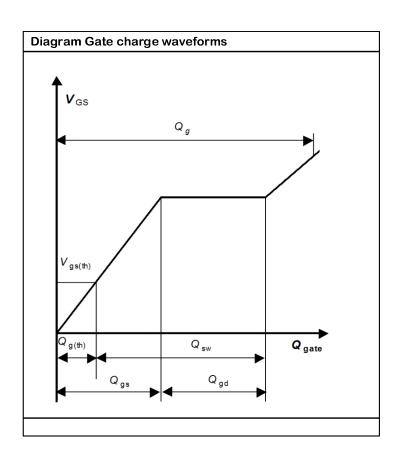






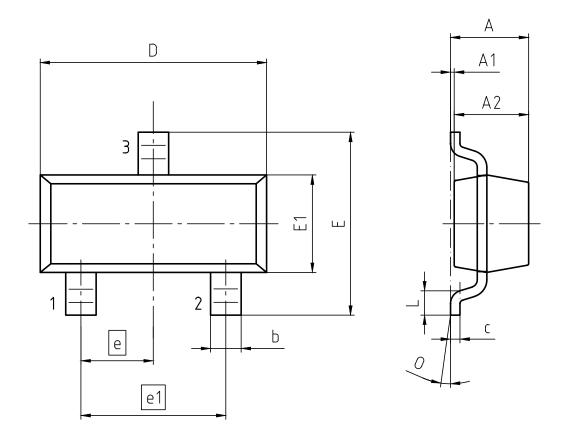








## 5 Package Outlines



PACKAGE - GROUF NUMBER:	23-3-U01					
REVISION: 01	DATE: (	09.12.2020				
DIMENSIONS	MILLIM	ETERS				
DIMENSIONS	MIN.	MAX.				
Α	0.89	1.12				
A1	0.01	0.10				
A2	0.88	1.02				
b	0.30	0.50				
С	80.0	0.20				
D	2.80	3.04				
E	2.10	2.64				
E1	1.20	1.40				
е	0.95					
e1	1.90					
L	0.15	0.60				
0	0°	8°				

Figure 1 Outline PG-SOT23, dimensions in mm





#### **Revision History**

BSS169I

Revision: 2021-03-17, Rev. 2.1

**Previous Revision** 

Revision	Date	Subjects (major changes since last revision)				
2.0	2021-01-26	Release of final version				
2.1	2021-03-17	Update technology naming				

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