

Description

The STD2LN60K3 uses advanced trench technology to provide excellent RDS(ON), low gate charge. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = 600V I_{D} = 2A$

 $R_{DS(ON)} < 4.5\Omega$ @ $V_{GS}=10V$

Application

Battery protection

Uninterruptible power supply

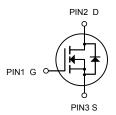
Load switch

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
STD2LN60K3	TO-252-2L(TO-252(DPAK))	HXY MOSFET	2500

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	600	V
Vgs	Gate-Source Voltage	±30	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ^[1]	2	Α
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ^[1]	['] GS @ 10V ^[1] 1.2	
Ідм	Pulsed Drain Current ^[2]	8	А
P _D @T _C =25°C	Total Power Dissipation ^[1]	31	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
$R_{ heta JA}$	Thermal Resistance Junction-ambient [6]	100	°C/W
Rejc	Thermal Resistance Junction-Case ^[1]	4.0	°C/W



N-Channel MOSFET



Electrical Characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Off characteristics							
Drain-source breakdown voltage	V(BR) DSS	V _G s=0V, I _D =250μA	600			V	
Zero gate voltage drain current	I _{DSS}	V _{DS} =600V, V _{GS} =0V			1.0	μΑ	
Gate-body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±30V			±100	nA	
On characteristics ^[4]							
Gate-threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.0	3.4	4.0	V	
Static drain-source on-sate resistance	RDS(on)	V _G s=10V, I _D =1A		3.7	4.5	Ω	
Dynamic characteristics ^[5]							
Input capacitance	C _{iss}), 05),(), 0),		322		pF	
Output capacitance	Coss	V _{DS} =25V,V _{GS} =0V, f =1MHz		38			
Reverse transfer capacitance	C _{rss}			7			
Gate resistance	Rg	f =1MHz		5.7		Ω	
Switching characteristics ^[5]			<u> </u>			1	
Total gate charge	Qg	V _{GS} =10V, V _{DS} =25V, I _D =2A		1.6		nC	
Gate-source charge	Q_{gs}			2.1			
Gate-drain charge	Q_{gd}	V _{DS} -23V, I _D -2A		6.2		1	
Turn-on delay time	$t_{d(on)}$			1.8			
Turn-on rise time	t r	V _{DD} =25V, V _{GS} =10V,		3.2			
Turn-off delay time	td(off)	R _G =18Ω, I _D =2A		7.4		- nS	
Turn-off fall time	t f			7.6			
Drain-Source Diode Characteristics			•				
Drain-source diode forward voltage ^[4]	V _{SD}	V _{GS} =0V, I _S =2A			1.4	V	
Continuous drain-source diode forward current [1]	Is				2.0	Α	
Pulsed drain-source diode forward current [2]	I _{SM}				8.0	Α	
Reverse recovery time	trr	dIF/dt = 100A/µs,		192		ns	
Reverse recovery charge	Qrr	$I_S = 2A, V_{DD} = 400V$		1027		nC	

Notes:

 $^{1.}T_{\text{C}}$ =25°C Limited only by maximum temperature allowed.

^{2.}P_W≤10µs, Duty cycle≤1%.

^{3.}EAS condition: V_{DD} =150V, V_{GS} =10V, L=10mH, Rg=25 Ω Starting T_J = 25 $^{\circ}$ C.

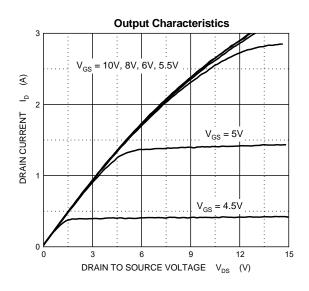
^{4.}Pulse Test : Pulse Width≤300µs, duty cycle ≤2%.

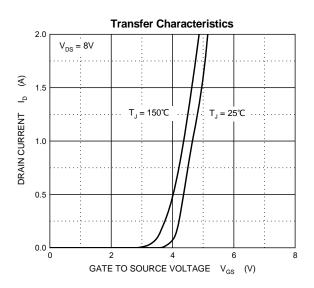
^{5.} Guaranteed by design, not subject to production.

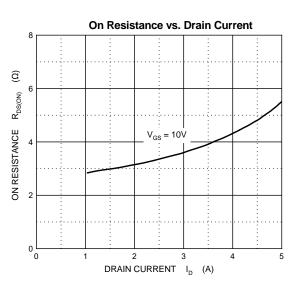
^{6.}The value of R0JA $\,$ is measured with the device $\,$ in a still air environment with T_A=25 $^{\circ}$.

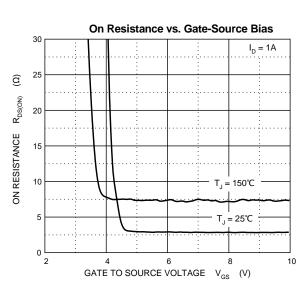


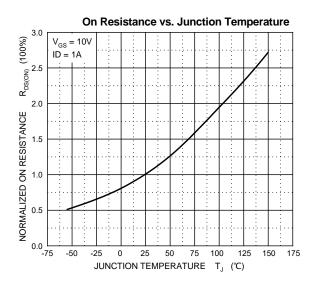
Typical Characteristics

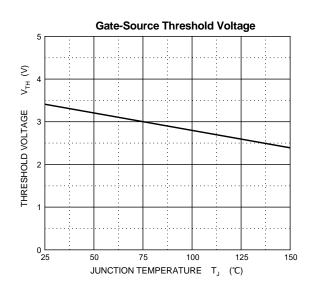




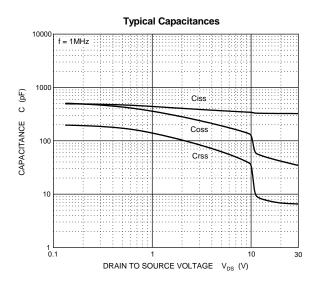


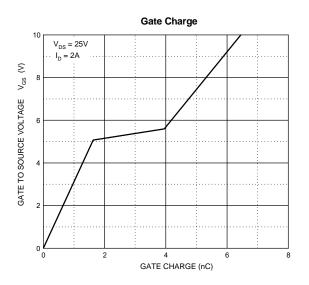


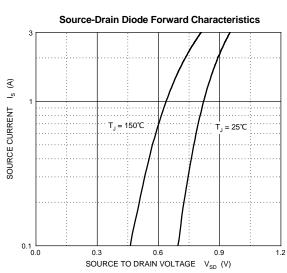


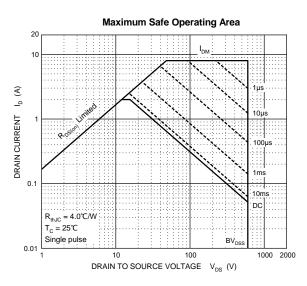


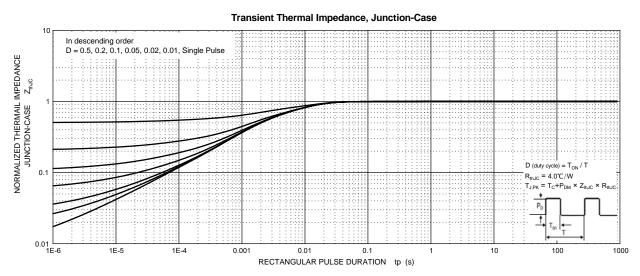






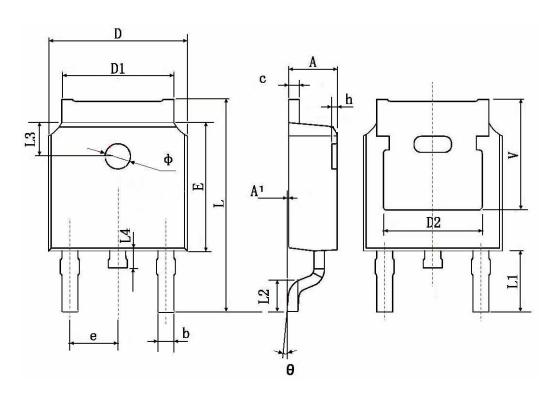








TO-252-2L(TO-252(DPAK)) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches			
	Min.	Max.	Min.	Max.		
Α	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.48	0.483 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.90	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600 TYP.		0.063 TYP.			
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350 TYP.		0.211 TYP.			

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