

N-Channel Enhancement Mode MOSFET

Feature

- 40V/334A $R_{DS(ON)} = 0.95 \text{ m}\Omega(\text{typ.}) \text{ @VGS} = 10V$
- 100% Avalanche Tested
- 100% DVDS
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

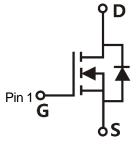
Applications

- Load Switch
- DC-DC
- Lithium battery protect board

Pin Description



TOLL



Pin 2,3,4,5,6,7,8

Single N-Channel MOSFET

Ordering and Marking Information



Package Code

TA: TOLL

Date Code XYMXXXXXX

Note: HUAYI halogen free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI halogen free products meet or exceed the halogen free require-ments of IPC/JEDEC J-STD-020 for MSL classification at halogen free peak reflow temperature. HUAYI defines "Green" to mean halogen free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Junction Temperature Range			°C
Тѕтс	Storage Temperature Range		-55 to 175	°C
ls	Source Current-Continuous(Body Diode) Tc=25°C		334	Α
Mounted on	Large Heat Sink	•	,	1
Ідм	Pulsed Drain Current *	Tc=25°C	1330	А
			334	Α
lo	Continuous Drain Current	Tc=100°C	236	А
	M	Tc=25°C	241	W
PD	P _D Maximum Power Dissipation Tc=100°C		120	W
R₀c	Thermal Resistance, Junction-to-Case		0.62	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		45	°C/W
Eas	Single Pulsed-Avalanche Energy *** L=0.3mH		776	mJ

- Note: * Repetitive rating; pulse width limited by max.junction temperature.
 - Surface mounted on 1in2 FR-4 board.
 - Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25 Ω , VGs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Comple al	Donomoton	Took Conditions	HYG011N04NS1			1114	
Symbol	Parameter	Parameter Test Conditions		Тур.	Max	Unit	
Static Cha	Static Characteristics						
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	40	-	-	V	
IDSS Drain-to-Source Leakage Cu	Drain to Course Leakers Current	V _{DS} =40V,V _{GS} =0V	-	-	1	μA	
	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μA	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	2	3	4	V	
lgss	Gate-Source Leakage Current	Vgs=±20V,Vps=0V	-	-	±100	nA	
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =40A	-	0.95	1.2	mΩ	
Diode Characteristics							
VsD	Diode Forward Voltage	Isp=40A,Vgs=0V	-	0.79	1.20	V	
trr	Reverse Recovery Time	lon-404 dlon/dt-1004/ug	-	47	-	ns	
Qrr	Reverse Recovery Charge	- Isb=40A,dIsb/dt=100A/μs	-	49	-	nC	

HYG011N04NS1TA



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Compleal	Devenuetes	Test Canditions	HYG011N04NS1			11:4
Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic (Dynamic Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=500KHz	-	1.6	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	5816	-	
Coss	Output Capacitance	V _{DS} =25V,	-	1312	-	pF
Crss	Reverse Transfer Capacitance	Frequency=500KHz	-	57	-	
td(ON)	Turn-on Delay Time		-	25	-	
Tr	Turn-on Rise Time	$V_{DD}=20V,R_{G}=4\Omega,$	-	75	-	
td(OFF)	Turn-off Delay Time	lps=40A,Vgs=10V	-	56	-	ns
Tf	Turn-off Fall Time		-	57	-	
Gate Charge Characteristics						
Qg	Total Gate Charge(V _{GS} =10V)		-	85	-	
Qgs	Gate-Source Charge	V -33V I -30A	-	28	-	nC
Qgd	Gate-Drain Charge	V_{DS} =32V, I_{DS} =20A	-	13	-	
V _{plateau}	Gate plateau voltage		-	4.6	-	V

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

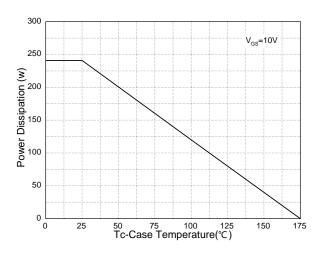


Figure 3: Safe Operation Area

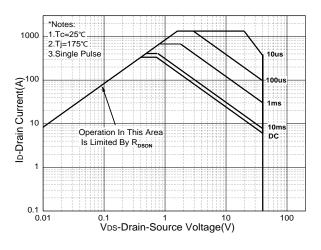


Figure 5: Output Characteristics

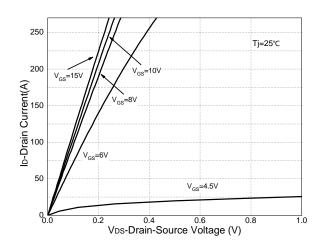


Figure 2: Drain Current

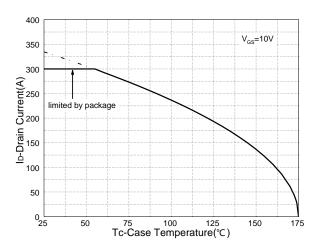


Figure 4: Thermal Transient Impedance

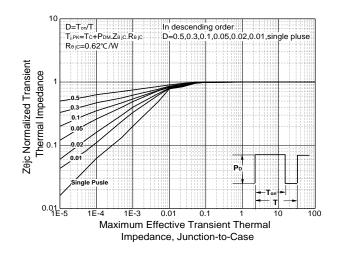
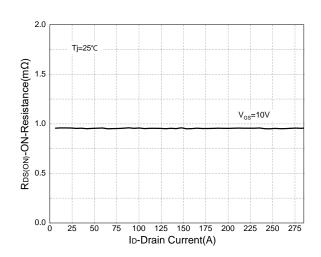


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

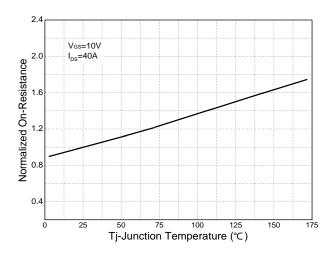


Figure 8: Source-Drain Diode Forward

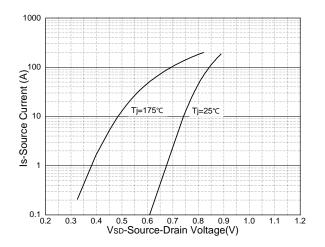


Figure 9: Capacitance Characteristics

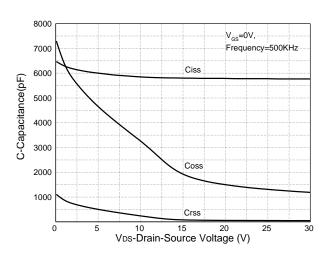
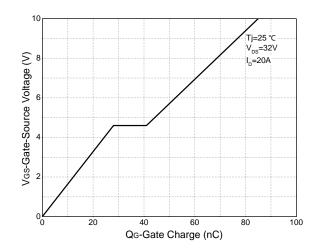
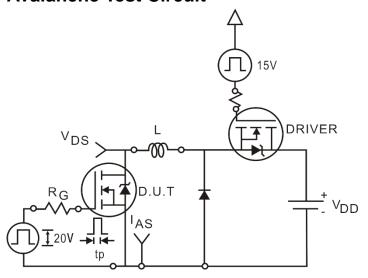


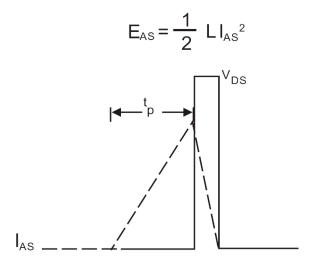
Figure 10: Gate Charge Characteristics



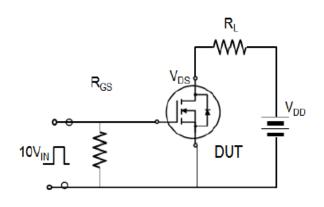


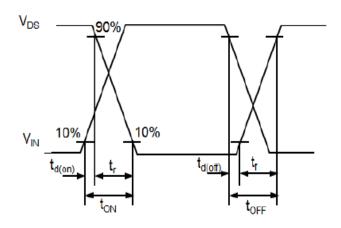
Avalanche Test Circuit



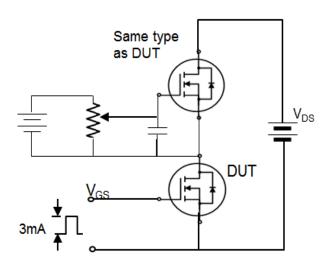


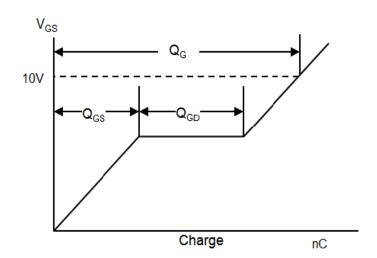
Switching Time Test Circuit





Gate Charge Test Circuit





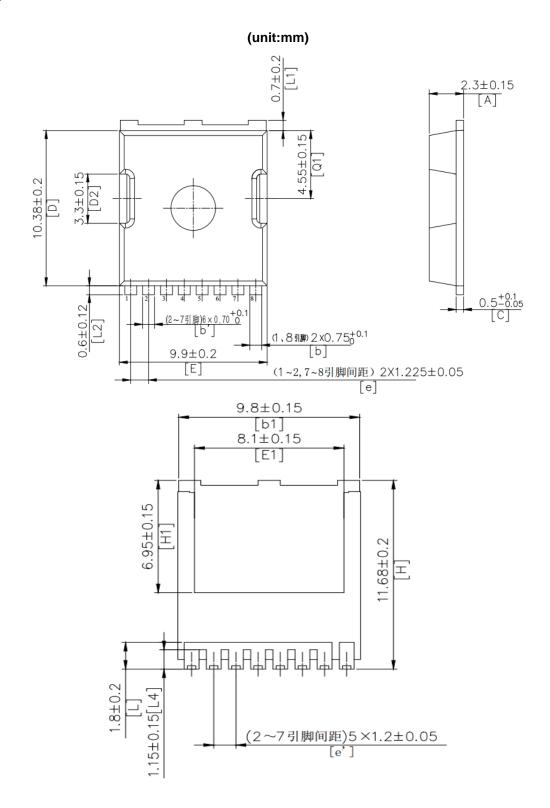


Device Per Unit

Package Type	Unit	Quantity
TOLL	Reel	1200

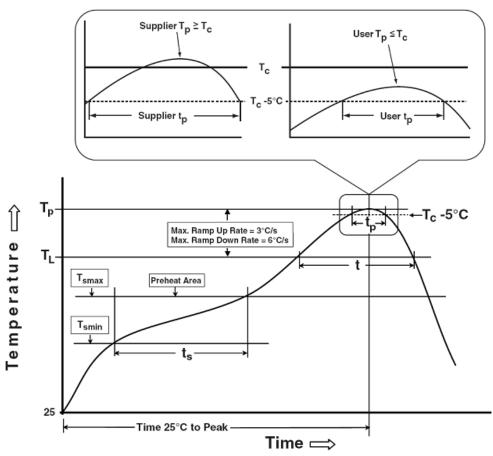
Package Information

TOLL





Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly		
Preheat & Soak				
Temperature min (T _{smin})	100 °C	150 °C		
Temperature max (T _{smax})	150 °C	200 °C		
Time (Tsmin to Tsmax) (t _s)	60-120 seconds	60-120 seconds		
Average ramp-up rate	3 °C/second max.	3°C/second max.		
(T _{smax} to T _P)	5 C/second max.	3 C/Second max.		
Liquidous temperature (TL)	183 °C	217 °C		
Time at liquidous (t _L)	60-150 seconds	60-150 seconds		
Peak package body Temperature	See Classification Temp in table 1	SacClassification Tompin table 2		
(T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2		
Time (t _P)** within 5°C of the specified	20**	30** seconds		
classification temperature (T _c)	20** seconds			
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.		
Time 25°C to peak temperature	6 minutes max.	8 minutes max.		

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

HYG011N04NS1TA



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm ³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150℃
HTGB	JESD-22, A108	168 /500/1000 Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	250/500/1000 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com Technical Support:Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: http://www.hymexa.com/