

• General Description

The AGM150P10AP combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{\text{DS(ON)}}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low R_{DS(ON)} to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

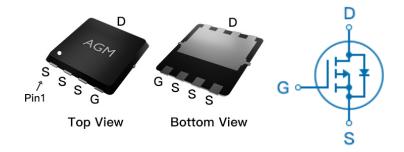
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
-100V	112mΩ	-16A

PDFN3.3*3.3 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM150P10	AGM150P10AP	PDFN3.3*3.3	330mm	12mm	5000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) (Note 1)	-16	Α
	Drain Current-Continuous(Tc=100℃)	-10.7	А
IDM (pluse)	Drain Current-Pulsed (Note 2)	-64	Α
PD	Maximum Power Dissipation(Tc=25℃)	69	W
	Maximum Power Dissipation(Tc=100℃)	27.8	W
EAS	Avalanche energy (Note 3)	72	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹		50	°C/W
RθJC	Thermal Resistance Junction-Case ¹		1.8	°C/W



Table 3. Electrical Characteristics (TJ=25°Cunless otherwise noted)

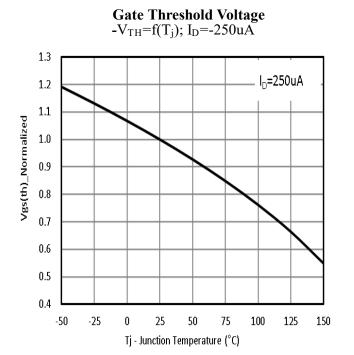
Table 3. Electrical Characteristics (TJ=25 [°] C unless otherwise noted)						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off St	ates					
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	-100			V
IDSS	Zero Gate Voltage Drain Current	VDS=-100V,VGS=0V			-1.0	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.2		-2.2	V
gFS	Forward Transconductance	VDS=-5V,ID=-3A		9.0		S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-5A		112	150	mΩ
()		VGS=-4.5V, ID=-3A		128	165	mΩ
Dynamic	Characteristics					
Ciss	Input Capacitance			700		pF
Coss	Output Capacitance	VDS=-50V,VGS=0V		56		pF
Crss	Reverse Transfer Capacitance	F=1MHZ		8.6		pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz		18		Ω
Switching	g Times					
td(on)	Turn-on Delay Time			5.9		nS
tr	Turn-on Rise Time			3.7		nS
td(off)	Turn-Off Delay Time	VGS=-10V,VDS=-50V, ID=-5A,RGEN=5Ω		39.5		nS
tf	Turn-Off Fall Time			25		nS
Qg	Total Gate Charge			13		nC
Qgs	Gate-Source Charge	VGS=-10V, VDS=-50V, ID=-5A		2.0		nC
Qgd	Gate-Drain Charge	- VDG00V, ID0/A		2.3		nC
Source-D	rain Diode Characteristics					
ISD	Source-Drain Current(Body Diode)				-16	А
VSD	Forward on Voltage	VGS=0V,IS=-5A			-1.2	V
trr	Reverse Recovery Time	IF=-5A , dl/dt=100A/µs ,		21		ns
Qrr	Reverse Recovery Charge	TJ=25℃		23		nc

Notes 1. The maximum current rating is package limited.

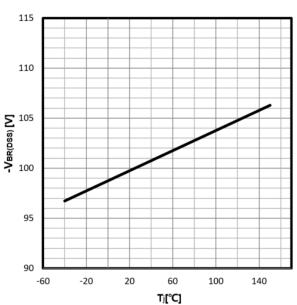
Notes 2. Repetitive Rating: Pulse width limited by maximum junction temperature

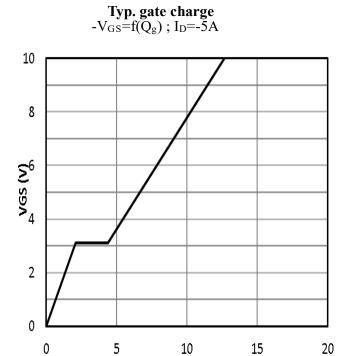
Notes 3.EAS condition: TJ=25 $^{\circ}\text{C}\text{,VDD}\text{=-50V,Vgs}\text{=-10V}\text{, ID}\text{=-17A,L=0.5mH,RG=25ohm}$





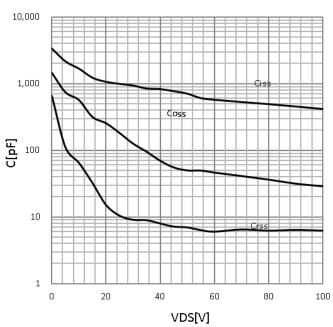
 $\begin{array}{c} \textbf{Drain-source breakdown voltage} \\ \textbf{-}V_{BR(DSS)} \!\!=\!\! f(T_j); \ I_D \!\!=\!\! -250 uA \end{array}$



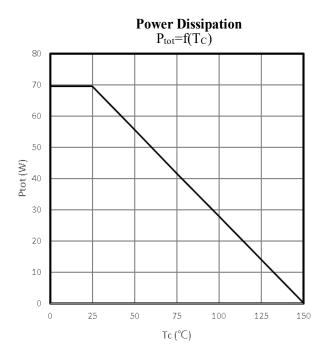


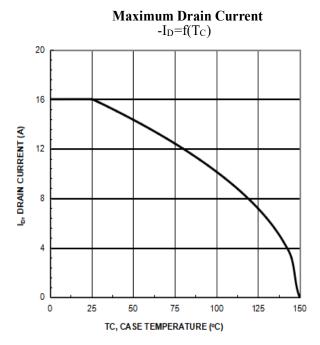
Qg (nC)

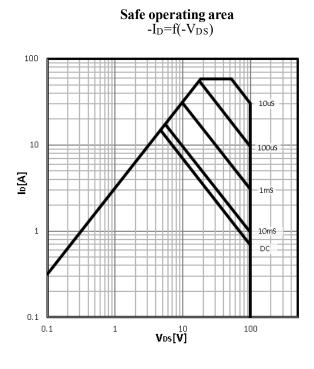
$$\label{eq:capacitances} \begin{split} & \textbf{Typ. capacitances} \\ & C = & f(\text{-}V_{DS}); \ V_{GS} = & 0V; \ f = & 1MHz \end{split}$$

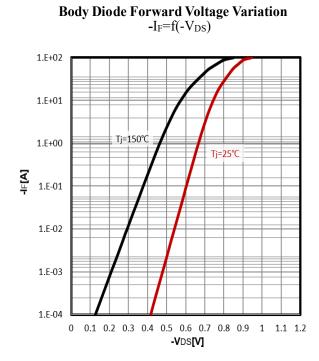




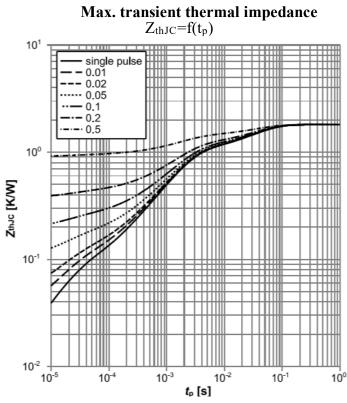






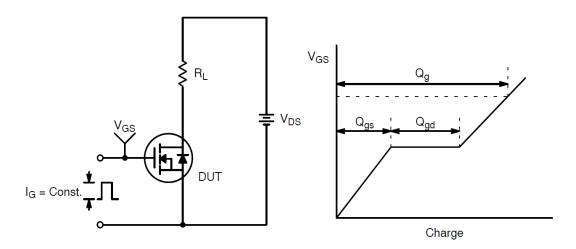




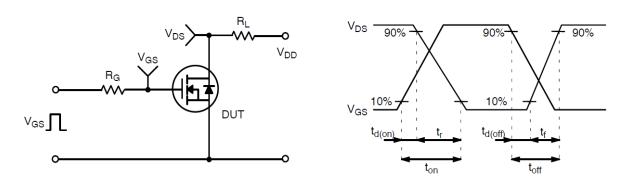




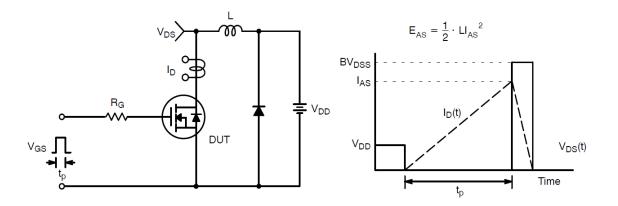
Test Circuit and Waveform:



Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

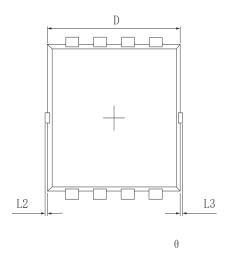


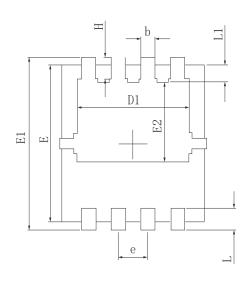
Unclamped Inductive Switching Test Circuit & Waveforms



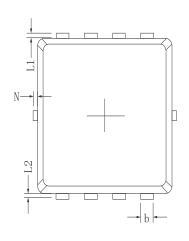
A2

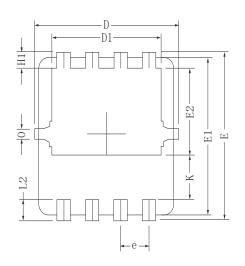
•Dimensions (PDFN3.3*3.3)

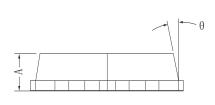


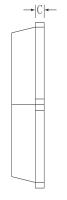


CVMDOI	MI	MILLIMETER		
SYMBOL	MIN	Тур.	MAX	
A	0.700	0.800	0.900	
A1	0.	152REF	`.	
A2		0~0.05		
D	3.000	3. 100	3.200	
D1	2.300	2. 450	2.600	
Е	2.900	3.000	3. 100	
E1	3. 150	3. 300	3.450	
E2	1.320	1.520	1.720	
b	0.200	0.300	0.400	
е	0.550	0.650	0.750	
L	0.300	0.400	0.500	
L1	0.180	0. 330	0.480	
L2	0~0.100			
L3	0~0.100			
Н	0.315	0.415	0.515	
θ	8°	10°	12°	





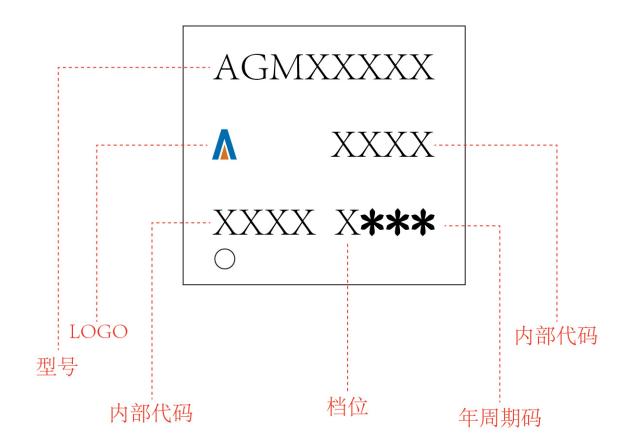




C 1 1	Millimeters				
Symbols	MIN.	NOM.	MAX.		
A	0.65	0.75	0.85		
b	0.25	0.30	0.35		
С	0.15	0.20	0.25		
D	3.00	3. 10	3.20		
D1	2.40	2.50	2.60		
Е	3. 20	3. 30	3.40		
E1	3.00	3. 10	3.20		
E2	1.60	1.70	1.80		
е	0.65 BSC.				
H1	0.21	0.31	0.41		
Н2	0.30	0.40	0.50		
K	0.78	0.88	0.98		
L1/L2	0.10 REF.				
θ	11°	12°	13°		
N	0	-	0.15		
0	0.2 REF.				



PDFN3.3*3.3 Marking Instructions:





Disclaimer:

The information provided in this document is believed to be accurate and reliable. However, Shenzhen Core Control Source Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Source Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the second version issued on April 10th, 2024. This document replaces all previously provided information.

It is a registered trademark of Shenzhen Core Control Source Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Source Electronics Technology Co., Ltd. all rights reserved.