

N-Channel Enhancement Mode MOSFET

Feature Description

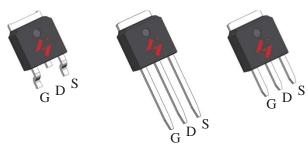
• 30V/62A

 $R_{DS(ON)}$ = 4.5m $\Omega(typ.)$ @V_{GS} = 10V

 $R_{DS(ON)} = 6.0 \text{m}\Omega(\text{typ.})$ @VGS = 4.5V

- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

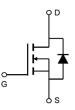
Pin Description



TO-252-2L TO-251-3L TO-251-3S

Applications

- Switching Application
- Power Management for DC/DC
- Battery Protection



N-Channel MOSFET

Ordering and Marking Information



Package Code

D: TO-252-2L U: TO-251-3L V:TO-251-3S

Date Code Assembly Material YYXXX WW G:Halogen Free

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			1
VDSS	Drain-Source Voltage		30	V
Vgss	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		150	°C
Tstg	Storage Temperature Range		-55 to 150	°C
ls	Source Current-Continuous(Body Diode)	Tc=25°C	62	А
Mounted on	Large Heat Sink		-	
Ідм	Pulsed Drain Current *	Tc=25°C	248	А
	Outliness Barin Outline	Tc=25°C	62	А
lσ	Continuous Drain Current	Tc=100°C	39	А
D	Maniana Bana Biada tina	Tc=25°C	36	W
PD	P _D Maximum Power Dissipation Tc=100°C		14.3	W
R _e uc	Thermal Resistance, Junction-to-Case		3.5	°C/W
R _{eJA}	Thermal Resistance, Junction-to-Ambient	Thermal Resistance, Junction-to-Ambient **		°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	132	mJ

Note: * Repetitive rating; pulse width limited by max. junction temperature.

** Surface mounted on FR-4 board.

*** Limited by TJmax , starting TJ=25°C, L = 0.3mH, Rg= 25Ω , Vgs =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Ol	Daramatar	Took Constitions		HY1603		11
Symbol	Symbol Parameter Test Conditions		Min	Тур.	Max	Unit
Static Cha	racteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250μA	30	-	-	V
lana	Drain to Source Leakage Current	V _{DS} =20V,V _{GS} =0V	-	-	1	μΑ
IDSS	Drain-to-Source Leakage Current	TJ=55°C	-	-	30	μΑ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250µA	1	1.4	2	V
Igss	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$	-	-	±100	nA
Pro/01/1*	Drain-Source On-State Resistance	V _{GS} =10V,I _{DS} =31A	-	4.5	5.5	mΩ
Rds(on)*	Dialii-Source Oii-State Resistance	V _{GS} =4.5V,I _{DS} =31A	-	6.0	8.0	11122
Diode Cha	racteristics					
V _{SD} *	Diode Forward Voltage	IsD=31A,Vgs=0V	-	0.8	1.1	V
trr	Reverse Recovery Time	lon=21	-	21	-	ns
Qrr	Reverse Recovery Charge	- IsD=31A,dIsD/dt=100A/μs	-	13	_	nC



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Council of	Paramatan.	Test Conditions		HY1603		l losis
Symbol	Symbol Parameter Test Cor		Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V, Frequency=1.0MHz	-	1.4	-	Ω
Ciss	Input Capacitance	V _{GS} =0V,	-	2082	-	
Coss	Output Capacitance	V _{DS} =25V,	-	140	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	78	-	
td(ON)	Turn-on Delay Time		-	15	-	
Tr	Turn-on Rise Time	V_{DD} =25 V , R_{G} =3.3 Ω ,	-	13	-	
td(OFF)	Turn-off Delay Time	Ips=31A,Vgs=10V	-	39	-	ns
Tf	Turn-off Fall Time		-	10	-]
Gate Cha	Gate Charge Characteristics					
Qg	Total Gate Charge	\/ -24\/ \/ -40\/	-	52.8	-	
Qgs	Gate-Source Charge	$V_{DS} = 24V, V_{GS} = 10V,$ $I_{D} = 31A$	-	4.4	-	nC
Qgd	Gate-Drain Charge	ID-21W	-	10.3	-	

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%



Typical Operating Characteristics

Figure 1: Power Dissipation

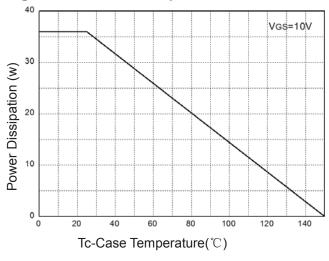


Figure 2: Drain Current

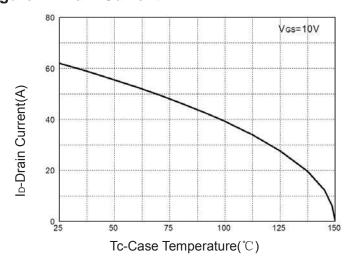


Figure 3: Safe Operation Area

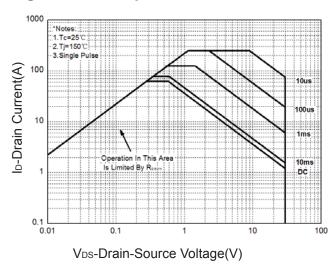
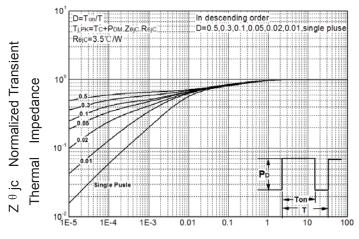


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 5: Output Characteristics

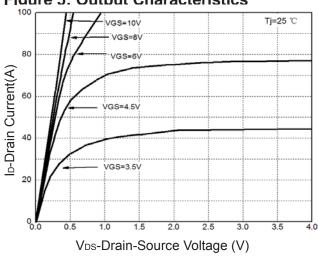
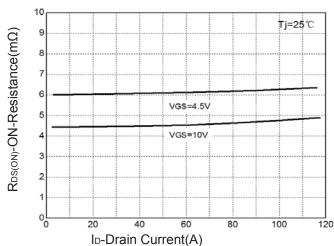


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

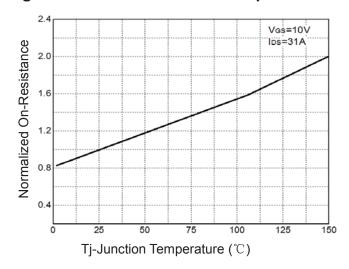


Figure 8: Source-Drain Diode Forward

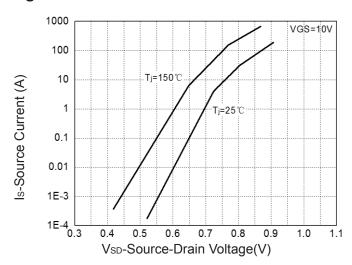


Figure 9: Capacitance Characteristics

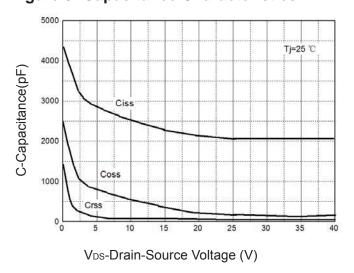
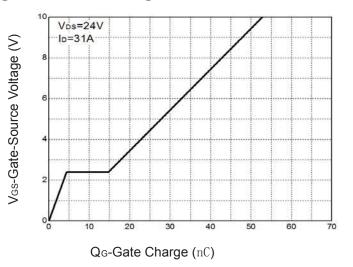
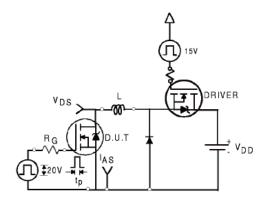


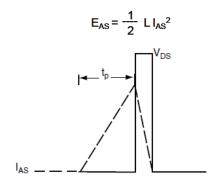
Figure 10: Gate Charge Characteristics



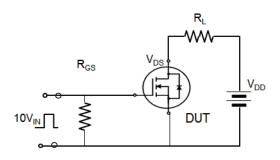


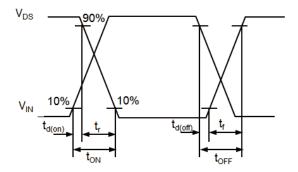
Avalanche Test Circuit



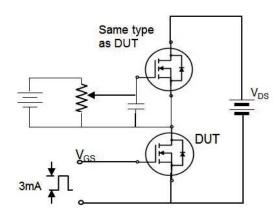


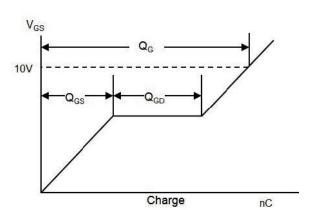
Switching Time Test Circuit





Gate Charge Test Circuit





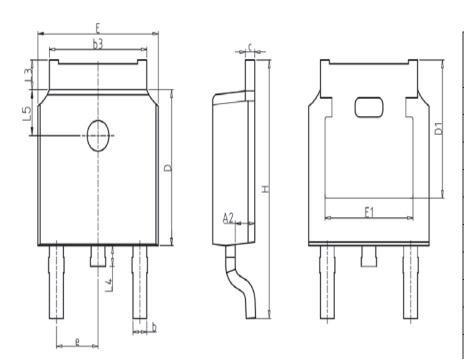


Device Per Unit

Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-252-2L	Reel	2500
TO-251-3L	Tube	75
TO-251-3S	Tube	75

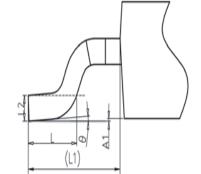
Package Information

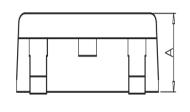
TO-252-2L



COMMON DIMENSIONS

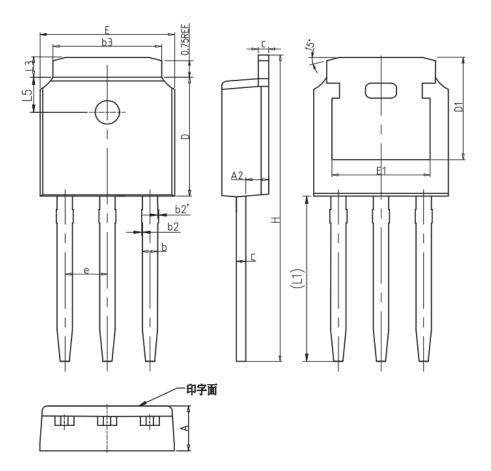
SYMBOL		mm			
OTNIDOL	MIN	NOM	MAX		
А	2.20	2.30	2.40		
A1	0.00	-	0.20		
A2	0.97	1.07	1.17		
b	0.68	0.78	0.90		
b3	5.20	5.33	5.50		
С	0.43	0.53	0.63		
D	5.98	6.10	6.22		
D1	5.30REF				
E	6.40	6.60	6.80		
E1	4.63	-	-		
е	:	2.286BS	O		
Н	9.40	10.10	10.50		
L	1.38	1.50	1.75		
L1		2.90REF	-		
L2	0.51BSC				
L3	0.88	-	1.28		
L4	-	-	1.00		
L5	1.65	1.80	1.95		
θ	0°	-	8°		







TO-251-3L

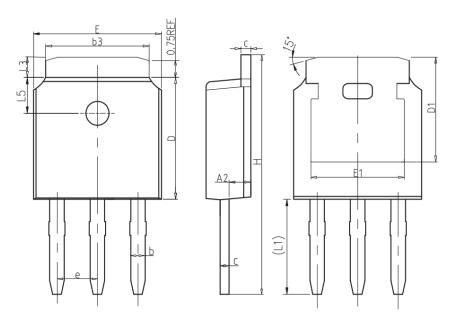


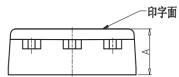
COMMON DIMENSIONS

0)/4/00/		mm	
SYMBOL	MIN	NOM	MAX
А	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
Е	6.40	6.60	6.80
E1	4.63	-	-
е	2.286BSC		
Н	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



TO-251-3S



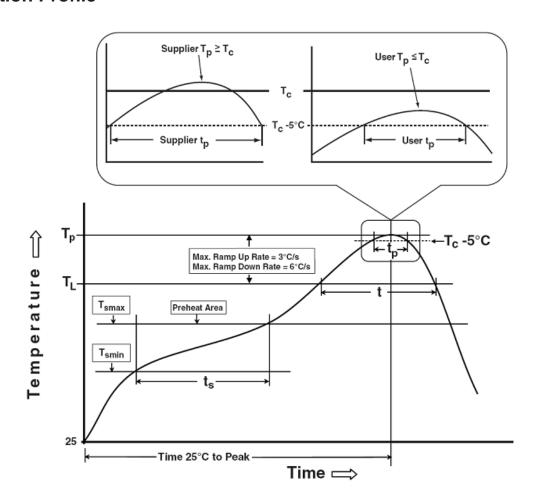


COMMON DIMENSIONS

SYMBOL		mm	
STIVIBUL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BSC	
Н	10.00	11.22	11.44
L1	3.90	4.10	4.30
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak	100 °C	150 °C	
Temperature min (T _{smin})	150 °C	200 °C	
Temperature max (T _{smax})	60-120 seconds	60-120 seconds	
Time (Tsmin to Tsmax) (t₅)	00-120 Seconds	00-120 Seconds	
Average ramp-up rate	3 °C/second max.	2°C/cocond mov	
(T _{smax} to T _P)	5 C/second max.	3°C/second max.	
Liquidous temperature (T _L)	183 °C	217 °C	
Time at liquidous (t∟)	60-150 seconds	60-150 seconds	
Peak package body Temperature	See Classification Temp in table 1	See Classification Temp in table 2	
(T _p)*	See Classification Temp in table 1	See Classification Temp in table 2	
Time (t _P)** within 5°C of the specified	20**	20**	
classification temperature (T _c)	20** seconds	30** seconds	
Average ramp-down rate (Tp to Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	

^{*}Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

HY1603D/U/V



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm ³
Thickness	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HOLT	JESD-22, A108	168 Hrs /500 Hrs /1000 Hrs, Bias @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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