### Siliup Semiconductor

**Product Summary** 

V <sub>(BR)DSS</sub>	R <sub>DS(on)TYP</sub>	ID
85V	3.8mΩ@10V	160A



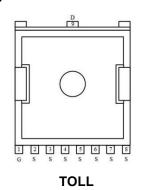
### **Feature**

- Fast Switching
- Low Gate Charge and Rdson
- 100% Single Pulse avalanche energy Test

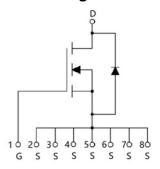
## **Applications**

- **PWM Application**
- Hard switched and high frequency circuits
- **Power Management**

## **Package**



## Circuit diagram



## Marking



SP85N04AGHTO : Product code : Week code

### **Order Information**

Device	Package	Unit/Tape
SP85N04AGHTO	TOLL	2000

1

**85V N-Channel Power MOSFET** 

## Absolute maximum ratings (Ta=25℃,unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	85	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Tc=25°C)	ID	160	Α
Continuous Drain Current (Tc=100°C)	I <sub>D</sub>	107	Α
Pulse Drain Current Tested	I <sub>DM</sub>	640	Α
Single Pulse Avalanche Energy <sup>1</sup>	Eas	646	mJ
Power Dissipation (Tc=25°C)	P <sub>D</sub>	246	W
Thermal Resistance Junction-to-Case	Rejc	0.51	°C/W
Maximum Junction Temperature	TJ	-55 to 150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C

## Electrical characteristics (Ta=25℃, unless otherwise noted)

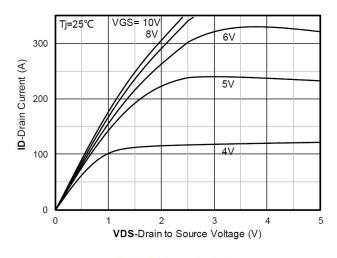
Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$I_D = 250\mu A, V_{GS} = 0V$	85	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 68V, V <sub>GS</sub> = 0V	-	-	1	uA
Gate Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	2.0	3.0	4.0	V
Drain-Source On-state Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 50A	-	3.8	4.6	mΩ
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>		-	3265	-	
Output Capacitance	Coss	VGS=0V, VDS=40V,F=1MHz	-	428	-	pF
Reverse Transfer Capacitance	Crss		-	23	-	
Total Gate Charge	Qg		-	42	-	
Gate-Source Charge	Q <sub>gs</sub>	VDS=40V, VGS=10V, ID=50A	-	15	-	nC
Gate-Drain Charge	Q <sub>gd</sub>			20	-	
Switching Characteristics						
Turn-On Delay Time	t <sub>d(on)</sub>		-	17	-	
Rise Time	t <sub>r</sub>	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	39	-	20
Turn-Off Delay Time	t <sub>d(off)</sub>	VDD=40V, ID=50A, VGS=10V, $R_G$ =3 $\Omega$	-	64	-	nS
Fall Time	t <sub>f</sub>			42	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	VGS=0V , IS=1A , TJ=25℃	-	-	1.2	<b>\</b>
Maximum Body-Diode Continuous Current	Is		-	-	160	Α
Reverse Recovery Time	Trr	L-50 A di/dt-100 A/us T-25°C	-	45	-	nS
Reverse Recovery Charge	Qrr	I <sub>s</sub> =50 A,di/dt=100 A/μs, T <sub>J</sub> =25℃		56	-	nC

#### Note:

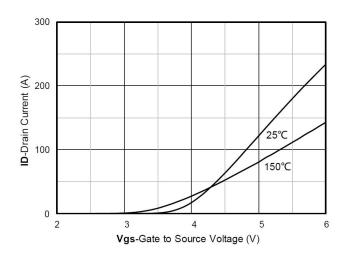
1. The test condition is VDD=40V,VGS=10V,L=0.5mH,RG=25 $\Omega$ 



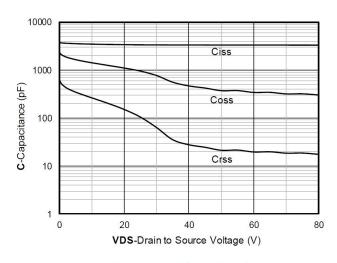
## **Typical Characteristics**



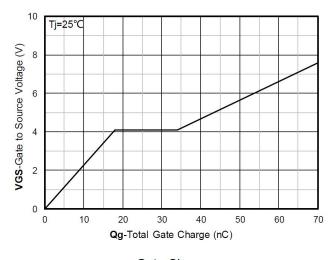
**Output Characteristics** 



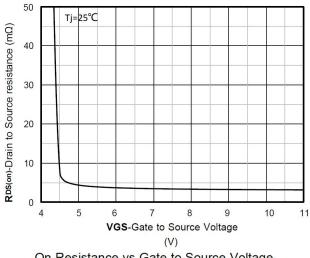
**Transfer Characteristics** 



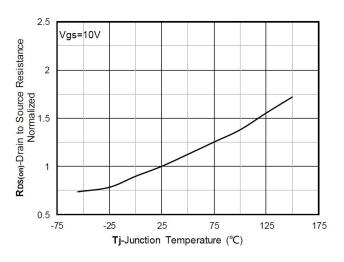
Capacitance Characteristics



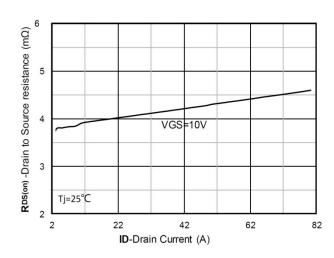
Gate Charge

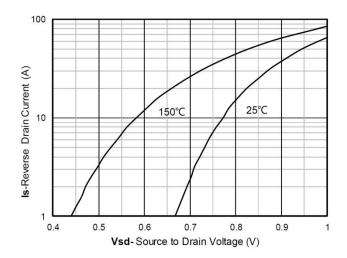


On-Resistance vs Gate to Source Voltage



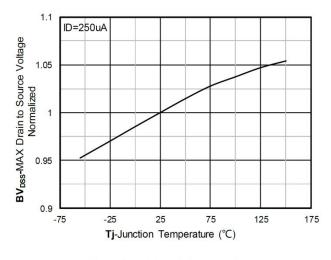
Normalized On-Resistance

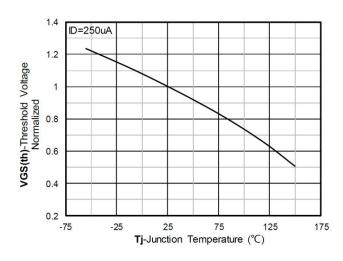




RDS(on) VS Drain Current

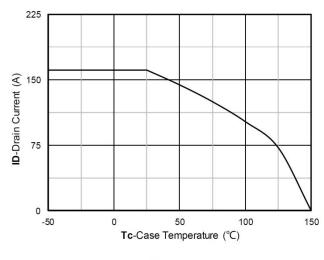
Forward characteristics of reverse diode

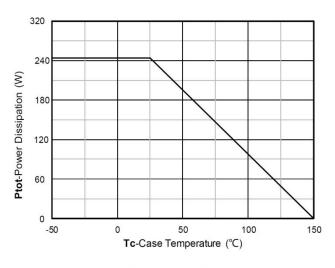




Normalized breakdown voltage

Normalized Threshold voltage

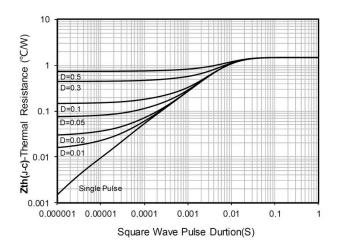




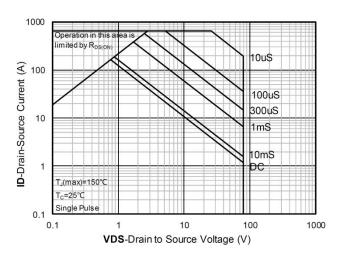
Current dissipation

Power dissipation

## **85V N-Channel Power MOSFET**



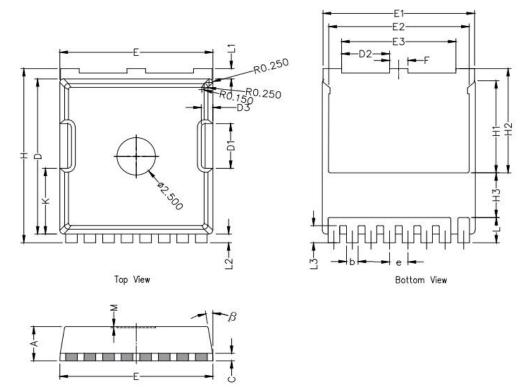




Safe Operation Area

## **85V N-Channel Power MOSFET**

# **TOLL Package Information**



Symbol	Dimensions In Millimeters				
	Min.	Nom.	Max.		
Α	2.20	2.30	2.40		
b	0.65	0.75	0.85		
С		0.508 REF			
D	10.25	10.40	10.55		
D1	2.85	3.00	3.15		
E	9.75	9.90	10.05		
E1	9.65	9.80	9.95		
E2	8.95	9.10	9.25		
E3	7.25	7.40	7.55		
е	1.20 BSC				
F	1.05	1.20	1.35		
Н	11.55	11.70	11.85		
H1	6.03	6.18	6.33		
H2	6.85	7.00	7.15		
H3	3.00 BSC				
L	1.55	1.70	1.85		
L1	0.55	0.7	0.85		
L2	0.45	0.6	0.75		
M	0.08 REF.				
β	8°	10°	12°		
К	4.25	4.40	4.55		