

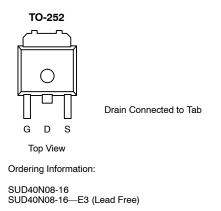


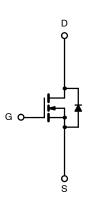
# N-Channel 80-V (D-S) 175°C MOSFET

| PRODUCT SUMMARY     |                                  |    |  |  |
|---------------------|----------------------------------|----|--|--|
| V <sub>DS</sub> (V) | $V_{DS}(V)$ $r_{DS(on)}(\Omega)$ |    |  |  |
| 80                  | 0.016 @ V <sub>GS</sub> = 10 V   | 40 |  |  |

### **FEATURES**

- TrenchFET® Power MOSFET
- 175°C Maximum Junction Temperature
- 100% R<sub>g</sub> Tested





N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED) |                                   |                  |                  |     |  |
|--|-----------------------------------|------------------|------------------|-----|--|
| Parameter  | Symbol                            | Limit            | Unit             |     |  |
| Drain-Source Voltage   |                                   | V <sub>DS</sub>  | 80               | .,  |  |
| Gate-Source Voltage  |                                   | V <sub>GS</sub>  | ±20              | _ v |  |
| 0 " D 1 0 1 7 1 0 b  | T <sub>C</sub> = 25°C             | - I <sub>D</sub> | 40               |     |  |
| Continuous Drain Current (T <sub>J</sub> = 175°C) <sup>b</sup>         | T <sub>C</sub> = 125°C            |                  | 30               |     |  |
| Pulsed Drain Current   |                                   | I <sub>DM</sub>  | 60               | A   |  |
| Continuous Source Current (Diode Conduction)                           |                                   | IS               | 40               |     |  |
| Avalanche Current  |                                   | I <sub>AR</sub>  | 40               |     |  |
| Repetitive Avalanche Energy (Duty Cycle ≤ 1%)                          | L = 0.1 mH                        | E <sub>AR</sub>  | 80               | mJ  |  |
|  | T <sub>C</sub> = 25°C             |                  | 136 <sup>b</sup> | 144 |  |
| Maximum Power Dissipation  | T <sub>A</sub> = 25°C             | P <sub>D</sub>   | 3 <sup>a</sup>   | W   |  |
| Operating Junction and Storage Temperature Range                       | T <sub>J</sub> , T <sub>stg</sub> | –55 to 175       | °C               |     |  |

| THERMAL RESISTANCE RATINGS       |              |                   |         |         |      |  |
|----------------------------------|--------------|-------------------|---------|---------|------|--|
| Parameter                        |              | Symbol            | Typical | Maximum | Unit |  |
|                                  | t ≤ 10 sec   | R <sub>thJA</sub> | 15      | 18      |      |  |
| Junction-to-Ambient <sup>a</sup> | Steady State |                   | 40      | 50      | °C/W |  |
| Junction-to-Case                 |              | R <sub>thJC</sub> | 0.85    | 1.1     |      |  |

#### Notes

a. Surface Mounted on 1" x1" FR4 Board.

b. See SOA curve for voltage derating.

## SUD40N08-16

# Vishay Siliconix



| Parameter                                     | Symbol               | Test Condition  | Min | Typ <sup>a</sup> | Max   | Unit |  |  |
|---|----------------------|---|-----|------------------|-------|------|--|--|
| Static  | <u> </u>             |   | II. | l                | I     |      |  |  |
| Drain-Source Breakdown Voltage                | V <sub>(BR)DSS</sub> | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$   | 80  |                  |       | - v  |  |  |
| Gate Threshold Voltage                        | V <sub>GS(th)</sub>  | $V_{DS} = V_{GS}, I_D = 250 \mu A$  | 2.0 |                  | 4.0   |      |  |  |
| Gate-Body Leakage                             | I <sub>GSS</sub>     | $V_{DS}$ = 0 V, $V_{GS}$ = $\pm 20$ V   |     |                  | ±100  | nA   |  |  |
|   |                      | V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V   |     |                  | 1     |      |  |  |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>     | V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125°C                             |     |                  | 50    | μΑ   |  |  |
|   |                      | V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175°C                             |     |                  | 250   |      |  |  |
| On-State Drain Current <sup>b</sup>           | I <sub>D(on)</sub>   | $V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$   | 60  |                  |       | Α    |  |  |
|   |                      | $V_{GS} = 10 \text{ V}, I_D = 40 \text{ A}$   |     | 0.013            | 0.016 |      |  |  |
| Drain-Source On-State Resistance <sup>b</sup> | r <sub>DS(on)</sub>  | $V_{GS}$ = 10 V, $I_{D}$ = 40 A, $T_{J}$ = 125°C  |     |                  | 0.027 | Ω    |  |  |
|   |                      | $V_{GS}$ = 10 V, $I_{D}$ = 40 A, $T_{J}$ = 175°C  |     |                  | 0.037 | )37  |  |  |
| Forward Transconductanceb                     | 9 <sub>fs</sub>      | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 40 A   |     | 45               |       | S    |  |  |
| Dynamic <sup>a</sup>                          |                      |   | - 1 | •                |       |      |  |  |
| Input Capacitance                             | C <sub>iss</sub>     |   |     | 1960             |       | pF   |  |  |
| Output Capacitance                            | C <sub>oss</sub>     | $V_{GS}$ = 0 V, $V_{DS}$ = 25 V, F = 1 MHz  |     | 370              |       |      |  |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>     |   |     | 200              |       |      |  |  |
| Total Gate Charge <sup>c</sup>                | Qg                   |   |     | 42               | 60    | nC   |  |  |
| Gate-Source Charge <sup>c</sup>               | Q <sub>gs</sub>      | $V_{DS}$ = 40 V, $V_{GS}$ = 10 V, $I_D$ = 40 A  |     | 7                |       |      |  |  |
| Gate-Drain Charge <sup>c</sup>                | Q <sub>gd</sub>      |   |     | 13               |       |      |  |  |
| Gate Resistance                               | R <sub>g</sub>       |   | 0.5 |                  | 2.7   | Ω    |  |  |
| Turn-On Delay Time <sup>c</sup>               | t <sub>d(on)</sub>   |   |     | 12               | 20    | ns   |  |  |
| Rise Time <sup>c</sup>                        | t <sub>r</sub>       | $V_{DD} = 40 \text{ V}, R_{I} = 1.0 \Omega$   |     | 52               | 80    |      |  |  |
| Turn-Off Delay Time <sup>c</sup>              | t <sub>d(off)</sub>  | $V_{DD}$ = 40 V, $R_L$ = 1.0 $\Omega$<br>$I_D \cong$ 40 A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 $\Omega$ |     | 25               | 38    |      |  |  |
| Fall Time <sup>c</sup>                        | t <sub>f</sub>       |   |     | 10               | 15    |      |  |  |
| Source-Drain Diode Ratings and                | Characteristic       | c (T <sub>C</sub> = 25°C)   |     |                  |       |      |  |  |
| Pulsed Current                                | I <sub>SM</sub>      |   |     |                  | 60    | А    |  |  |
| Diode Forward Voltage <sup>b</sup>            | V <sub>SD</sub>      | I <sub>F</sub> = 40 A, V <sub>GS</sub> = 0 V  |     | 1.0              | 1.5   | V    |  |  |
| Source-Drain Reverse Recovery Time            | t <sub>rr</sub>      | I <sub>F</sub> = 40 A, di/dt = 100 A/μs   |     | 45               | 70    | ns   |  |  |

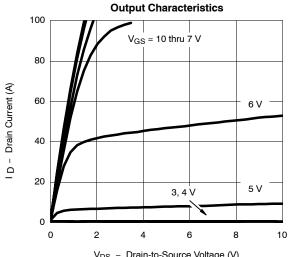
#### Notes

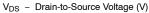
- hotes a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ . c. Independent of operating temperature.

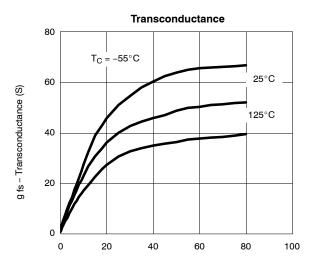




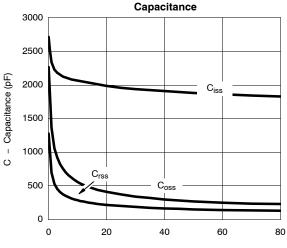
## TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



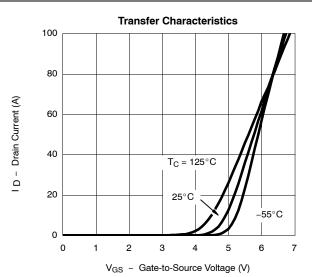


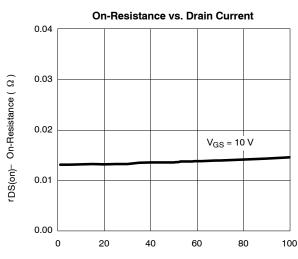


I<sub>D</sub> - Drain Current (A)

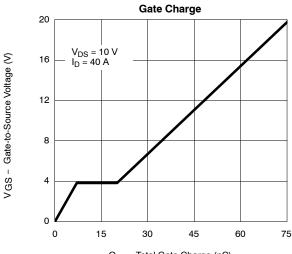


V<sub>DS</sub> - Drain-to-Source Voltage (V)





I<sub>D</sub> - Drain Current (A)

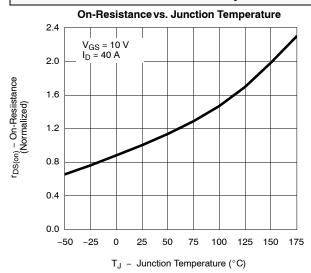


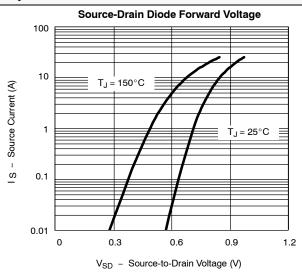
Q<sub>g</sub> - Total Gate Charge (nC)

## **Vishay Siliconix**

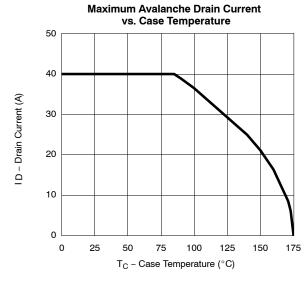


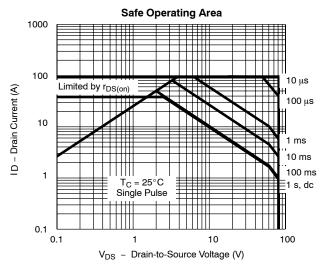
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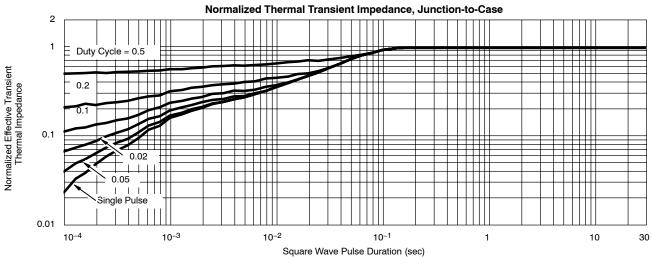




### THERMAL RATINGS









Vishay

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