

OptiMOS™3 Power-Transistor

Features

- Ideal for high frequency switching and sync. rec.
- Optimized technology for DC/DC converters
- Excellent gate charge x R DS(on) product (FOM)
- · Superior thermal resistance
- N-channel, normal level
- 100% avalanche tested
- Pb-free plating; RoHS compliant
- Qualified according to JEDEC 1) for target applications
- Halogen-free according to IEC61249-2-21

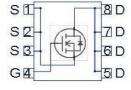
| Туре | BSC123N08NS3 G |
|---------|---|
| | 1 0 8 7 6 5 1 2 3 4 4 1 5 1 5 1 5 1 5 1 5 1 5 1 5 1 5 1 5 |
| Package | PG-TDSON-8 |
| Marking | 123N08NS |

Product Summary

| V _{DS} | 80 | ٧ |
|---------------------------|------|----|
| $R_{\mathrm{DS(on),max}}$ | 12.3 | mΩ |
| ID | 55 | Α |







| Maximum ratings, at T | ′ _i =25 °C, | unless | otherwise | specified |
|-----------------------|------------------------|--------|-----------|-----------|
|-----------------------|------------------------|--------|-----------|-----------|

| Parameter | Symbol | Conditions | Value | Unit |
|------------------------------------|----------------------|--|-------|------|
| Continuous drain current | I _D | V _{GS} =10 V, T _C =25 °C | 55 | А |
| | | V _{GS} =10 V, T _C =100 °C | 35 | |
| | | V _{GS} =10 V, T _A =25 °C, R _{thJA} =50 K/W ²⁾ | 11 | |
| Pulsed drain current ³⁾ | / _{D,pulse} | T _C =25 °C | 220 | |
| Avalanche energy, single pulse | E _{AS} | $I_{\rm D}$ =33 A, $R_{\rm GS}$ =25 Ω | 70 | mJ |
| Gate source voltage | V _{GS} | | ±20 | V |

¹⁾ J-STD20 and JESD22

 $^{^{2)}}$ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm2 (one layer, 70 μ m thick) copper area for drain connection. PCB is vertical in still air.

³⁾ See figure 3 for more detailed information

⁴⁾ See figure 13 for more detailed information



Maximum ratings, at $T_{\rm j}$ =25 °C, unless otherwise specified

| Parameter | Symbol | Conditions | Value | Unit |
|-------------------------------------|-------------------------|---|-----------|------|
| Power dissipation | P_{tot} | T _C =25 °C | 66 | W |
| | | T _A =25 °C, R _{thJA} =50 K/W ²⁾ | 2.5 | |
| Operating and storage temperature | $T_{\rm j},T_{\rm stg}$ | | -55 150 | °C |
| IEC climatic category; DIN IEC 68-1 | | | 55/150/56 | |

| Parameter | Symbol | Conditions | | Values | | Unit |
|-----------|--------|------------|------|--------|------|------|
| | | | min. | typ. | max. | |

Thermal characteristics

| Thermal resistance, junction - case | R _{thJC} | bottom | - | - | 1.9 | K/W |
|-------------------------------------|-------------------|--|---|---|-----|-----|
| | | top | | | 18 | |
| Device on PCB | $R_{ m thJA}$ | minimal footprint | 1 | - | 62 | |
| | | 6 cm ² cooling area ²⁾ | - | - | 50 | |

Electrical characteristics, at \mathcal{T}_j =25 °C, unless otherwise specified

Static characteristics

| Drain-source breakdown voltage | V _{(BR)DSS} | V _{GS} =0 V, I _D =1 mA | 80 | - | - | V |
|----------------------------------|----------------------|--|----|------|------|----|
| Gate threshold voltage | $V_{\rm GS(th)}$ | $V_{\rm DS} = V_{\rm GS}, I_{\rm D} = 33 \mu {\rm A}$ | 2 | 2.8 | 3.5 | |
| Zero gate voltage drain current | I _{DSS} | V _{DS} =80 V, V _{GS} =0 V, T _j =25 °C | ı | 0.1 | 1 | μA |
| | | V _{DS} =80 V, V _{GS} =0 V, T _j =125 °C | - | 10 | 100 | |
| Gate-source leakage current | I _{GSS} | V _{GS} =20 V, V _{DS} =0 V | - | 10 | 100 | nA |
| Drain-source on-state resistance | R _{DS(on)} | V _{GS} =10 V, I _D =33 A | - | 10.3 | 12.3 | mΩ |
| | | V _{GS} =6 V, I _D =16 A | - | 14.1 | 24 | |
| Gate resistance | R _G | | - | 2 | - | Ω |
| Transconductance | g fs | V _{DS} >2 I _D R _{DS(on)max} , I _D =33 A | 22 | 44 | - | s |



| Parameter | Symbol | Conditions | | Values | | Unit |
|---|----------------------|--|------|--------|------|------|
| | | | min. | typ. | max. | |
| Dynamic characteristics | | | | | | |
| Input capacitance | C iss | Vcs=0 V. Vps=40 V. | - | 1430 | 1870 | pF |
| Output capacitance | C _{oss} | V _{GS} =0 V, V _{DS} =40 V, f=1 MHz | - | 385 | 517 | |
| Reverse transfer capacitance | C _{rss} | | - | 15 | - | |
| Turn-on delay time | t _{d(on)} | | - | 12 | - | ns |
| Rise time | t _r | V _{DD} =40 V, V _{GS} =10 V, | - | 18 | - | 1 |
| Turn-off delay time | $t_{\text{d(off)}}$ | $I_{\rm D}$ =20 A, $R_{\rm G}$ =1.6 Ω | - | 19 | - | 1 |
| Fall time | t _f |] | - | 4 | - | 1 |
| Gate Charge Characteristics ⁵⁾ | - | | | - | | |
| Gate to source charge | Q _{gs} | | - | 6.3 | - | nC |
| Gate charge at threshold | Q _{g(th)} | | - | 3.6 | - |] |
| Gate to drain charge | Q _{gd} | V _{DD} =40 V, I _D =20 A, | - | 3.8 | - | 1 |
| Switching charge | Q _{sw} | V _{GS} =0 to 10 V | - | 6.5 | - | |
| Gate charge total | Qg |] | - | 19 | 25 | |
| Gate plateau voltage | V _{plateau} |] | - | 4.9 | - | ٧ |
| Output charge | Q _{oss} | V _{DD} =40 V, V _{GS} =0 V | - | 25 | 34 | |
| Reverse Diode | • | | | | | |
| Diode continuous forward current | Is | T -25 °C | - | - | 55 | А |
| Diode pulse current | I _{S,pulse} | T _C =25 °C | - | - | 220 | 1 |
| Diode forward voltage | V _{SD} | V _{GS} =0 V, I _F =33 A, T _j =25 °C | - | 0.9 | 1.2 | ٧ |
| Reverse recovery time | t _{rr} | | - | 45 | - | ns |
| Reverse recovery charge | Q _{rr} | V_R =40 V, I_F =20A, d i_F /d t =100 A/ μ s | - | 54 | - | nC |

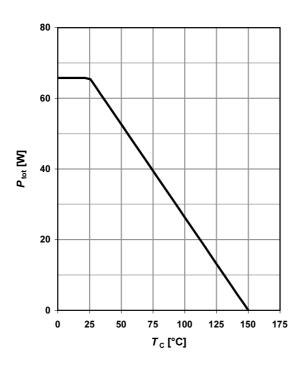
⁵⁾ See figure 16 for gate charge parameter definition

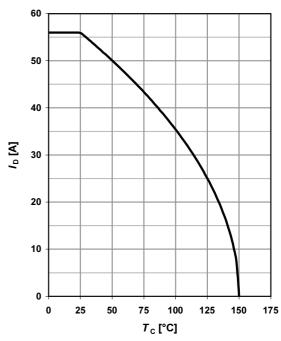


1 Power dissipation

P_{tot} =f(T_{C})

2 Drain current

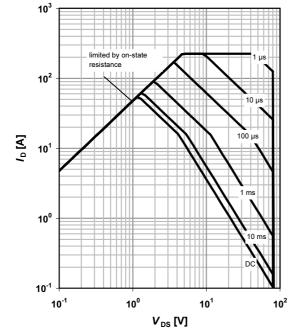




3 Safe operating area

$$I_D$$
=f(V_{DS}); T_C =25 °C; D =0

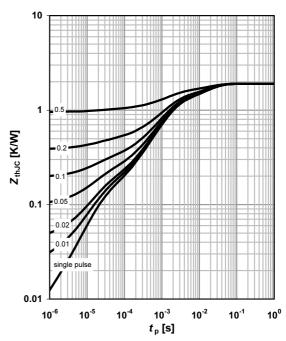
parameter: t_p



4 Max. transient thermal impedance

$$Z_{thJC}$$
=f(t_p)

parameter: $D = t_p/T$

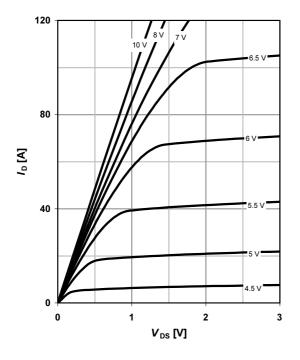




5 Typ. output characteristics

 I_D =f(V_{DS}); T_j =25 °C

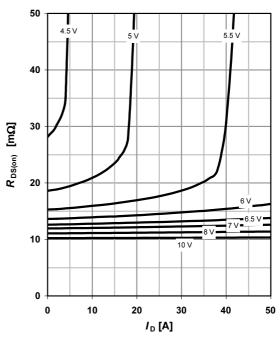
parameter: V_{GS}



6 Typ. drain-source on resistance

 $R_{DS(on)}$ =f(I_D); T_j =25 °C

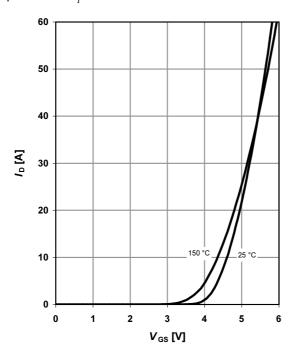
parameter: V_{GS}



7 Typ. transfer characteristics

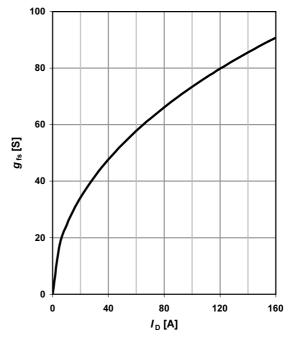
 $I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$

parameter: $T_{\rm j}$



8 Typ. forward transconductance

$$g_{fs}$$
=f(I_D); T_j =25 °C



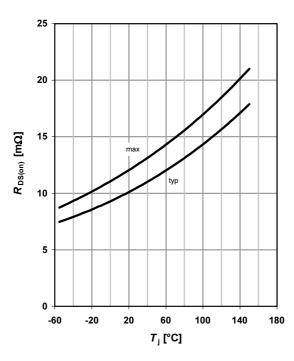


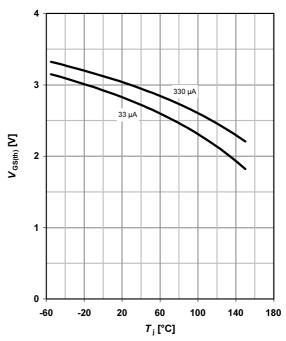
9 Drain-source on-state resistance

$R_{DS(on)}$ =f(T_j); I_D =33 A; V_{GS} =10 V

10 Typ. gate threshold voltage

$$V_{GS(th)}$$
=f(T_j); V_{GS} = V_{DS}



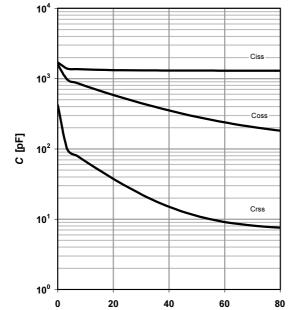


11 Typ. capacitances

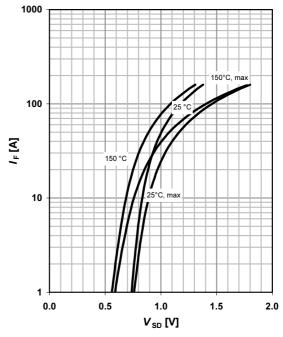
 $C = f(V_{DS}); V_{GS} = 0 V; f = 1 MHz$

12 Forward characteristics of reverse diode

 $I_{\text{F}} = f(V_{\text{SD}})$ parameter: T_{j}



 $V_{DS}[V]$

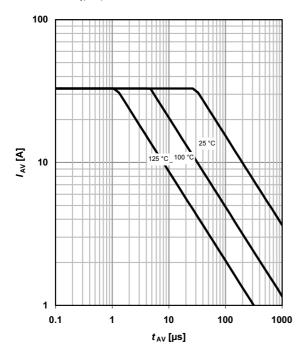




13 Avalanche characteristics

 I_{AS} =f(t_{AV}); R_{GS} =25 Ω

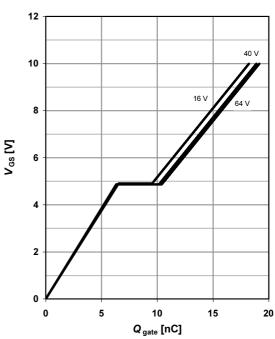
parameter: $T_{j(start)}$



14 Typ. gate charge

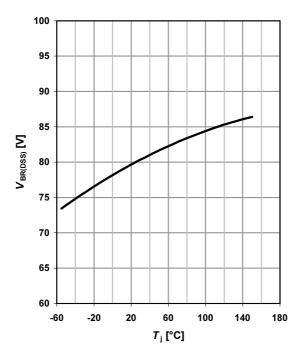
 $V_{\rm GS}$ =f(Q_{gate}); $I_{\rm D}$ =20 A pulsed

parameter: V_{DD}

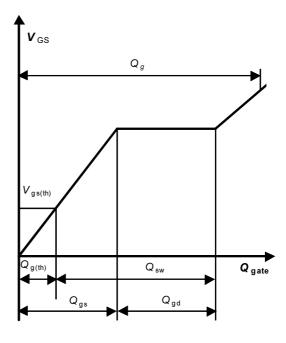


15 Drain-source breakdown voltage

 $V_{BR(DSS)}$ =f(T_j); I_D =1 mA

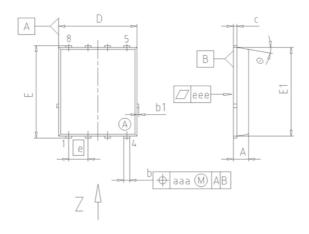


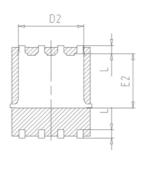
16 Gate charge waveforms

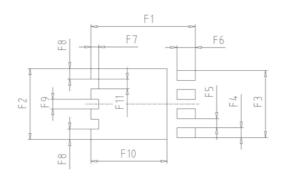


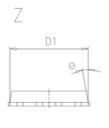


PG-TDSON-8









| DIM | MILLIM | ETERS | INCI | HES |
|------|--------|-------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.90 | 1.10 | 0.035 | 0.043 |
| b | 0.34 | 0.54 | 0.013 | 0.021 |
| b1 | 0.02 | 0.22 | 0.001 | 0.008 |
| С | 0.15 | 0.35 | 0.006 | 0.014 |
| D=D1 | 4.95 | 5.35 | 0.195 | 0.211 |
| D2 | 4.20 | 4.40 | 0.165 | 0.173 |
| E | 5.95 | 6.35 | 0.234 | 0.250 |
| E1 | 5.70 | 6.10 | 0.224 | 0.240 |
| E2 | 3.40 | 3.80 | 0.134 | 0.150 |
| e | 1.2 | 27 | 0.0 | 050 |
| N | | 8 | | 8 |
| L | 0.45 | 0.65 | 0.018 | 0.026 |
| | 8.5° | 11.5° | 8.5° | 11.5° |
| aaa | 0.2 | 25 | 0.0 | 010 |
| eee | 0.0 |)5 | 0.0 | 002 |
| F1 | 6.75 | 6.95 | 0.266 | 0.274 |
| F2 | 4.60 | 4.80 | 0.181 | 0.189 |
| F3 | 4.36 | 4.56 | 0.172 | 0.180 |
| F4 | 0.55 | 0.75 | 0.022 | 0.030 |
| F5 | 0.52 | 0.72 | 0.020 | 0.028 |
| F6 | 1.10 | 1.30 | 0.043 | 0.051 |
| F7 | 0.40 | 0.60 | 0.016 | 0.024 |
| F8 | 0.60 | 0.80 | 0.024 | 0.031 |
| F9 | 0.53 | 0.73 | 0.021 | 0.029 |
| F10 | 4.90 | 5.10 | 0.193 | 0.201 |
| F11 | 0.53 | 0.73 | 0.021 | 0.029 |

| DOCUME | NT NO. |
|--------------------|------------|
| Z8B0000 | 3332 |
| SCALE | 0 |
| 0 2.5 | 2.5 5mm |
| EUROPEAN PI | ROJECTION |
| | |
| 1SSUE D 08-03-2 | |
| REVIS | ION |



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