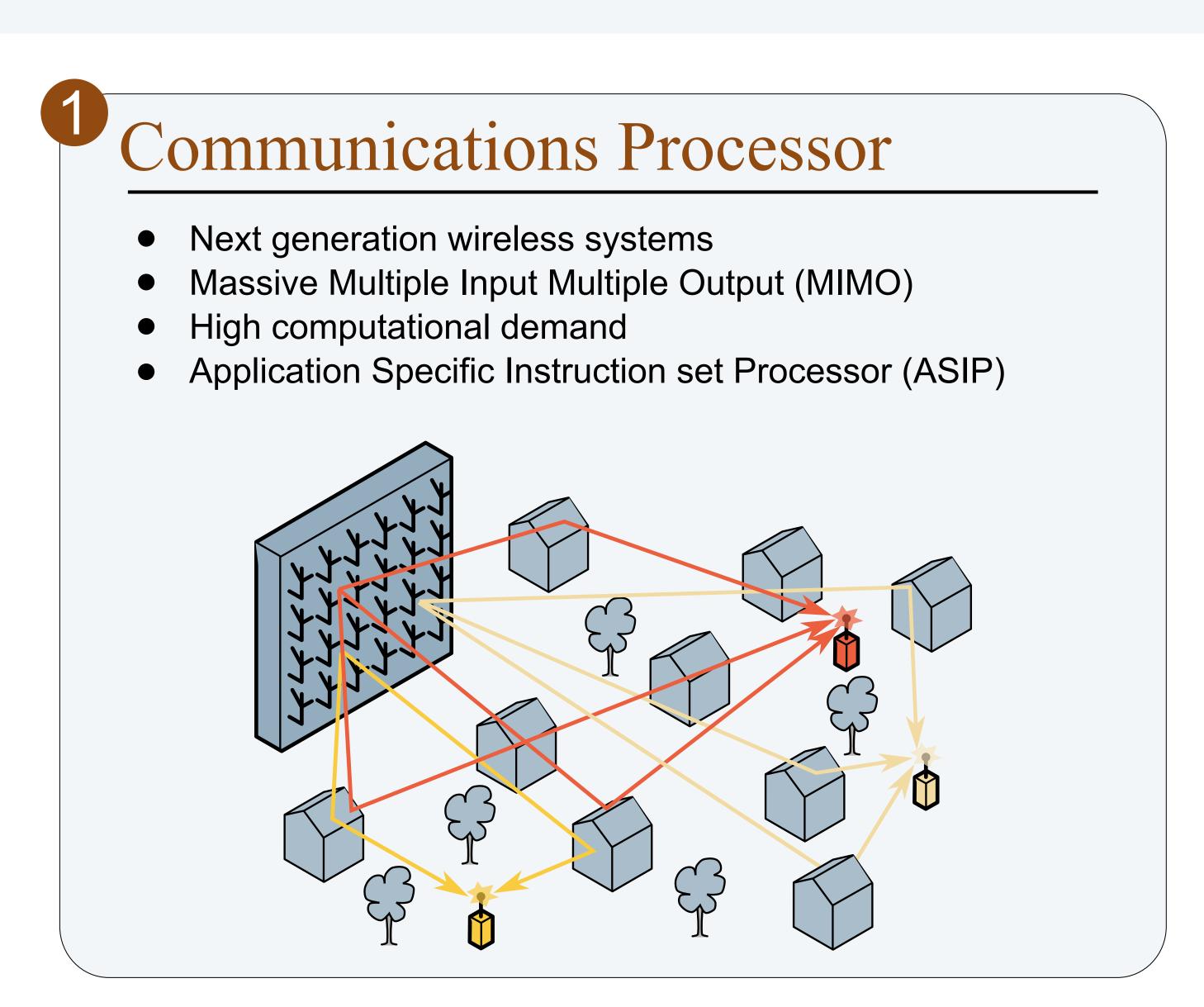
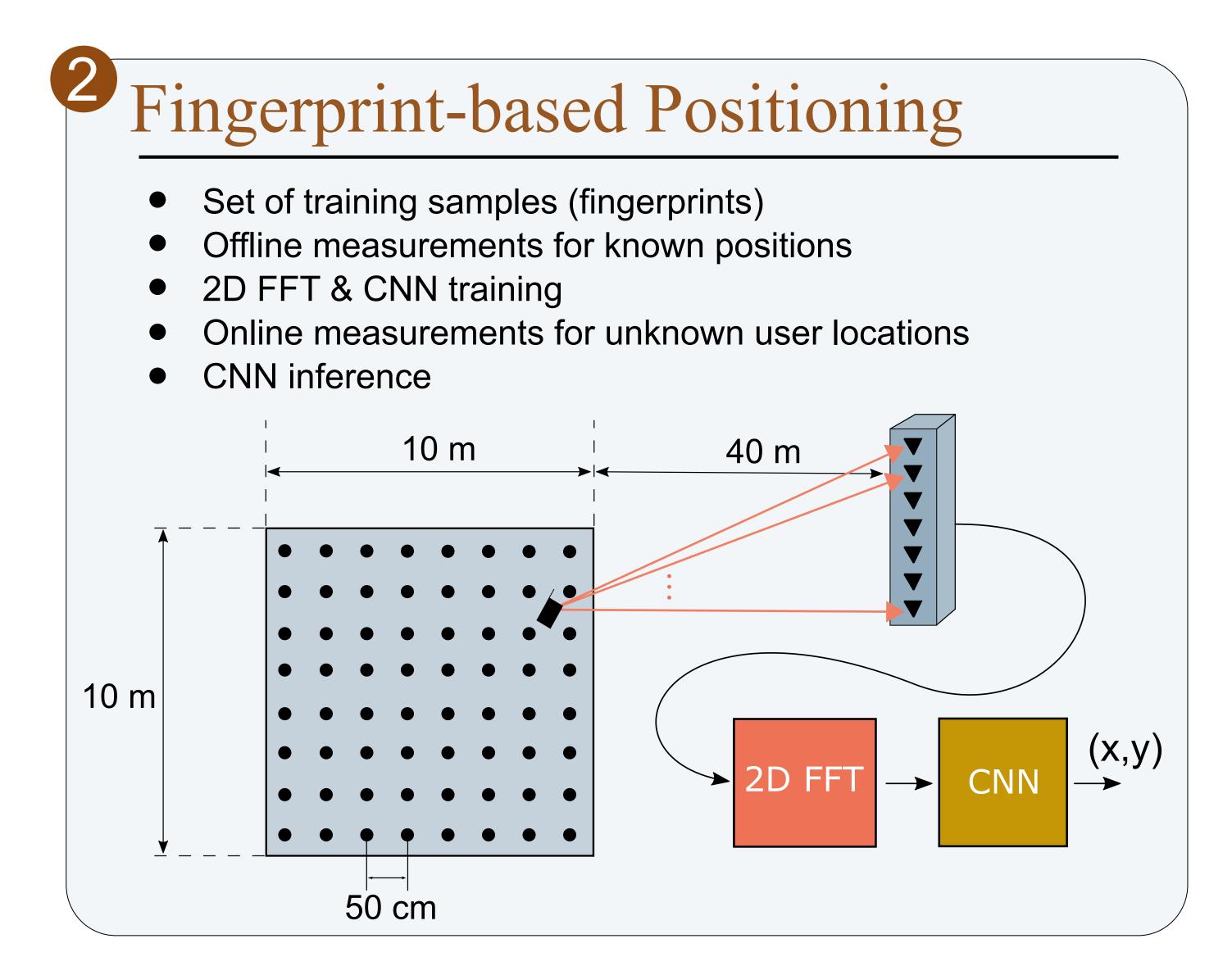
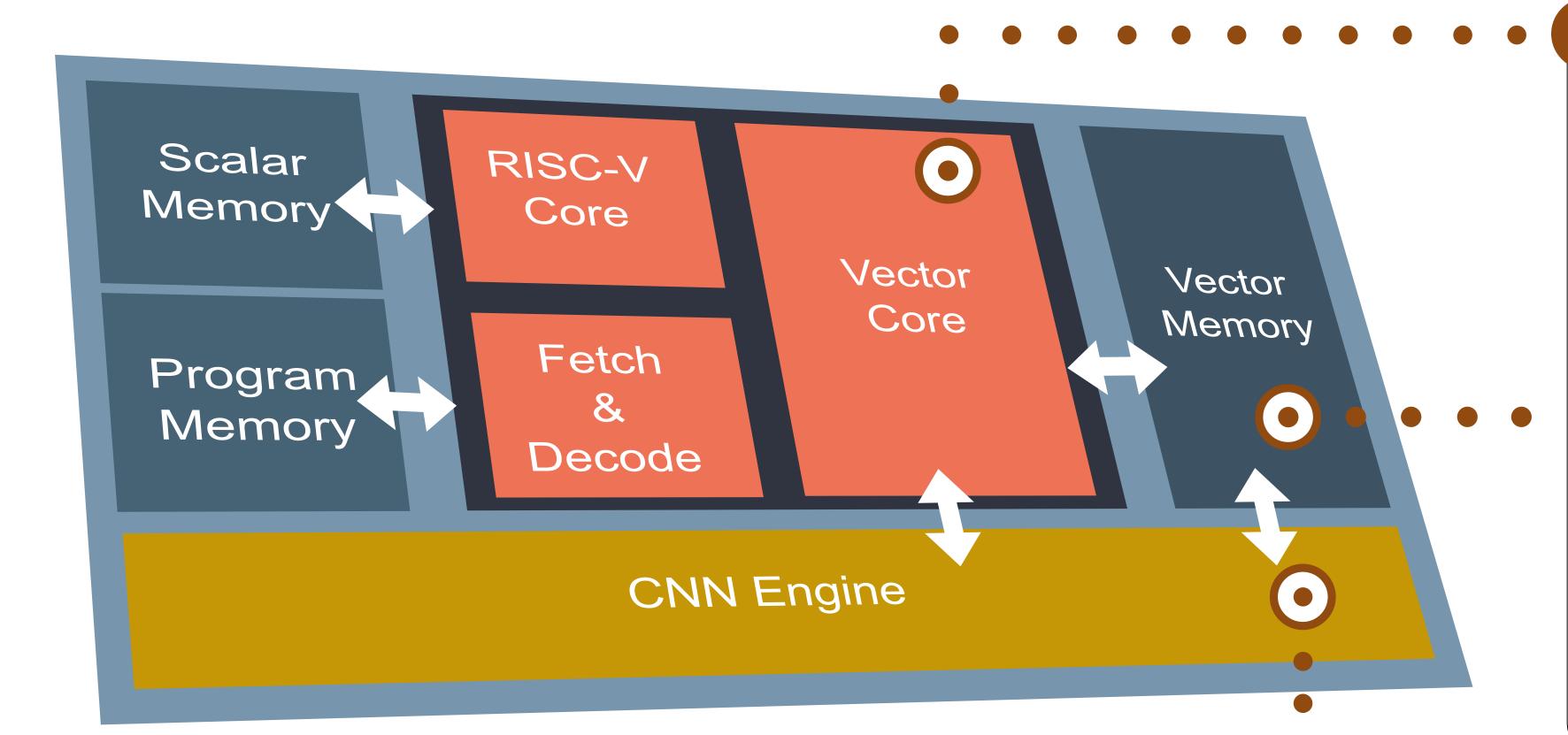


An Application Specific Processor for CNN-Based Massive MIMO Positioning

Mohammad Attari, Jesus Rodriguez Sanchez, Liang Liu, and Steffen Malkowsky Dept. of Electrical and Information Technology, Lund University, Sweden







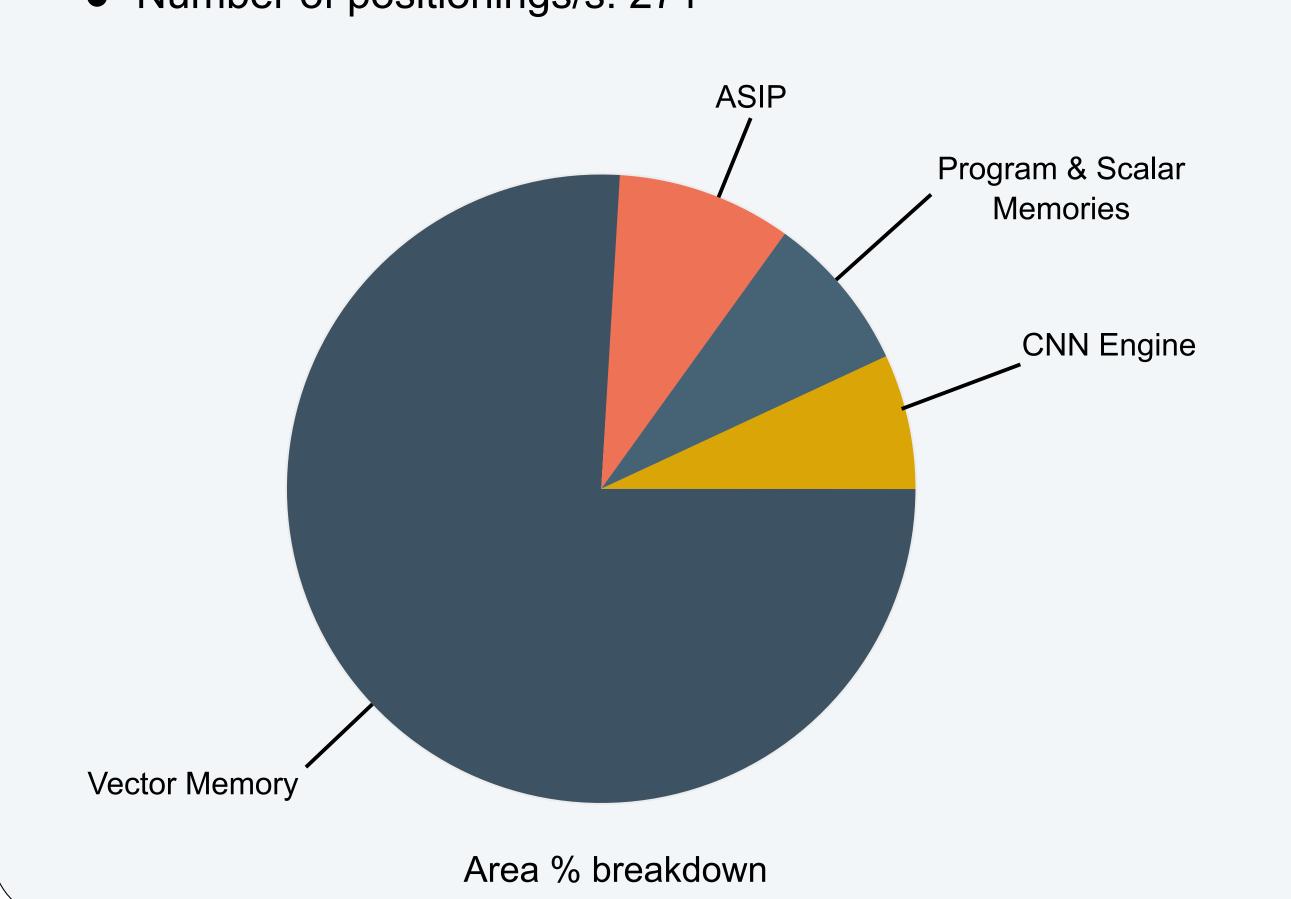
• 3 Programmable Processor

- Programmable in C
- Programmer-visible intrinsics
- 8-lane Single Instruction Multiple Data (SIMD)
- One-cycle memory access (row, column, diagonal)

void dummy_func(vcfrac* A, vcfrac* B)
{
 vcfrac va = load_row(A, 0);
 vcfrac vb = load_column(B, 3);
 vcfrac vc = a * b;
 cfrac c = dot(a, b);
}

Results & Conclusion

- Design tool: ASIP Designer from Synopsys
- Technology node: 22 nm
- Clock frequency: 555 MHz
- Average power consumption: 150 mW
- Area: 1 mm²
- Number of positionings/s: 271



CNN Accelerator Configurable CNN engine 2D convolution based on 1D primitives Convolution with 3 Primitve Units (PU) Row buffer to reduce repetitive access Vector Primitive Primitive Primitive Row Memory Unit 3 Unit 1 Unit 2 600 KB Buffer from Vector Memory / Row Buffer activation row n 17 | 16 filter row 1 partial sum row n

Reference & Funding



