## Quantum Platform View

### **Goal**

**Create a current-state snapshot of vendor QPU capabilities, software stacks, and platform APIs. Use this to guide integration strategies and future-proof the open stack against evolving hardware and software interfaces.**

### **Session Chairs**

### **ORNL: Daniel Chaves Claudino**

### **IonQ: Coleman Collins**

### **Prompts**

* What key hardware metrics (qubit count, connectivity, coherence, gate sets, error rates) do vendors publish—and in what formats?
* What type of features can be exposed by the hardware? Calibration? gate operations? other?
* What platform information should be exposed so that system administrators and operational people can make decisions on scheduling/orchestration/maintenance?
* How do different providers categorize and expose platform APIs for resource management, runtime control, and device maintenance?
* What examples can be shared for mid-circuit measurements and related error correction/mitigation activity? How do they see these changing from NISQ to FT?
* Who are the consumers/roles of this layer?
* What are the non-quantum features that you're hearing from customers that we can standardize on, which are not differentiable for business purpose? logs? storage? scheduling?
* Where do you draw the line between the job of the QPU vs the common software stack. IE: "everything else that you need to turn the QPU into a useful computational device"?