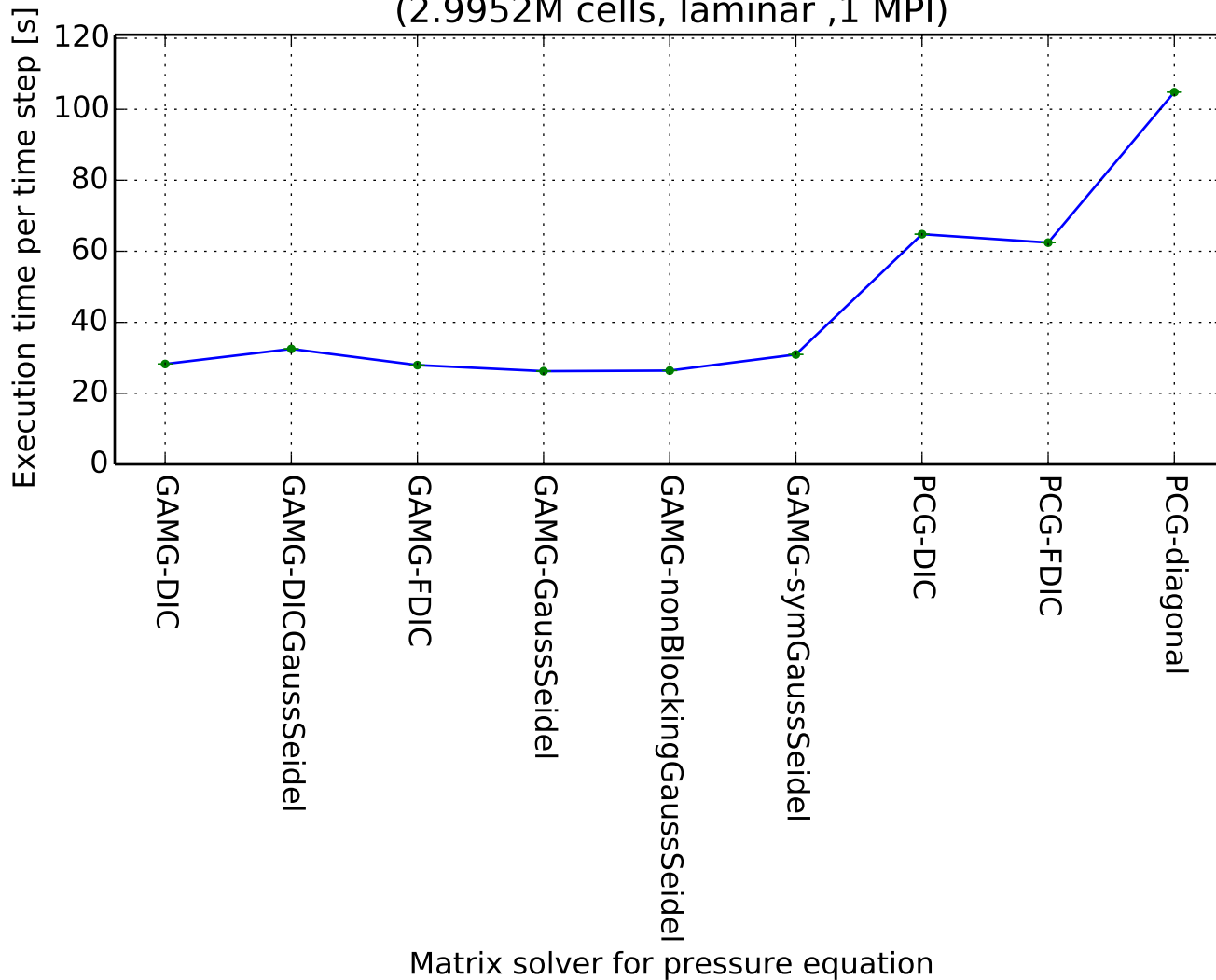
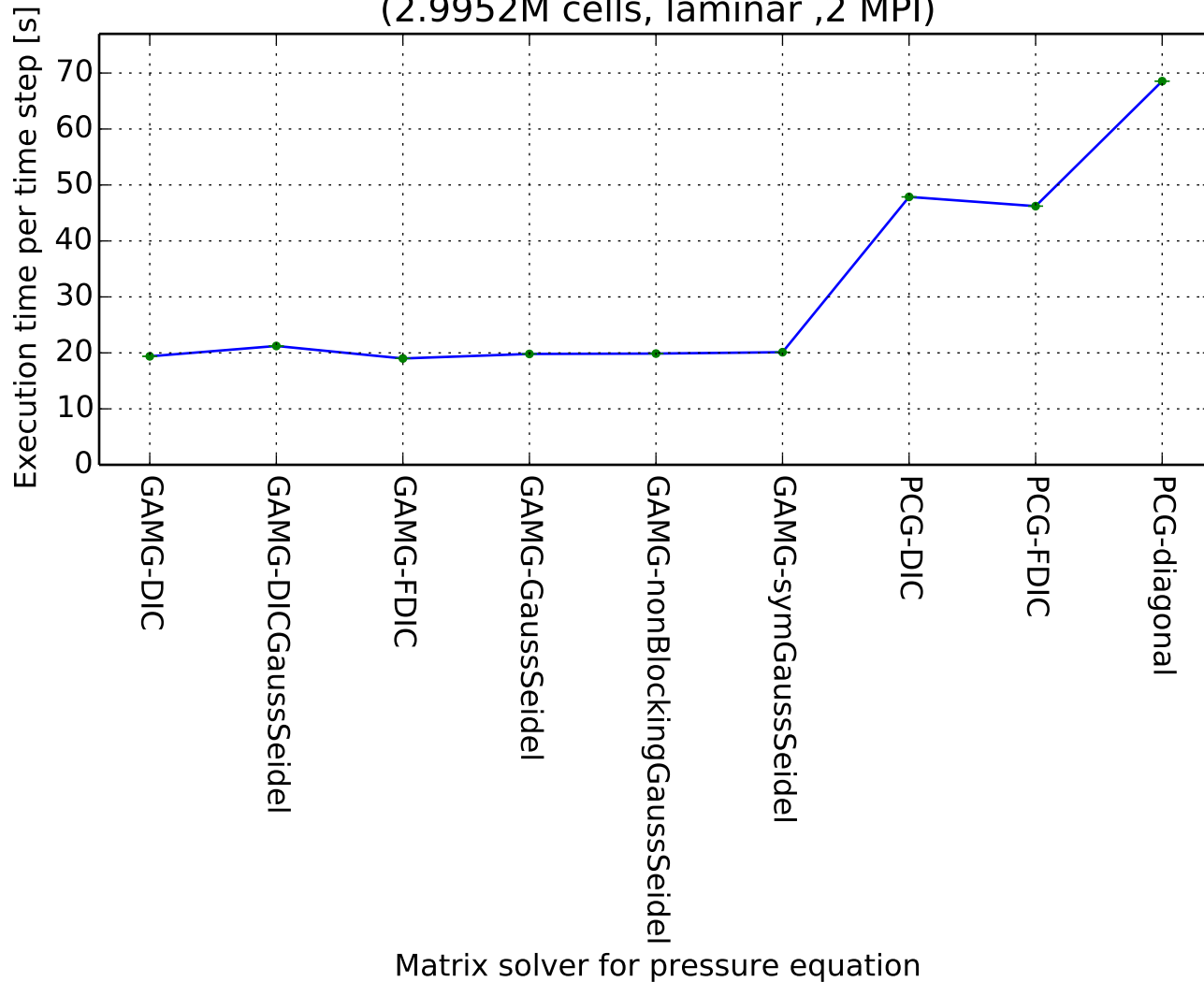


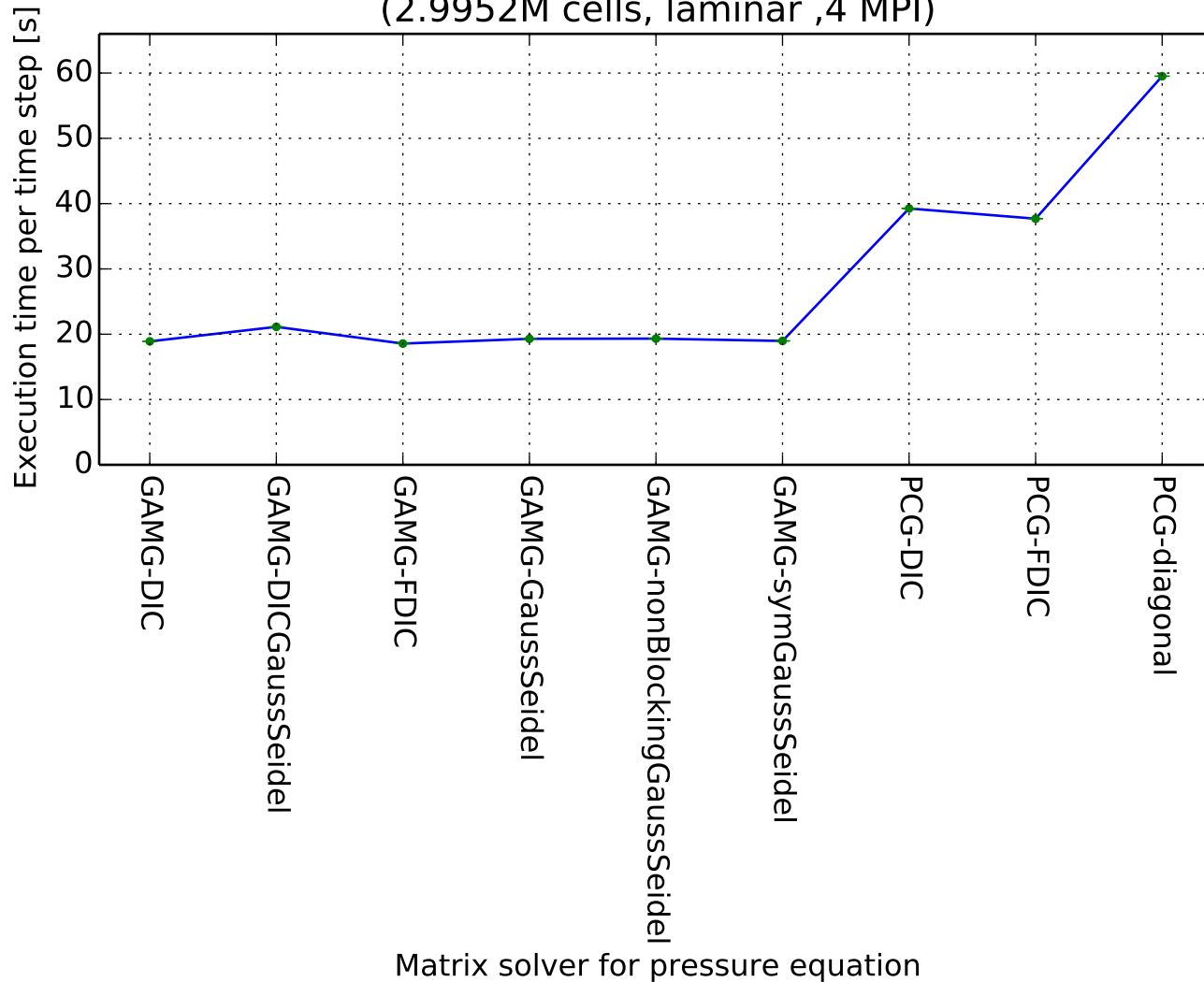
Execution time per time step  
(2.9952M cells, laminar ,1 MPI)



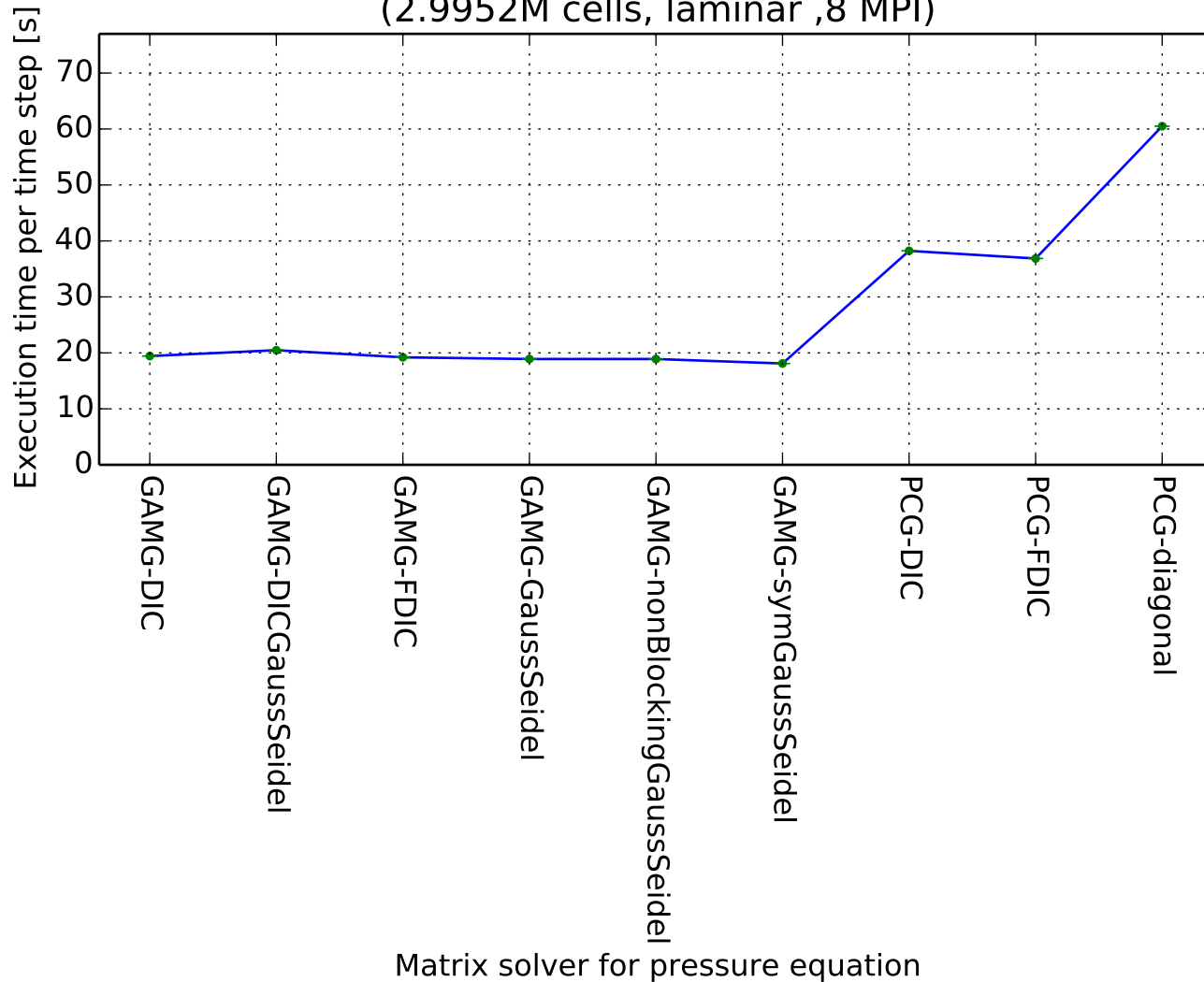
Execution time per time step  
(2.9952M cells, laminar ,2 MPI)



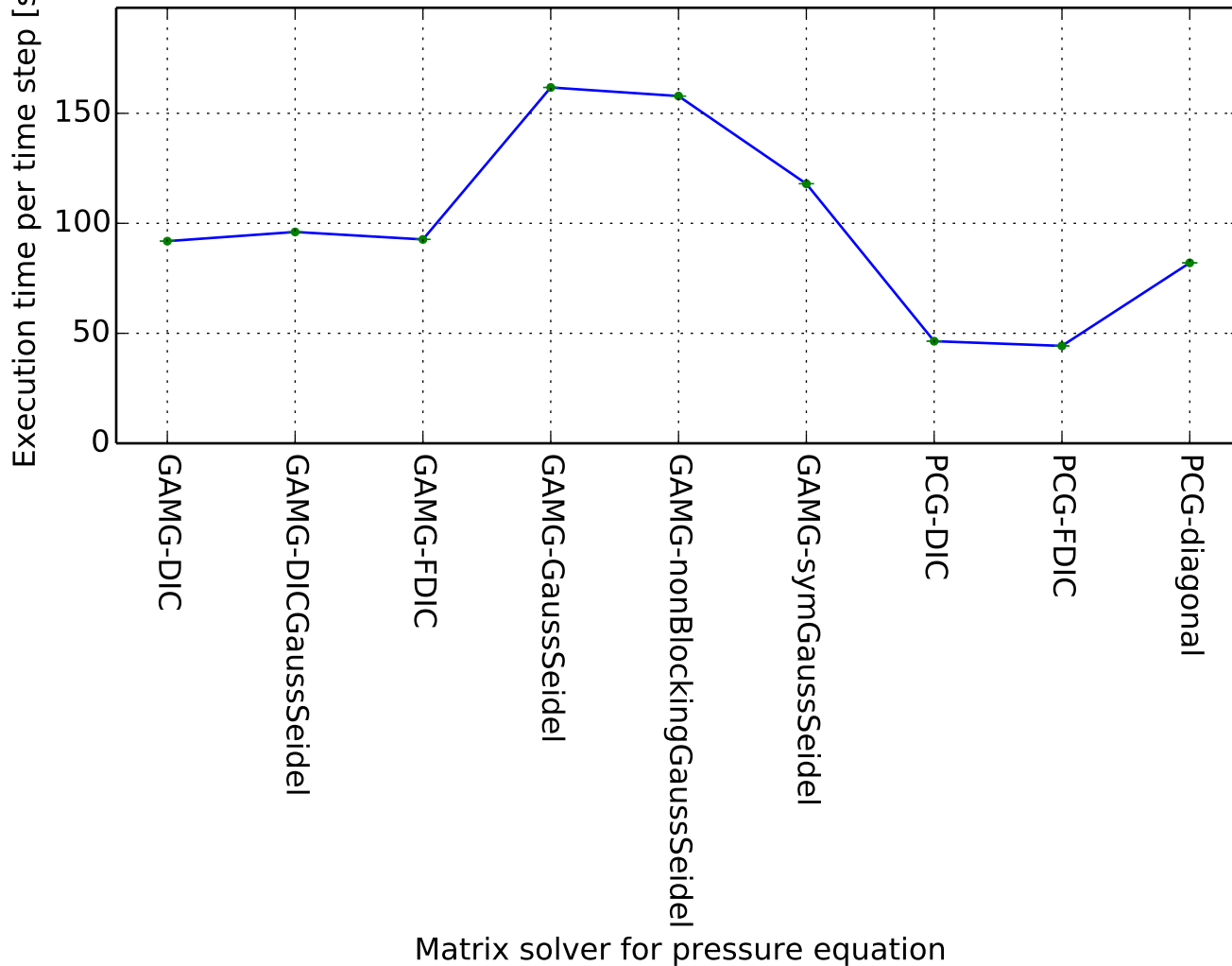
Execution time per time step  
(2.9952M cells, laminar ,4 MPI)



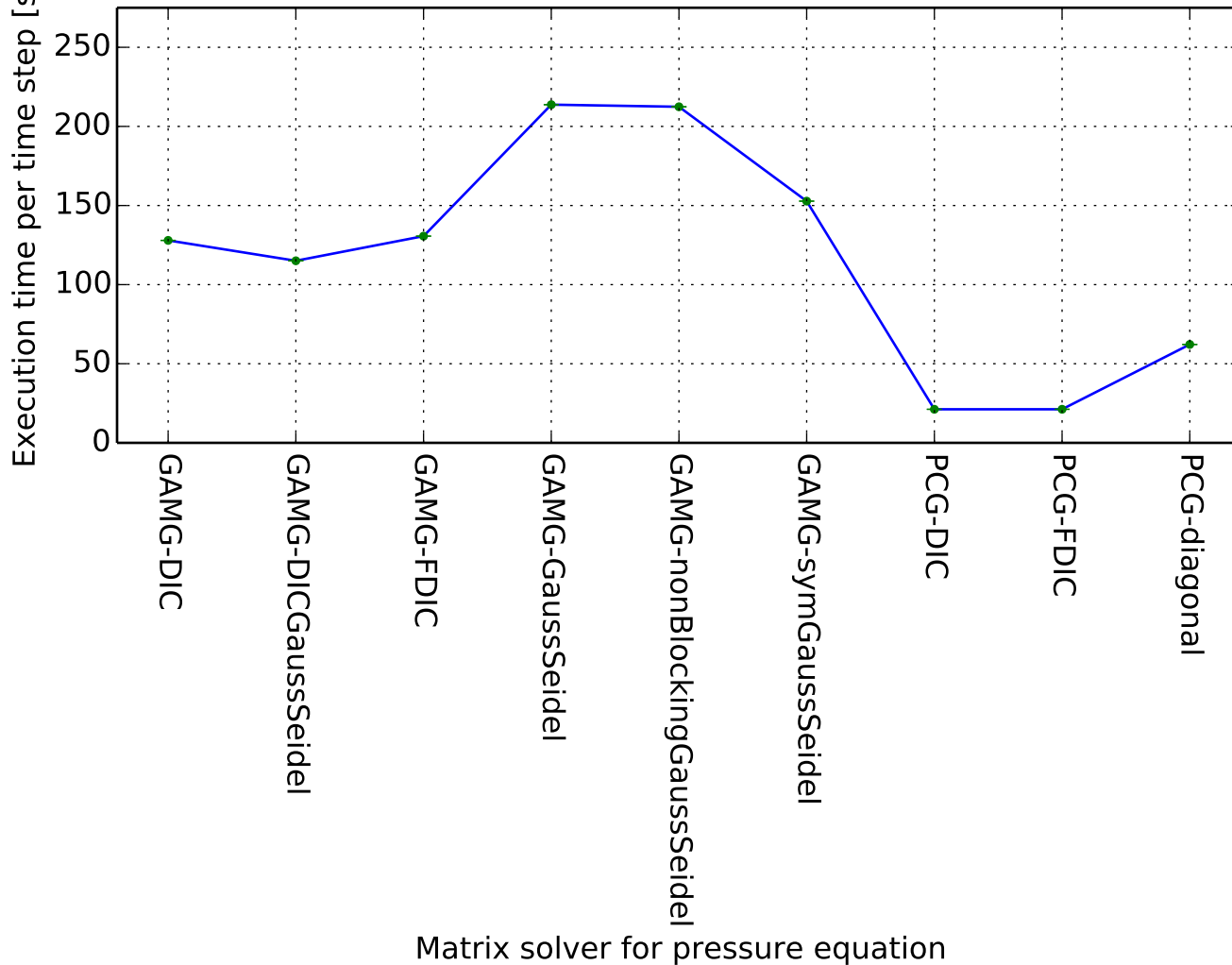
Execution time per time step  
(2.9952M cells, laminar ,8 MPI)



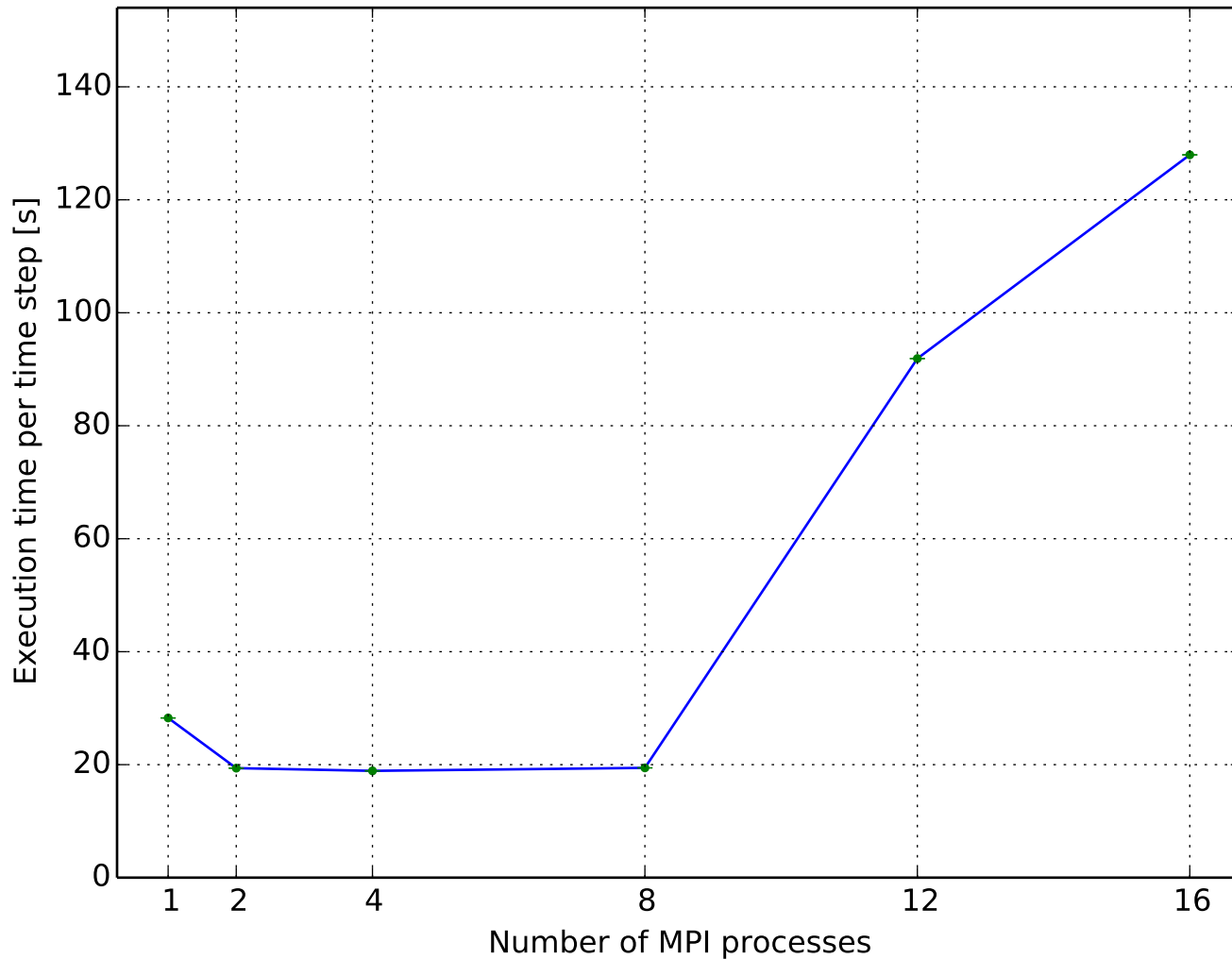
Execution time per time step  
(2.9952M cells, laminar ,12 MPI)



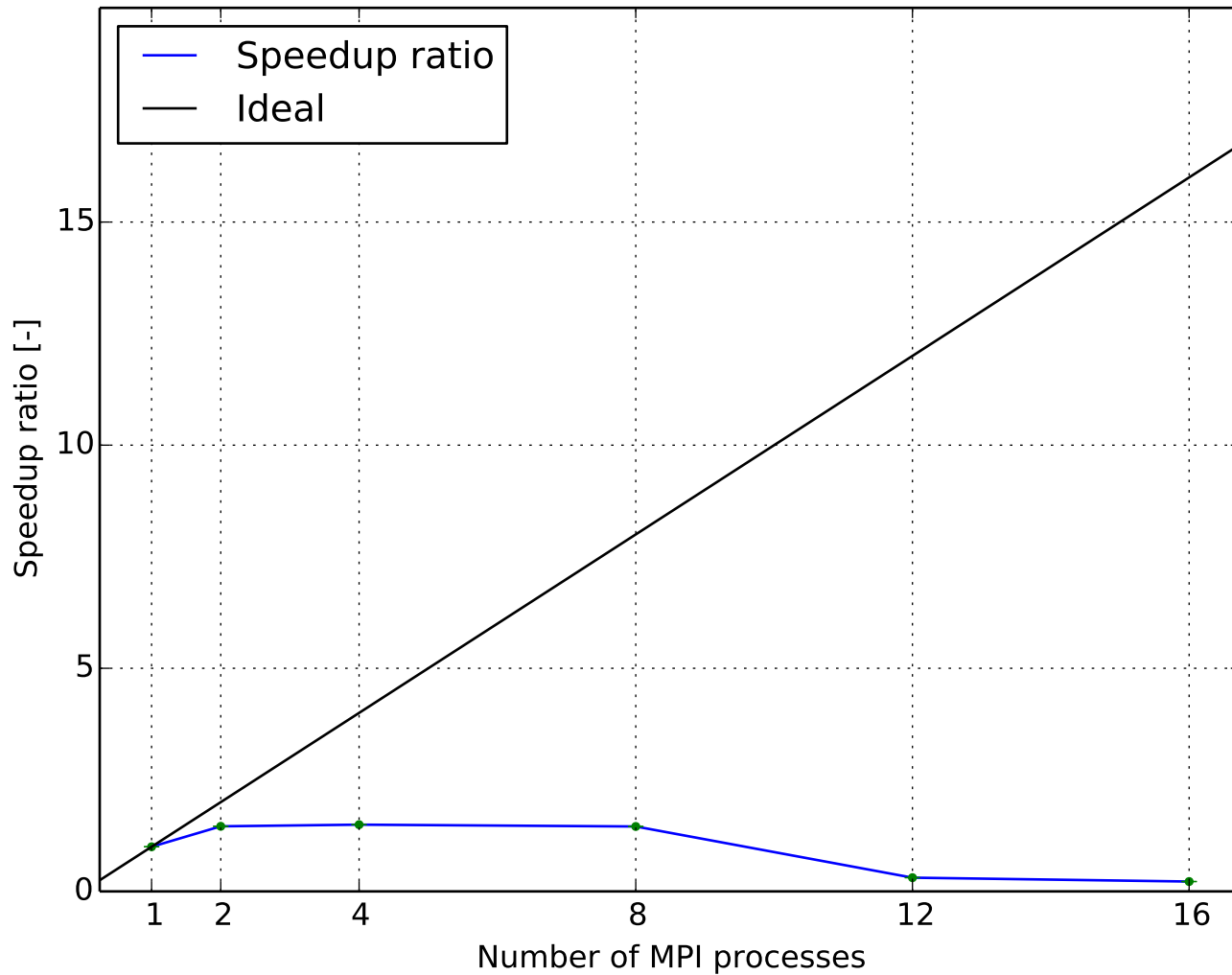
Execution time per time step  
(2.9952M cells, laminar ,16 MPI)



Execution time per time step  
(2.9952M cells, laminar ,GAMG-DIC)

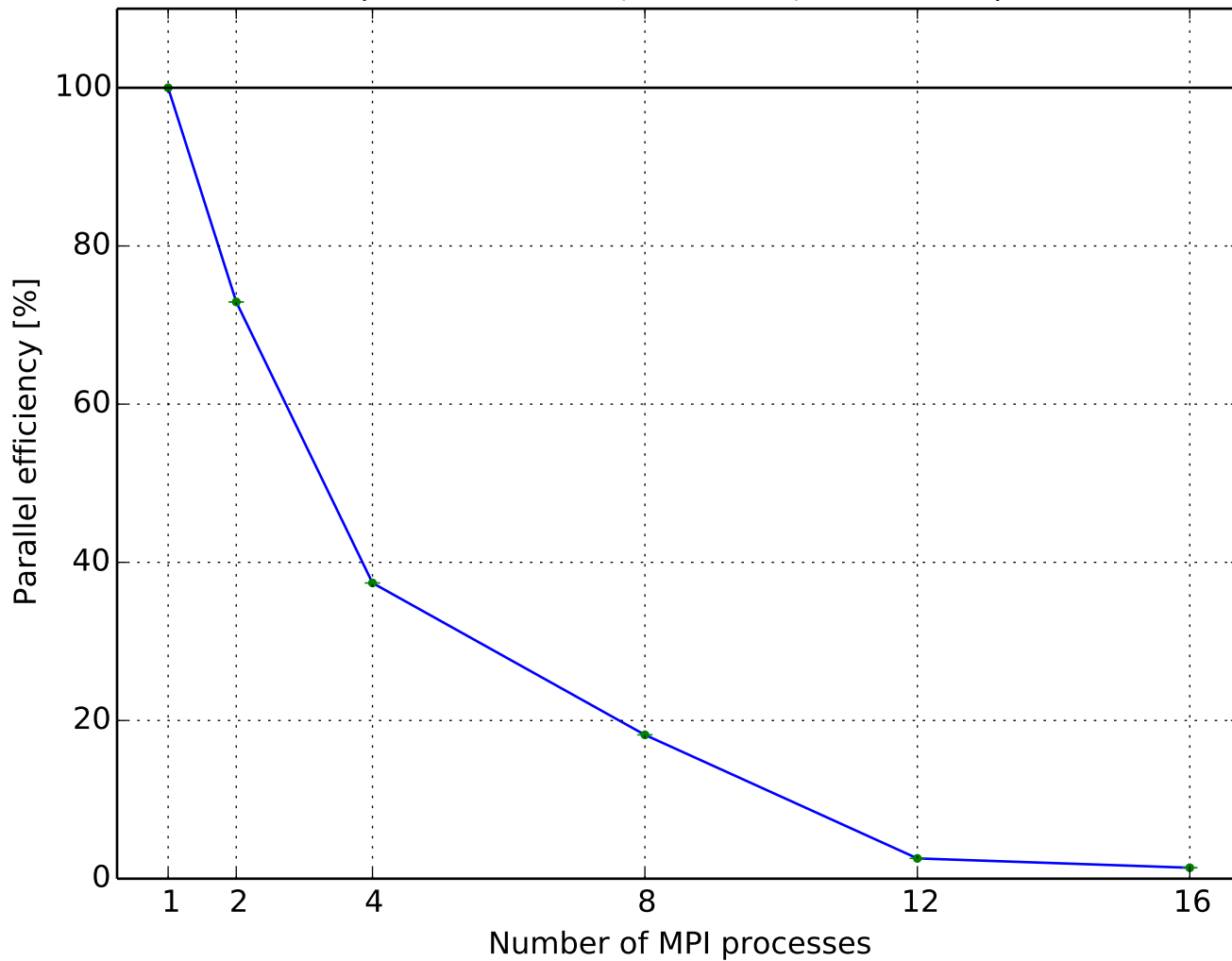


Speedup ratio  
(2.9952M cells, laminar ,GAMG-DIC)

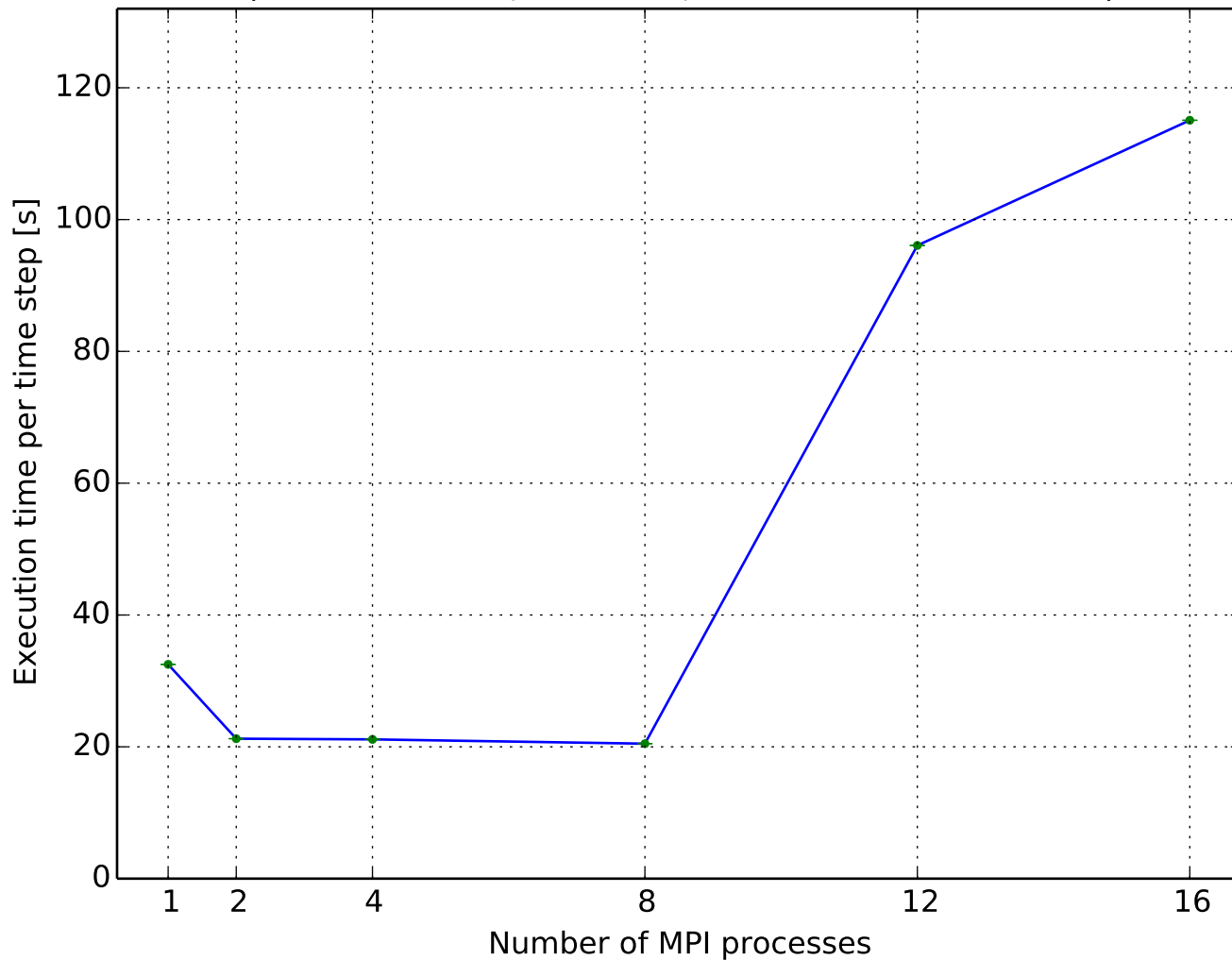




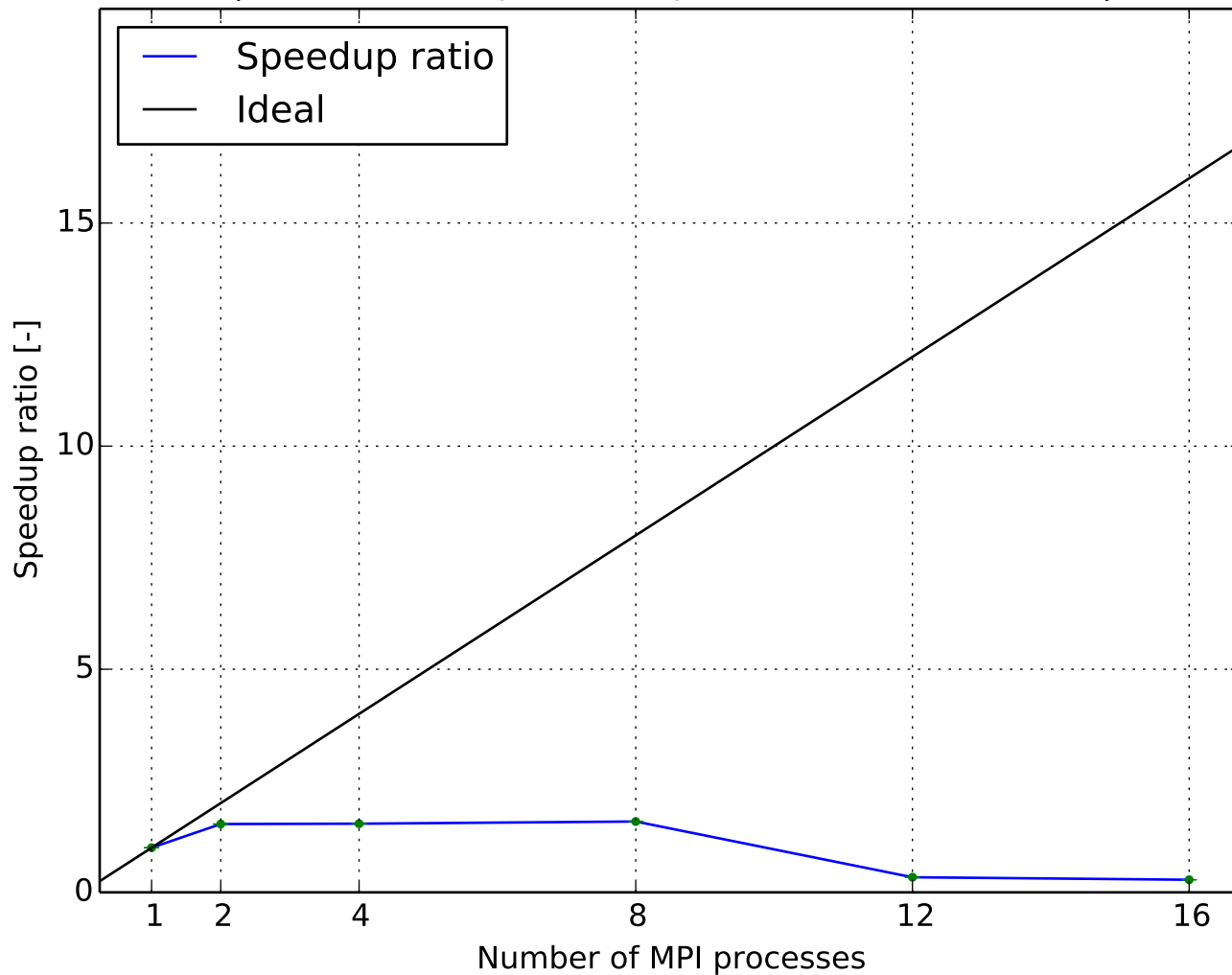
Parallel efficiency  
(2.9952M cells, laminar ,GAMG-DIC)



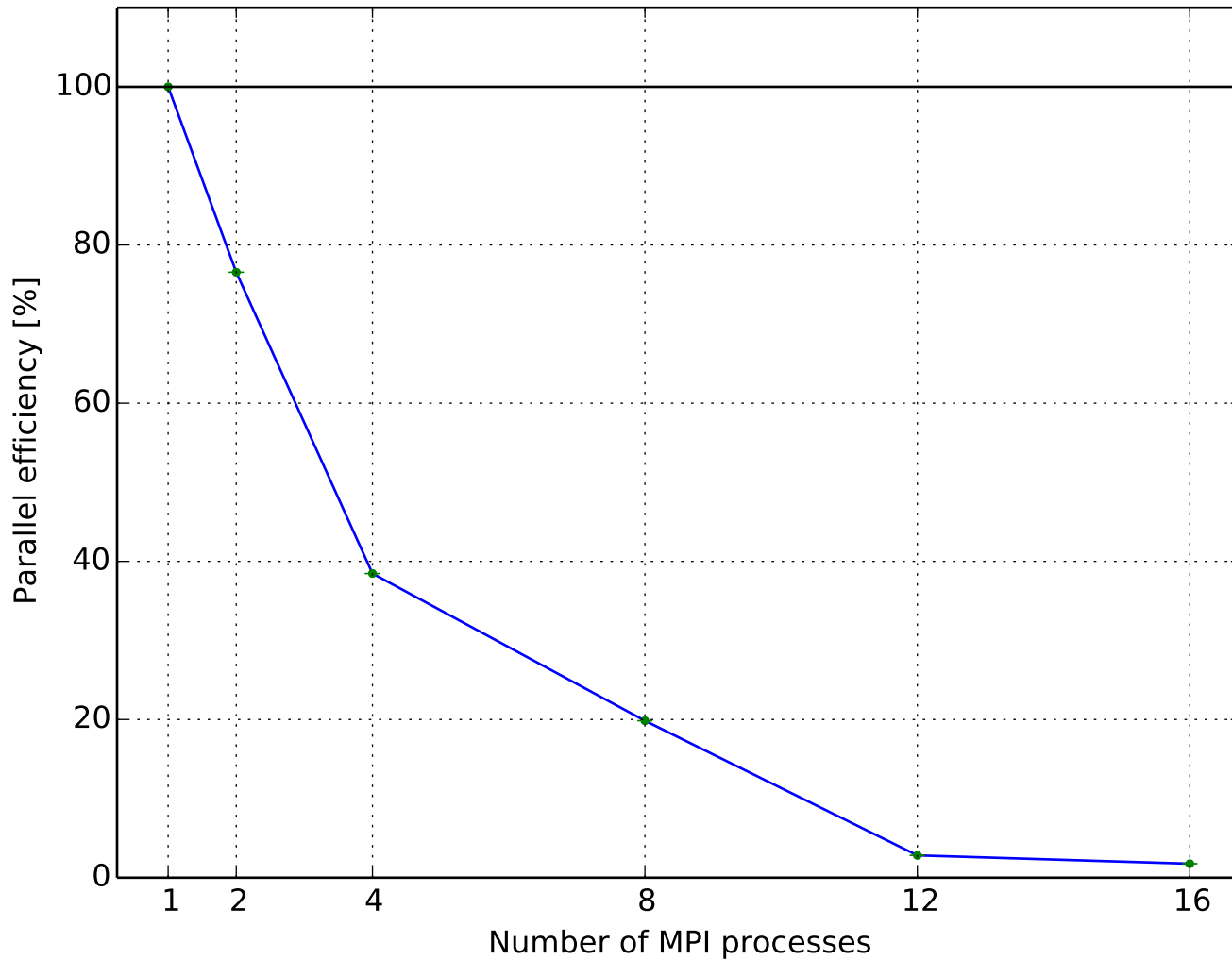
Execution time per time step  
(2.9952M cells, laminar ,GAMG-DICGaussSeidel)



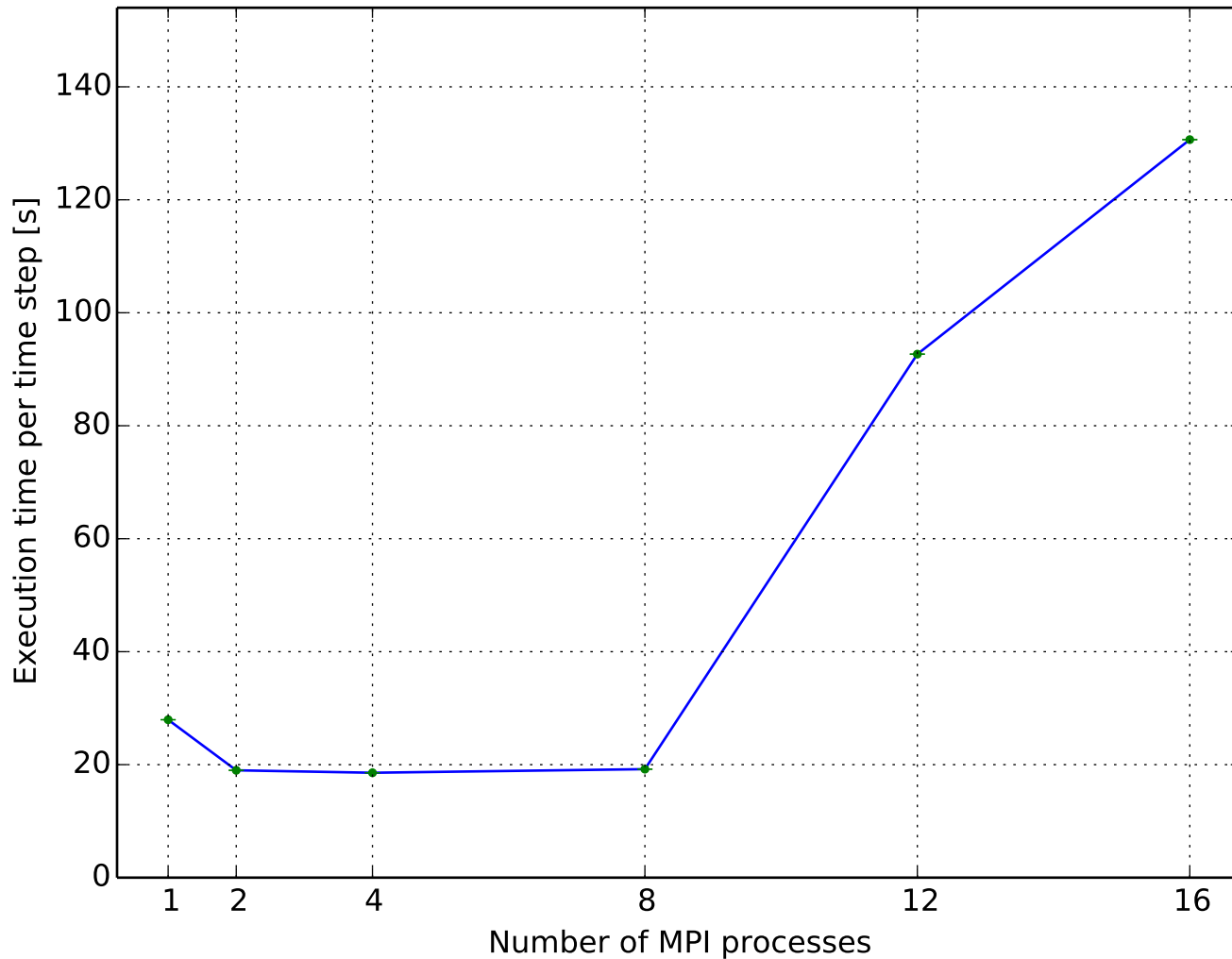
Speedup ratio  
(2.9952M cells, laminar ,GAMG-DICGaussSeidel)



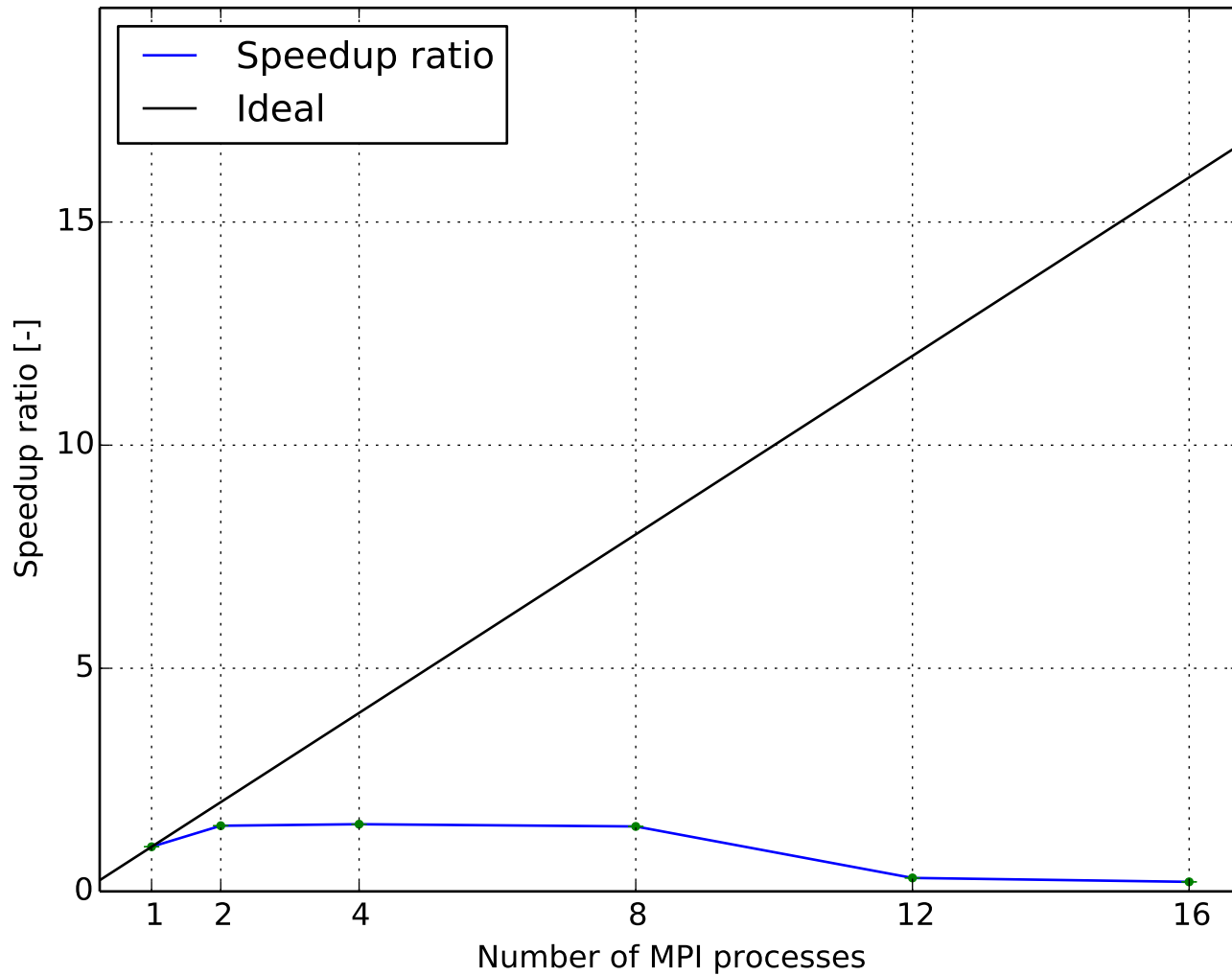
Parallel efficiency  
(2.9952M cells, laminar ,GAMG-DICGaussSeidel)



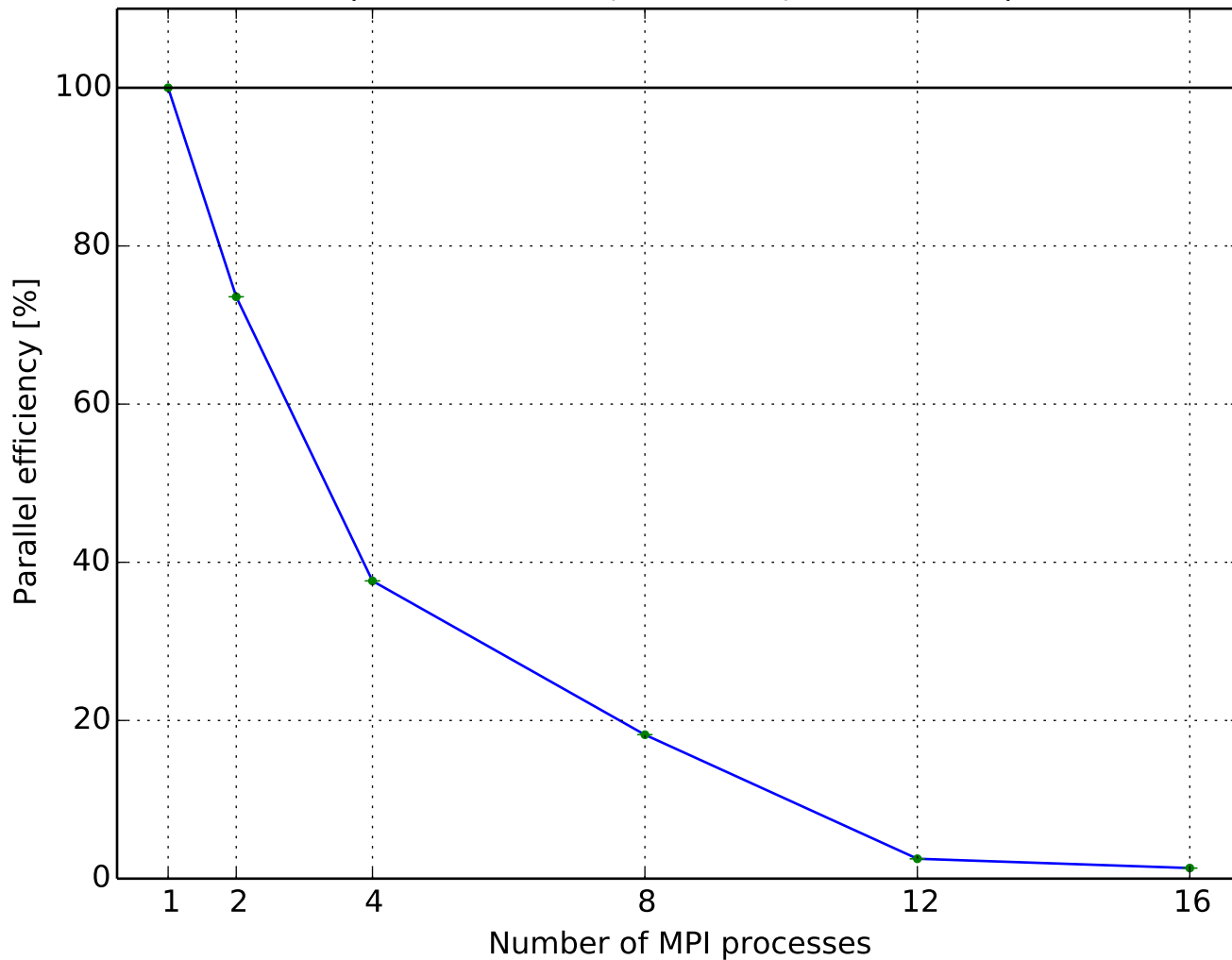
Execution time per time step  
(2.9952M cells, laminar ,GAMG-FDIC)



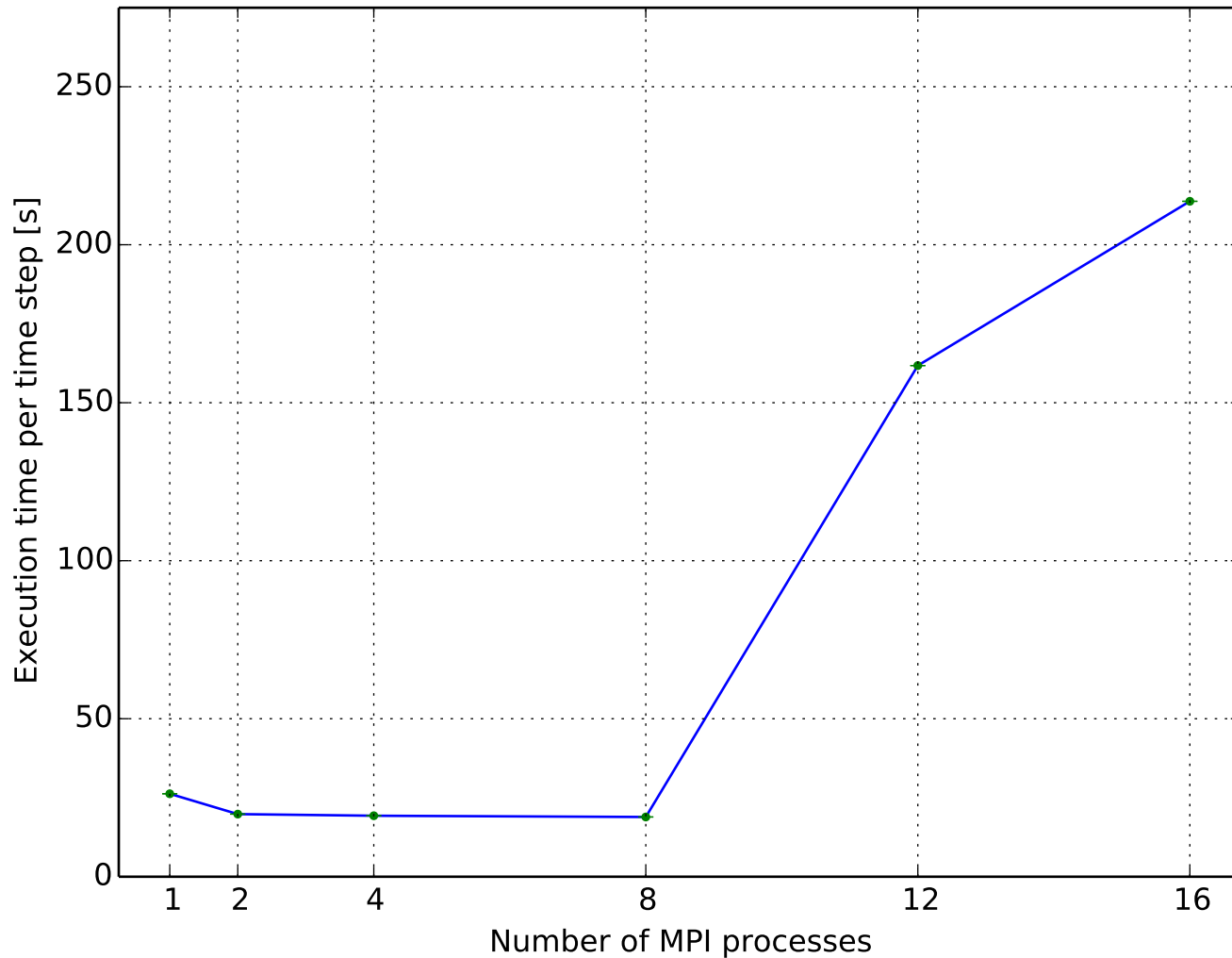
Speedup ratio  
(2.9952M cells, laminar ,GAMG-FDIC)



Parallel efficiency  
(2.9952M cells, laminar ,GAMG-FDIC)

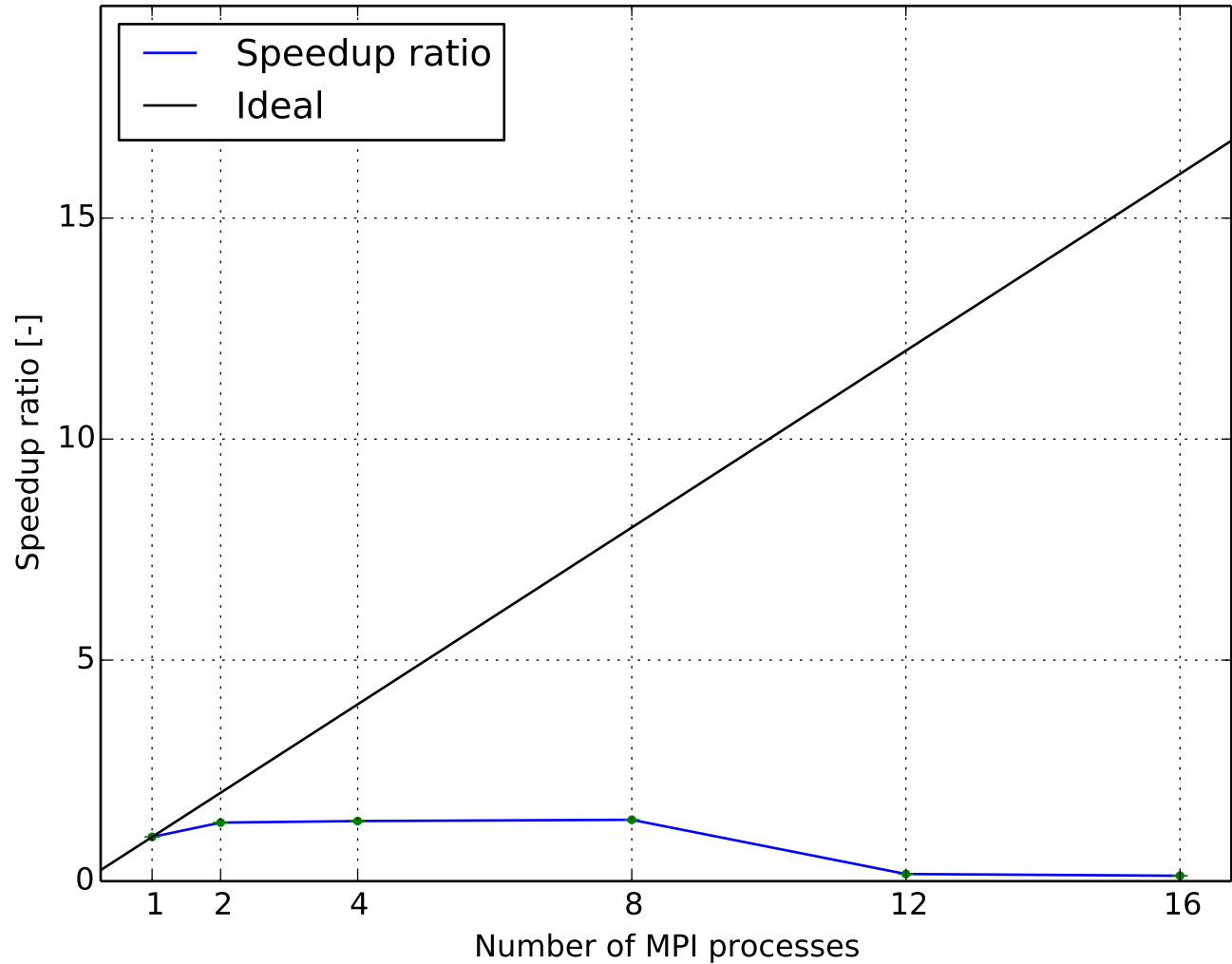


Execution time per time step  
(2.9952M cells, laminar ,GAMG-GaussSeidel)

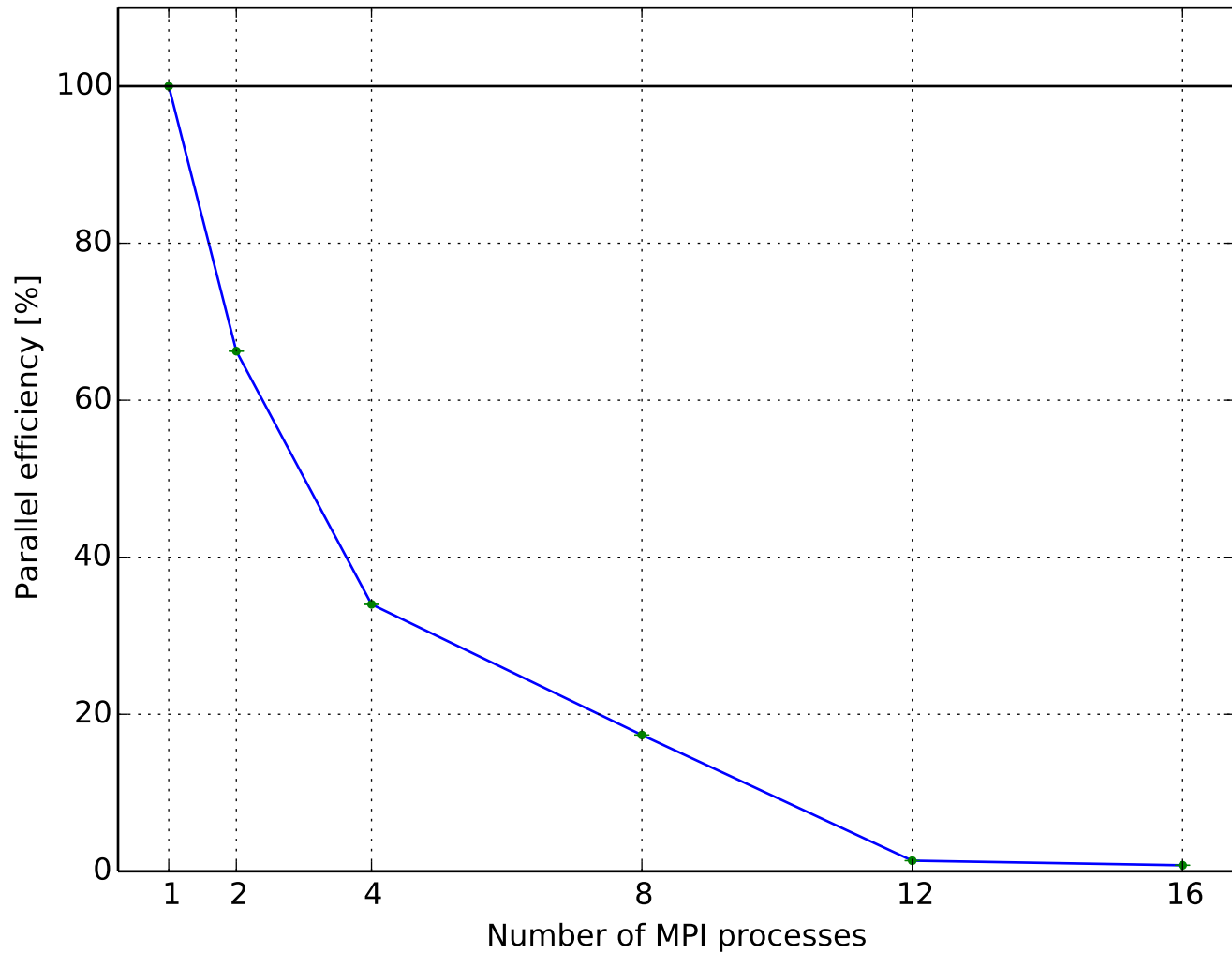




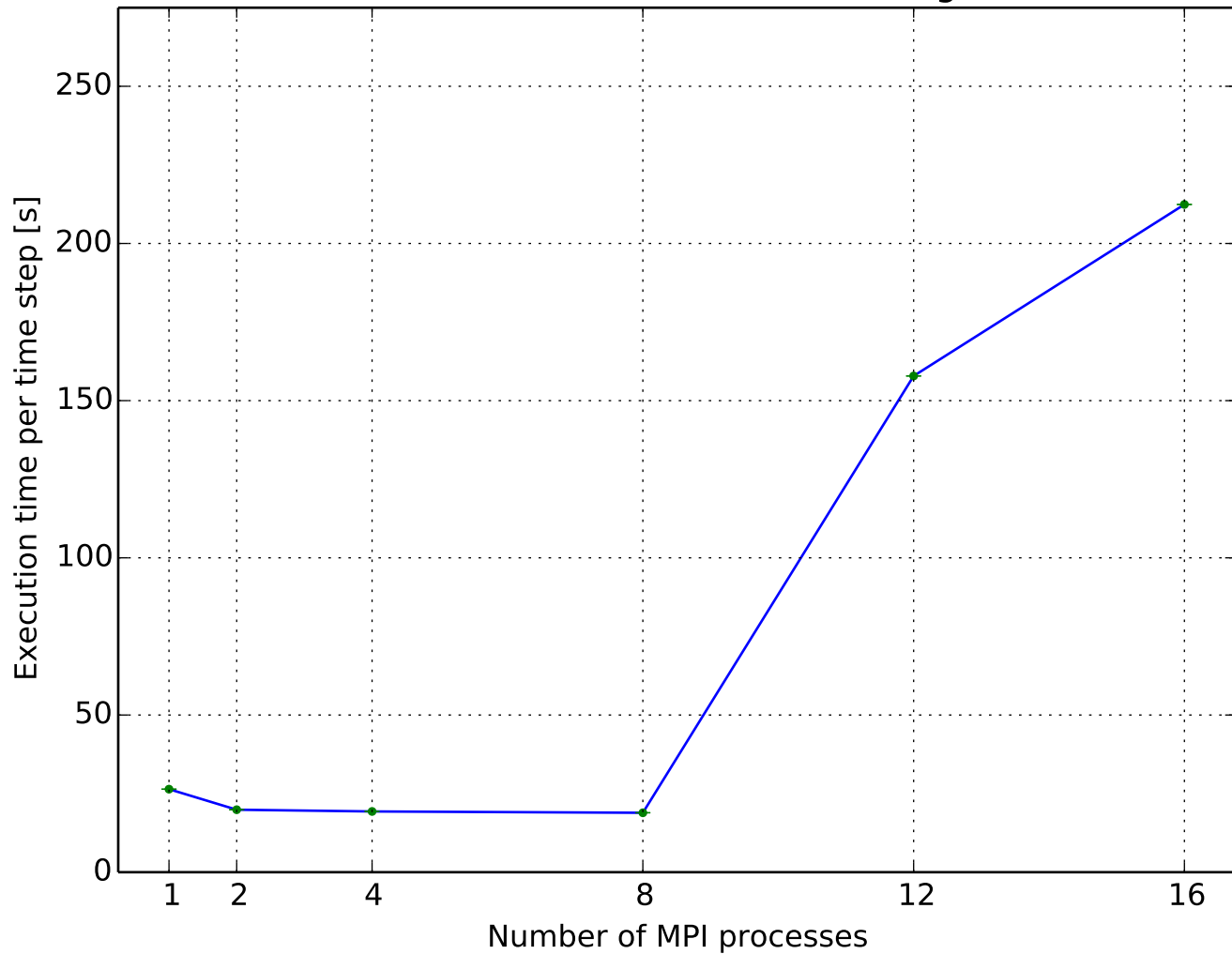
Speedup ratio  
(2.9952M cells, laminar ,GAMG-GaussSeidel)



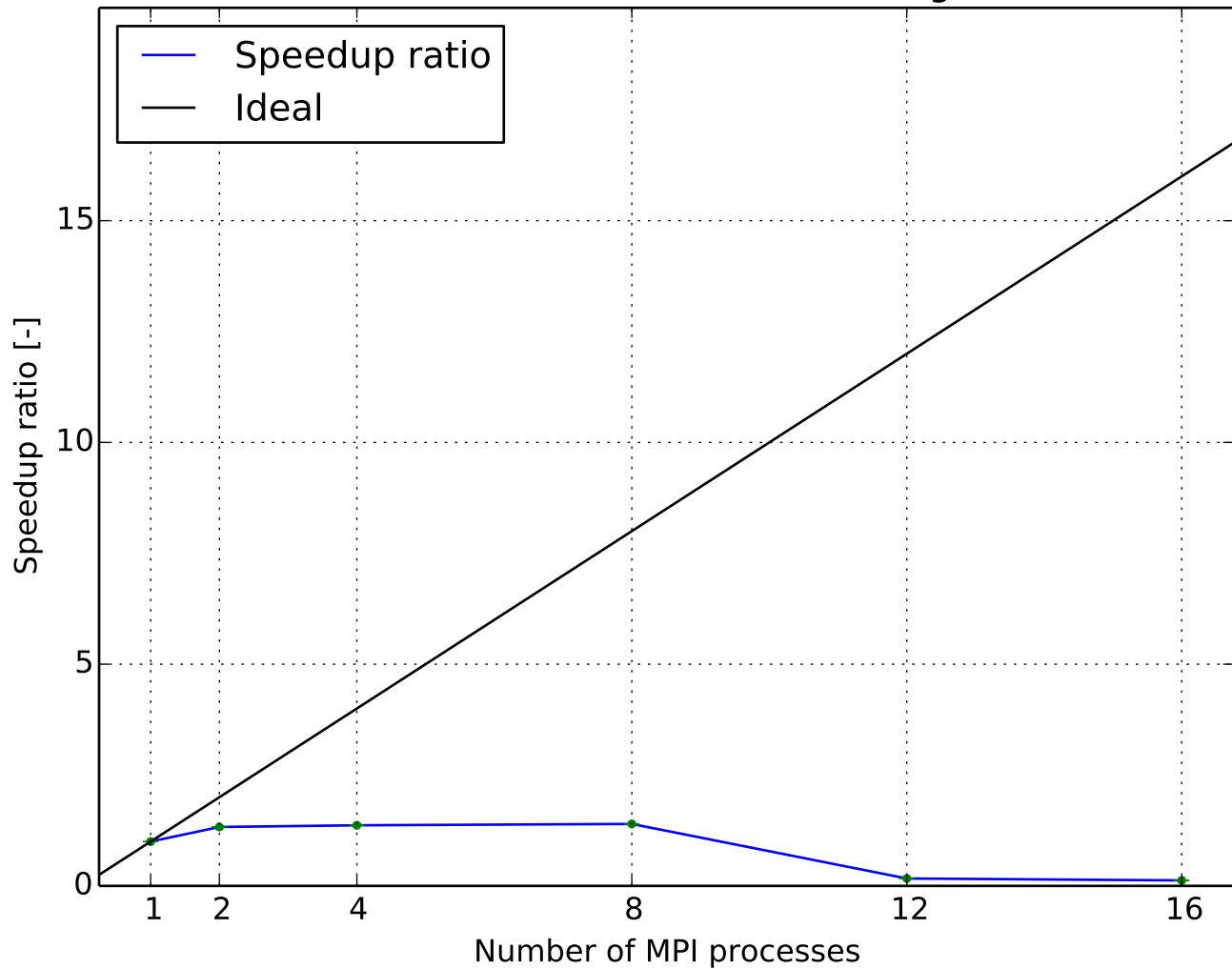
Parallel efficiency  
(2.9952M cells, laminar ,GAMG-GaussSeidel)



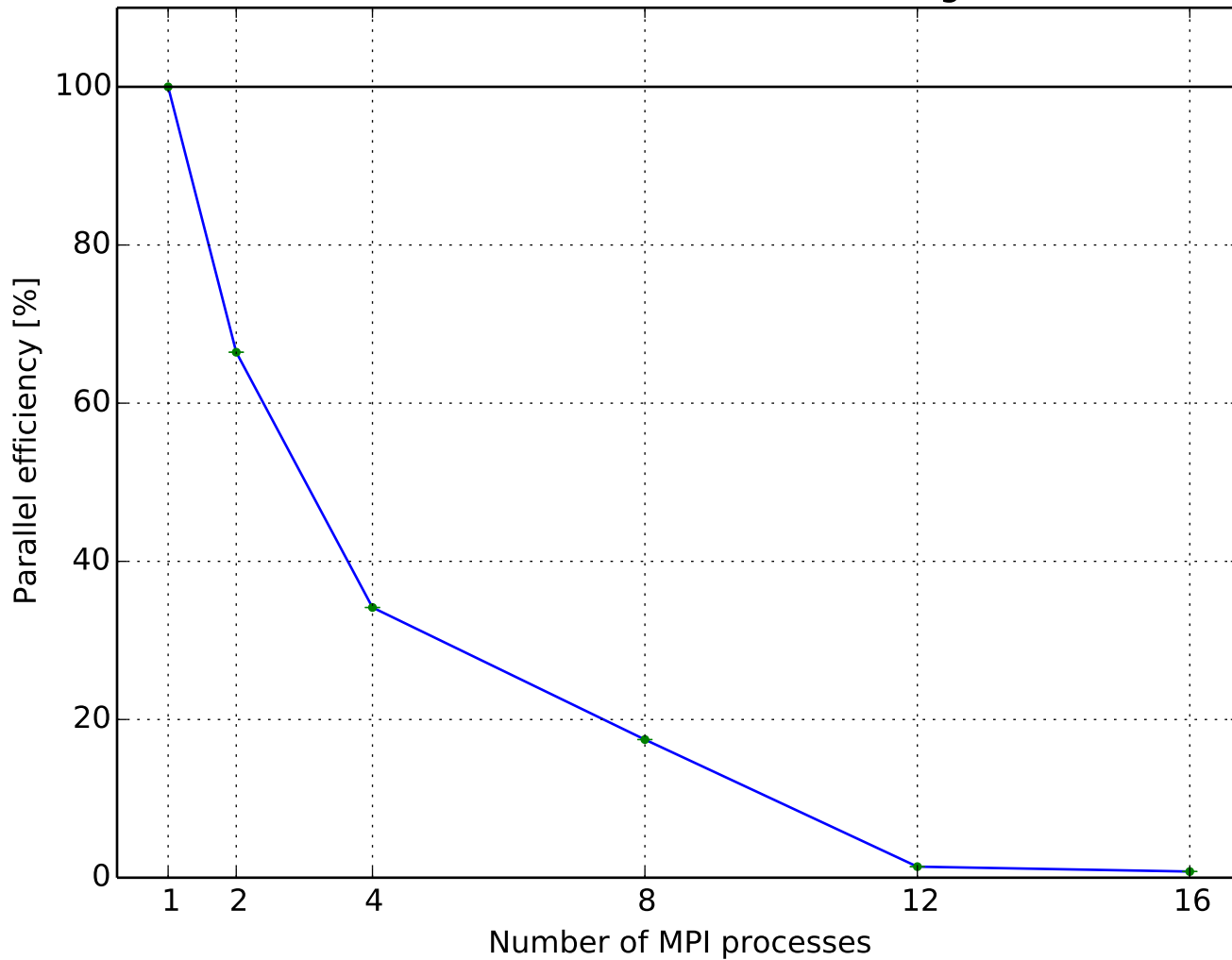
Execution time per time step  
(2.9952M cells, laminar ,GAMG-nonBlockingGaussSeidel)



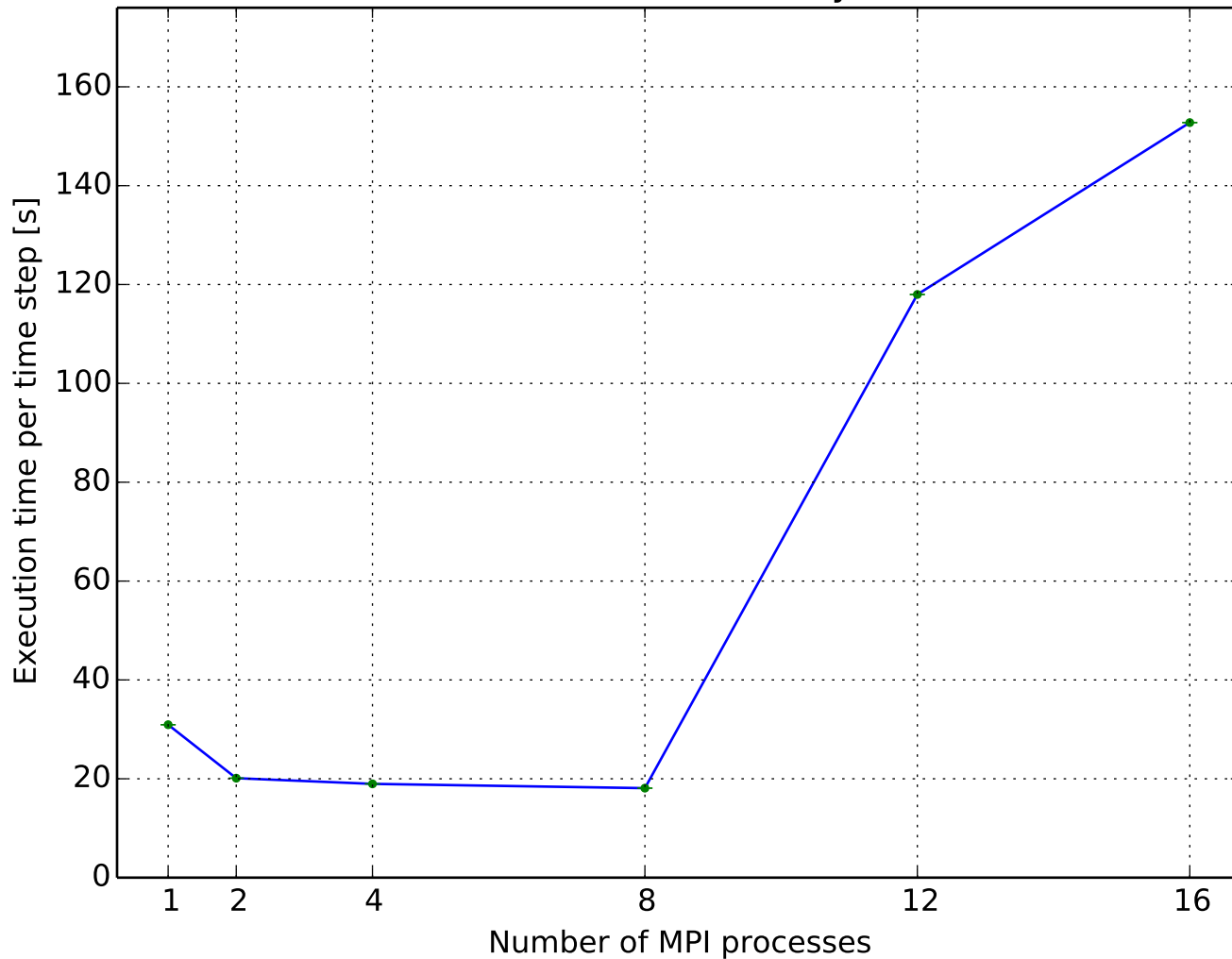
Speedup ratio  
(2.9952M cells, laminar ,GAMG-nonBlockingGaussSeidel)



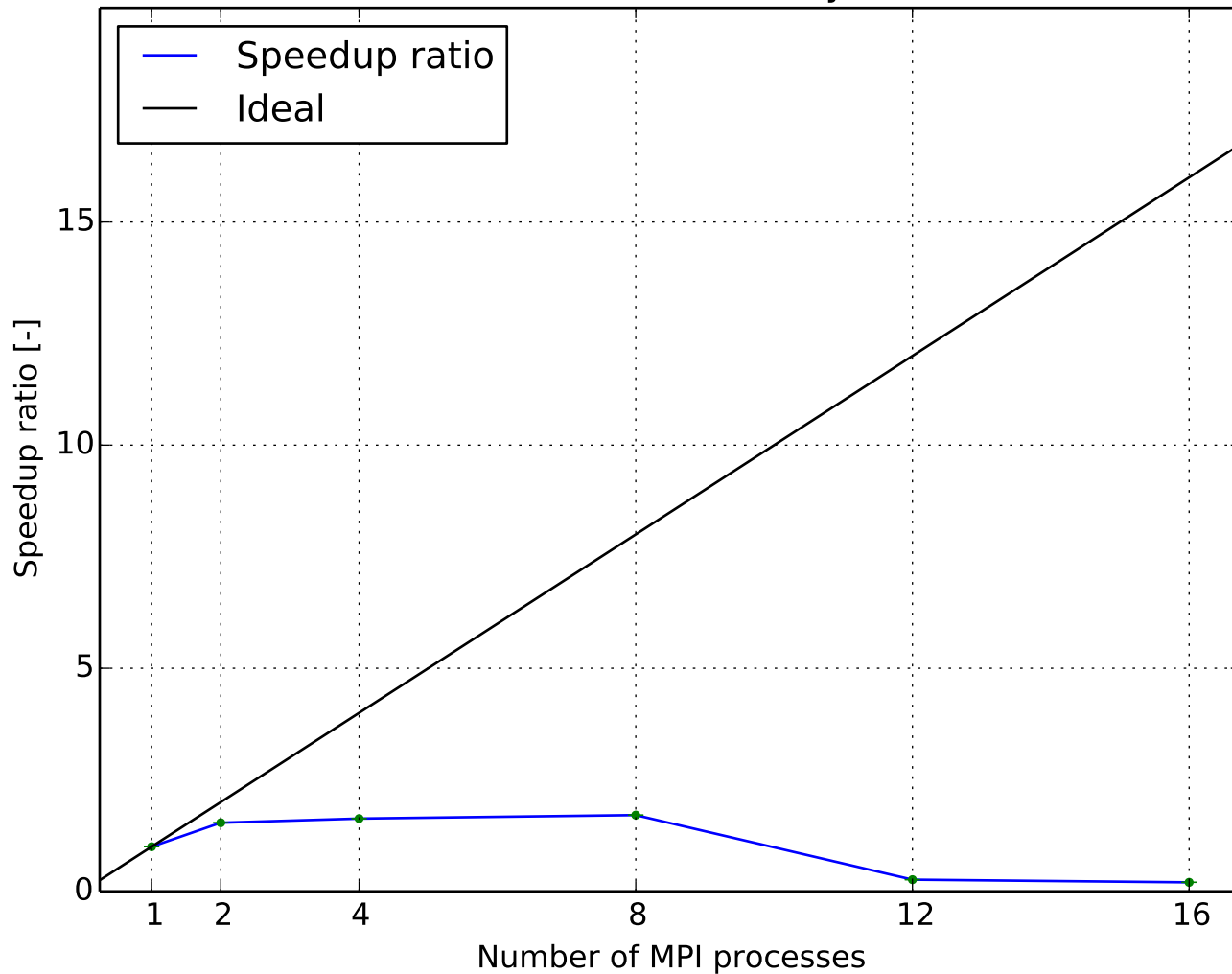
Parallel efficiency  
(2.9952M cells, laminar ,GAMG-nonBlockingGaussSeidel)



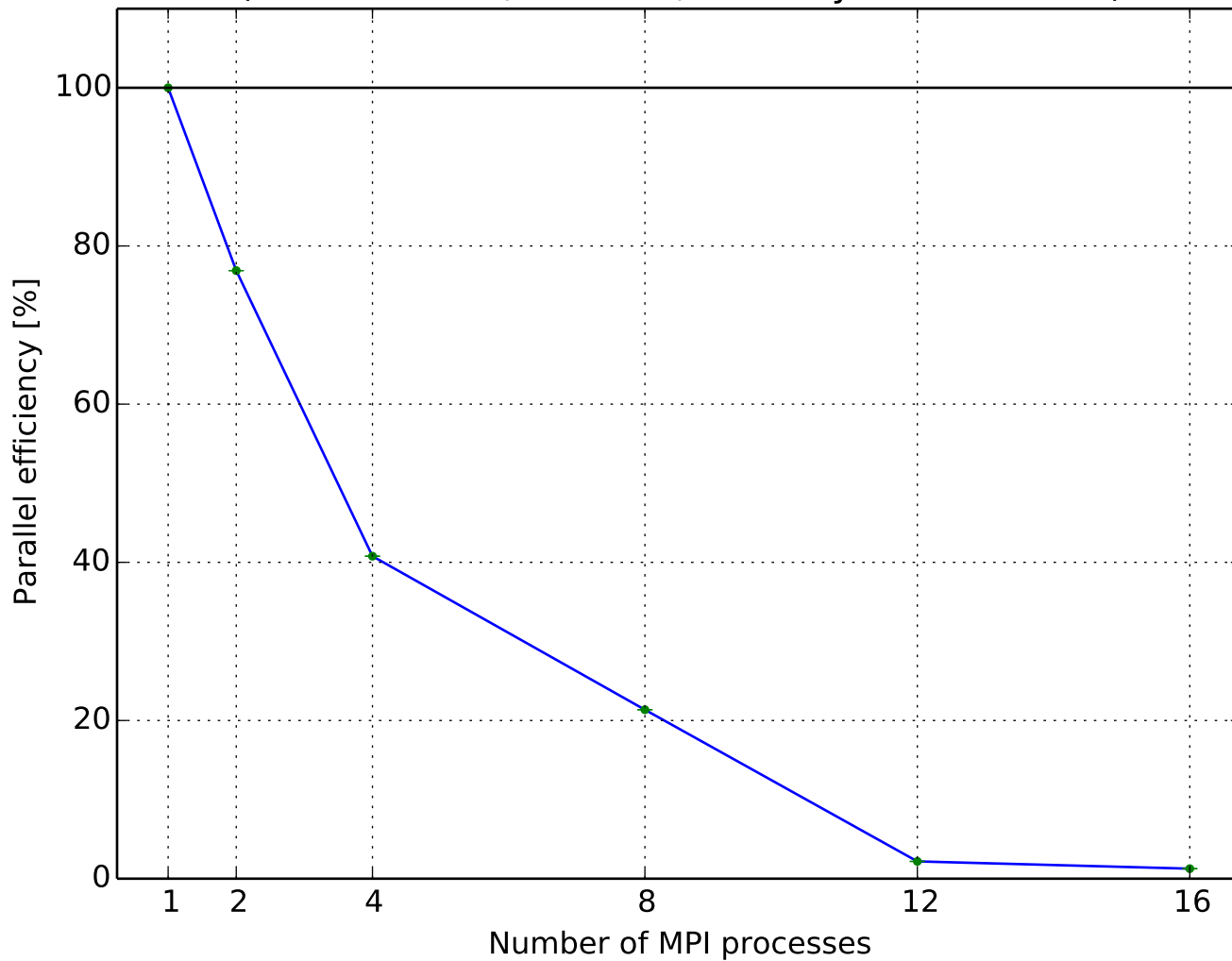
Execution time per time step  
(2.9952M cells, laminar ,GAMG-symGaussSeidel)



Speedup ratio  
(2.9952M cells, laminar ,GAMG-symGaussSeidel)

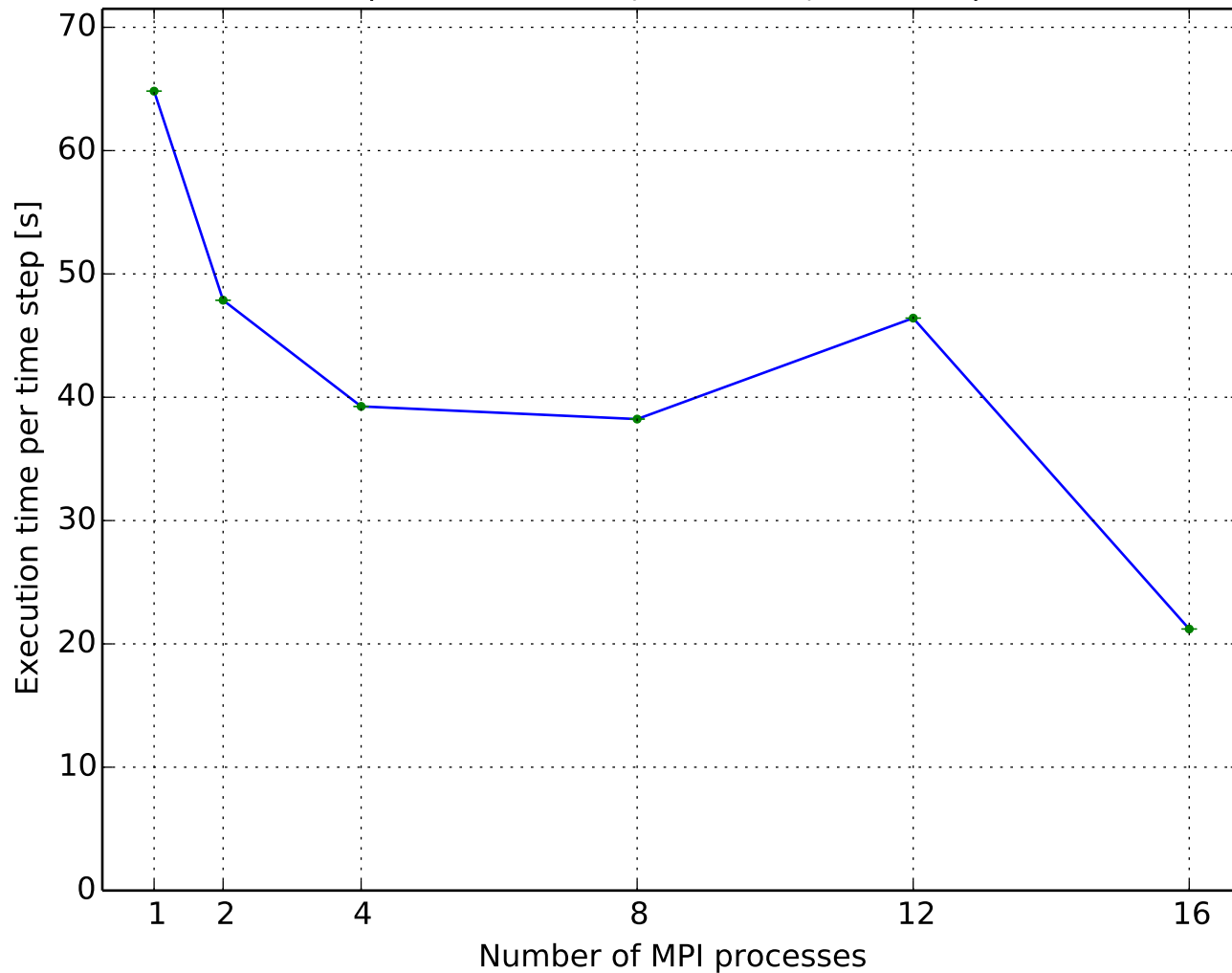


Parallel efficiency  
(2.9952M cells, laminar ,GAMG-symGaussSeidel)

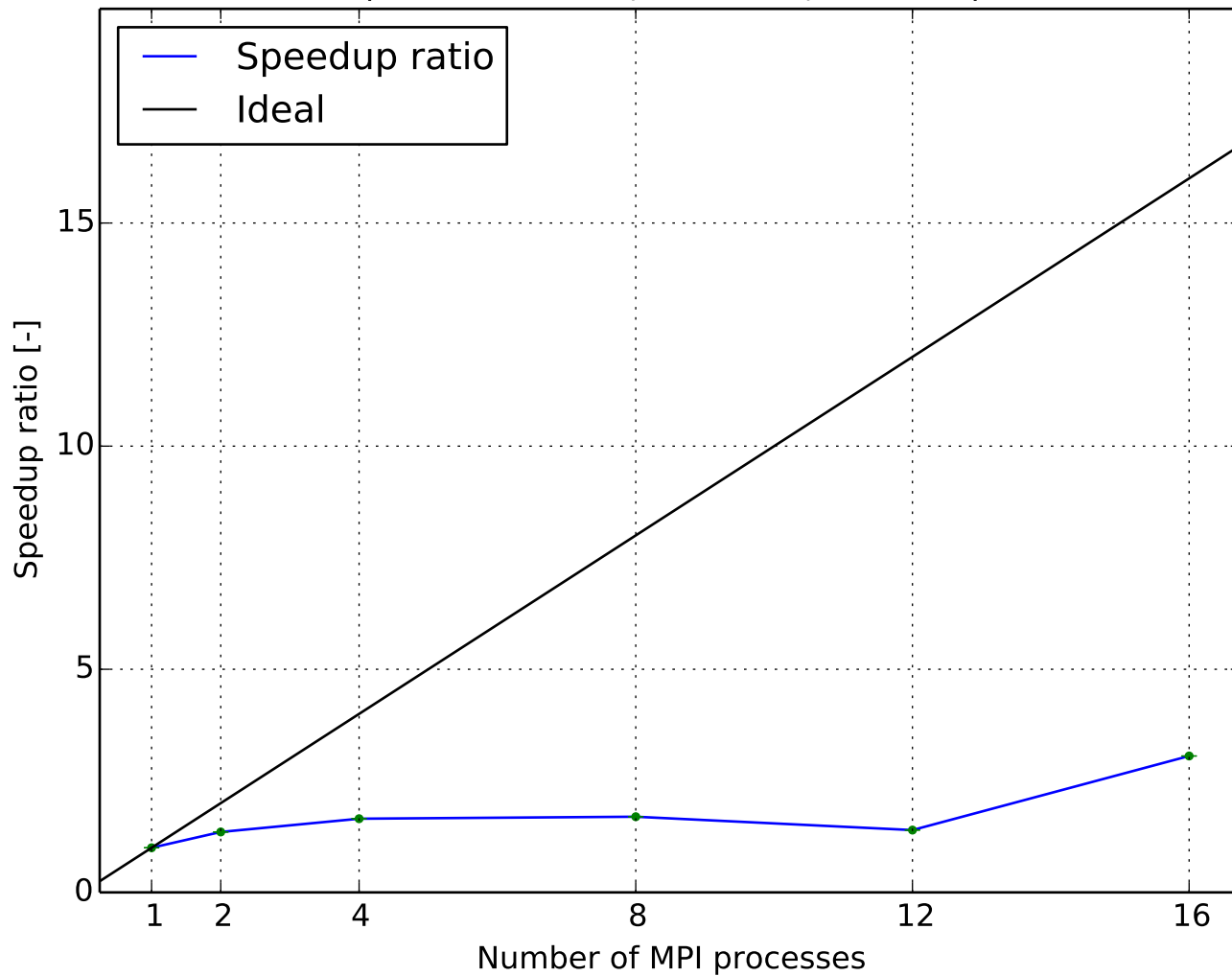




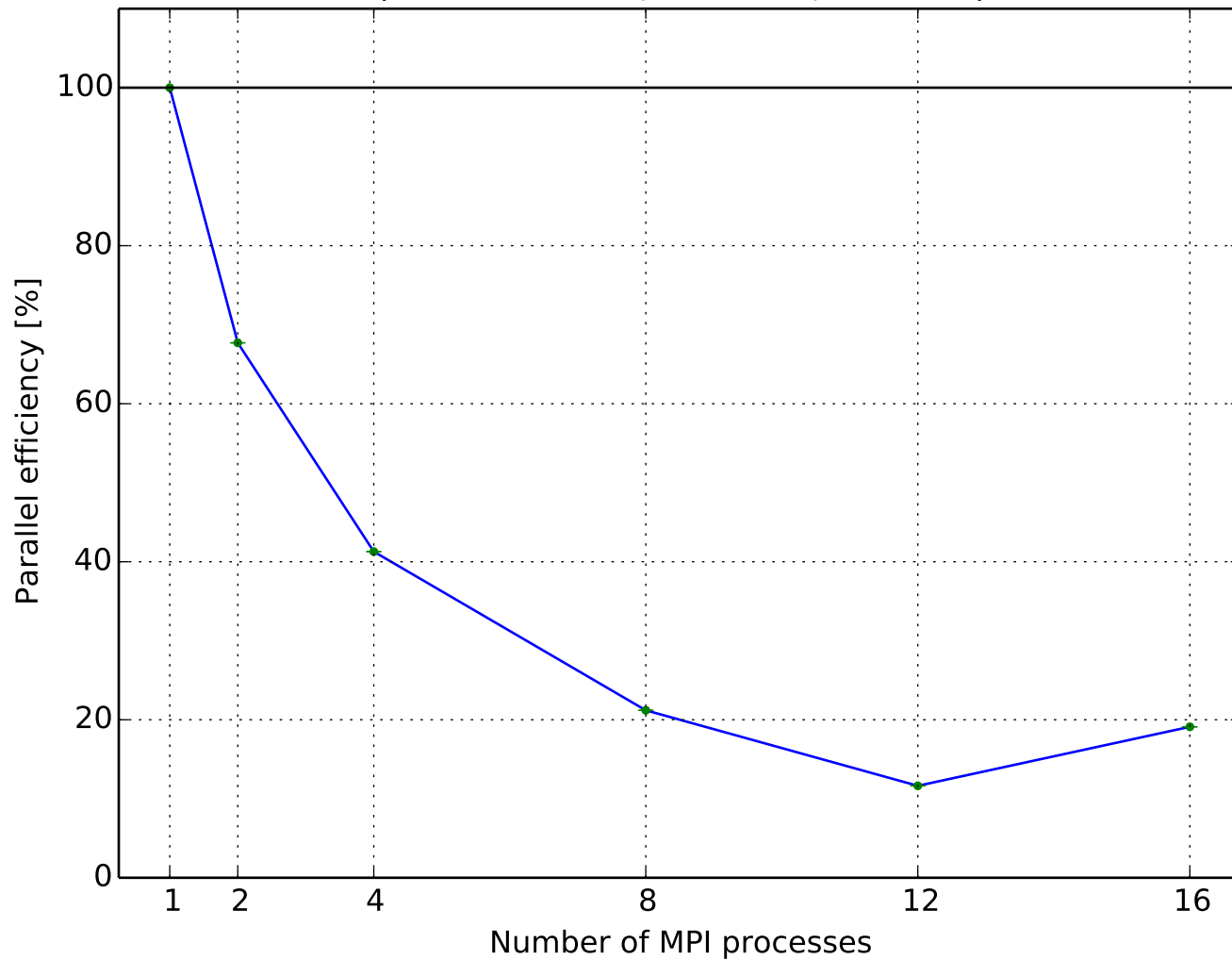
Execution time per time step  
(2.9952M cells, laminar ,PCG-DIC)



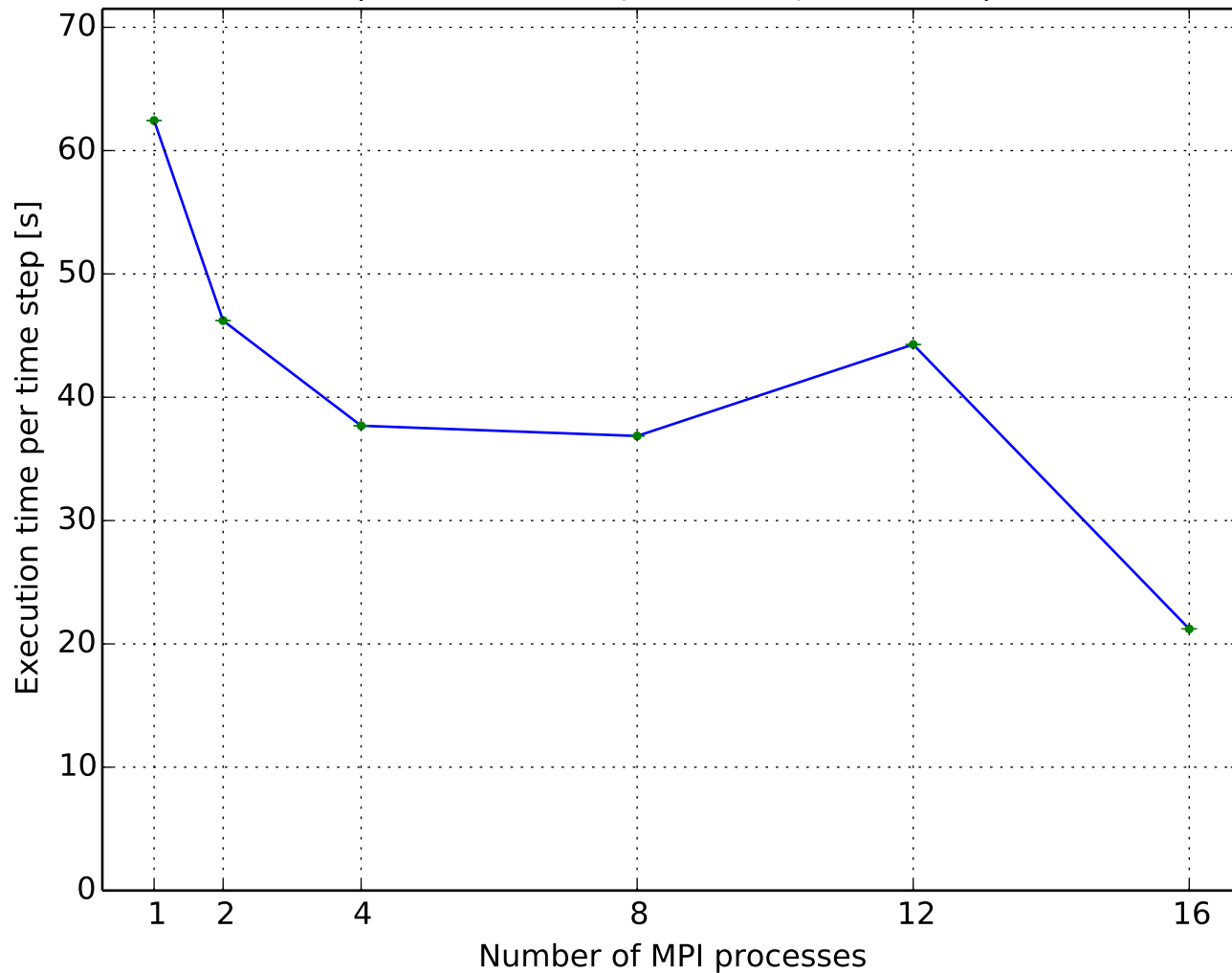
Speedup ratio  
(2.9952M cells, laminar ,PCG-DIC)



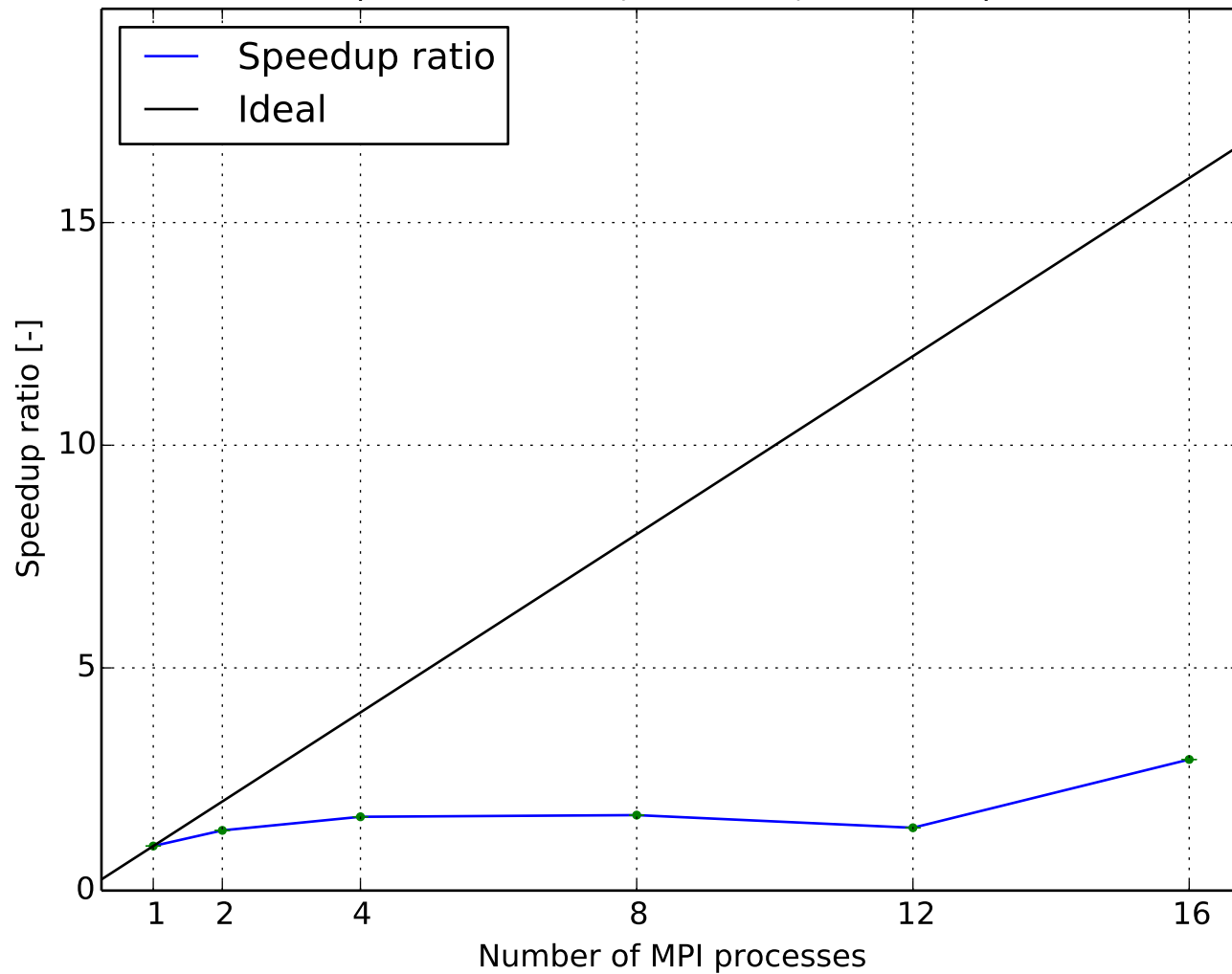
Parallel efficiency  
(2.9952M cells, laminar ,PCG-DIC)



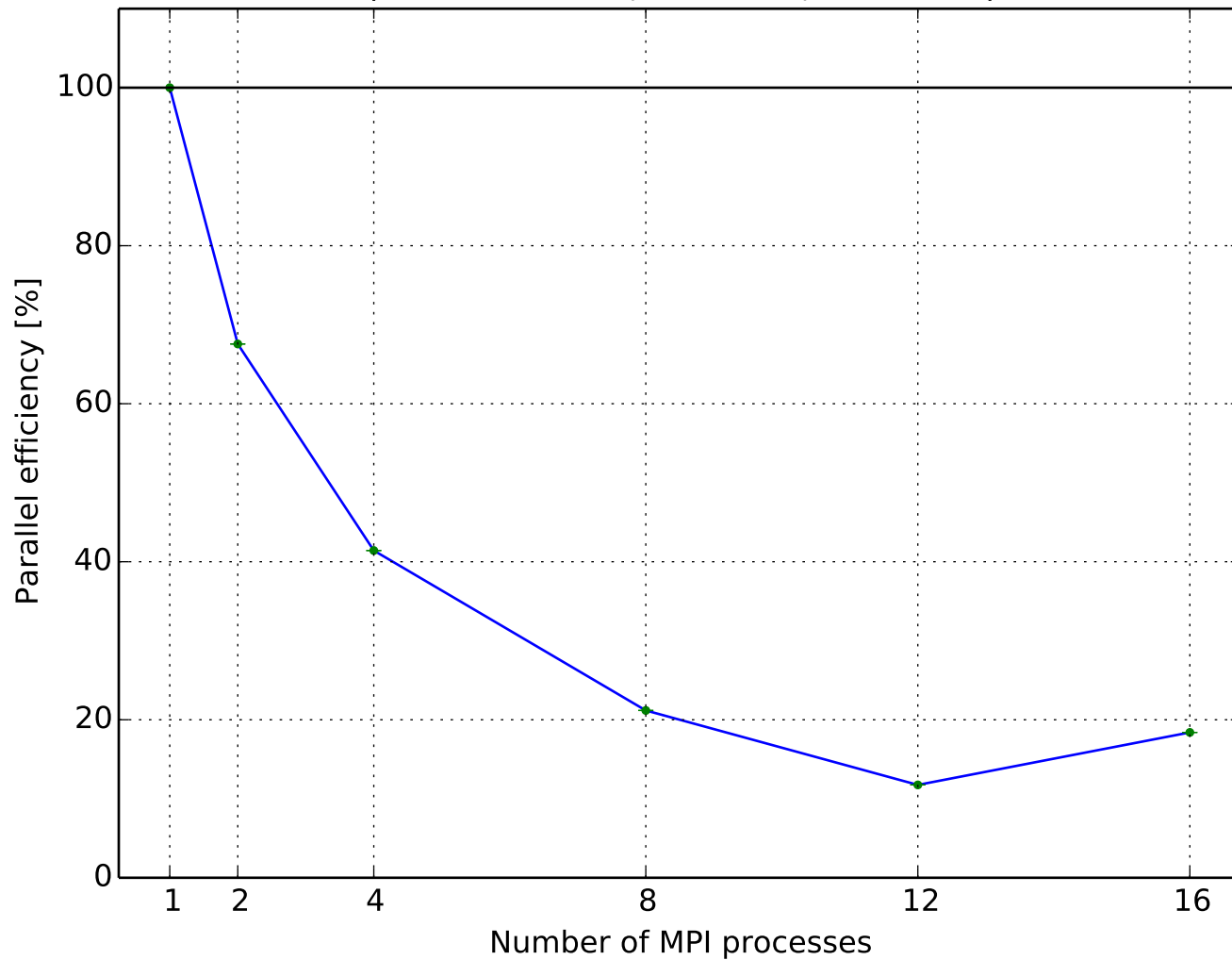
Execution time per time step  
(2.9952M cells, laminar ,PCG-FDIC)



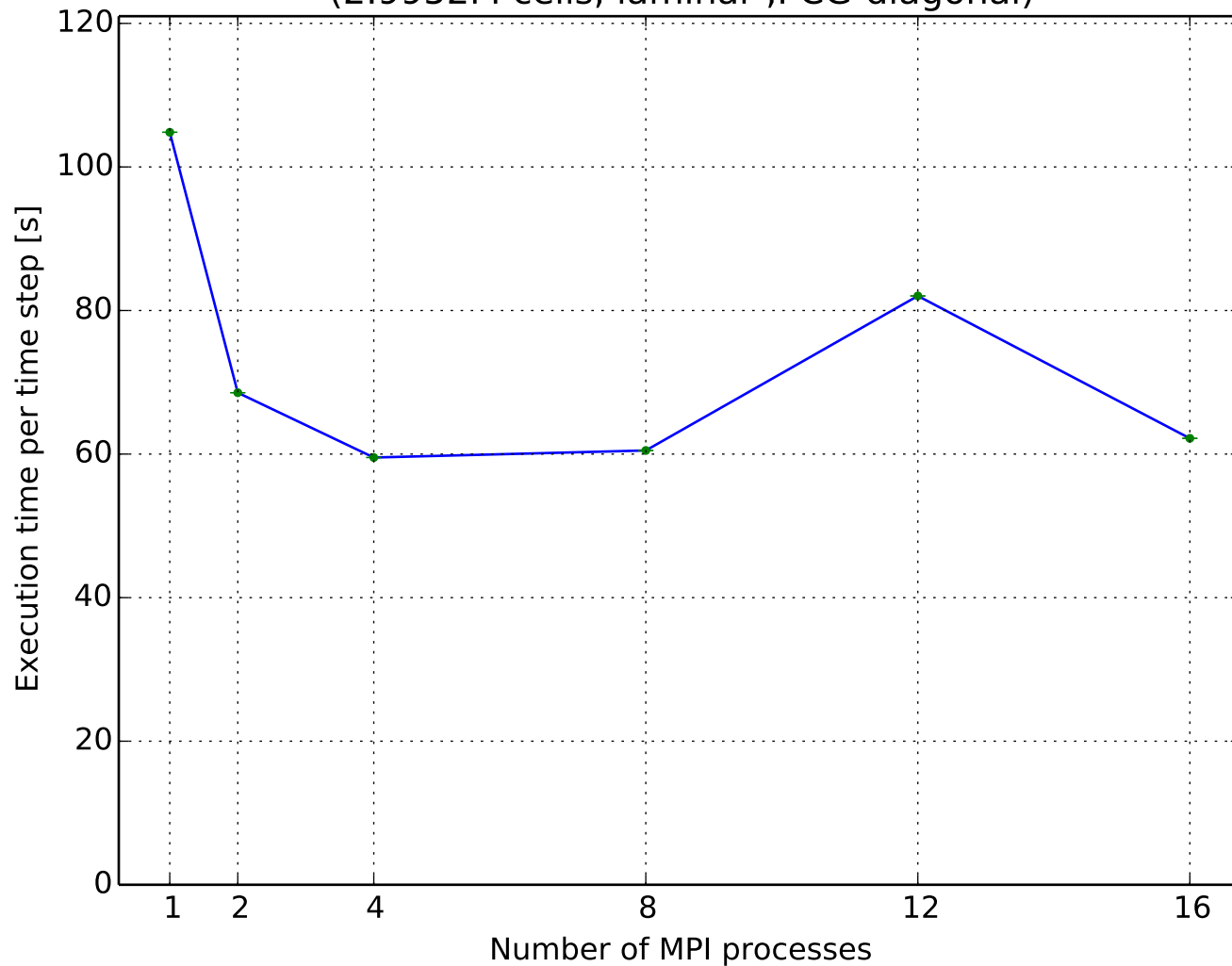
Speedup ratio  
(2.9952M cells, laminar ,PCG-FDIC)



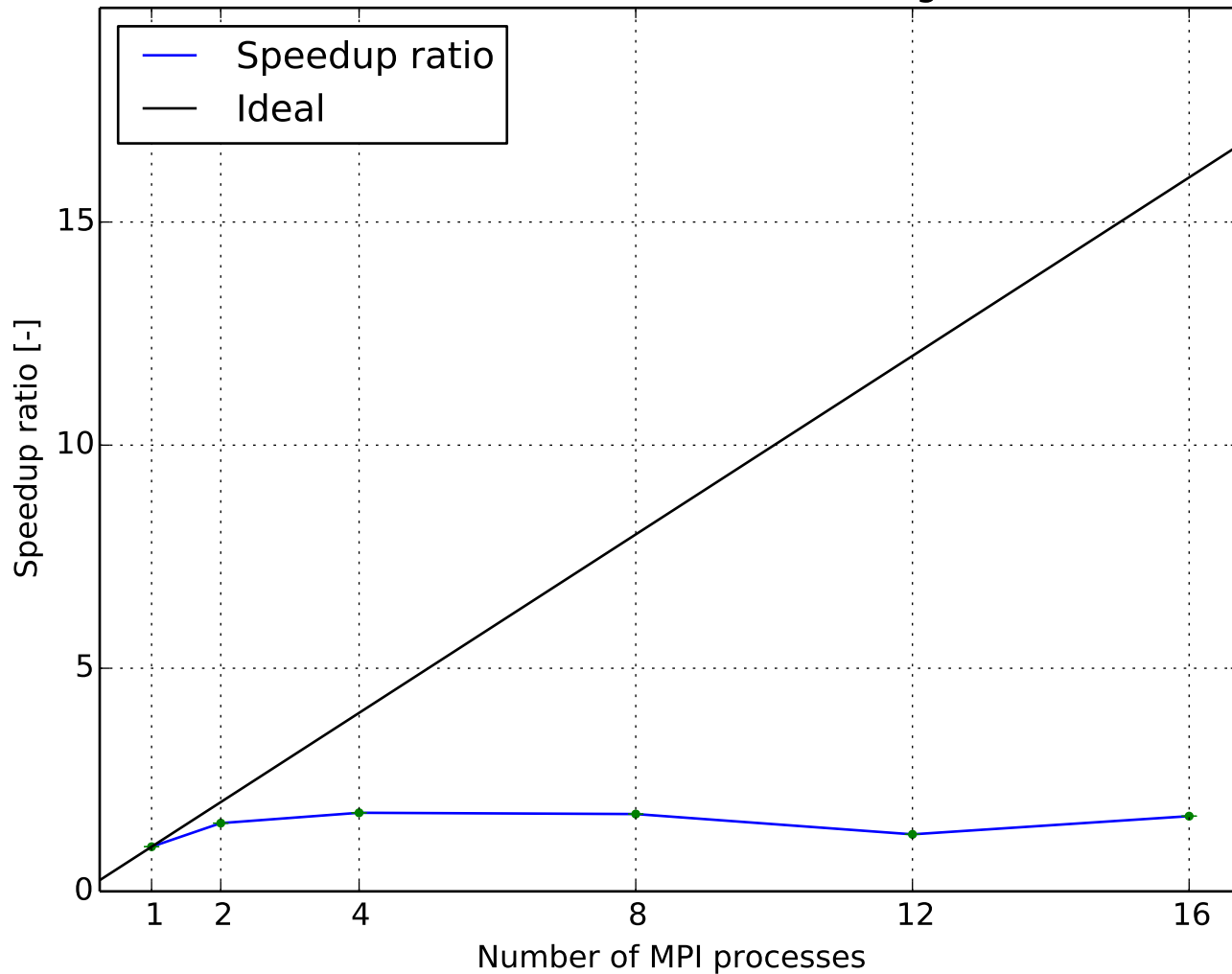
Parallel efficiency  
(2.9952M cells, laminar ,PCG-FDIC)



Execution time per time step  
(2.9952M cells, laminar ,PCG-diagonal)



Speedup ratio  
(2.9952M cells, laminar ,PCG-diagonal)





Parallel efficiency  
(2.9952M cells, laminar ,PCG-diagonal)

