

OpenCPI

HSMC Loopback Stratix IV (alst4) Test App Guide

Version 1.4

Revision History

Revision	Description of Change	Date
v1.4	Converted to application from unit test structure.	9/2018

1 Description

Intended to test HSMC slots on the alst4 platform.

2 Hardware Setup

x86 machine with the Stratix IV PCIE card with both HSMC slots populated with a Loopback HSMC Card[1].

3 Prerequisites

empty assembly w/ the cnt_hsmc_loopback_card_hsmc_alst4_a_hsmc_alst4_b container built for alst4 platform.

4 Execution

4.1 OCPI_LIBRARY_PATH Requirements/Considerations

None.

4.2 Command(s)

Execution must occur from within the hsmc_loopback_test directory.

```
./<target-dir>/hsmc_loopback_test
```

5 Verification

Application exit status indicates success (status of 0 indicates success, status of 1 indicates failure, status of 100 indicates test unexpectedly could not complete). Either PASSED or FAILED is also printed to stdout/stderr, respectively.

References

- [1] Terasic - Components - Extras - HSMC Debug & Loopback Connector Package <https://www.terasic.com.tw/cgi-bin/page/archive.pl?Language=English&No=495>