

# OpenCPI

## E3xx TX Test App Guide

Version 1.5

### *Revision History*

Revision	Description of Change	Date
v1.4	Initial release	9/2018
v1.5	Version bump only	4/2019

## 1 Description

This application is intended to perform a hardware-in-the-loop test of the e3xx\_tx.rcc worker. At a minimum, it tests to ensure that the default values and the expected possible min/max values are applied successfully for the various RF frontend parameters.

## 2 Hardware Portability

This application is intended to test e3xx\_tx.rcc, which is by design specific to the e3xx platform.

## 3 Execution

### 3.1 Prerequisites

The following must be true before application execution:

- The following assets are built and their build artifacts (FPGA bitstream file/shared object file) are contained within the directory list of the OCPI\_LIBRARY\_PATH environment variable.
  - for e3xx/xilinx13\_4 HDL/RCC platforms:
    - \* empty/cnt\_1rx\_1tx\_bypassasm\_mode\_2\_cmos\_e3xx assembly/container
    - \* e3xx\_mimo\_xcvr\_filter\_proxy.rcc
    - \* e3xx\_tx.rcc
    - \* e3xx\_rx.rcc
    - \* ad9361\_config\_proxy.rcc (from assets project)
- The current directory is the applications/e3xx\_tx\_test directory.

### 3.2 Command(s)

```
./<target-dir>/e3xx_tx_test
```

## 4 Verification

An application exit status of 0 indicates success, and non-zero indicates failure.