

Automated Unit Test Suite

Application Execution

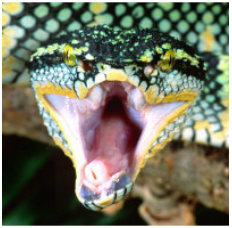
- Two ways for executing applications

- 1) ocpirun

- Simple command-line program
- Only supports static control
- **Used by the Automated Unit Test Suite**

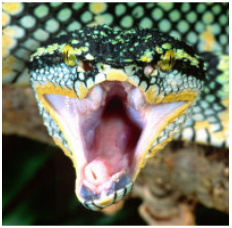
- 2) OpenCPI Application Control Interface (ACI)

- Written in C++ or Python2
- Allows for dynamic control
- Sometimes referred to as "C API"



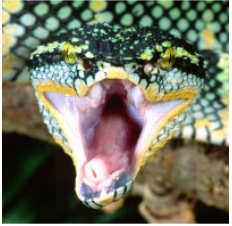
Automated Unit Test Suite

- Overview
 - Allows *functional* testing of all workers implementing a single component spec (OCS)
 - Stands alone from any user application
 - User provides "golden" input and output files or custom verification scripts
- Framework interactions tested as well
 - HDL "backpressure" randomly enforced to ensure worker properly respects flow control
 - HDL input metadata manipulations ensure worker can handle protocol edge conditions, e.g. start-of-message without data until a later clock

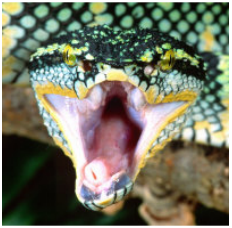
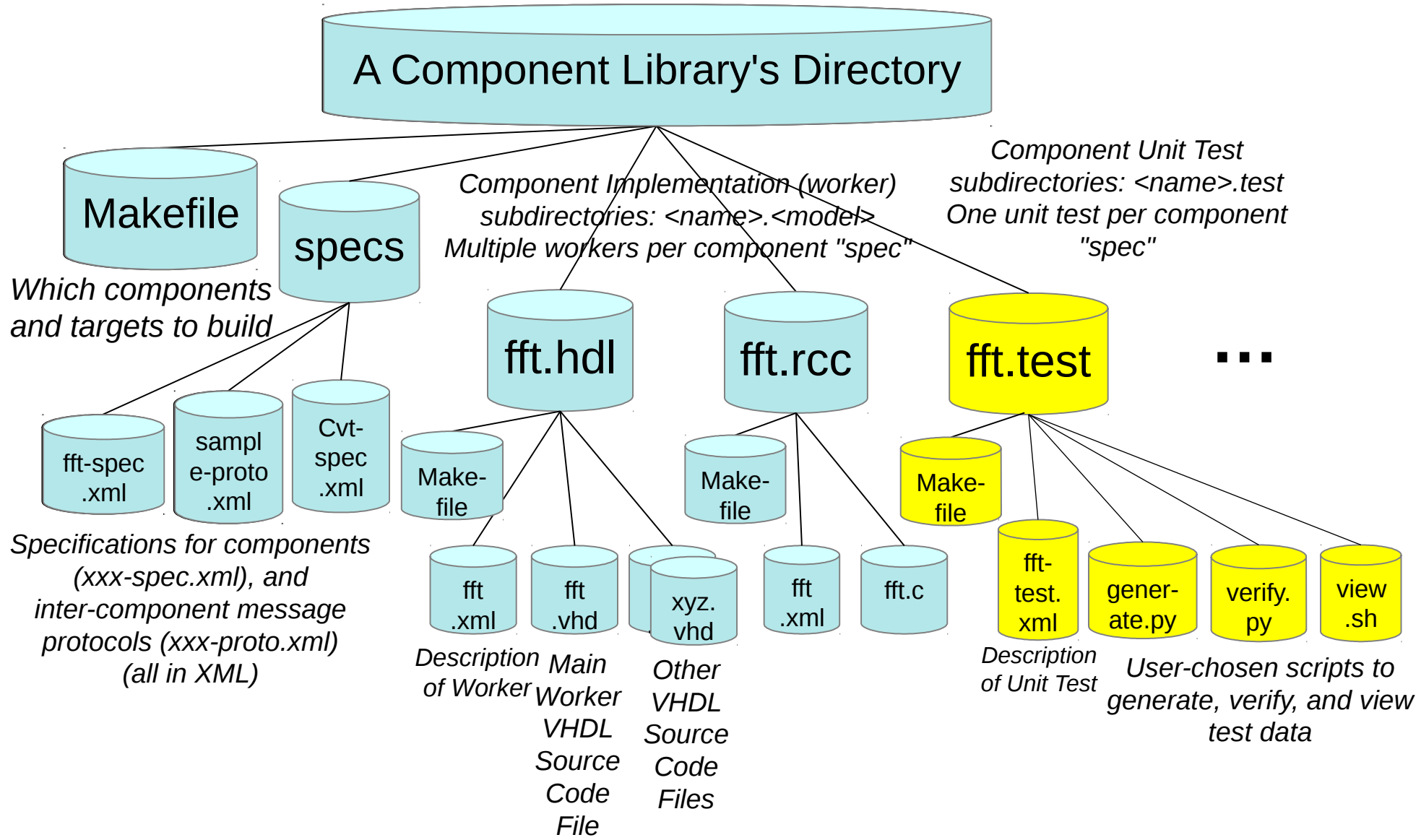


Automated Unit Test Suite

- Based on a single Component Specification
 - <component>.test/
- Creates individual parameterized test Cases based on
 - HDL hardware, simulation, and RCC target platforms
 - Multiple workers
 - one OCS implemented by many Workers (Authoring Models and/or different implementations)
 - <worker>-build.xml - Worker build configurations
 - Build-time/Compile-time (property parameters)
 - <component>-test.xml
 - Run-time settable property values from OCS and OWD
 - Run-time test properties (optional)
 - Customized Cases (ability to over-ride the default of "all combinations" of the above)
- Supports
 - Application Workers (focus of this training)
 - Device Workers with Emulators



Component Directory Layout



Create Component Unit Test directory

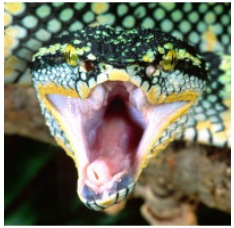


- **\$ ocpidev create test <components-spec>**
 - creates <component>.test/ containing skeleton files
 - Makefile
 - <component>-test.xml
 - generate.py (optionally generate input test data; one script per input port)
 - verify.py (optionally verify output test data; one script per output port)
 - view.sh (optionally view data)
- **GUI using the Asset Wizard**
 - creates same files as above

Unit Test – Five Phases

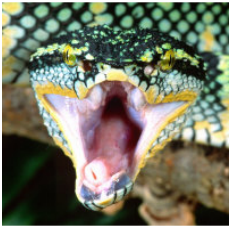
- **Generate** – creates the "gen" sub-directory
 - generates input data, OAS, OHAD, case configuration(s)
- **Build** (HDL workers only; no-op for RCC workers)
 - builds HDL subassemblies for target platforms (bitstream/executable)
- **Prepare** – creates the "run" sub-directory
 - examines available built workers and platforms; creates execution scripts
- **Run**
 - executes tests for all workers, configurations, test cases, and platforms
- **Verify**
 - verifies results from the execution of test cases on workers and platforms

AV IDE allows various combinations of these



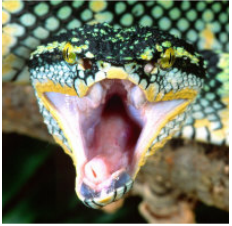
Unit Test – Step 1: Generate

- Discovers
 - OCS associated with this test directory
 - Workers in the same component library that implement this OCS
 - Build configurations (parameter values) for each worker per each <worker>-build.xml or <worker>.build
- Derives
 - Test cases that are "baselined" for build (parameter) configurations from any worker
 - The actual tests appropriate for all build (parameter) combinations vs. the actual worker build configurations they apply to
 - **default** is all, but may be reduced by the property element in the Unit Test Suite Description XML file
- Generates
 - OAS application XML files for launching unit tests
 - OHAD HDL assembly XML files that are built for HDL platforms (hardware and simulation)
 - If necessary, generates input and/or property value files



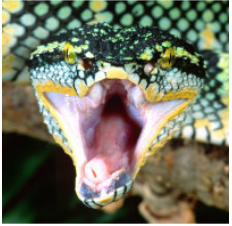
Unit Test – Step 2: Build (HDL only)

- Builds the generated HDL assemblies for specified platforms (hardware and simulators) in gen/assemblies/
- `<component>_<ParamConfig>/<component>_<ParamConfig>.xml`
 - OHAD is metadata_stressor \Rightarrow UUT \Rightarrow backpressure (data I/O uses external ports)
 - may be used in both physical hardware and co-simulation unit tests – simulates the data plane (slow)
- `<component>_<ParamConfig>_frw/<component>_<ParamConfig>_frw.xml`
 - OHAD includes same as above but file_read / file_write HDL workers for data I/O
 - used in simulation only – does NOT simulate the data plane (fast)
- `<ParamConfig>` is found in `<worker>.hdl/gen/<component>-params.mk`
 - look-up table listing all parameter values for each particular worker build configuration

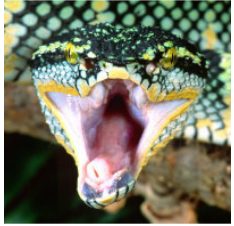


Unit Test – Step 3: Prepare

- Discovers available
 - Execution platforms, both local and remote (network)
 - Built artifacts that can be executed on available platforms
- Generates
 - Test scripts to perform all feasible tests on all available platforms



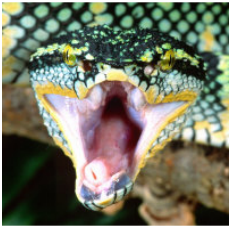
Unit Test – Steps 4 & 5: Run and Verify



- Interleaved (default) or Sequential
- Run
 - All scripts are run per subcase
 - Scripts can access parameter properties and run-time properties of the subcase being tested
- Verify
 - Optionally execute view.sh to view test input/output data
 - “ ocpidev run test ... --view ” or “ make runtest ... View='1' ”
 - Simulation data
 - Deleted upon successful verification (PASSED) by default
 - “ ocpidev run test ... --keepsimulations ” or “ make runtest ... KeepSimulations='1' ” retains output data after verification, regardless of results

Unit Test Suite Description XML File

- `<component>-test.xml`
 - specifies test cases and defaults that apply to *all* test cases
- input
 - defines a prepared input file or generator script
- output
 - defines an output file or verify script
- property
 - defines property values for all test cases (OCS, OWD, or test)
- case
 - used to define a non-default test case when needed
 - necessary when the automatic parameterization of the default test case is invalid or excessive for testing the worker(s)
 - when no case element is defined, the default is used (common)



Unit Test Suite Description XML File Example 1



(from training_project/components library)

```
<!-- This is the test xml for testing component "complex_mixer" -->
```

```
<Tests UseHDLFileIo='true'>
```

```
  <!-- Here are typical examples of generating for an input port and verifying results at an output port-->
```

```
    <Input Port='in' Script='generate.py 100 12.5 32767 16384'></Input>
```

```
    <Output Port='out' Script='verify.py 100 16384' View='view.sh'></Output>
```

```
    <!-- Set properties here. Use Test='true' to create a test-exclusive property. -->
```

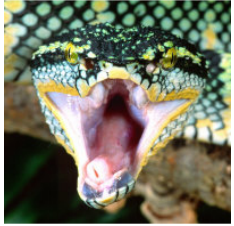
```
    <Property Name='phs_inc' Values='8192'></Property>
```

```
    <Property Name='enable' Values='0,1'></Property>
```

```
    <Property Name="data_select" Values="0,1"></Property>
```

```
</Tests>
```

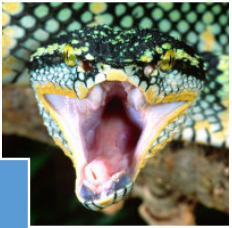
Unit Test Suite Description XML File Example 2



(from ocpi_projects/assets/dsp_comps library)

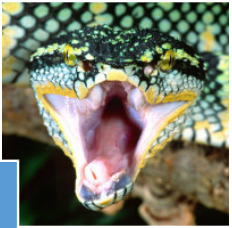
```
<!-- This is the test xml for testing component "cic_int" -->
<tests timeout='20000' useHDLFileIo='true'>
  <input port='in' stressorMode='full' script='generate.py 32767' messagesize='8192'/>
  <output port='out' script='verify.py 32767' view='view.sh'/>
  <property name='TARGET_FREQ' test='true'/>
  <property name='VIVADO_ILA_p' values='0'/>
  <!-- Exclude higher R value test cases on simulators at to reduce test time -->
  <case>
    <!-- R=2048 is not excluded when TARGET_FREQ=0 to test the case of the -->
    <!-- output port producing multiple EOMs after EOF on input in simulation -->
    <property name='TARGET_FREQ' value='0'/>
    <property name='R' values='8191,8192' exclude='*sim'/>
  </case>
  <case>
    <property name='TARGET_FREQ' value='1'/>
    <property name='R' values='2048,8191,8192' exclude='*sim'/>
  </case>
</tests>
```

Unit Test Suite Description XML File (cont)



Tests element Attribute	Data Type	Description
Spec	string	Overrides the default behavior where the OCS is inferred from the name of the <component>.test directory, and found in the ../specs/<component>-spec.xml file.
UseHdlFileIO	boolean	Applies only when HDL workers are being tested on simulation platforms. When true, file I/O is handled in the simulator using file read/write HDL workers (faster). When false (default), file I/O is handled using file read/write RCC workers outside the simulator.
ExcludeWorkers	string	A list of comma-separated workers that should <i>not</i> be tested, even if they implement the spec of this test suite.
OnlyWorkers	string	A list of comma-separated workers that should be the <i>only</i> ones tested, and others found to implement the same spec will be ignored.
ExcludePlatforms	string	A list of comma-separated platforms that should <i>not</i> be tested. Any other available platforms that have built artifacts will be used.
OnlyPlatforms	string	A list of comma-separated platforms that should be the <i>only</i> ones tested. Any other available platforms will be ignored.
Duration	integer	An amount of time the application should run before being considered successfully done . <u>Cannot be used with Timeout.</u>
Timeout	integer	An amount of wall clock time in seconds after which the execution of a test subcase is considered a failure. <u>Cannot be used with Duration.</u>

Unit Test Suite Description XML File (cont)

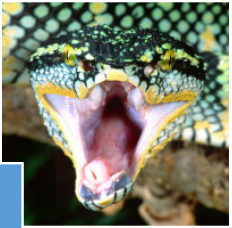


Tests element child element	Attribute	Type	Description
input	name	string	Optionally specifies the name of this input source. Not necessary if applied to all cases, but useful when shared across multiple cases. More common to use the port attribute if it applies only to a specific port. Must specify either name or port.
	port	string	Optionally specifies the name of the port that this input source will always apply to. If there is only one input source for a port, it will be used for all cases. Must specify either name or port.
	script	string	Used to indicate a command to execute to produce data. Can be a command name followed by command arguments, where the last argument is implicitly the file to be written into. Must have execute permissions.
	file	string	Used to specify the name of a file to be used as the source of data.
	messageSize	integer	A positive number that specifies the size of messages (in Bytes).
	messagesInFile	boolean	Indicates that the input data contains message boundaries and opcodes.

```
<input port="in" script="generate.py 32768"/>
```

```
<input port="in" file="../../../applications/FSK/idata/Os.jpeg"/>
```


Unit Test Suite Description XML File (cont)

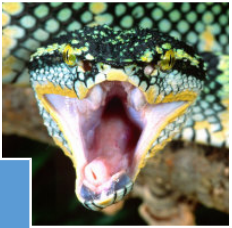


Tests element child element	Attribute	Type	Description
output	name	string	Optionally specifies the name of this output source. Not necessary if applied to all cases, but useful when shared across multiple cases. More common to use the port attribute if it applies only to a specific port. Must specify either name or port.
	port	string	Optionally specifies the name of the port that this output source will always apply to. If there is only one output source for a port, it will be used for all cases. Must specify either name or port.
	script	string	Used to indicate a command to execute to validate data. Can be a command name followed by command arguments, where the last arguments are implicitly the output filename from the given port followed by each input port filename in the order the ports are declared in the OCS. Must have execute permissions.
	file	string	Used to specify the name of a golden file used to compare to output data.
	view	string	Optionally "view" the data for the port in some viewing or plotting window. Takes all of the same arguments as the verification script mentioned above.

```
<output port="out" script="verify.py 2048" view="view.sh 2048"/>
```

```
<output port="out" file="goldfile.bin" view="view.sh"/>
```

Unit Test Suite Description XML File (cont)



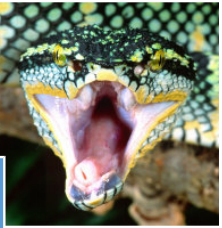
Tests element child element	Attribute	Type	Description
property	name	string	Required. Identifies a property defined in the OCS or OWD, or declares a new test property.
	test	boolean	Optionally indicates that the property is a test property and not a property of the worker(s) being tested. Must be a unique name not already used in the OCS/OWD. Used to generate other test cases not defined simply by the values of worker properties.
	value		Specifies a single value to be tested.
	values		Specifies a comma-separated sequence of values to be tested.
	valueFile	string	Specifies the name of a file containing a single value to be tested. Multiple lines in the file are considered elements of a sequence or array value.
	valuesFile	string	Specifies the name of a file containing multiple values to be tested. Multiple lines are considered separate values to be tested.
	generate	string	Specifies a command to execute to create a file containing a value to be tested. Used when a property depends on others in a complex way. The last argument is implicitly the file to be written into. Must have execute permissions.

```
<property name="ref" value="0x1B26"/>  
<property name="data_select" values="0,1"/>
```

```
<property name="taps" generate="gen_lpf_taps.py"/>  
<property name="samplesPerBaud" test="true" value="25"/>
```

Unit Test Suite Description XML File (cont)

Tests element child element	Attribute	Type	Description
case	Name	string	Optionally specifies the name of the test case. If omitted, the name of the case is case followed by a case number (zero-based) with at least two digits. May be used when specifying that only certain cases (rather than all) should be executed or verified.
	ExcludeWorkers	string	A list of comma-separated workers that should <i>not</i> be tested, even if they implement the spec of this test suite.
	OnlyWorkers	string	A list of comma-separated workers that should be the <i>only</i> ones tested, where others found to implement the same spec will be ignored.
	ExcludePlatforms	string	A list of comma-separated platforms that should <i>not</i> be tested. Any other available platforms that have built artifacts will be used.
	OnlyPlatforms	string	A list of comma-separated platforms that should be the <i>only</i> ones tested. Any other available platforms will be ignored.



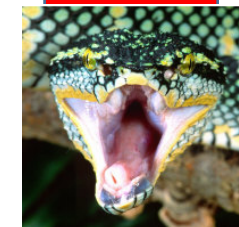
Unit Test Suite Description XML File (cont)



Case element child element	Description
input	Overrides the default input per port. If no input is defined for an input port, the default input is used as defined for the port under the top-level tests element. The port attribute may be used, but if the name attribute names an input source at the top-level that already has a port attribute, it may be omitted. When the name attribute is used both file/script attributes are disallowed.
output	Overrides the default output per port. If no output is defined for an output port, the default output is used as defined for the port under the top-level tests element. The port attribute may be used, but if the name attribute names an output source at the top-level that already has a port attribute, it may be omitted. When the name attribute is used both file/script attributes are disallowed.
property	Overrides default value(s). If no property element is present for a property under a case element, then the value(s) defined at the top-level are used. For parameter properties, the default value(s) tested are derived from value(s) defined in all worker(s)' build configurations, but this automatic default may be overridden (limited) by a property element either at the top-level or under a case element. A single test case can have multiple values for any property, where sub-cases are automatically generated for all combinations of property values for the test case whether specified at the top level as defaults sets of values or specified for the test case in the case element.

Additional Configurations

- The test XML also allows more advanced configurations, including
 - File I/O with embedded opcodes (MessagesInFile)
 - Manipulating incoming ports' metadata to help ensure worker handshaking compliance (StressorMode)
 - Disabling backpressure on output ports (disableBackpressure)
 - **NOT** recommended; HDL worker implementations have had problems respecting backpressure in the past

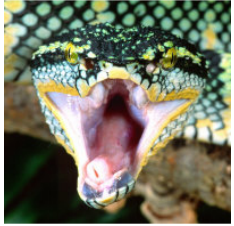


Unit Test Suite Makefile



- Typically includes only one line – other mods are for the power-user
 - `include $(OCPI_CDK_DIR)/include/test.mk`
- Make variables are available to control various phases of unit testing (set on the make command-line or within Makefile)
 - HdlPlatform(s) – Build phase
 - OnlyPlatform(s), ExcludePlatforms(s) – Build/Prepare/Run/Verify phases
 - Recommend instead setting these in the Unit Test Suite Description XML file
 - View = 1 causes the "view" script to be run whenever verification is requested
 - TestVerbose = 1 causes execution and verification output to be output to the console/shell, in addition to per platform/subcase log files
 - KeepSimulations = 1 causes the contents of the simulations directory to be retained after successful execution on simulation platforms
 - Cases = is a wildcard pattern indicating which cases/subcases should be executed or verified

Unit Test Suite Scripts



- All Parameter and Initial/Writable run-time property values are exposed to each script as environment variables
 - OCPI_TEST_<myprop>
 - Output scripts also have access to final values of Writable and Volatile properties
- Scripts may be parameterized by these values for the subcase being generated
- Exit status of zero indicates success, while a non-zero exit status indicates failure
 - Framework prints green/red colors for PASSED/FAILED based on the exit status (terminal settings permitting)
 - May write other informational messages to STDERR to be logged
- Scripts must be executable and found in the <component>.test directory
- C/C++
 - `getenv("OCPI_TEST_myprop")`
- Python
 - `os.environ.get("OCPI_TEST_myprop")`
 - `#!/usr/bin/env python`
- Bash shell
 - `${OCPI_TEST_myprop}`
 - `#!/bin/bash --noprofile`

Unit Test Suite view.sh Examples



- Should return "immediately" by launching viewers in the background

```
#!/bin/bash --noprofile
```

```
# Plot the time/fft of input file ($2)
```

```
${OCPI_PROJECT_DIR}/scripts/plotAndFft.py $2 complex 32768 100 &
```

```
# Plot the time/fft of output file ($1)
```

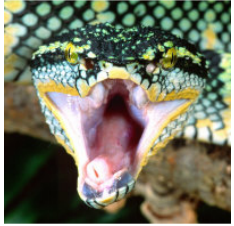
```
${OCPI_PROJECT_DIR}/scripts/plotAndFft.py $1 complex 32768 100 &
```

```
#!/bin/bash --noprofile
```

```
# Plot the time/fft of output file ($1), and use property value (${OCPI_TEST_num_zeros}) to  
calculate the number of real samples
```

```
${OCPI_PROJECT_DIR}/scripts/plotAndFft.py $1 real `echo "${OCPI_TEST_num_zeros}+1*100"  
| bc` 1 &
```

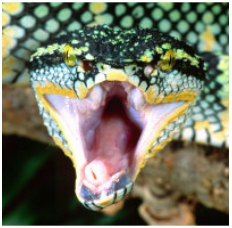

Unit Test Generate/Build Summary



- Create the unit test suite via ocpidev create test <OCS> or GUI
- Modify the Makefile (rarely needed)
- Modify the <component>-test.xml file with input/output/property/case
- Prepare input data files and/or input data generator scripts
- Prepare output data gold files and/or output verification scripts
- Execute the Generate phase
- Examine the gen/cases.txt report to verify the generated subcases
- For HDL workers, execute the Build phase to build HDL assemblies
- All Generate/Build results are found in the gen/ directory

Unit Test Suite Limitations

- Supports Application Workers and Device Workers
 - Otherwise, unit tests are "hand crafted"
- Unit Tests solely use ***ocpirun*** – no mechanism for an ACI
 - Simple "delayed" properties are available
- Fixed Application XML (OAS)
 - Application Worker **MUST** propagate Zero-Length Messages on Opcode 0
 - **OR** set the top-level duration / timeout attributes of the <component>-test.xml
 - Any file I/O limited to capabilities of file_read and file_write



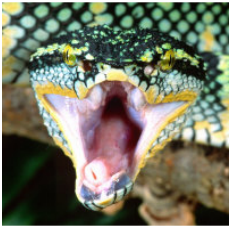
Executing Tests on Remote Test Systems



- Extends the unit test system beyond the current development system
- Remote platforms specified by **OCPI_REMOTE_TEST_SYSTEMS**
 - Colon separated list of Remote Test Systems with four fields, each separated by "="
 - **Host name/IP address = SSH user name = SSH password = Project mount path on remote system**
 - Example: 10.11.12.13=root=root=/mnt/myproj:10.11.12.14=root=root=/mnt/myproj
- Remote Test Systems are accessed via **SSH**
- Project's directory on the development system **MUST** be NFS mounted by the Remote Test System and its mounted directory name must match the project's directory, as indicated by the fourth field above
- Remote and development systems should use the same release version
- Can be set before GUI launch, or from within

Tests Execution and Verification

- Execute the Prepare phase
 - By **Default**, all local and remote platforms are considered
 - RCC, HDL hardware, and HDL simulators
 - Ability to filter which Platforms are used for execution
 - **OnlyPlatform(s)="xsim, centos7"** and **ExcludePlatform(s)="xsim, e3xx"**
 - Discovers available RCC built artifacts in the component library, and **local** HDL artifacts built during the Build phase from generated **HDL test assemblies**
 - From the combination of available platforms and available artifacts it determines which sub-cases can be run on which platforms, and generates test execution scripts accordingly in each run/<platform> sub-directory
 - Execution of unit tests overrides any user-specified OCPI_LIBRARY_PATH
 - Limits the artifact search to local RCC artifacts in the component library, the gen/assemblies directory for HDL artifacts, and the installed CDK on both local and remote systems for both file_read/file_write when RCC implementations are used



Tests Execution and Verification (cont)



- Execute the Run phase
 - Test applications are executed for all possible cases/sub-cases per platform
 - Ability to filter which Platforms are used for execution
 - **OnlyPlatform(s)="xsim, centos7"** and **ExcludePlatform(s)="xsim, e3xx"**
 - The recorded results, per sub-case and platform, are
 - output data from each output port
 - final values of all properties, including volatile properties
 - a log file of the actual test execution

Tests Execution and Verification (cont)



- Execute the Verify phase
 - By **Default**, all output results are verified
 - RCC, HDL hardware, and HDL simulators
 - Ability to filter which Platforms are used for execution
 - **OnlyPlatform(s)="xsim, centos7"** and **ExcludePlatform(s)="xsim, e3xx"**
 - Relies on previous execution of appropriate sub-cases for all platforms
 - Performs verification using results previously recorded in the Run phase
 - The Verify phase by itself does not involve execution or access to local or remote platforms and may be performed off-line
 - The View = 1 option enables running the view scripts during verification
 - this happens per sub-case before each verification
 - The Verify phase will fail if any sub-cases fail by returning non-zero exit status
 - each sub-case individually reports PASSED/FAILED status