

SDIO_D7	TIM3_CH2	TIM8_CH2	1283_MCK	DCMI_D1		D07	D0/	—	12S2 MCK	SD10_D6	TIM3_CH1	TIM8_CH1	
\0_\\	11M3_0H2	FSMC_NCE2	FSMC NE1	USART2_CK		PC7	PC6		FSMC_NWAT	USART2_RX	111110_0111	11MO_0111	
1283_CK	DCMI_D8	USART3_TX	SDIO_D2			PD7	PD6		DOMI D12				
1235_0K				SP13_SCK		PC10	PG15		DCMI_D13	USART6_CTS	CDIO D2	LICADTA DV	
OTC ES SOE	DCMI_D11	TIM3_ETR	SDIO_CMD	UART5_RX		PD2	PC11		DCMI_D4	SPI3_MISO	SDIO_D3	USART3_RX	
OTG_FS_SOF	TIM1_CH1	1203_SCL	MCO1	USART1_CK		PA8	PC12	-	SPI3_MOSI	12S2_SD	USART3_CK	DCMI_D9	
	12S_CKIN	DCM1_D3	MCO2	TIM3_CH4		PC9	PC8		TIM8_CH3	USART6_CK	SD10_D0	DCMI_D2	l
			FSMC_INT3	USART6_CK		PG7	PG6		FSMC_INT2				
	CAN1_RX	OTG_FS_DM	USART1_CTS	TIM1_CH4		PA11	PG8		ETH_PPS_OUT	USART6_RT	S	•	
TIM8_BKIN	TIM3_CH1	DCMI_PIXCLK	SPI1_MISO	ADC12_IN6		PA6	PA12		TIM1_ETR	USART1_RTS	OTG_FS_DP	CAN1_TX	
TIM3_CH2	DCMI_D10	SPI1_MOSI	CAN2_RX	12C1_SMBA		PB5	PB6	•	DCMI_D5	TIM4_CH1	USART1_TX	1201_SCL	
OTG_	_HS_ULPI_D4	1202_SDA	TIM2_CH4	USART3_RX		PB11	PB3	-	TIM2_CH2	SP13_SCK	1283_CK	JTD0	
T	IM2_CH1_ETR	1283_WS	SPI1_NSS	JTDI		PA15	PB10	-	SP12_SCK	1202_SCL	TIM2_CH3	12S2_CK	USART3_TX
			CAN1_TX	FSMC_D3		PD1	PDO	-	FSMC_D2	CAN1_RX			
			USART2_RTS	FSMC_NOE		PD4	PD3	-	FSMC_CLK	USART2_CTS			
		USART6_RX	FSMC_NCE3	FSMC_NE2		PG9	PD5	—	FSMC_NWE	USART2_TX			
DCMI_VSYNC	TIM4_CH2	USART1_RX	12C1_SDA	FSMC_NL		PB7	PG10	•	FSMC_NCE4_1	FSMC_NE3			
			DCMI_D3	FSMC_NBL1		PE1	PE0		TIM4_ETR	FSMC_NBL0	DCMI_D2		
		OTG_H	S_ULPI_STP	ADC123_IN10		PC0	PC1	─	ETH_MDC	ADC123_IN11			
	DCMI_D6	TIM9_CH1	TRACED2	FSMC_A21		PE5	PE6	•	TIM9_CH2	FSMC_A22	DCMI_D7	TRACED3	
			TRACED0	FSMC_A19		PE3	PE2	-	TRACECLK	FSMC_A23	ETH_MII_TX	D3	
			USART6_RTS	FSMC_NE4		PG12	PG11	-	FSMC_NCE4_2	ETH_RMII_	ΓX_EN ETH_N	III_TX_EN	
ETH_F	RMII_TXD1 E	TH_MII_TXD1	USART6_TX	FSMC_A25		PG14	PG13	←	FSMC_A24	USART6_CTS	ETH_MII_TX	(DO	
				VBAT		VBAT	PB4	•	NJTRST	SP13_MISO	TIM3_CH1	SPI1_MISO	
				5V0)	5V	GND	-	GND				
				5V0		5V	GND		GND				