

Version	Date	By	Change Dsecrption
V1.0	2025-4-9	lijian	1:Initial release version

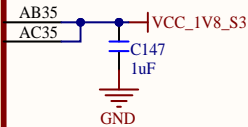
Title: 01_ATK_RK3588S_ReviSion_History.SchDoc		
Project: QuarkPI-CA2 V1.0.PrjPcb		
Size: A4	Author: ALIENTEK	
Date: 2025/4/2	Version: V1.0	Sheet: * of *



EMMCIO Domain

Operating Voltage=1.8V Only

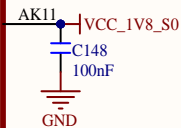
FSPI_D0_M0	/	EMMC_D0	/	GPIO2_D0_u	Y41	eMMC_D0
FSPI_D1_M0	/	EMMC_D1	/	GPIO2_D1_u	Y40	eMMC_D1
FSPI_D2_M0	/	EMMC_D2	/	GPIO2_D2_u	AA41	eMMC_D2
FSPI_D3_M0	/	EMMC_D3	/	GPIO2_D3_u	AA42	eMMC_D3
UART5_RX_M2	/	I2C1_SCL_M3	/	EMMC_D4	AB42	eMMC_D4
UART5_TX_M2	/	I2C1_SDA_M3	/	EMMC_D5	AD42	eMMC_D5
FSPI_CS0N_M0	/	EMMC_D6	/	GPIO2_D5_u	AD41	eMMC_D6
FSPI_CS1N_M0	/	EMMC_D7	/	GPIO2_D6_u	AE41	eMMC_D7
						AE42 eMMC_CMD
FSPI_CLK_M0	/	EMMC_CMD	/	GPIO2_A0_u	AB41	R16 eMMC_CLKOUT
UART5_CTSN_M1	/	I2C2_SDA_M2	/	EMMC_CLKOUT	AC40	DR eMMC_DATA_STROBE
UART5_RTSN_M1	/	I2C2_SCL_M2	/	EMMC_DATA_STROBE	AD40	eMMC_RSTn
						GPIO2_A2_d
						GPIO2_A3_d

EMMCIO_IV8_1
EMMCIO_IV8_2**EMMCIO VCCIO2****VCCIO2 Domain**

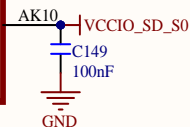
Operating Voltage=1.8V/3.3V

PWM8_M1	/	I2C3_SCL_M4	/	PDM1_SDI3_M0	/	JTAG_TCK_M1	/	UART2_TX_M1	/	SDMMC_D0	/	GPIO4_D0_u	AV2	SDMMC0_D0
PWM9_M1	/	I2C3_SDA_M4	/	PDM1_SDI2_M0	/	JTAG_TMS_M1	/	UART2_RX_M1	/	SDMMC_D1	/	GPIO4_D1_u	AR2	SDMMC0_D1
													AV1	SDMMC0_D2
													AT1	SDMMC0_D3
PWM10_M1	/	I2C8_SCL_M0	/	PDM1_SDI1_M0	/	JTAG_TCK_M0	/	UART5_CTSN_M0	/	SDMMC_D2	/	GPIO4_D2_u	AU1	SDMMC0_CMD
PWM10_M1	/	I2C8_SDA_M0	/	PDM1_SDI0_M0	/	JTAG_TMS_M0	/	UART5_RTSN_M0	/	SDMMC_D3	/	GPIO4_D3_u	AR1	R17 SD_CLK
PWM7_IR_M1	/	CAN0_TX_M1	/	PDM1_CLK1_M0	/	MCU_JTAG_TCK_M0	/	UART5_RX_M0	/	SDMMC_CMD	/	GPIO4_D4_u		
TEST_CLKOUT_M0	/	CAN0_RX_M1	/	PDM1_CLK0_M0	/	MCU_JTAG_TMS_M0	/	UART5_TX_M0	/	SDMMC_CLK	/	GPIO4_D5_d		

VCCIO2_IV8



VCCIO2





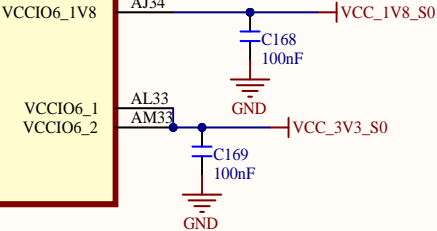
VCCIO6

VCCIO6 Domain

Operating Voltage=1.8V/3.3V

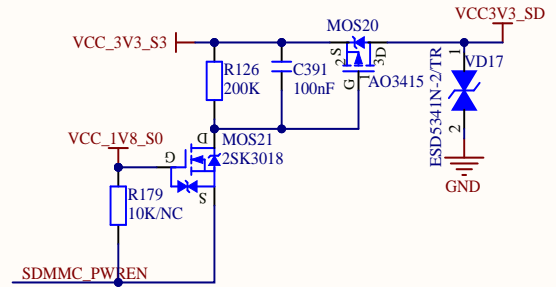
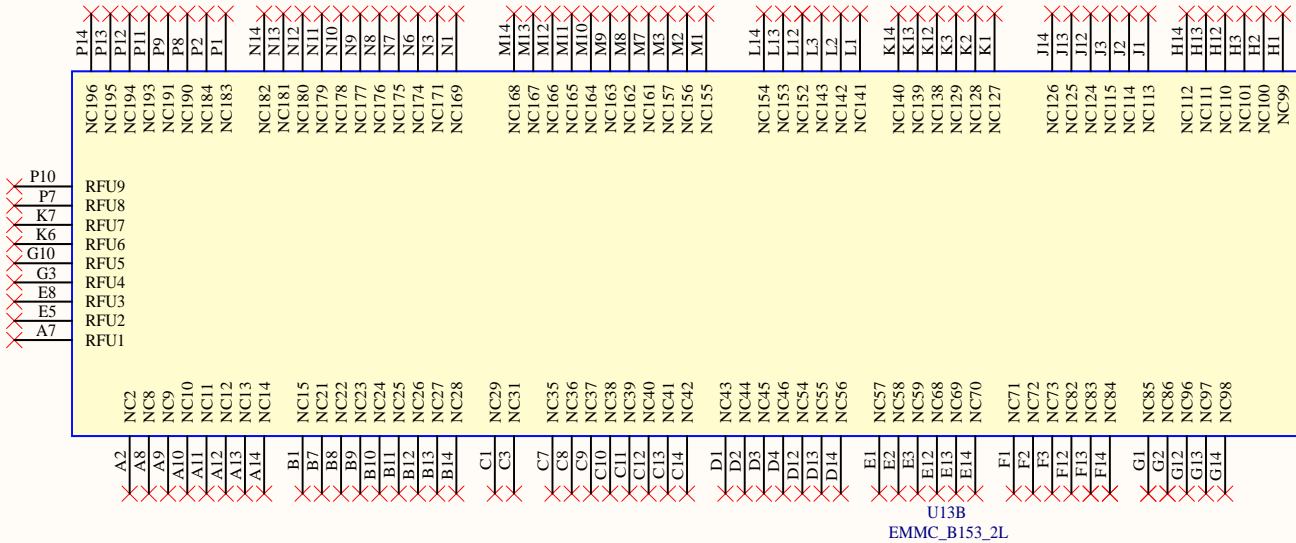
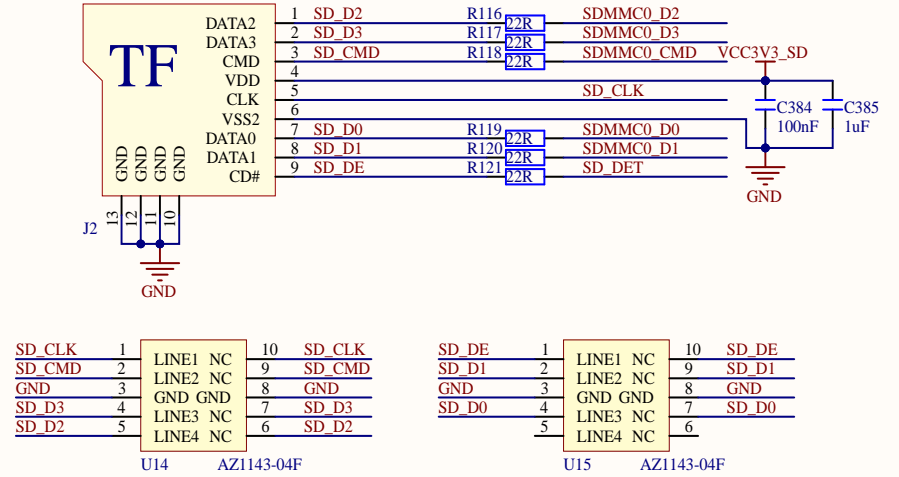
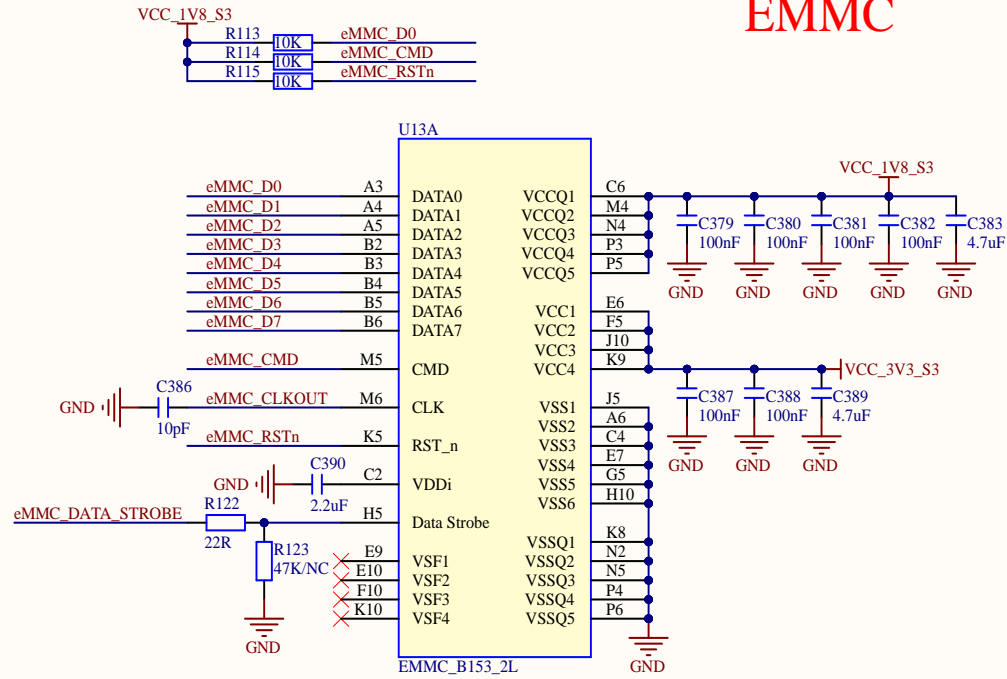
BT1120_D0	/	CIF_D0	/	/	I2S1_MCLK_M0	/	UART9_RTSN_M1	/	SPI0_MISO_M1	/	/	PCIE20X1_1_CLKREQN_M1	/	GPIO4_A0_d	AV19	GPIO4_A0			
BT1120_D1	/	CIF_D1	/	/	I2S1_SCLK_TX_M0	/	UART9_CTSN_M1	/	SPI0_MOSI_M1	/	/	PCIE20X1_1_WAKEN_M1	/	GPIO4_A1_d	AW18	GPIO4_A1			
BT1120_D2	/	CIF_D2	/	/	I2S1_LRCK_TX_M0	/	/	/	SPI0_CLK_M1	/	/	PCIE20X1_1_PERSTN_M1	/	GPIO4_A2_d	AV26	GPIO4_A2			
BT1120_D3	/	CIF_D3	/	/	I2S1_SCLK_RX_M0	/	UART0_TX_M2	/	/	/	/	/	/	GPIO4_A3_d	AY19	GPIO4_A3			
BT1120_D4	/	CIF_D4	/	/	I2S1_LRCK_RX_M0	/	UART0_RX_M2	/	SPI2_MISO_M1	/	I2C3_SCL_M2	/	/	GPIO4_A4_d	AW19	GPIO4_A4			
BT1120_D5	/	CIF_D5	/	/	I2S1_SDI0_M0	/	UART3_TX_M2	/	SPI2_MOSI_M1	/	I2C3_SDA_M2	/	/	GPIO4_A5_d	AU15	GPIO4_A5			
BT1120_D6	/	CIF_D6	/	/	I2S1_SDI1_M0	/	UART3_RX_M2	/	SPI2_CLK_M1	/	I2C5_SCL_M2	/	/	GPIO4_A6_d	AV18	I2C5_SCL_M2			
BT1120_D7	/	CIF_D7	/	/	I2S1_SDI2_M0	/	/	/	SPI2_CS0_M1	/	I2C5_SDA_M2	/	/	GPIO4_A7_d	AW26	I2C5_SDA_M2			
BT1120_CLKOUT	/	CIF_CLKIN	/	/	I2S1_SDI3_M0	/	UART8_TX_M0	/	SPI2_CS1_M1	/	I2C6_SDA_M3	/	/	GPIO4_B0_d	AW27	GPIO4_B0			
SPDIF1_TX_M1	/	MIPI_CAMERA0_CLK_M0	/	SATA2_ACT_LED_M0	/	I2S1_SDO0_M0	/	UART8_RX_M0	/	SPI0_CS1_M1	/	I2C6_SCL_M3	/	/	GPIO4_B1_u	AU22	GPIO4_B1		
CAN1_RX_M1	/	BT1120_D8	/	CIF_HREF	/	PWM14_M1	/	I2S1_SDO1_M0	/	UART8_RTSN_M0	/	SPI0_CS0_M1	/	I2C7_SCL_M3	/	PCIE20X1_1_BUTTON_RSTN	GPIO4_B2_u	AT15	GPIO4_B2
CAN1_TX_M1	/	BT1120_D9	/	CIF_VSYNC	/	PWM15_IR_M1	/	I2S1_SDO2_M0	/	UART8_CTSN_M0	/	/	I2C7_SDA_M3	/	PCIE20X1_2_BUTTON_RSTN	GPIO4_B3_u	AV23	GPIO4_B3	
BT1120_D10	/	CIF_CLKOUT	/	PWM11_IR_M1	/	I2S1_SDO3_M0	/	UART9_TX_M1	/	/	/	DP0_HPDIN_M0	/	SPDIF0_TX_M1	/	GPIO4_B4_u	AV27	GPIO4_B4	
BT1120_D11	/	/	/	PWM12_M1	/	/	/	UART9_RX_M1	/	SPI3_MISO_M1	/	/	/	/	GPIO4_B5_d	AU23	GPIO4_B5		
BT1120_D12	/	/	/	PWM13_M1	/	/	/	/	/	SPI3_MOSI_M1	/	I2C5_SCL_M1	/	SATA0_ACT_LED_M0	/	GPIO4_B6_d	AW22	HDMI0_TX_ON_H	
BT1120_D13	/	/	/	/	/	HDMI_TX0_SCL_M0	/	/	/	SPI3_CLK_M1	/	I2C5_SDA_M1	/	PCIE20X1_2_CLKREQN_M1	/	GPIO4_B7_u	AV22	HDMITX0_SCL_M0	
BT1120_D14	/	/	/	/	/	HDMI_TX0_SDA_M0	/	/	/	SPI3_CS0_M1	/	I2C8_SCL_M3	/	PCIE20X1_2_WAKEN_M1	/	GPIO4_C0_u	AW23	HDMITX0_SDA_M0	
BT1120_D15	/	SPDIF1_TX_M2	/	PWM6_M1	/	HDMI_TX0_CEC_M0	/	/	/	SPI3_CS1_M1	/	I2C8_SDA_M3	/	PCIE20X1_2_PERSTN_M1	/	GPIO4_C1_d	AY26	HDMITX0_CEC_M0	

Note:
BT1120 Only Support Output



EMMC

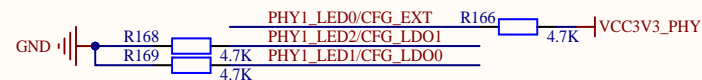
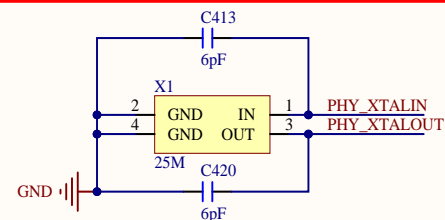
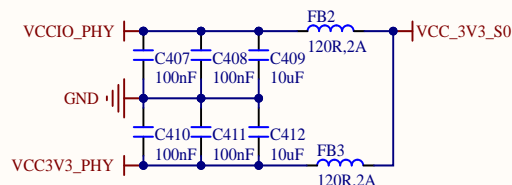
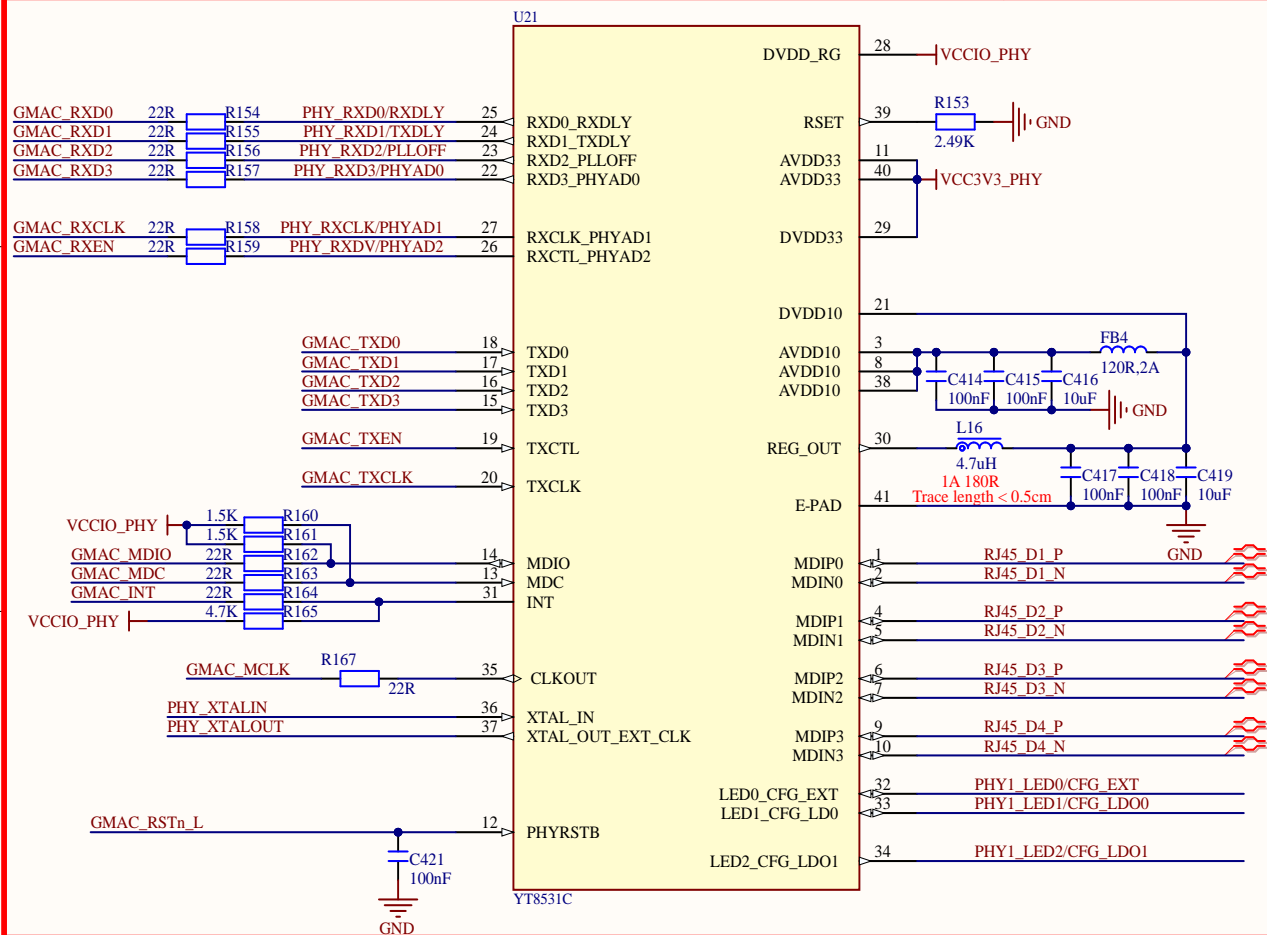
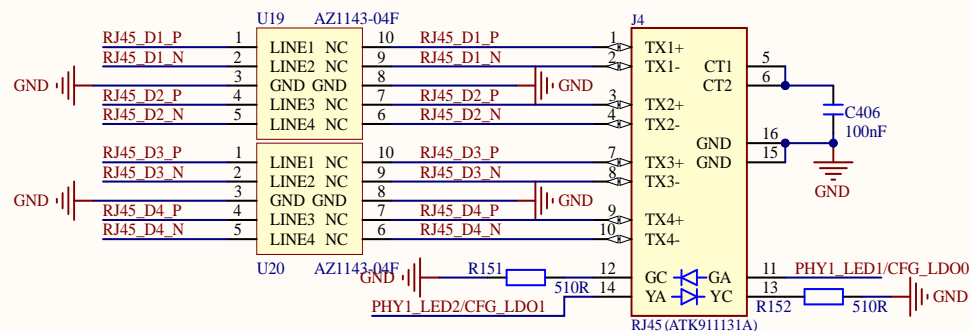
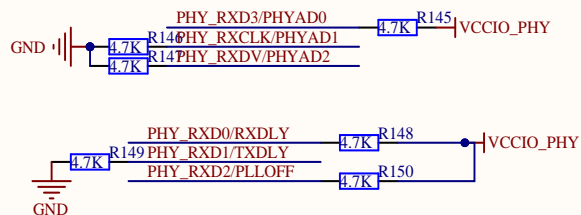
TFCARD



Title: 17_ATK_RK3588S_EMMC_SD.SchDoc		
Project: QuarkPI-CA2 V1.0.PrfPcb		
Size: A4	Author: ALIENTEK	
Date: 2025/4/2	Version: V1.0	Sheet: * of *



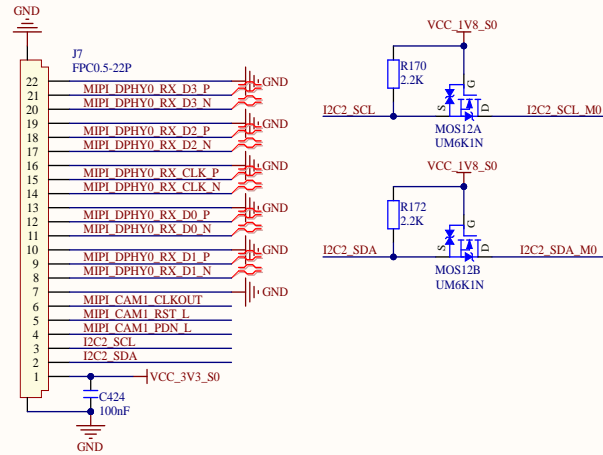
NET



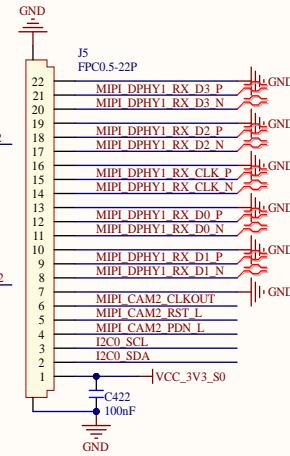
Title: 18_ATK_RK3588S_GMAC.SchDoc		
Project: QuarkPI-CA2 V1.0.PrfPcb		
Size: A4	Author: ALIENTEK	
Date: 2025/4/2	Version: V1.0	Sheet: * of *



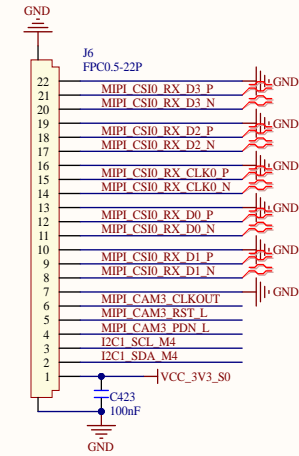
MIPI CSI



CAM1

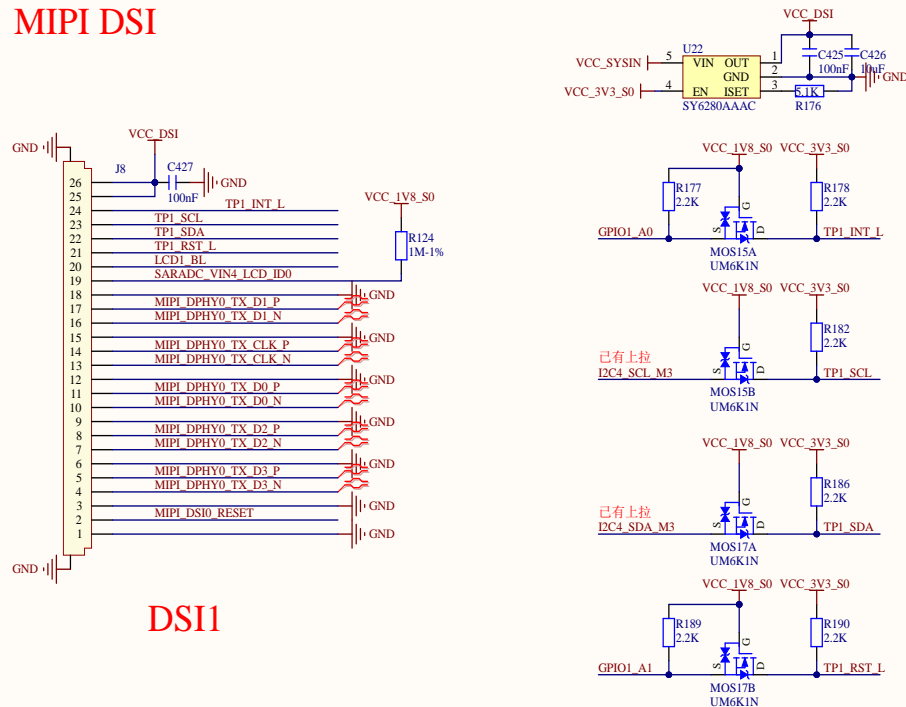


CAM2

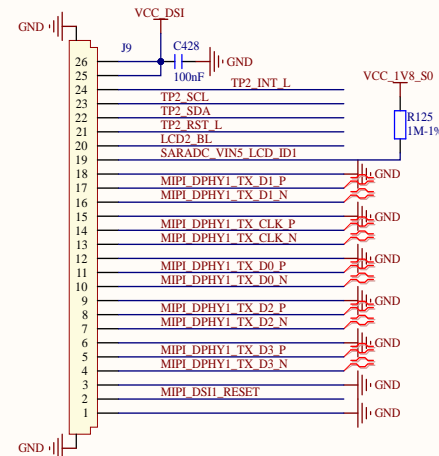


CAM3

MIPI DSI

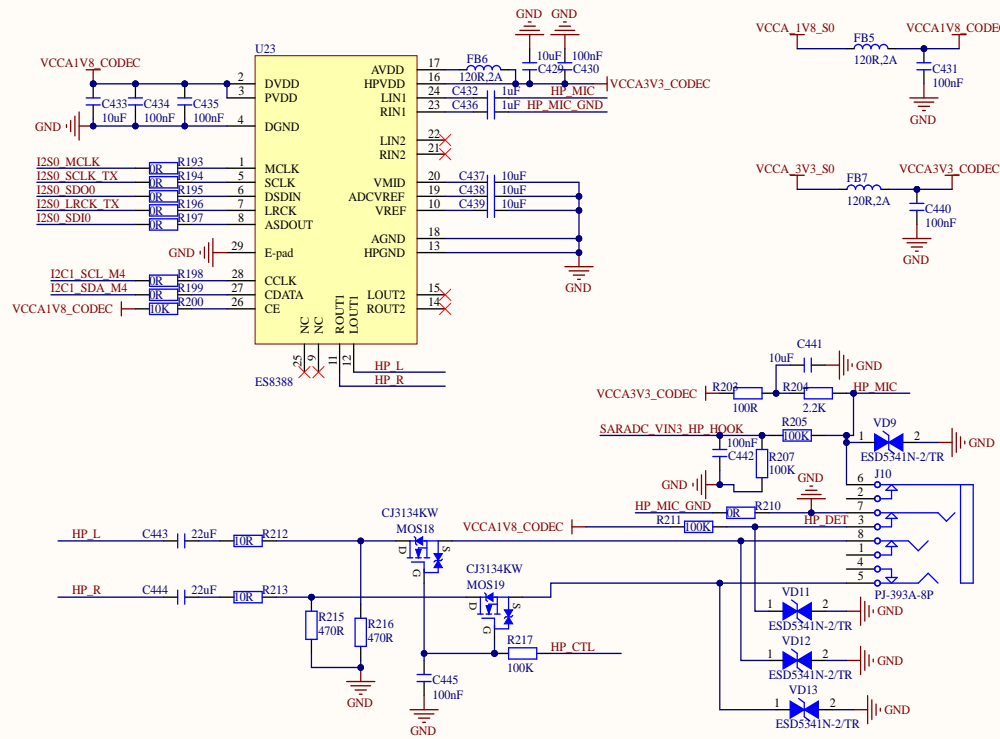


DSI1

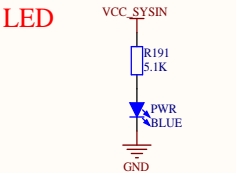


DSI2

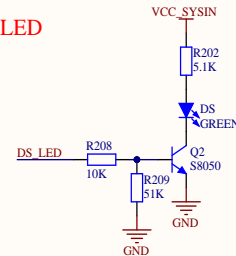
AUDIO



PWR LED

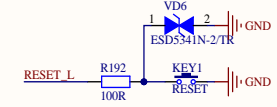


USER LED

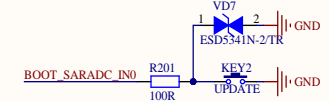


KEY

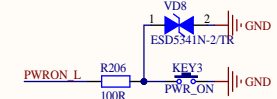
RESET



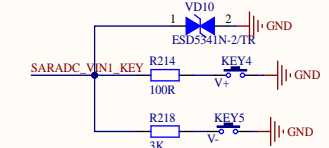
UPDATE



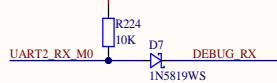
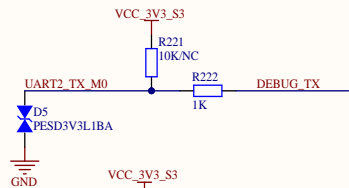
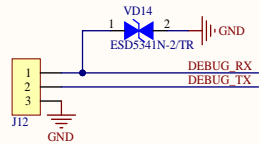
PWR_ON



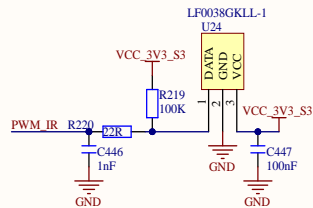
VOL



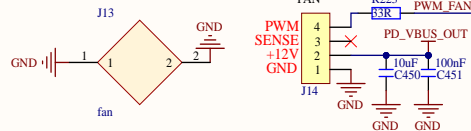
DEBUG UART



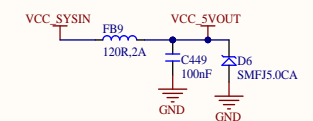
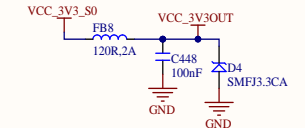
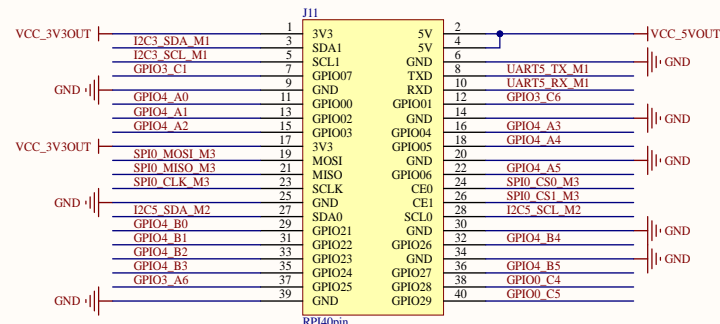
IR



FAN



40PIN



A



C



M.2 EKEY

