

AN14090

i.MX 93 Application Processor Estimated Power-on Hours

Rev. 1.0 — 9 November 2023

Application note

Document information

Information	Content
Keywords	AN14090, Power-on Hours, POH, i.MX93
Abstract	This document describes the estimated product Power-on Hours (PoH) for the i.MX 93 applications processor ("device") based on the criteria used in the qualification process.



1 Introduction

This document describes the estimated product Power-on Hours (PoH) for the i.MX 93 applications processor ("device") based on the criteria used in the qualification process.

The product PoH described here are estimates and do not represent a guaranteed lifetime for a product.

This document is intended to provide guidance on how to interpret the different device qualification levels in terms of the target operating voltage, the maximum supported junction temperature (T_j), and how these relate to the PoH of the device.

The data presented in this document is provided for convenience. However, it does not represent all potential failing mechanisms and may not accurately represent the behavior for all mission profiles or applications. The data is based on a single activation energy and voltage acceleration parameter, using the Arrhenius equation for temperature acceleration and Power Law for voltage acceleration, along with the data collected during High Temperature Operating Life (HTOL), to demonstrate how temperature could impact the PoH of the product.

2 Device qualification level and available PoH

Each qualification level supported (Automotive, Industrial, and Commercial) defines several Power-on Hours (PoH) available to the device under a given set of conditions such as:

- The target core voltage (VDD_SOC) for the application.
- The junction temperature of the device (T_j).
 - It is important to note that while the device can operate at the maximum T_j listed in its data sheet, operating the device at this temperature for an extended period reduces its operating PoH.
 - Always ensure that the device is thermally managed and the maximum junction temperature is not exceeded.

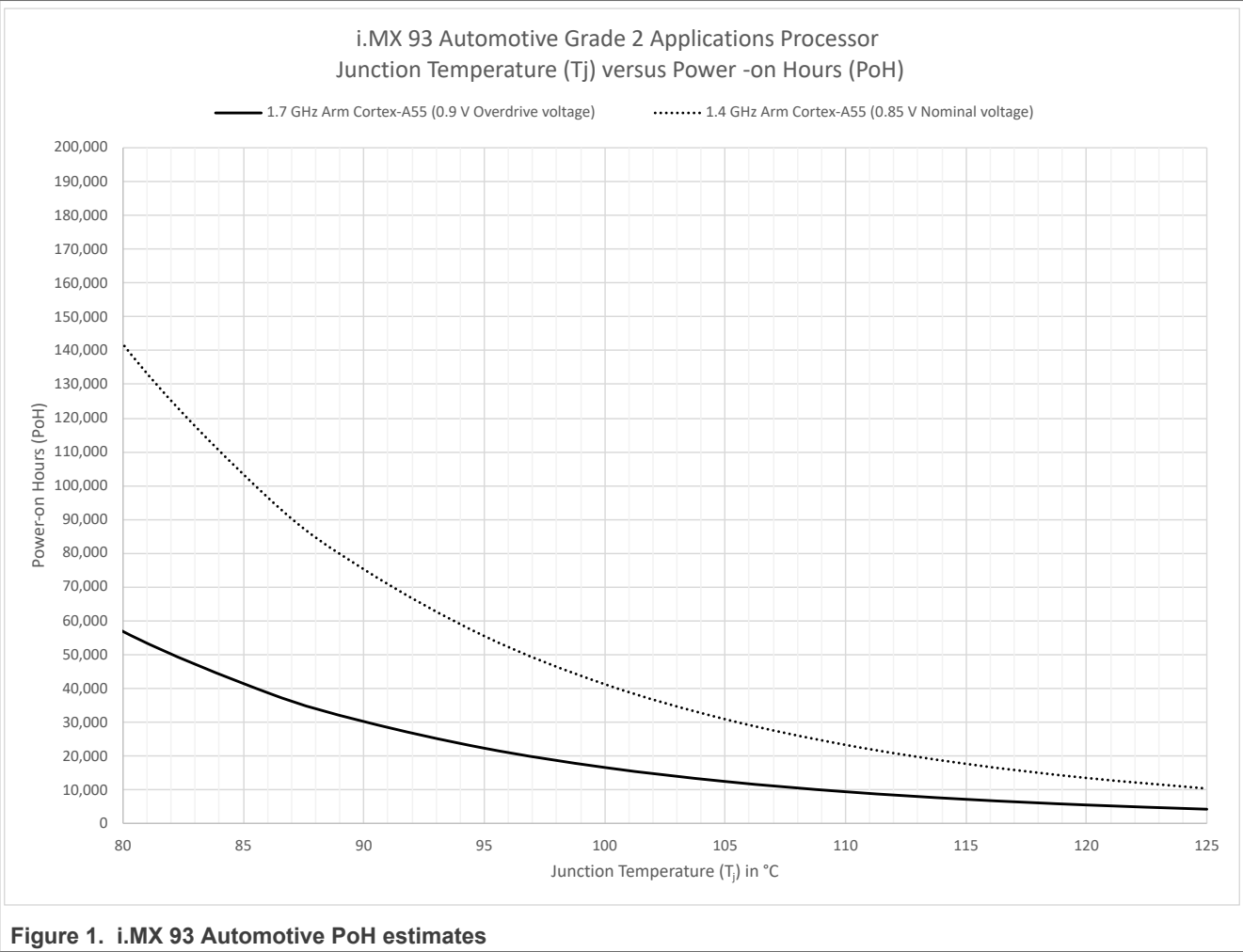
The junction temperature (T_j) of the device is the temperature of the transistors in the device. It is a different measurement than the case and the ambient temperature. Most applications do not have a constant T_j during operation.

The charts in this document show the relationship between the T_j and PoH. The percentage of on-time at different temperatures is part of what defines each Mission Profile. When the junction temperature is not constant during the customer application, the Effective Junction Temperature (T_{j-eff}) can be calculated using weighing with the Arrhenius factor (For more details on T_{j-eff} , see [Section 3](#)).

Note: Data provided in this document are estimates for PoH based on qualification test data and experience with this product. These estimates must not be viewed as a limit on an individual device lifetime, or be construed as a guarantee by NXP as to the actual lifetime of the device. Sales and warranty terms and conditions still apply.

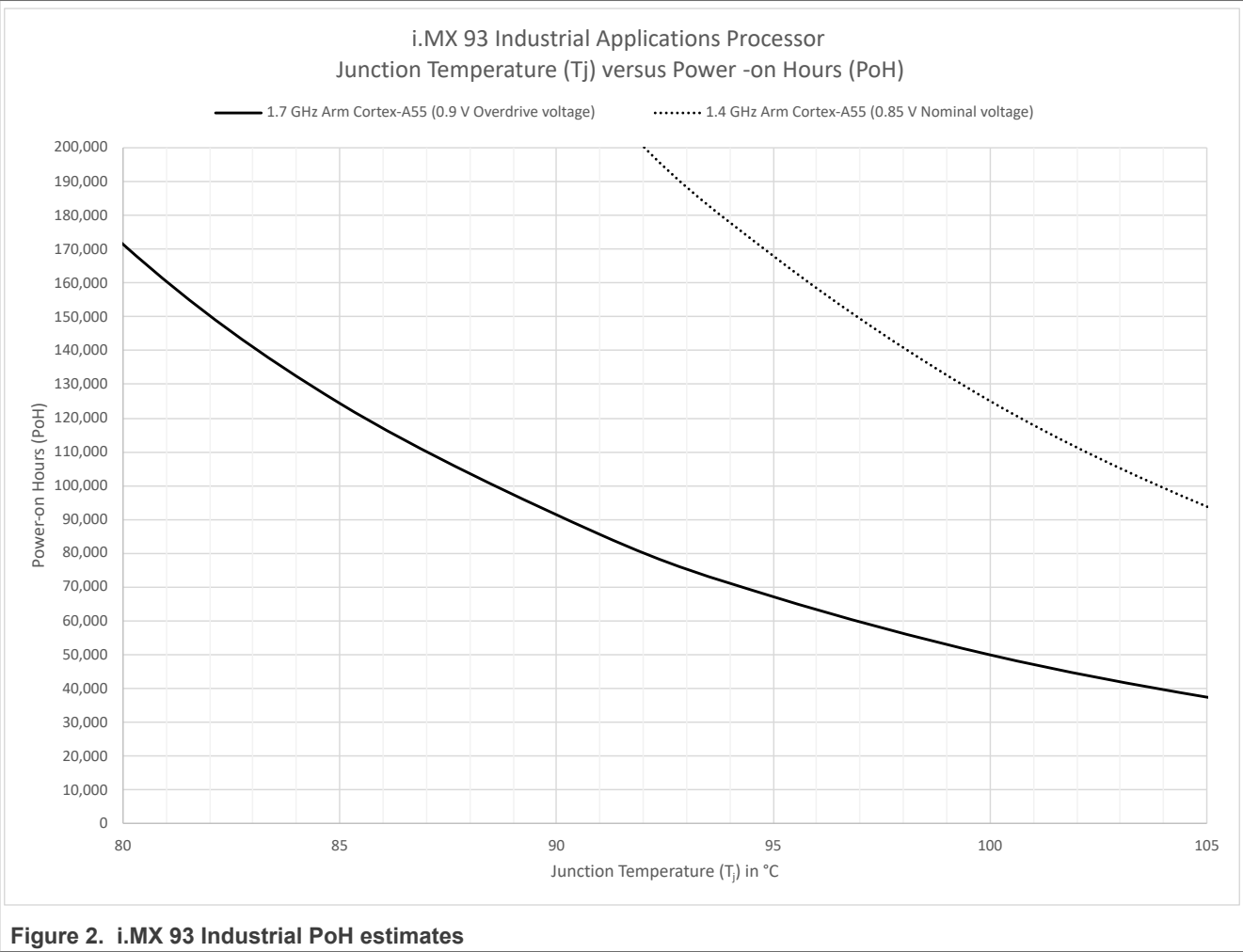
2.1 Automotive PoH estimates

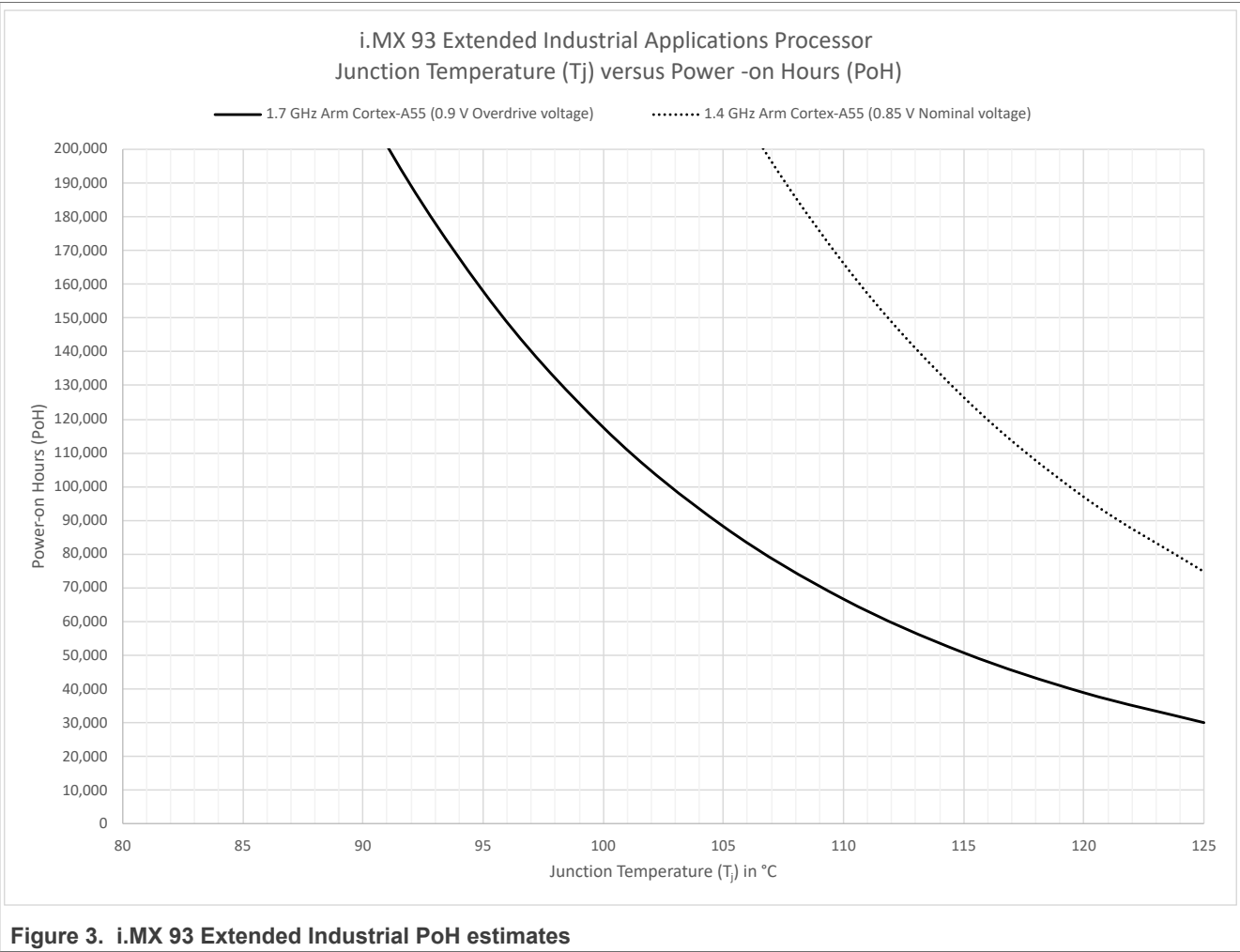
[Figure 1](#) establishes guidelines for estimating PoH as a function of junction temperature. PoH can be read directly from the curves below to determine the impact of junction temperature at the listed conditions.



2.2 Industrial qualification

Figure 2 and Figure 3 provide several PoH for the use conditions of the Industrial and Extended Industrial device. The PoH value assumes that the product is powered on and active for 100 % of the time (100 % duty cycle). PoH can be read directly from the curves below to determine the impact of junction temperature at the listed conditions.





2.3 Commercial qualification

Figure 4 provides the number of PoH for the use conditions of the Commercial device. PoH can be read directly from the curves below to determine the impact of junction temperature at the listed conditions.

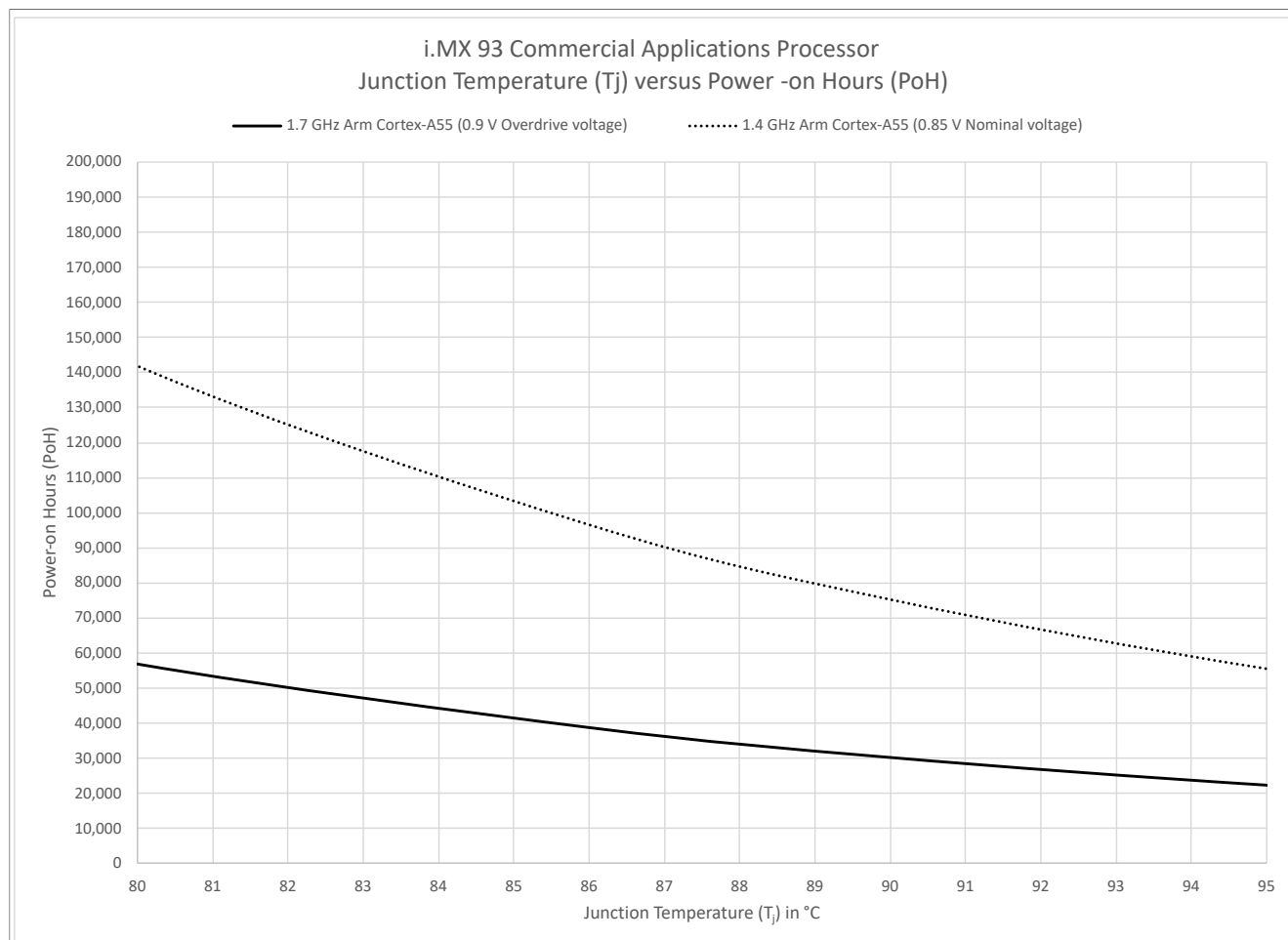


Figure 4. i.MX 93 Commercial PoH estimates

3 Effective Junction Temperature

The junction temperature (T_j) of the device is the temperature of the transistors in the device. It is a different measurement than the case and the ambient temperature. Most applications do not have a constant T_j during operation.

The charts in this document show the relationship between the T_j and PoH. The percentage of on-time at different temperatures is part of what defines each Mission Profile. The Effective Junction Temperature (T_{j-eff}) is the single T_j that represents the mission profile and can be used to extrapolate the PoH in the charts above.

- The T_{j-eff} depends only on the temperatures during on-time duty cycles of a Mission Profile. Temperatures when the device is powered off do not affect T_{j-eff} .
- T_{j-eff} is not a simple average of temperatures, as the on-time at higher temperatures consumes more operating life than on-time at lower temperatures.
- When the junction temperature is not constant during the customer application, the T_{j-eff} can be calculated using weighting with the Arrhenius factor.

3.1 Calculating T_{j-eff}

Assuming that the temperature dependence follows Arrhenius behavior, one can calculate the T_{j-eff} using the following method:

1. Determine the percentage of time (t_n) that the application is powered on at a small set of discrete temperatures (T_n).
2. Calculate the average failure rate using the Arrhenius method.

$$FR_{AV} = \left[t_1 \cdot e^{\left(\frac{-E_A}{kT_1}\right)} + t_2 \cdot e^{\left(\frac{-E_A}{kT_2}\right)} + \dots + t_n \cdot e^{\left(\frac{-E_A}{kT_n}\right)} \right] \quad (1)$$

3. The effective temperature can then be calculated.

$$T_{j-eff} = \frac{-E_A}{k \ln(FR_{AV})} \quad (2)$$

Here are some notes on the variables and constants used in the formulas above:

E_A = Activation Energy. A typical value is 0.7 eV and is used to generate the charts in this document.

k = Boltzmann constant; 8.62×10^{-5} .

T_n = The temperature must be noted in Kelvin. The result for T_{j-eff} must also be in Kelvin.

t_n = The percentage of time at a given temperature must be noted in decimal. For instance, 50% becomes 0.50.

Here is a simple example calculating the T_{j-eff} of an application that has two different constant values. In this scenario, the T_j of the device is at 100°C for 50% of the time, and 50°C for the other 50% of the time the device is powered on, resulting in an average of 75°C.

$$FR_{AV} = \left[0.5 \cdot e^{\left(\frac{-0.7}{k373.15}\right)} + 0.5 \cdot e^{\left(\frac{-0.7}{k323.15}\right)} \right] = 1.83 \times 10^{-10} \quad (3)$$

$$T_{j-eff} = \frac{-0.7}{k \ln(FR_{AV})} = 362.18 \text{ K} = 89.03^\circ\text{C} \quad (4)$$

In this example, you can see that the T_{j-eff} of 89°C is higher than the average temperature of 75°C, showing that higher temperatures have a bigger impact on the life of the device.

4 Conclusion

Selecting the optimal operating performance point and thermal envelope is critical to meet the target application PoH. Trade-offs between the target operating voltage/frequency of the device and the operating junction temperature (T_j) of the device can greatly improve the PoH of the device.

Lowering the operating junction temperature in the application is the most effective means to increase the PoH of the device without affecting the performance of the device. This can be accomplished by increasing the thermal dissipation capacity in the application. In cases where the thermal properties cannot be altered, a lower operating voltage can be used to increase the PoH of the device. Lowering the voltage may result in lower performance; the operating frequency may have to be adjusted lower to match the voltage specified in the data sheet.

The data and examples provided in this application are a reference to support the customer in their application development.

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5 Revision history

[Table 1](#) summarizes the revisions done to this document.

Table 1. Revision history

Revision number	Release date	Description
1	9 November 2023	Initial public release

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