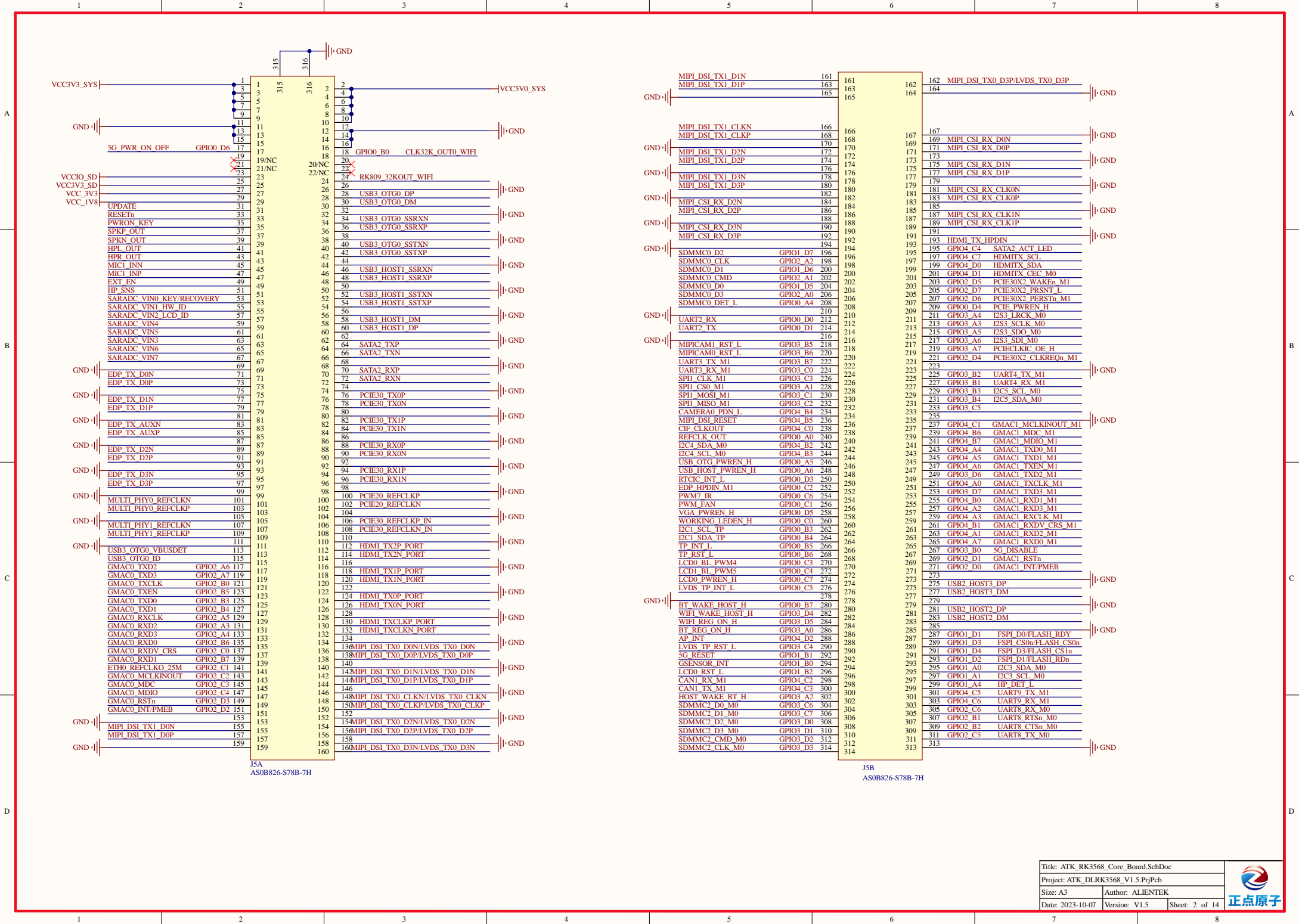
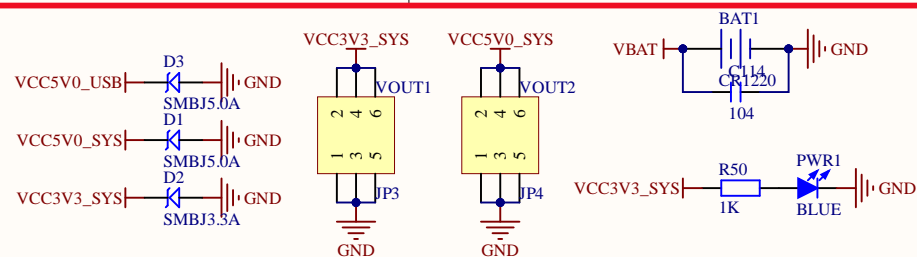
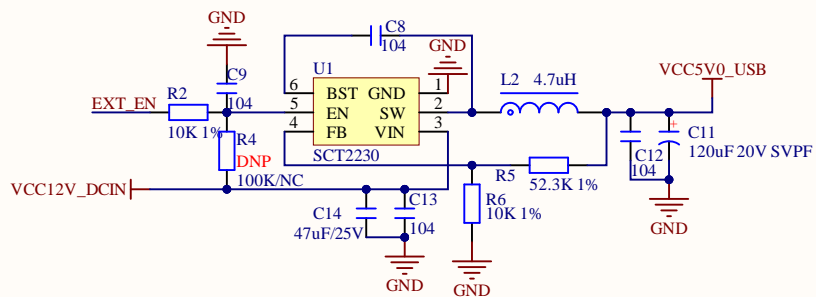
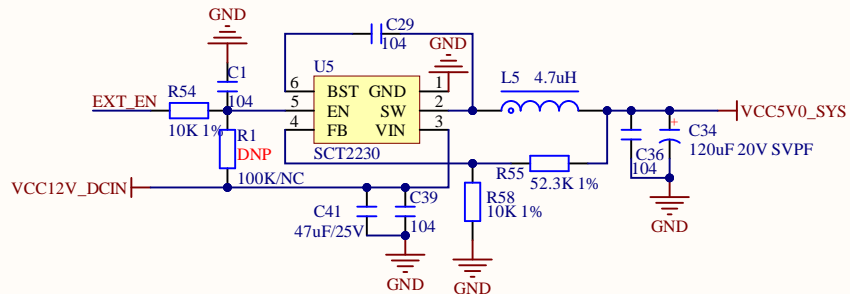
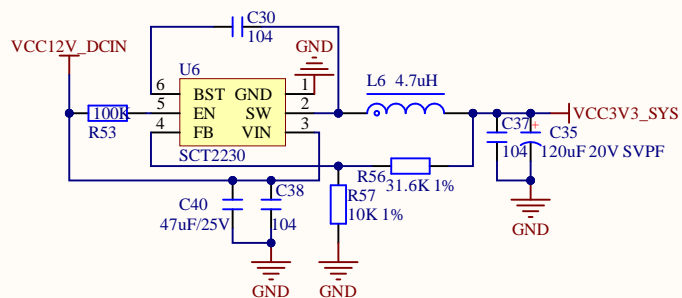
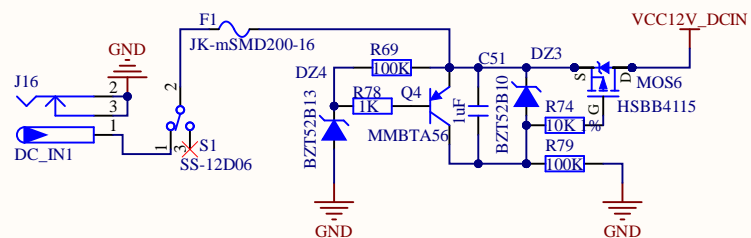


Revision History

Version	Date	By	Change Description
V1.5	2023-6-30	zuozhongkai	1:Initial release version



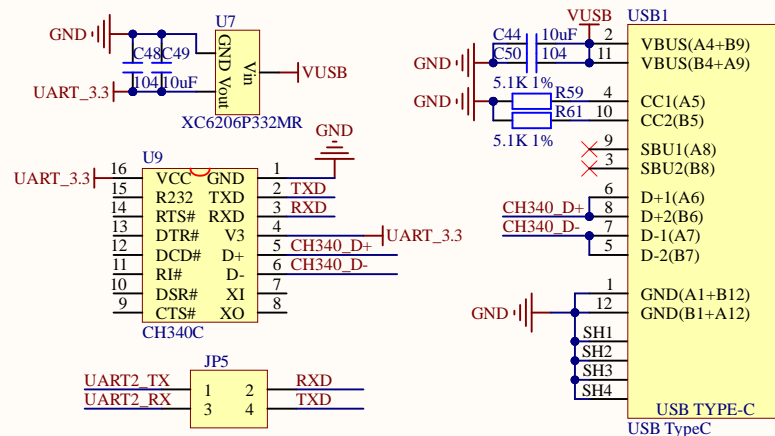
DC POWER IN



IO

1.8V	GPIO4_B2	I2C4_SDA_M0	1	2	I2C3_SDA_M0	GPIO1_A0 3.3V
1.8V	GPIO4_B3	I2C4_SCL_M0	3	4	I2C3_SCL_M0	GPIO1_A1 3.3V
3.3V	GPIO3_A1	SPI1_CS0_M1	5	6	I2C1_SCL_TP	GPIO0_B3 3.3V
3.3V	GPIO3_C1	SPI1_MOSI_M1	7	8	I2C1_SDA_TP	GPIO0_B4 3.3V
3.3V	GPIO3_C2	SPI1_MISO_M1	9	10	UART9_TX_M1	GPIO4_C5 3.3V
3.3V	GPIO3_C3	SPI1_CLK_M1	11	12	UART9_RX_M1	GPIO4_C6 3.3V
3.3V	GPIO1_B1	5G_RESET	13	14	REFCLK_OUT	GPIO0_A0 3.3V
3.3V	GPIO0_C0	WORKING_LEDEN_H	15	16	MIPICAM1_RST_L	GPIO3_B5 3.3V
3.3V	GPIO3_B0	5G_DISABLE	17	18	GBC_KEY	GPIO3_C4 3.3V
3.3V			19	20	GBC_LED	GPIO3_C5 3.3V
			21	22		

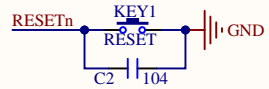
USB USART&USB POWER



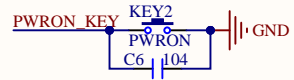
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Project: ATK_DLRK3568_V1.5.PjPcb		
Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 3 of 14



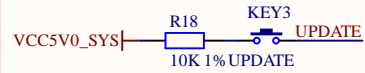
RESET



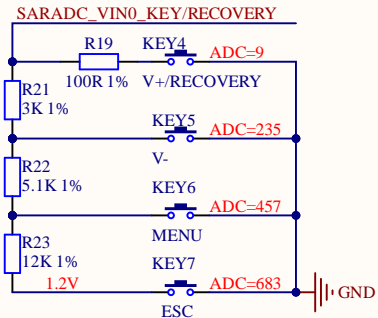
PWRON



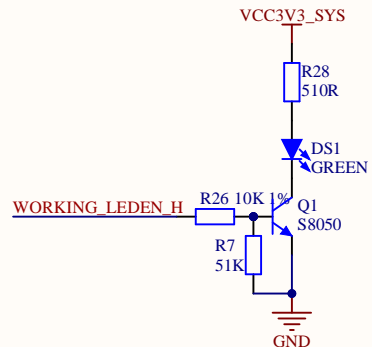
UPDATE



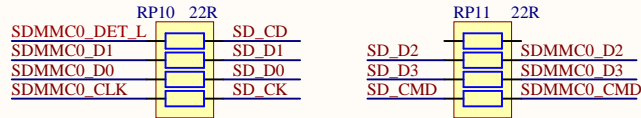
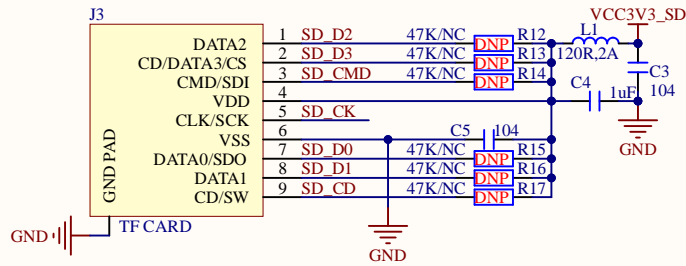
KEY



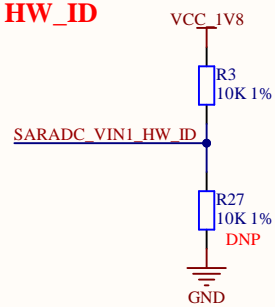
LED



TF Card

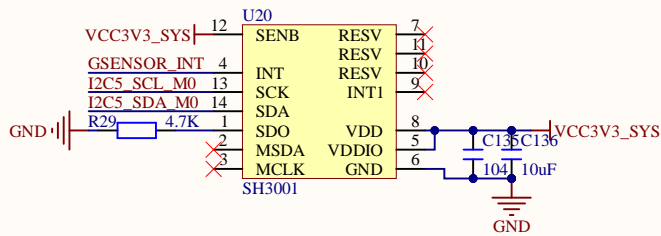


HW_ID



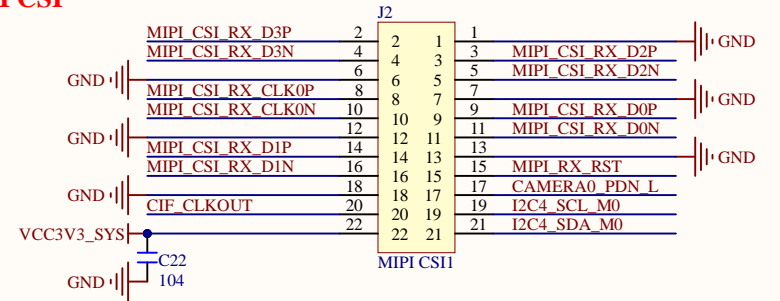
HW_ID	Rup	Rdown	ADC	
EVB1	10K	DNP	1023	1.8V
EVB2	20K	100K	852	1.5V
EVB3	18K	36K	681	1.2V
EVB4	51K	51K	512	0.9V
EVB5	36K	18K	340	0.6V
EVB6	100K	20K	170	0.3V
EVB7	DNP	10K	0	0V

6 AXIS SENSOR

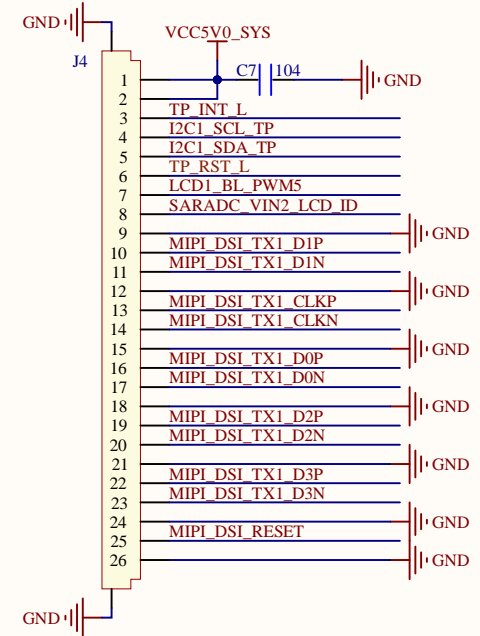


SH3001 IIC Address:0X36

MIPI CSI



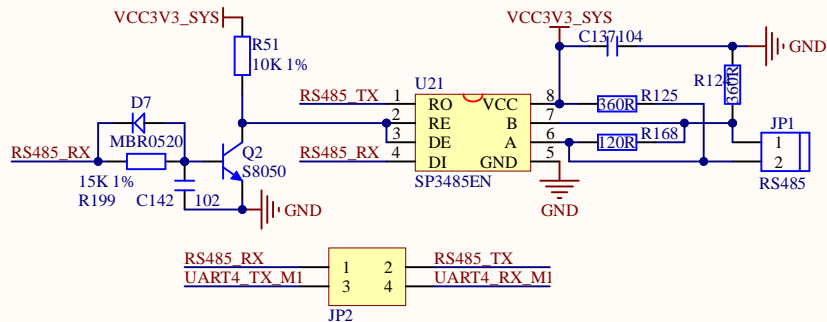
MIPI DSI



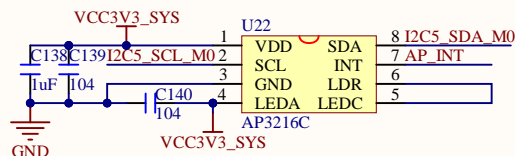
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Project: ATK_DLRK3568_V1.5.PjPcb		
Size: A4	Author: zuo	
Date: 2023-10-07	Version: V1.5	Sheet: 4 of 14



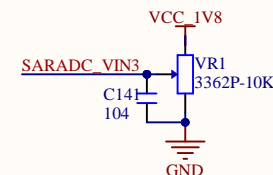
RS485



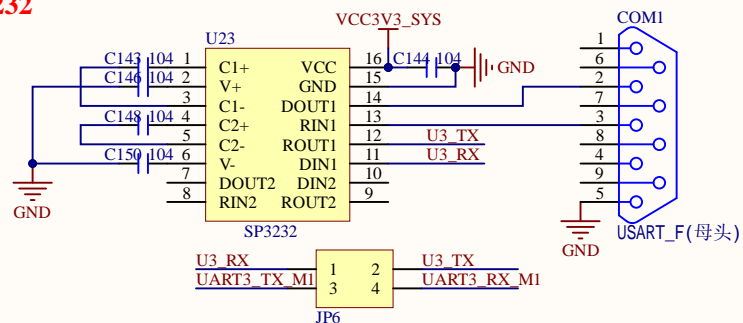
ALS&PS SENSOR



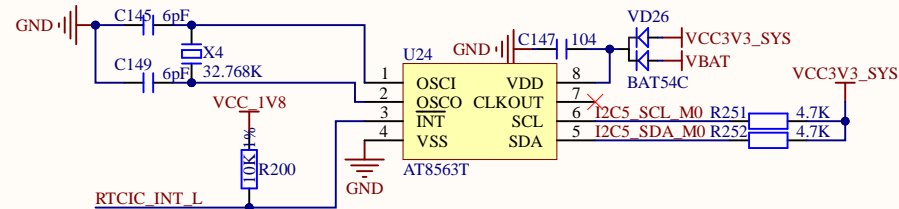
ADC



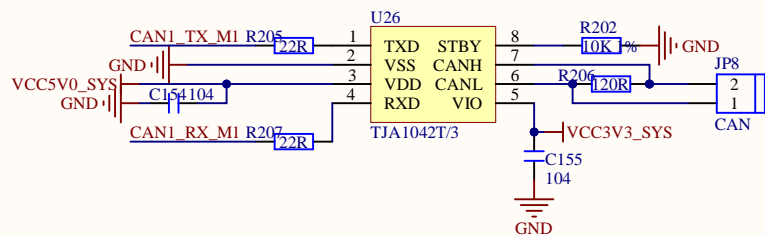
RS232



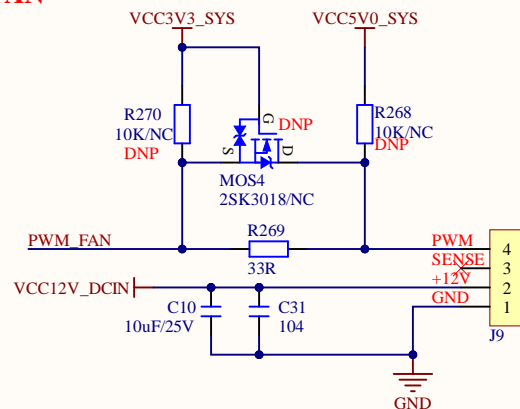
RTC



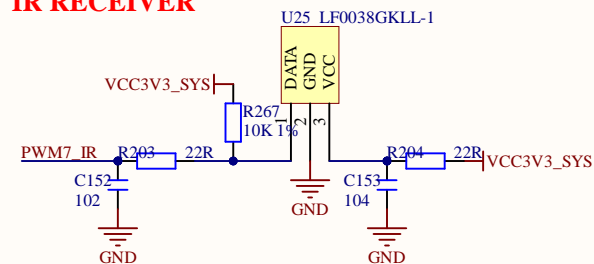
CAN

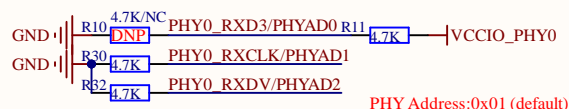


FAN

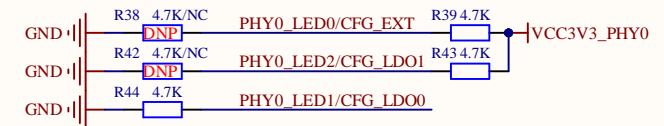
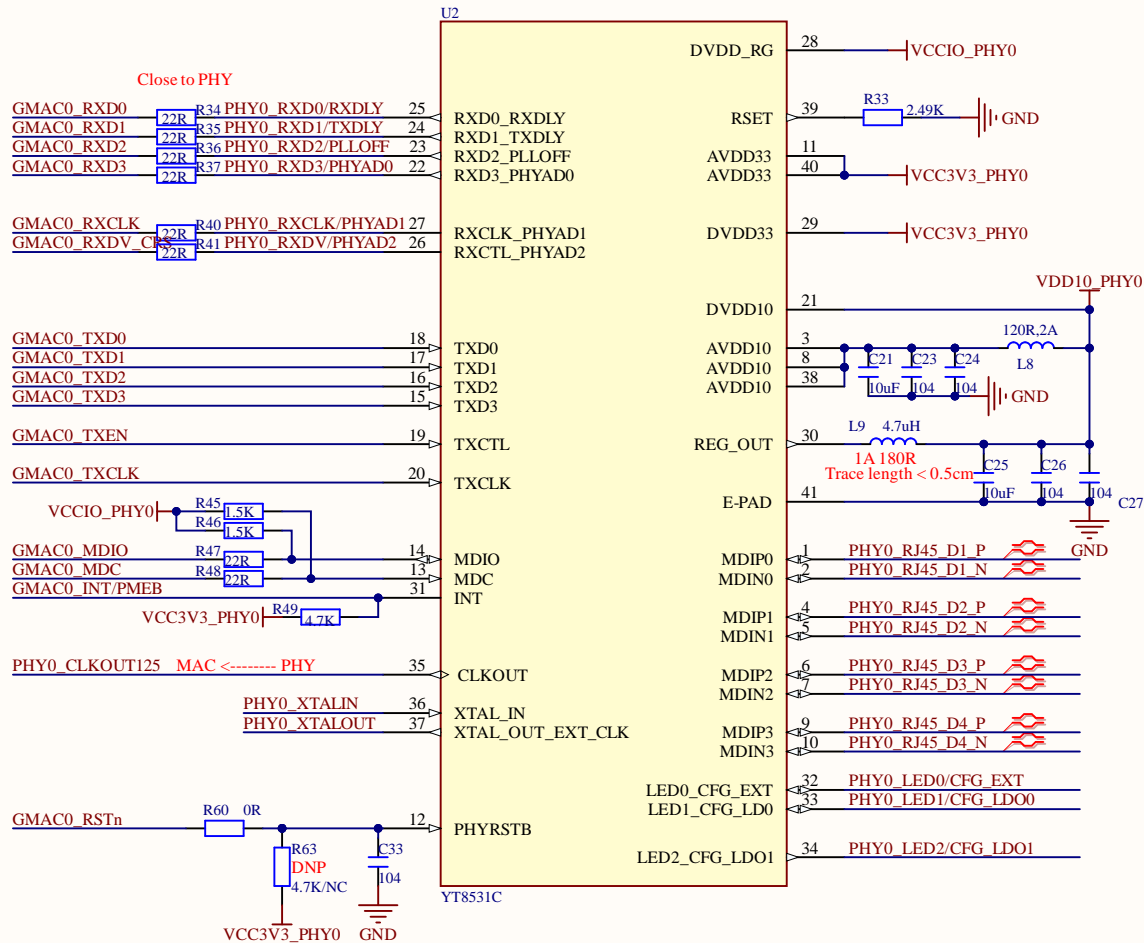
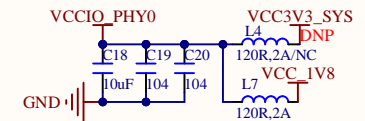
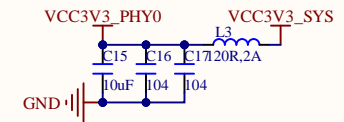
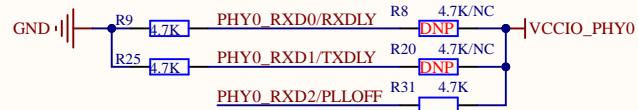


IR RECEIVER

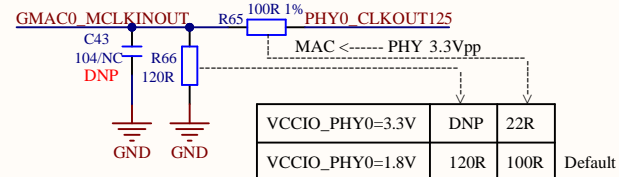
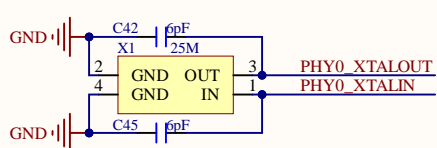
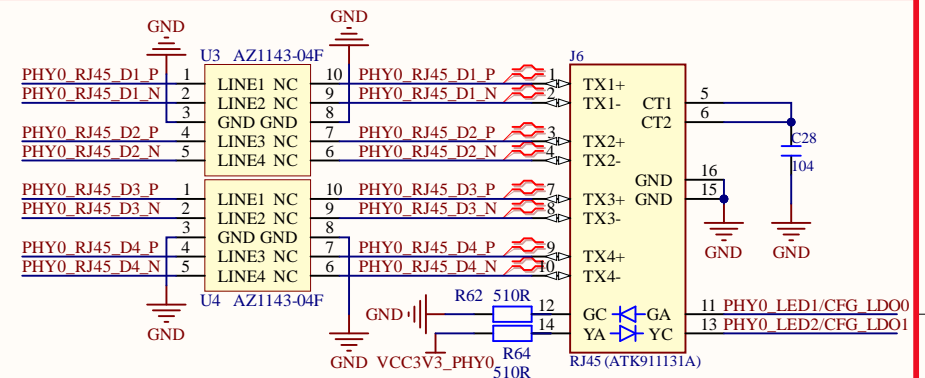


GMAC0 10/100/1000M

PHY Address:0x01 (default)



RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V	1'b1	2'b00
External 1.8V (default)	1'b1	2'b00
External 1.8V	1'b0	2'b10

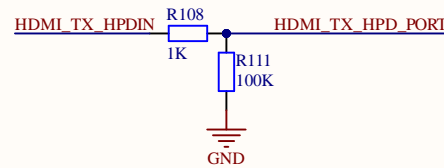
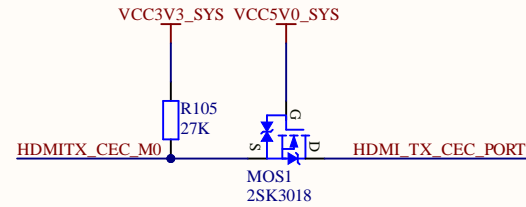
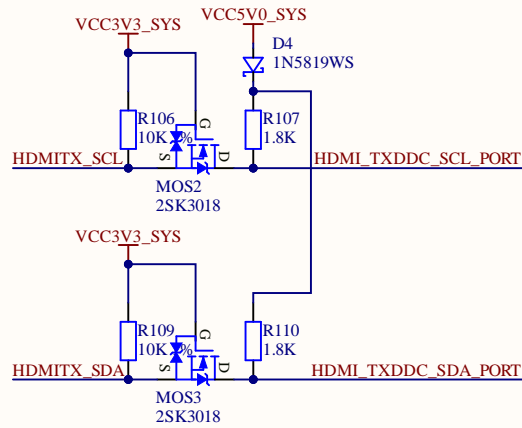
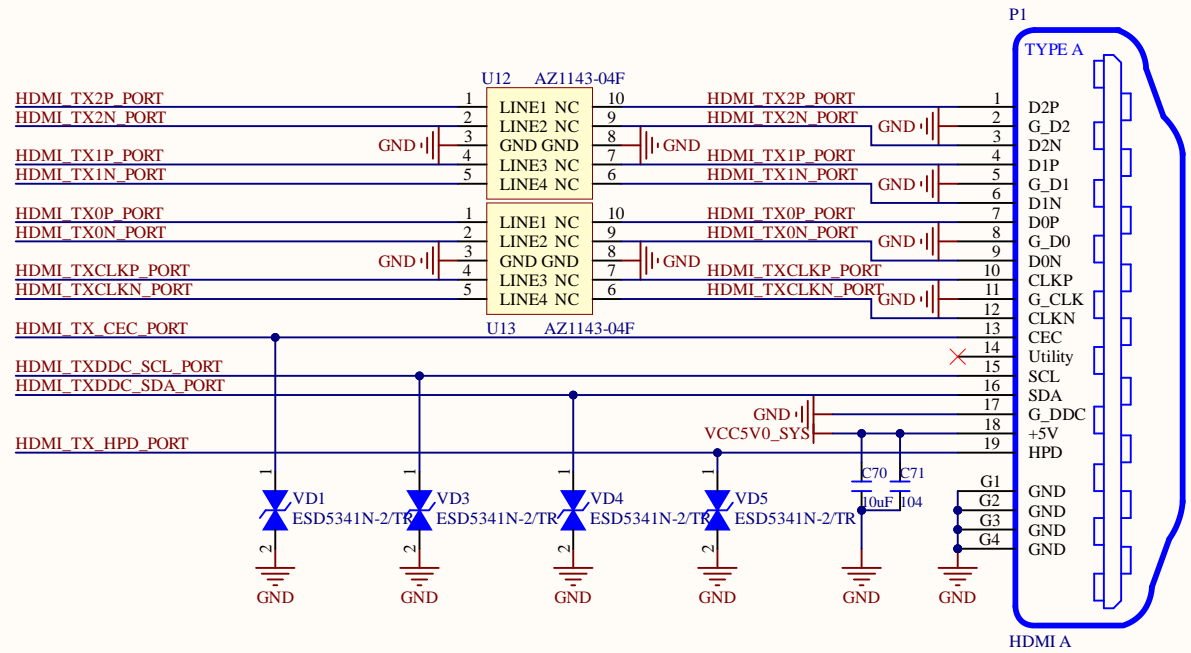


VCCIO_PHY0=3.3V	DNP	22R	Default
VCCIO_PHY0=1.8V	120R	100R	

Title: ATK_RK3568_NET0.SchDoc		
Project: ATK_DLRK3568_V1.5.PjPcb		
Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 6 of 14



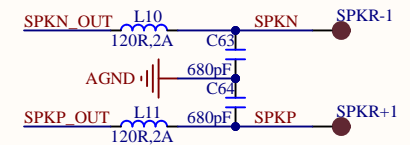
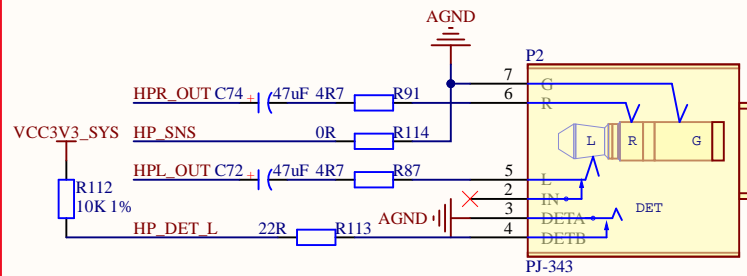
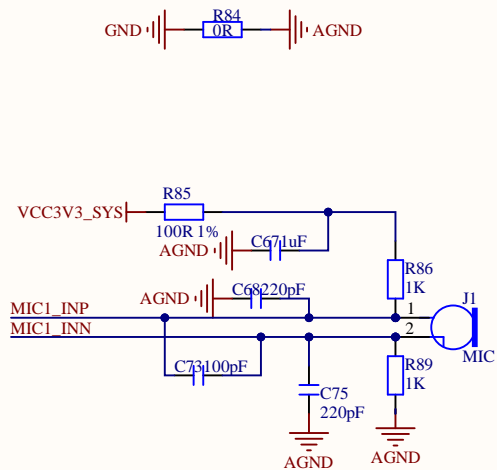
HDMI



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Project: ATK_DLRK3568_V1.5.PjPcb		
Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 8 of 14



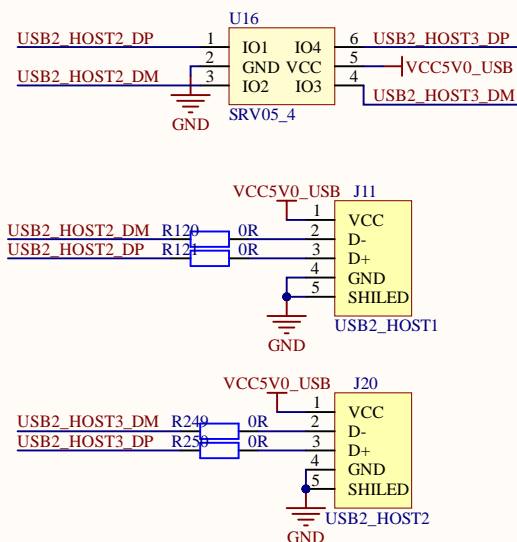
AUDIO



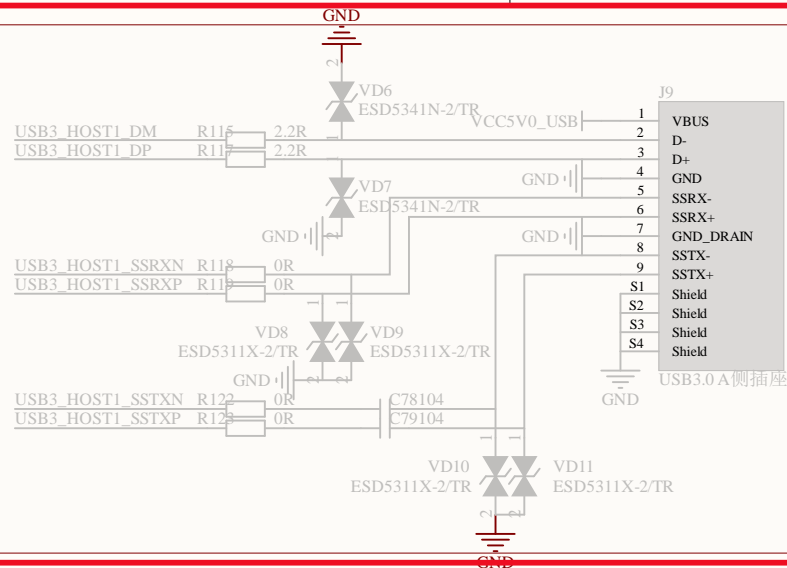
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Project: ATK_DLRK3568_V1.5.PrjPcb		
Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 9 of 14



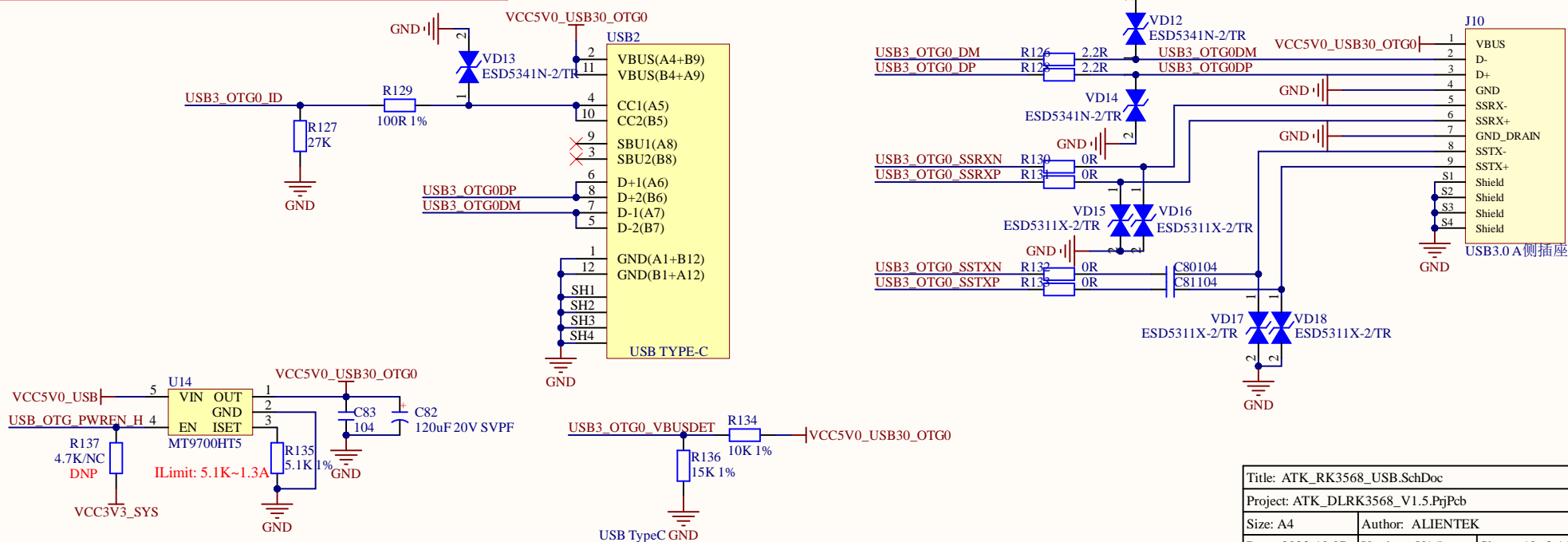
USB 2.0 HOST



USB 3.0 HOST



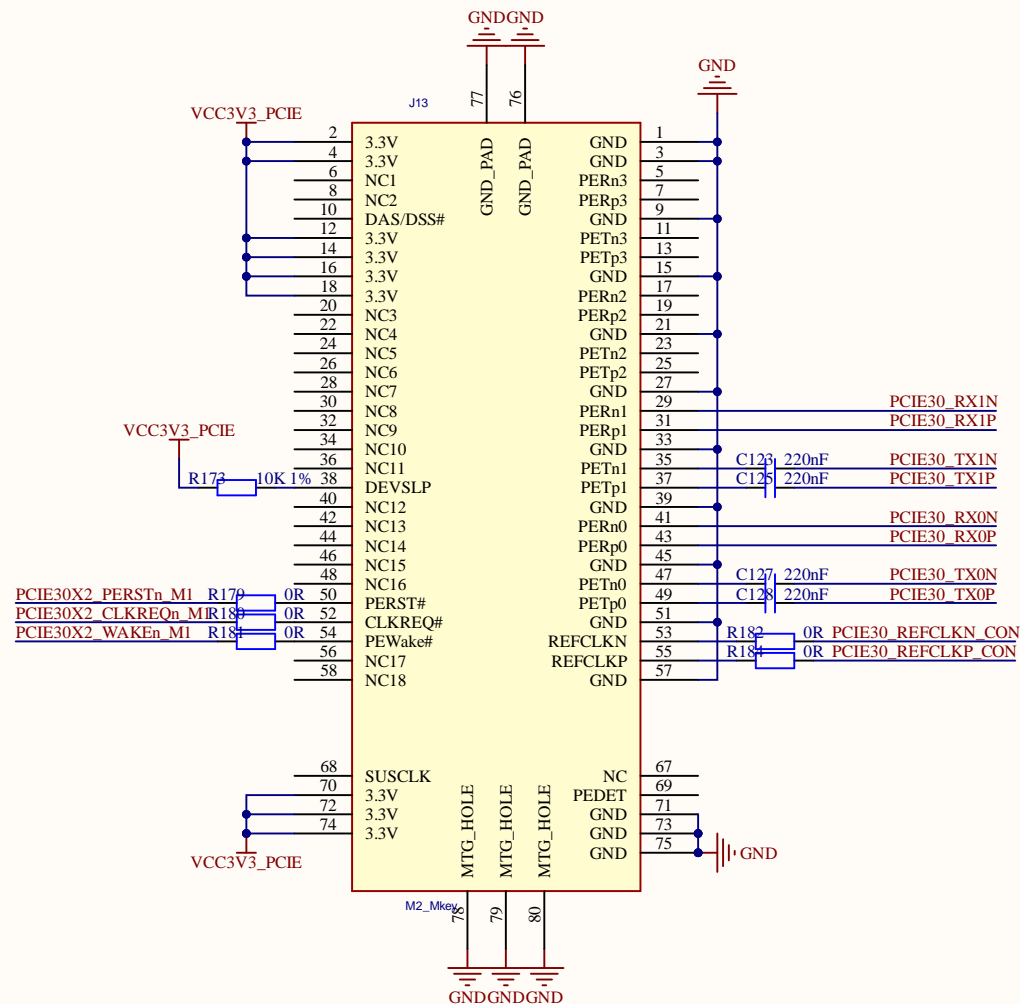
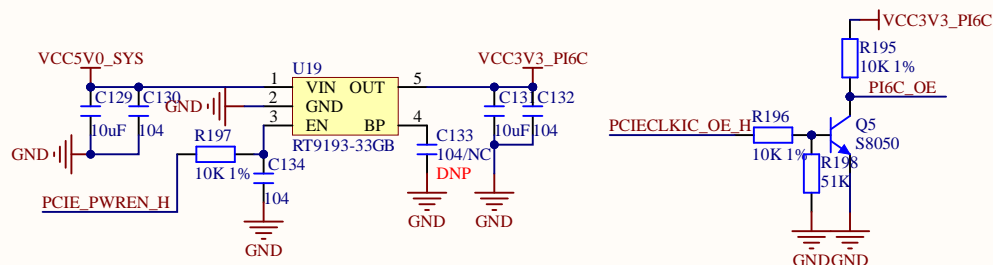
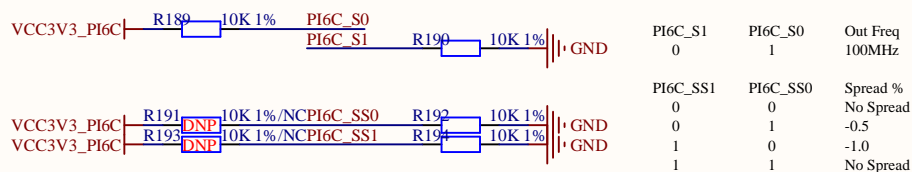
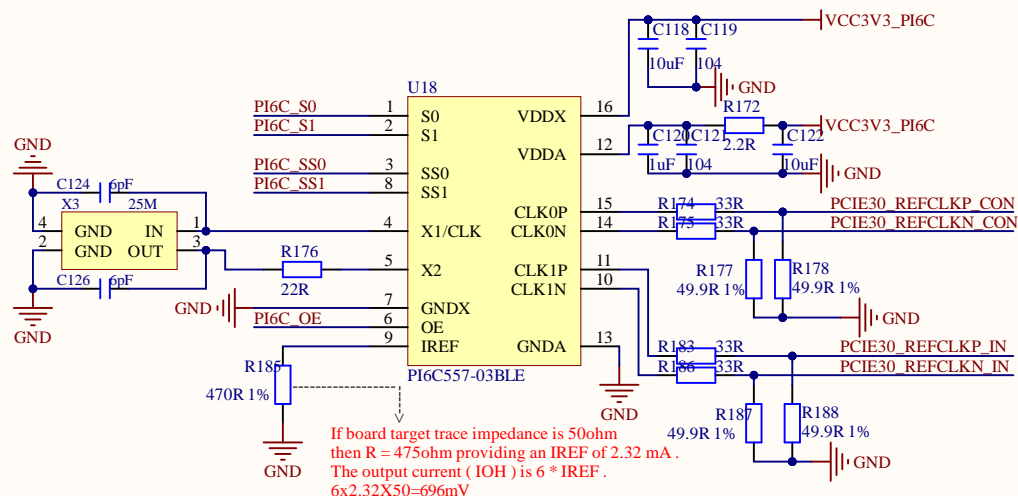
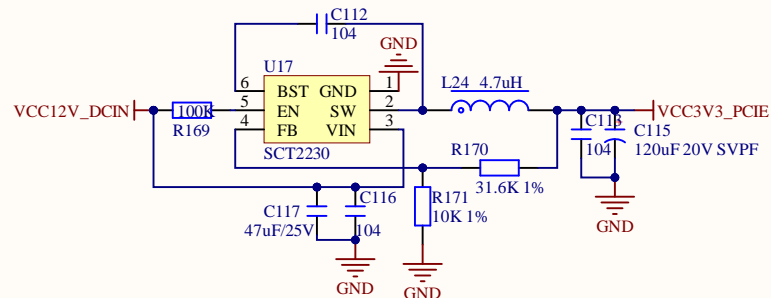
USB 3.0 OTG



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Project: ATK_DLRK3568_V1.5.PjPcb	
Size: A4	Author: ALIENTEK
Date: 2023-10-07	Version: V1.5
Sheet: 10 of 14	



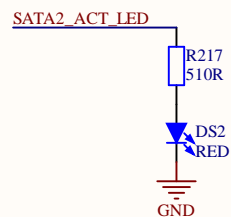
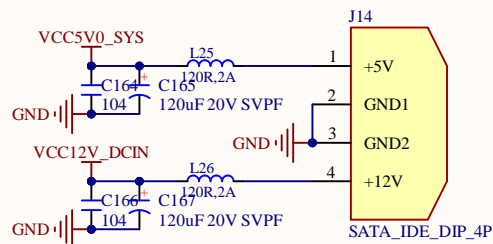
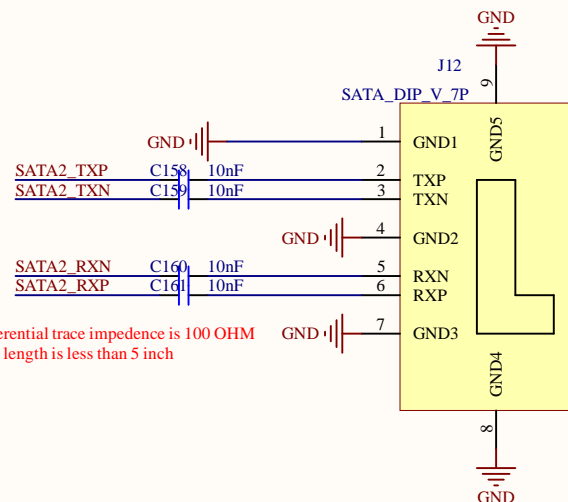
PCIE M.2



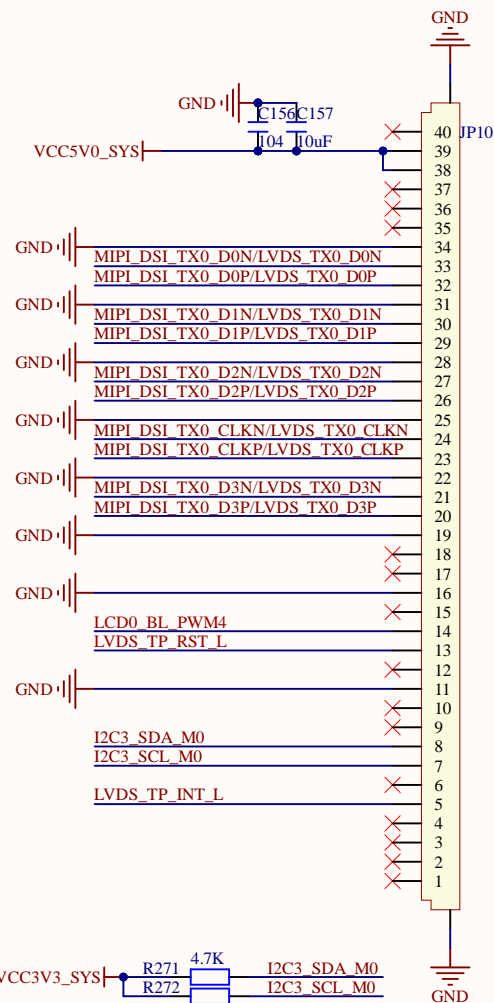
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Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 11 of 14



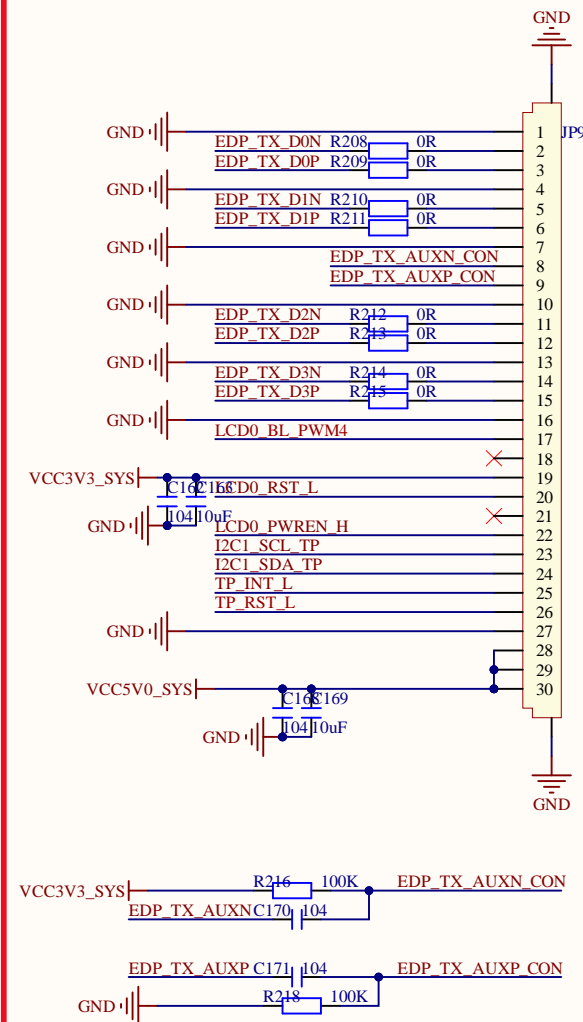
SATA3.0



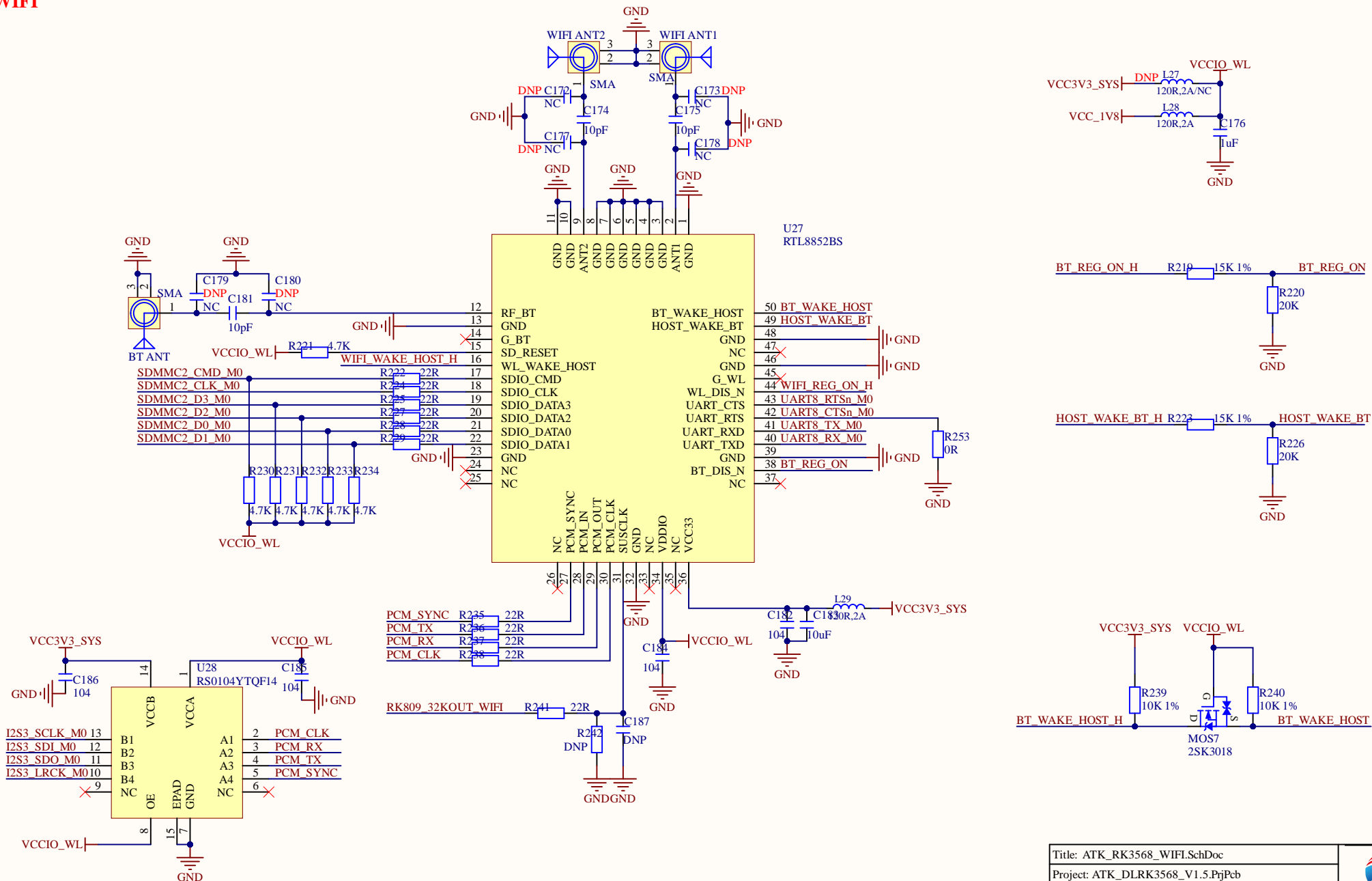
LVDS



EDP



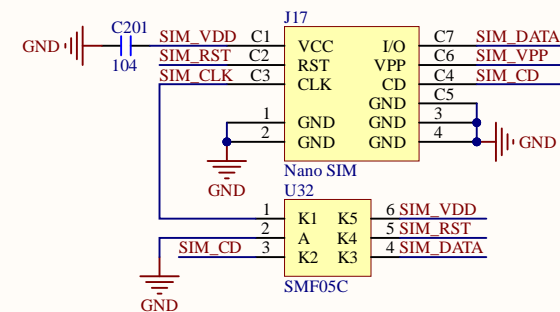
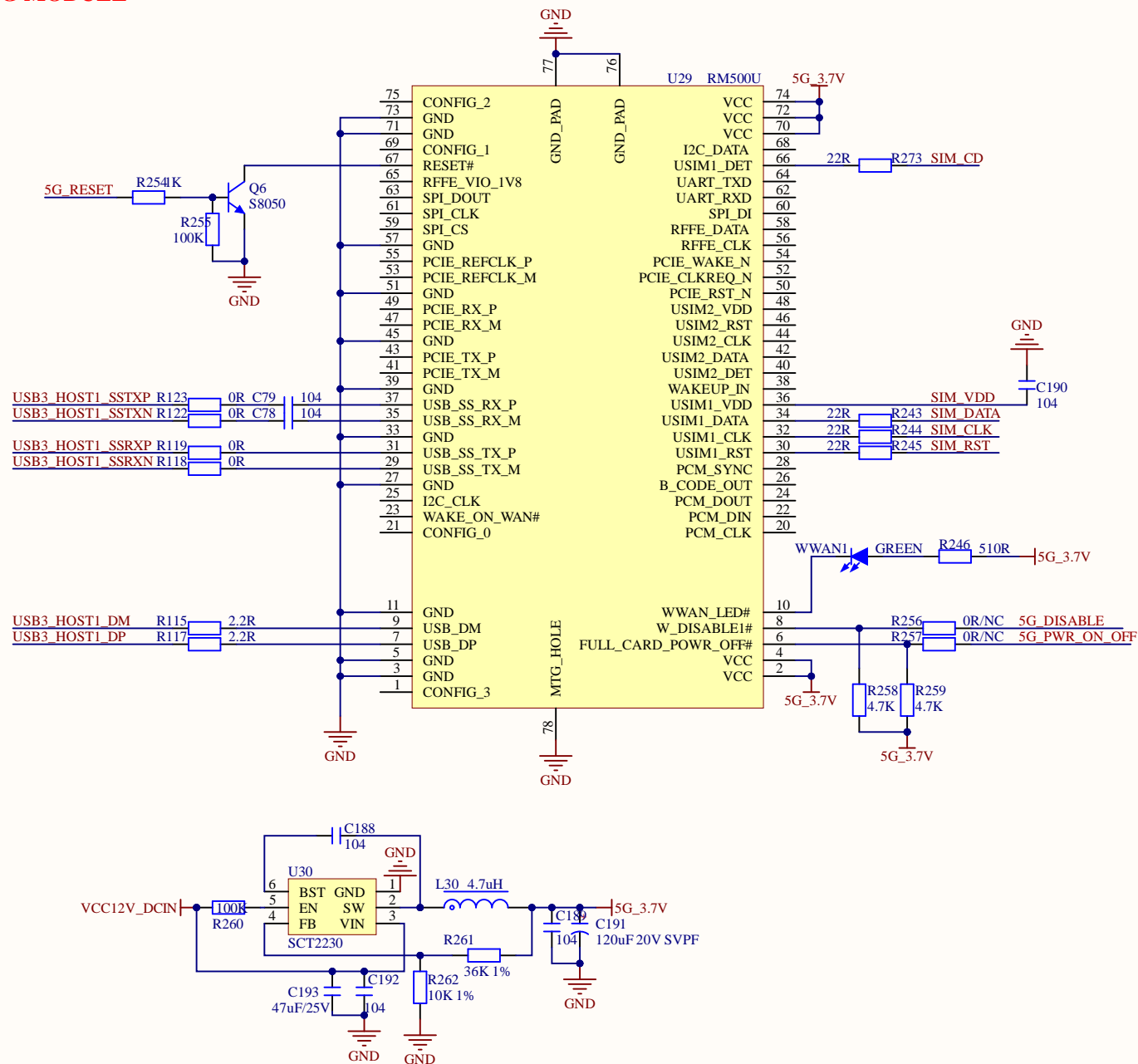
WIFI



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Project: ATK_DLRK3568_V1.5.PrjPcb		
Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 13 of 14



5G MODULE



Title: ATK_RK3568_5G.SchDoc		
Project: ATK_DLRK3568_V1.5.PrjPcb		
Size: A4	Author: ALIENTEK	
Date: 2023-10-07	Version: V1.5	Sheet: 14 of 14

