

**ESD5341N**

**1-Line, Uni-directional, Low Capacitance  
Transient Voltage Suppressor**

<http://www.sh-willsemi.com>

**Descriptions**

The ESD5341N is a low capacitance TVS (Transient Voltage Suppressor) designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by ESD (Electrostatic Discharge).

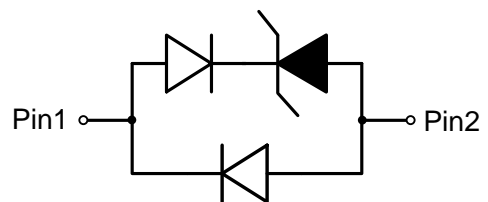
The ESD5341N incorporates one pair of low capacitance steering diodes plus a TVS diode.

The ESD5341N may be used to provide ESD protection up to  $\pm 20\text{kV}$  (contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to 4A (8/20 $\mu\text{s}$ ) according to IEC61000-4-5.

The ESD5341N is available in DFN1006-2L package. Standard products are Pb-free and Halogen-free.



**DFN1006-2L (Bottom View)**



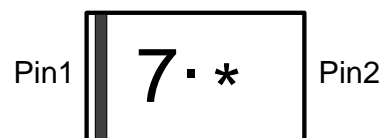
**Circuit diagram**

**Features**

- Stand-off voltage: 5V max.
- Transient protection for each line according to IEC61000-4-2 (ESD):  $\pm 20\text{kV}$  (contact discharge)  
IEC61000-4-4 (EFT): 40A (5/50ns)  
IEC61000-4-5 (surge): 4A (8/20 $\mu\text{s}$ )
- Low capacitance:  $C_J = 1.0\text{pF}$  typ.
- Ultra-low leakage current:  $I_R < 1\text{nA}$  typ.
- Low clamping voltage:  $V_{CL} = 18\text{V}$  typ. @  $I_{PP} = 16\text{A}$  (TLP)
- Solid-state silicon technology

**Applications**

- USB Interface
- HDMI Interface
- DVI
- Portable Electronics
- Notebooks



7 = Device code

\* = Month code (A~Z)

**Marking (Top View)**

**Order information**

Device	Package	Shipping
ESD5341N-2/TR	DFN1006-2L	10000/Tape&Reel

**Absolute maximum ratings**

Parameter	Symbol	Rating	Unit
Peak pulse power ( $t_p = 8/20\mu s$ )	$P_{pk}$	60	W
Peak pulse current ( $t_p = 8/20\mu s$ )	$I_{PP}$	4	A
ESD according to IEC61000-4-2 air discharge	$V_{ESD}$	$\pm 20$	kV
ESD according to IEC61000-4-2 contact discharge		$\pm 20$	
Junction temperature	$T_J$	125	$^{\circ}C$
Operating temperature	$T_{OP}$	-40~85	$^{\circ}C$
Lead temperature	$T_L$	260	$^{\circ}C$
Storage temperature	$T_{STG}$	-55~150	$^{\circ}C$

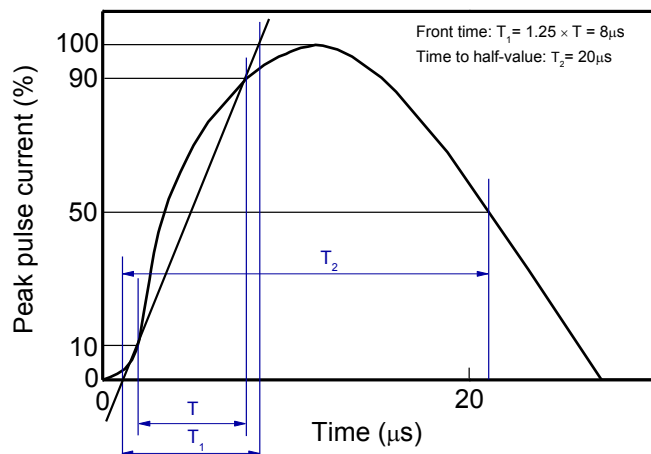
**Electrical characteristics ( $T_A = 25^{\circ}C$ , unless otherwise noted)**

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse maximum working voltage	$V_{RWM}$				5.0	V
Reverse leakage current	$I_R$	$V_{RWM} = 5V$		<1	100	nA
Reverse breakdown voltage	$V_{BR}$	$I_T = 1mA$	7.0	8.0	9.0	V
Forward voltage	$V_F$	$I_T = 10mA$	0.6	0.9	1.2	V
Clamping voltage <sup>1)</sup>	$V_{CL}$	$I_{PP} = 16A, t_p = 100ns$		18.0		V
Dynamic resistance <sup>1)</sup>	$R_{DYN}$			0.6		$\Omega$
Clamping voltage <sup>2)</sup>	$V_{CL}$	$I_{PP} = 1A, t_p = 8/20\mu s$			11	V
		$I_{PP} = 4A, t_p = 8/20\mu s$			15	V
Junction capacitance	$C_J$	$V_R = 0V, f = 1MHz$		1.0	1.4	pF

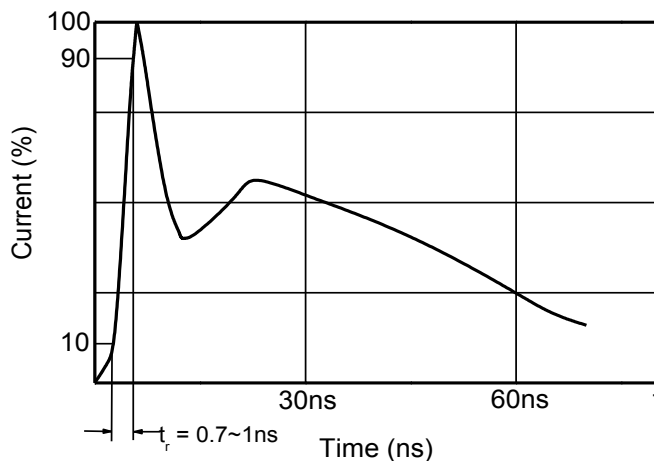
Notes:

- 1) TLP parameter:  $Z_0 = 50\Omega$ ,  $t_p = 100ns$ ,  $t_r = 2ns$ , averaging window from 60ns to 80ns.  $R_{DYN}$  is calculated from 4A to 16A.
- 2) Non-repetitive current pulse, according to IEC61000-4-5.

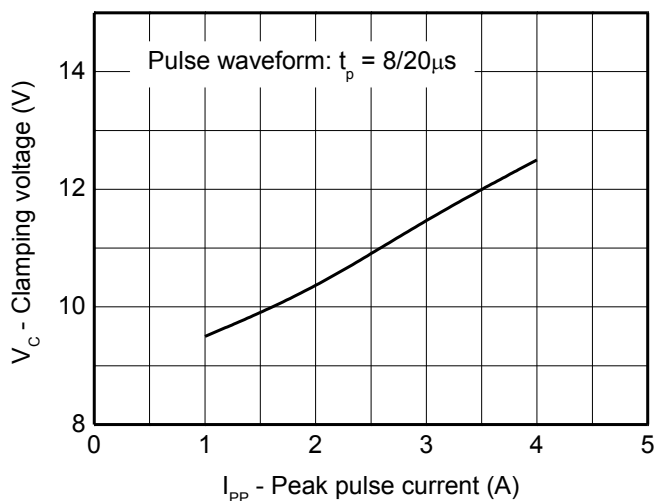
Typical characteristics ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)



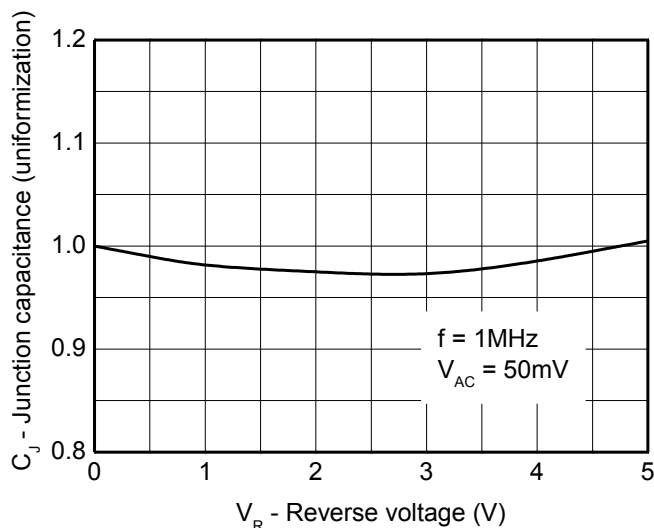
8/20 $\mu\text{s}$  waveform per IEC61000-4-5



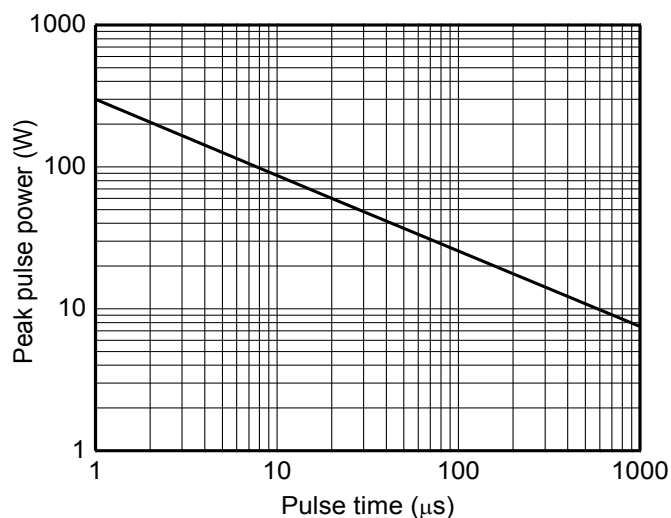
Contact discharge current waveform per IEC61000-4-2



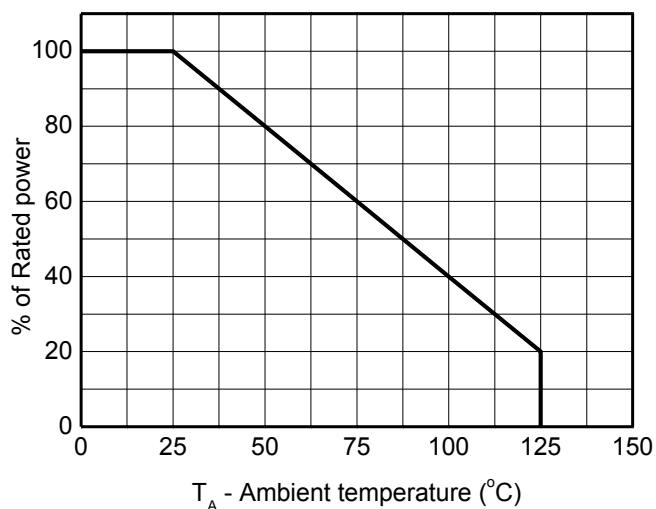
Clamping voltage vs. Peak pulse current



Capacitance vs. Reverse voltage

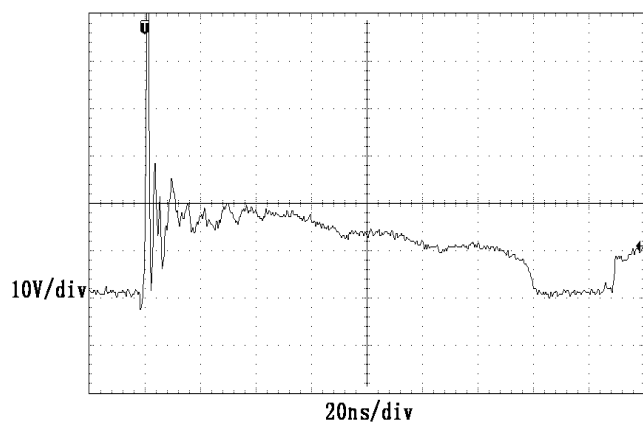


Non-repetitive peak pulse power vs. Pulse time

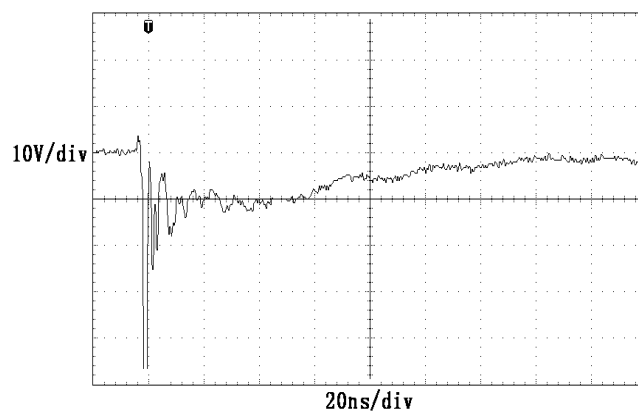


Power derating vs. Ambient temperature

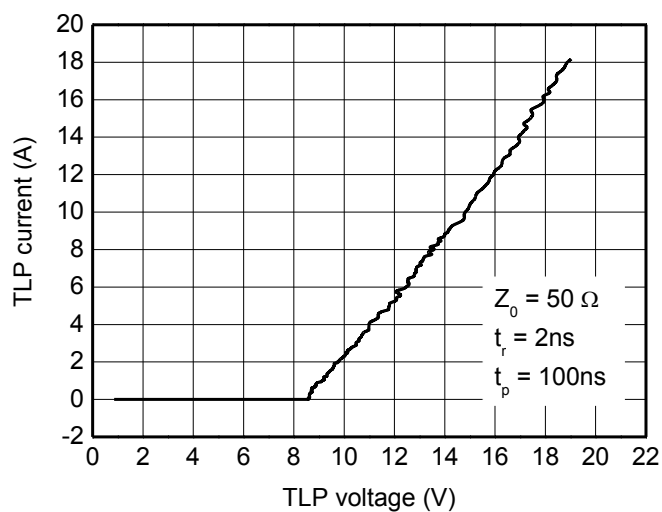
Typical characteristics ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)



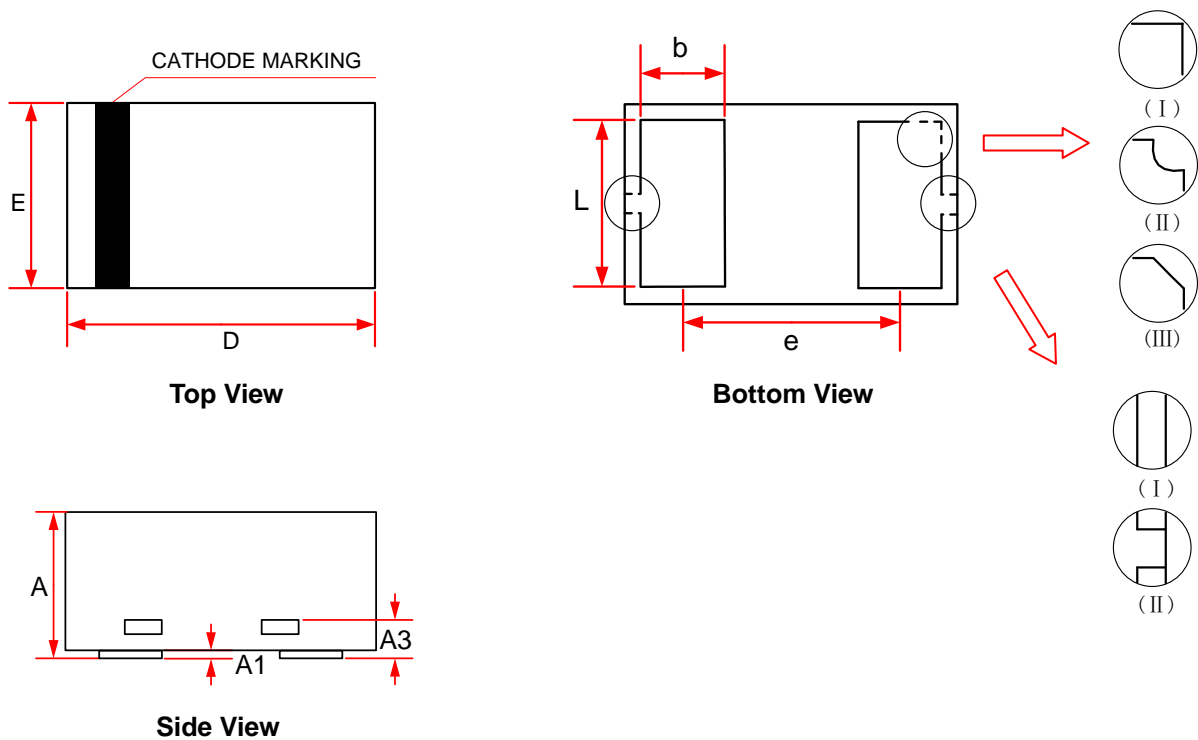
**ESD clamping**  
(+8kV contact discharge per IEC61000-4-2)



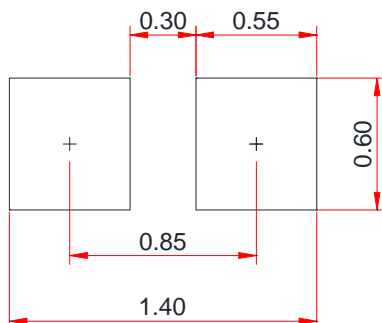
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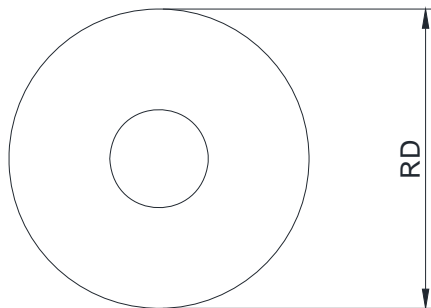
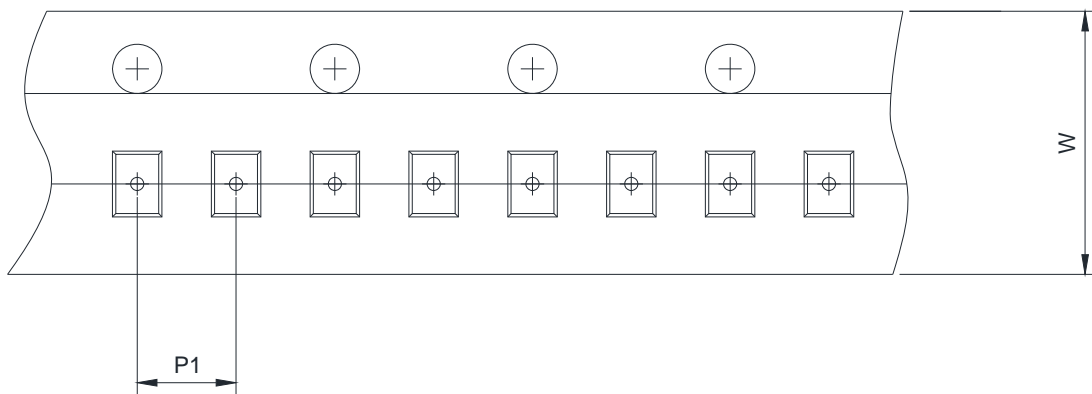
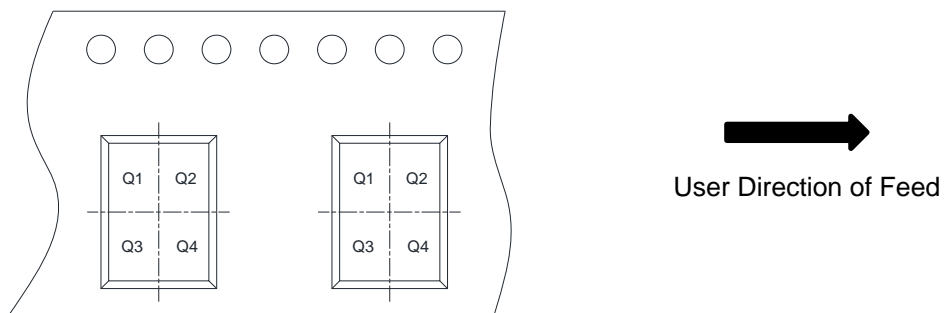
**TLP Measurement**

**PACKAGE OUTLINE DIMENSIONS**
**DFN1006-2L**


Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.340	0.450	0.530
A1	0.000	0.020	0.050
A3	0.125 Ref.		
D	0.950	1.000	1.075
E	0.550	0.600	0.675
b	0.200	0.250	0.300
L	0.450	0.500	0.550
e	0.650 BSC		

**Recommended PCB Layout (Unit: mm)**

**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

**TAPE AND REEL INFORMATION**
**Reel Dimensions**

**Tape Dimensions**

**Quadrant Assignments For PIN1 Orientation In Tape**


RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch	
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm	
P	Pitch between successive cavity centers	<input checked="" type="checkbox"/> 2mm	<input type="checkbox"/> 4mm	<input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input checked="" type="checkbox"/> Q1	<input checked="" type="checkbox"/> Q2	<input type="checkbox"/> Q3 <input type="checkbox"/> Q4