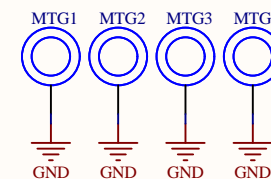
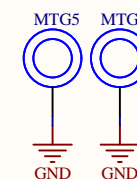


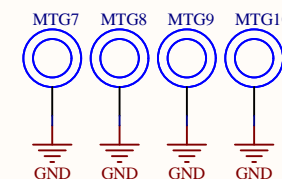
液晶模块固定螺母



4G模块固定螺母




亚克力盖板固定螺母



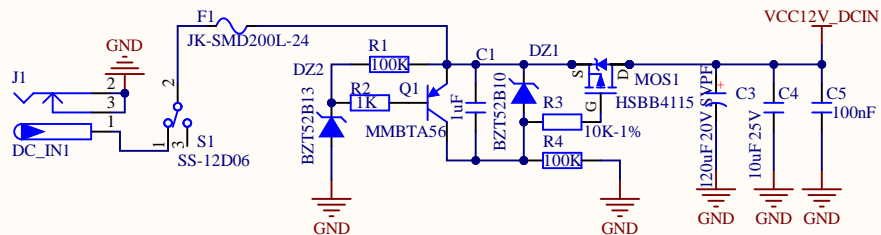
MARK点



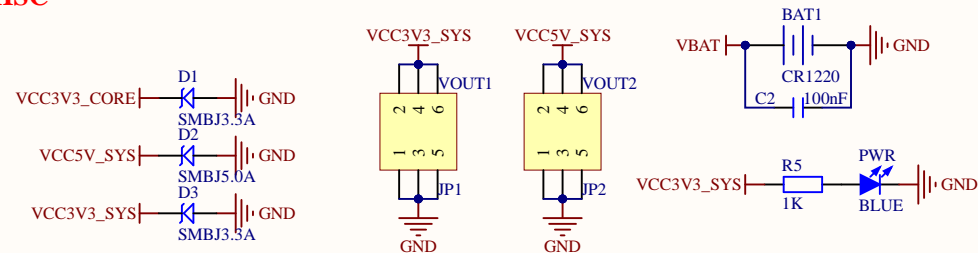
Title: 02_ATK_T113i_CORE.SchDoc			 正点原子
Project: ATK-DLT113IS.PrjPcb			
Size: A4	Author: *		
Date: 2025/2/20	Version: V1.0	Sheet: of	



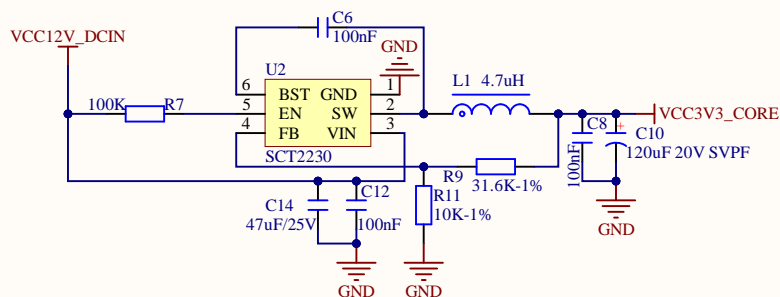
12V DCIN



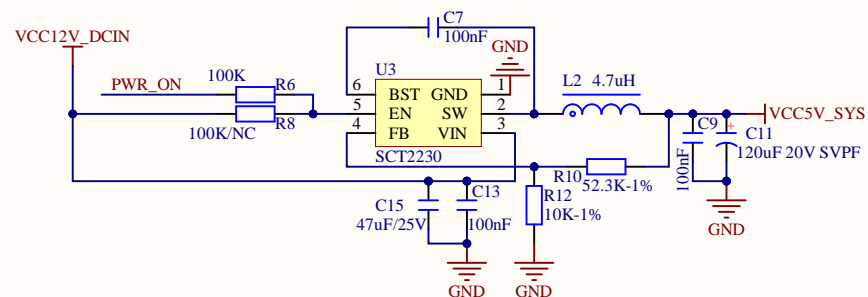
MISC



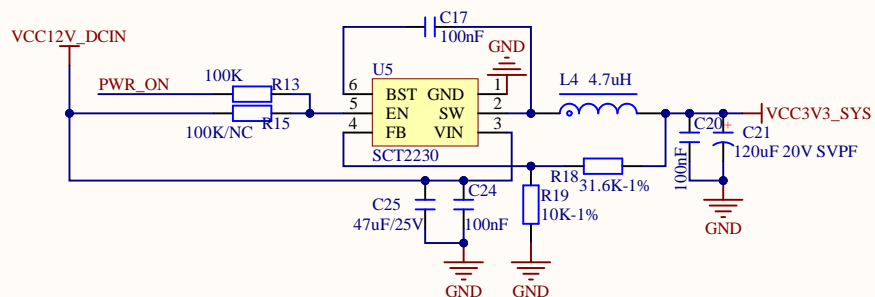
3V3_CORE



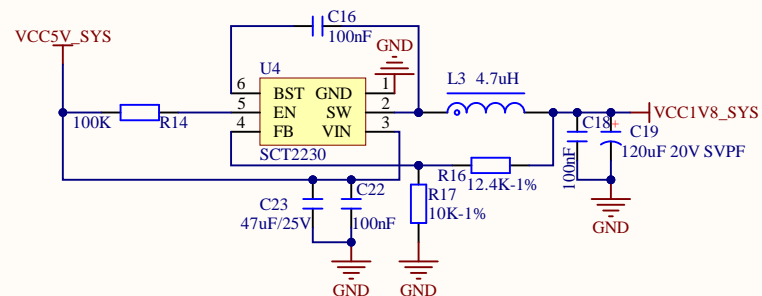
5V0_SYS



3V3_SYS



1V8_SYS



Title: 03_ATK_T113i_POWER.SchDoc

Project: ATK-DLT113IS.PrjPcb

Size: A4

Author: *

Date: 2025/2/20

Version: V1.0

Sheet: of

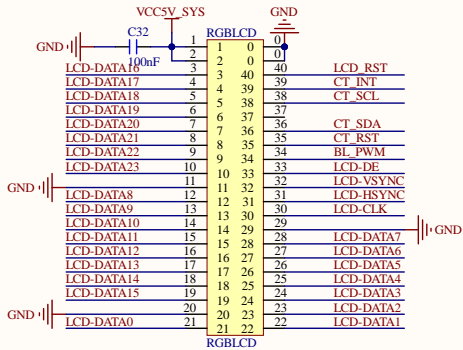


DISPLAY

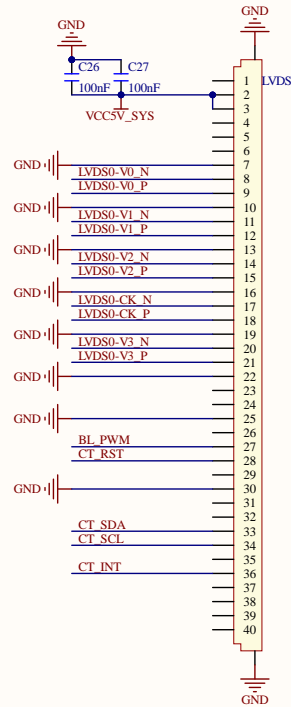
PD0	LCD-DATA2	22R	R20	LVDS0-V0_P	22R	R21	DSI-D0_P
PD1	LCD-DATA3	22R	R22	LVDS0-V0_N	22R	R23	DSI-D0_N
PD2	LCD-DATA4	22R	R24	LVDS0-V1_P	22R	R25	DSI-D1_P
PD3	LCD-DATA5	22R	R26	LVDS0-V1_N	22R	R27	DSI-D1_N
PD4	LCD-DATA6	22R	R28	LVDS0-V2_P	22R	R29	DSI-CK_P
PD5	LCD-DATA7	22R	R30	LVDS0-V2_N	22R	R31	DSI-CK_N
PD6	LCD-DATA10	22R	R32	LVDS0-CK_P	22R	R33	DSI-D2_P
PD7	LCD-DATA11	22R	R34	LVDS0-CK_N	22R	R35	DSI-D2_N
PD8	LCD-DATA12	22R	R36	LVDS0-V3_P	22R	R37	DSI-D3_P
PD9	LCD-DATA13	22R	R38	LVDS0-V3_N	22R	R39	DSI-D3_N
PD10	LCD-DATA14	22R					
PD11	LCD-DATA15						
PD12	LCD-DATA18						
PD13	LCD-DATA19						
PD14	LCD-DATA20						
PD15	LCD-DATA21						
PD16	LCD-DATA22						
PD17	LCD-DATA23						
PD18	LCD-CLK						
PD19	LCD-DE						
PD20	LCD-HSYNC						
PD21	LCD-VSYNC						

RGB

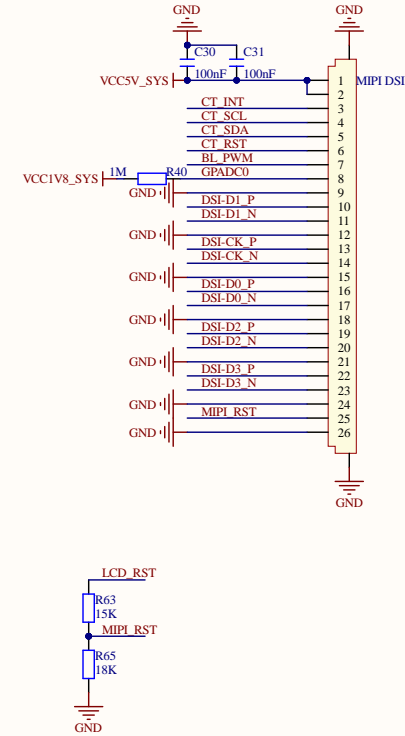
PB2	0R/NC		R50	LCD-DATA0	47K	R51	
PB3	0R/NC		R52	LCD-DATA1	47K	R53	
PB4	0R/NC		R54	LCD-DATA8	47K	R55	
PB5	0R/NC		R56	LCD-DATA9	47K	R57	
PB6	0R/NC		R58	LCD-DATA16	47K	R59	
PB7	0R/NC		R60	LCD-DATA17	47K	R61	



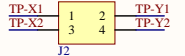
LVDS



MIPI



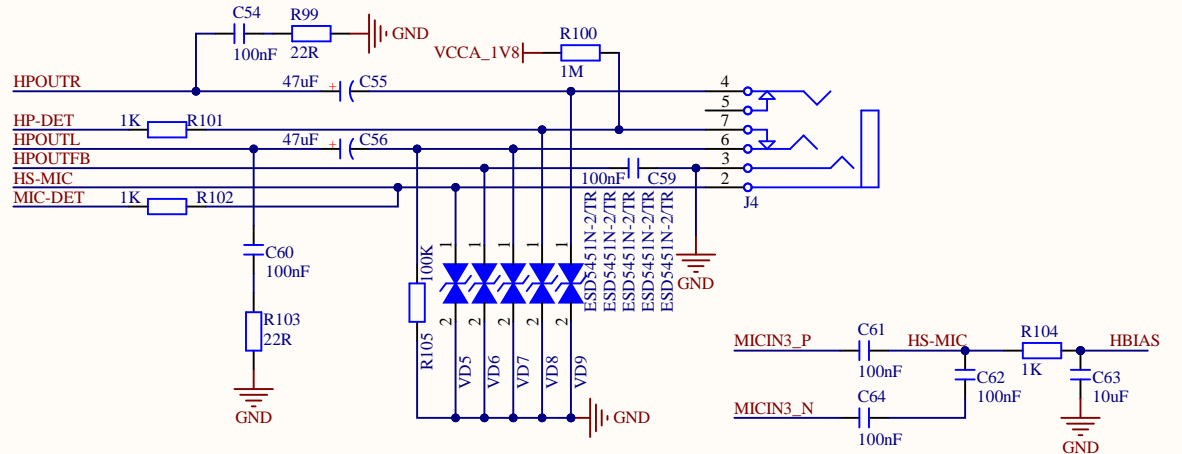
RTP



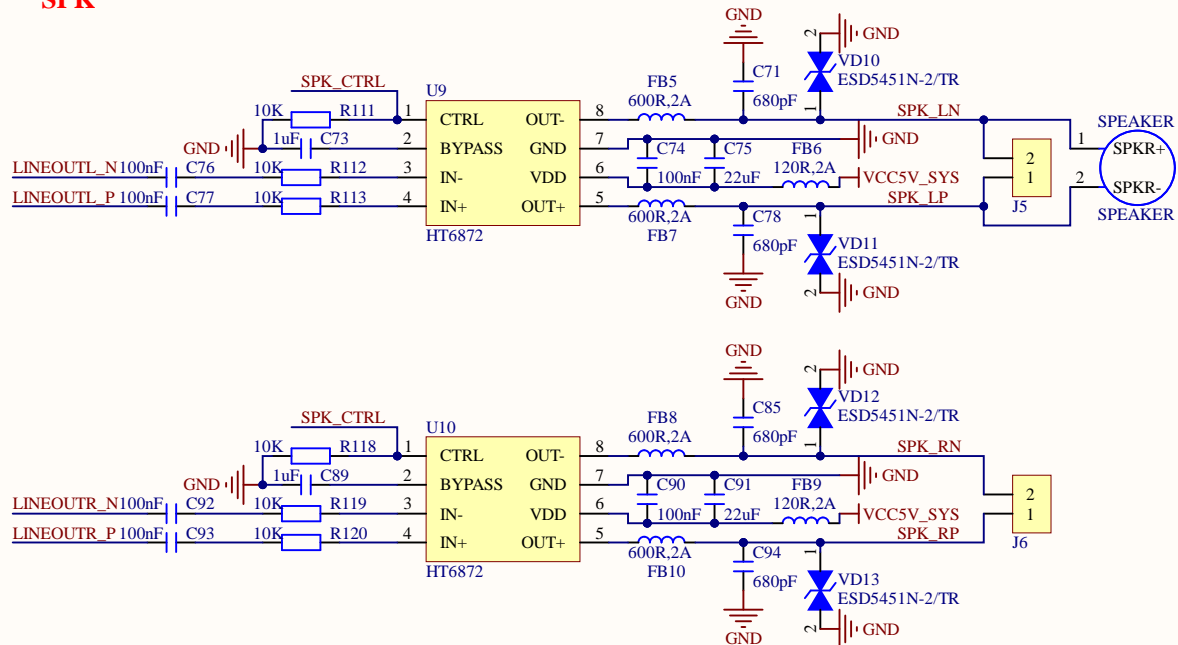
1

1

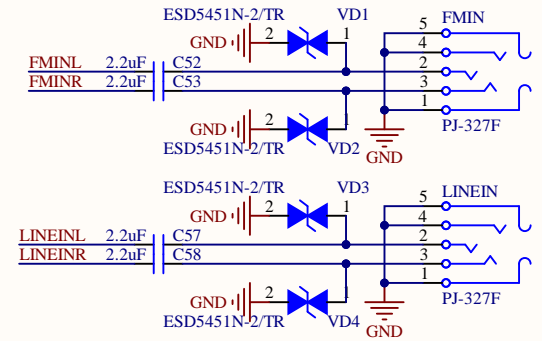
HP&MIC



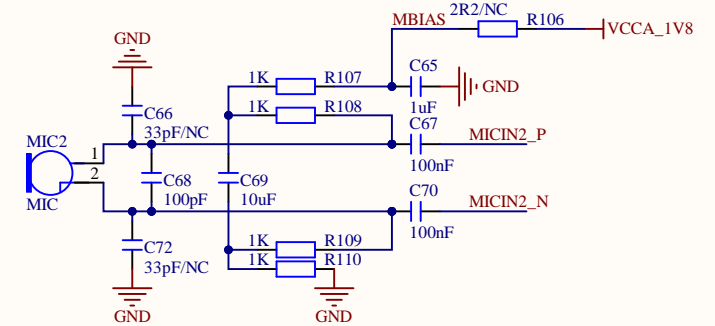
SPK



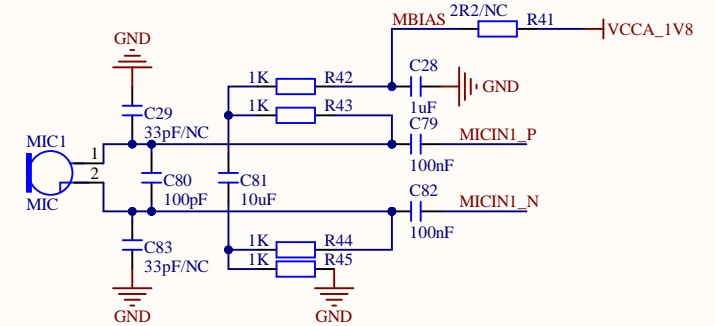
FMIN&LINEIN



MIC2



MIC1

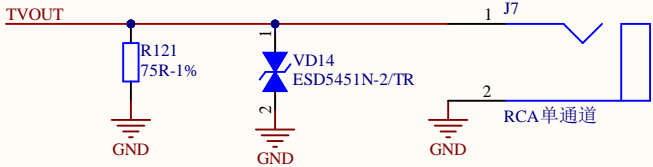


Title: 06_ATK_T113i_AUDIO.SchDoc		
Project: ATK-DLT113IS.PrjPcb		
Size: A3	Author: *	
Date: 2025/2/20	Version: V1.0	Sheet: of

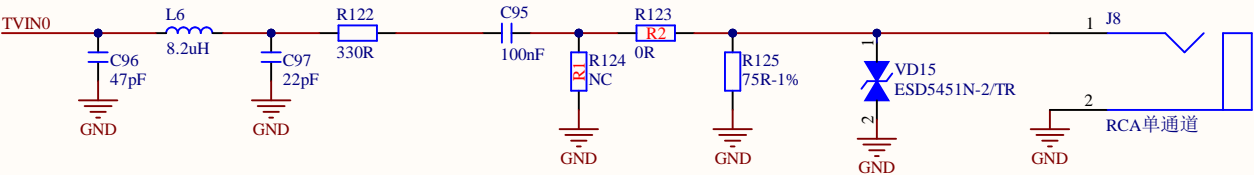


TV-OUT

TVOUT、TVIN0和TVI
N1走线阻抗匹配37.5欧
，电阻相关元件靠近
座子摆放



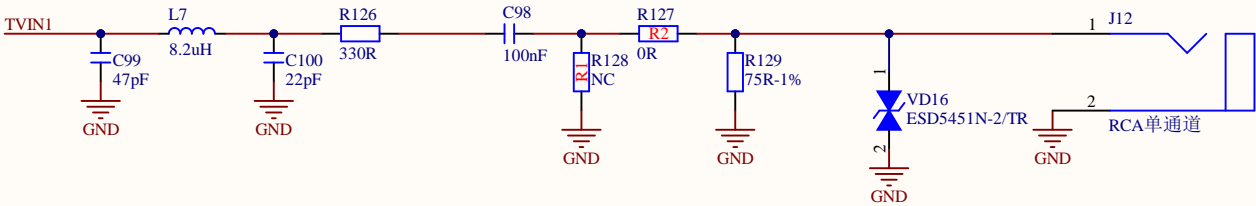
TV-IN0



When TVIN-Vth>1.8V;
R2=10K,R1=4.7K
When TVIN-Vth<=1.8V;
R2=NC,R1=0R

当cvbs输入电压大于1.8v
时
R2=10K,R1=4.7K
当cvbs输入电压小于或等
于1.8v时
R2=NC,R1=0R

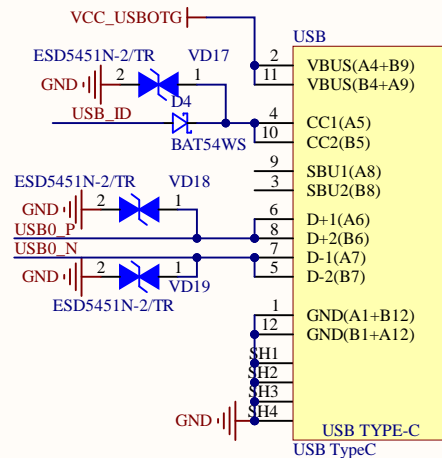
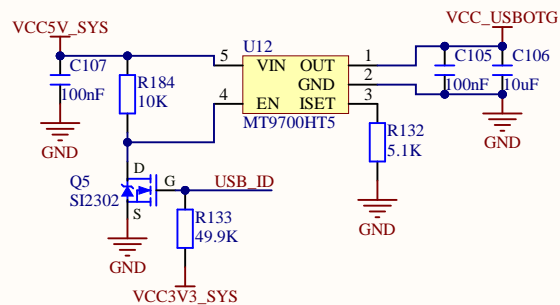
TV-IN1



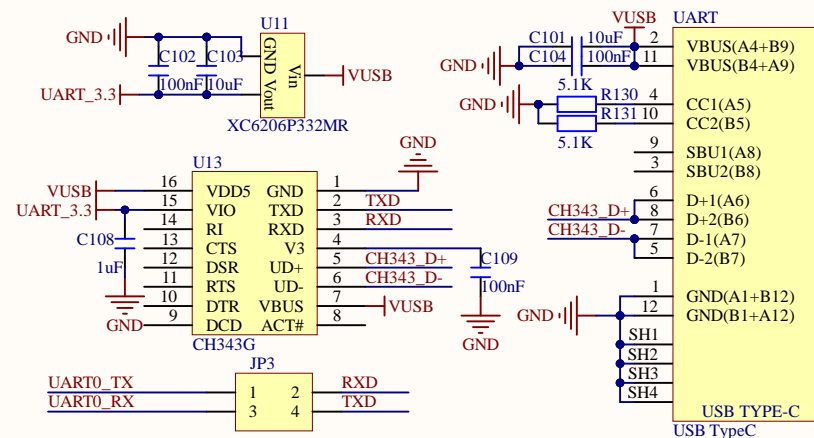
Title: 07_ATK_T113i_TV-INOUT.SchDoc		
Project: ATK-DLT113IS.PrjPcb		
Size: A4	Author: *	
Date: 2025/2/20	Version: V1.0	Sheet: of



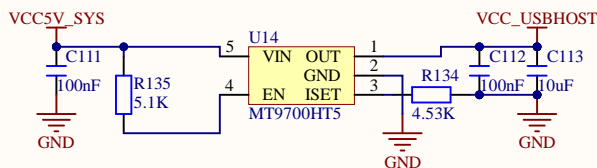
USB-OTG



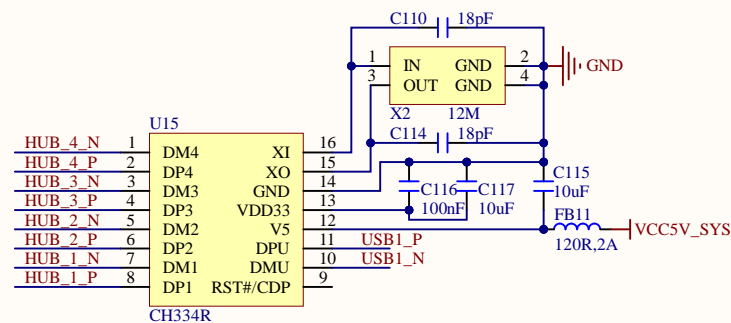
USB-TTL



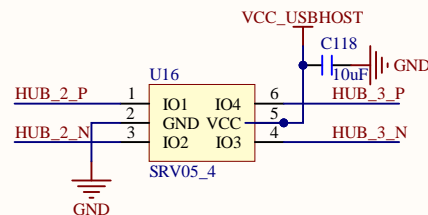
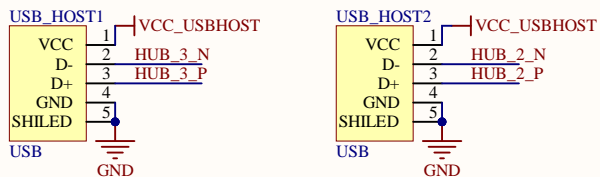
USB-HUB



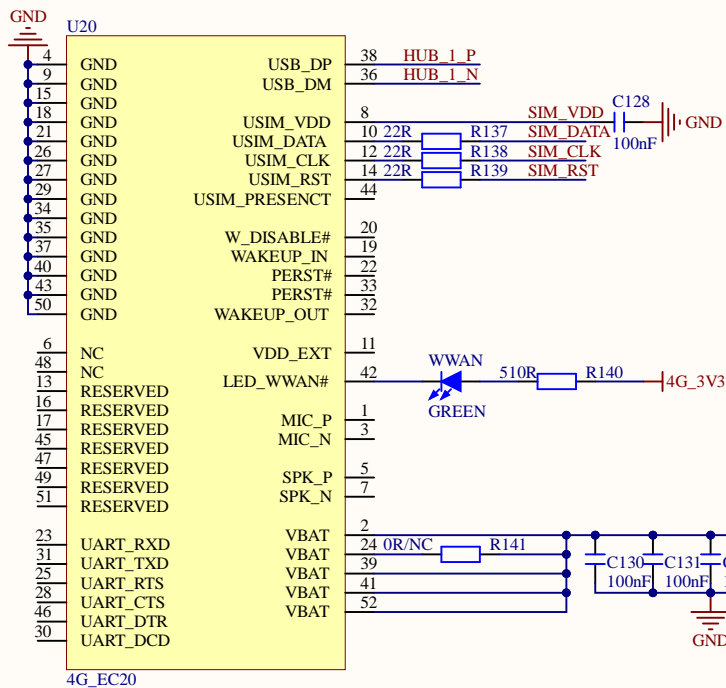
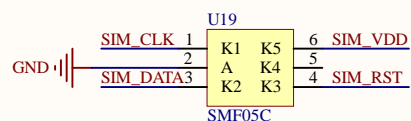
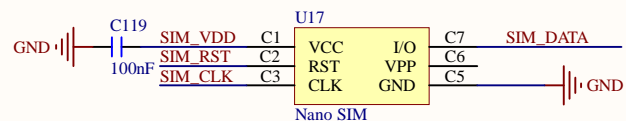
ESD走线不顺可以调整一下线序，以及对应的端口



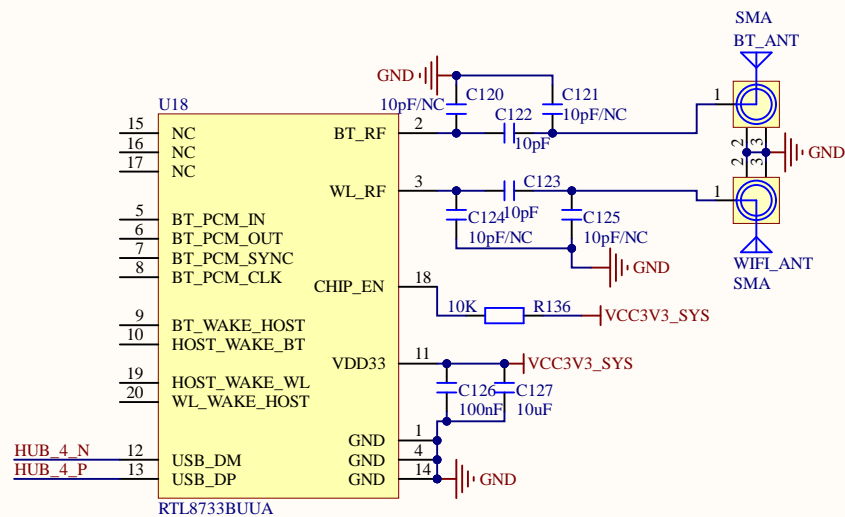
ESD走线不顺可以调整一下线序，以及对应的端口




4G

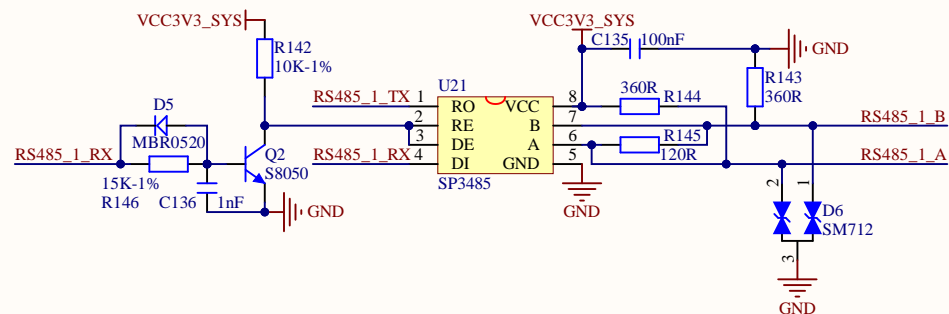


WIFI&BT



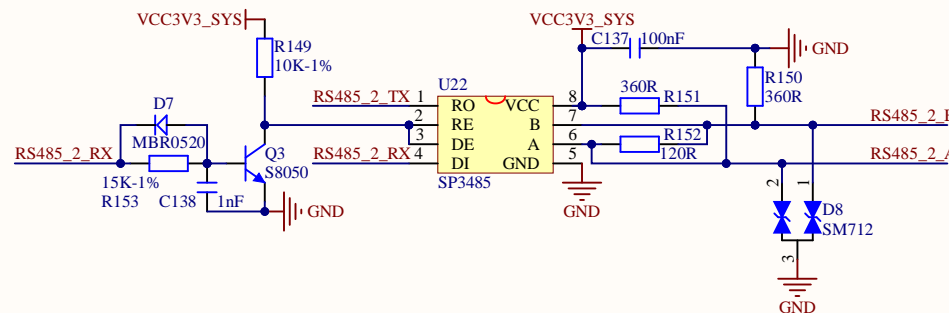
Title: 09_ATK_T113i_4GWIFIBT.SchDoc			 正点原子
Project: ATK-DLT113IS.PrjPcb			
Size: A4	Author: *		
Date: 2025/2/20	Version: V1.0	Sheet: of	

RS485_1



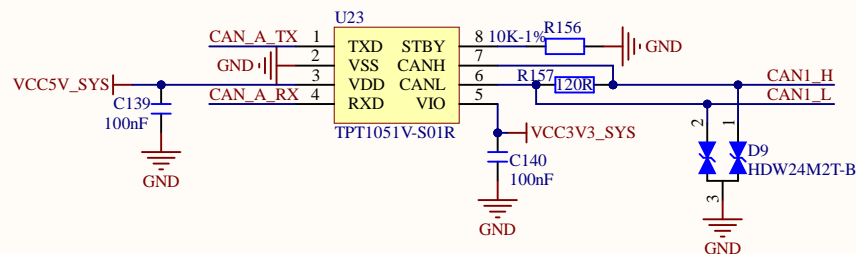
UART4_RX R147 RS485_1_TX
UART4_TX R148 RS485_1_RX

RS485_2



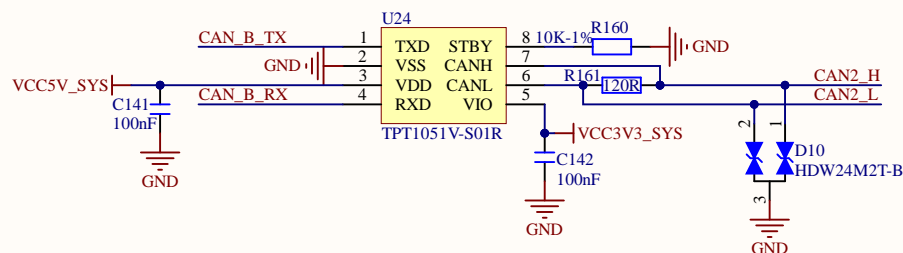
UART3_RX R154 RS485_2_TX
UART3_TX R155 RS485_2_RX

CAN1



CAN0_TX R158 CAN_A_TX
CAN0_RX R159 CAN_A_RX

CAN2



CAN1_TX R162 CAN_B_TX
CAN1_RX R163 CAN_B_RX

CAN&RS485 HEADER

	J9
RS485_1_A	1
RS485_1_B	2
RS485_2_A	3
RS485_2_B	4
CAN1_H	5
CAN1_L	6
CAN2_H	7
CAN2_L	8

KF2EDGR-3.81-8P

Title: 10_ATK_T113i_CAN485.SchDoc

Project: ATK-DLT113IS.PrjPcb

Size: A4

Author: *

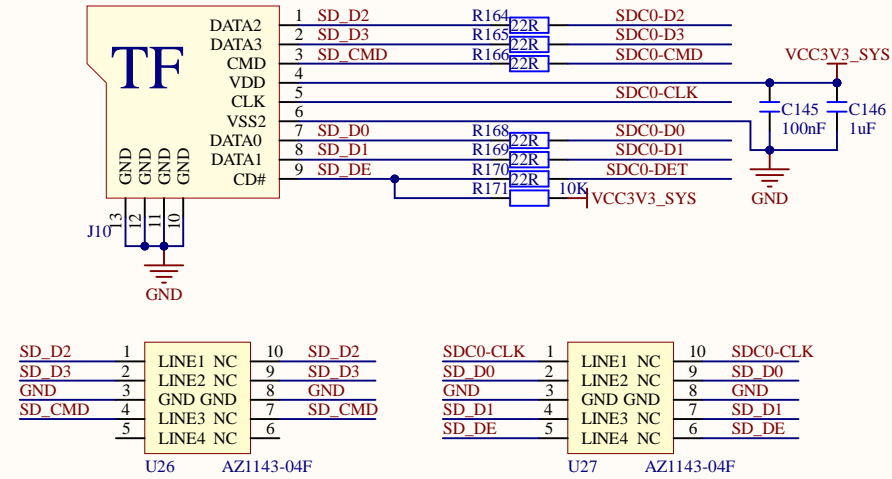
Date: 2025/2/20

Version: V1.0

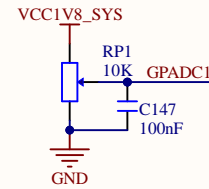
Sheet: of



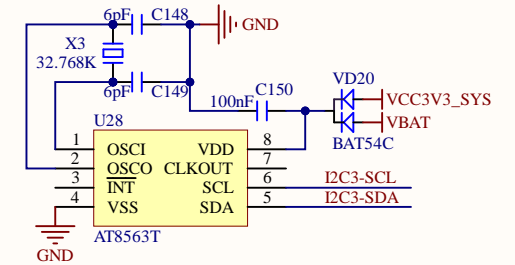
TF CARD



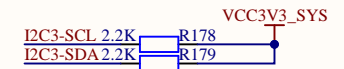
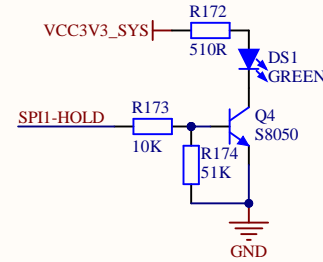
ADC



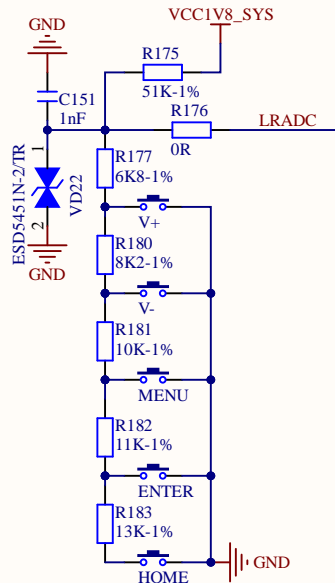
RTC



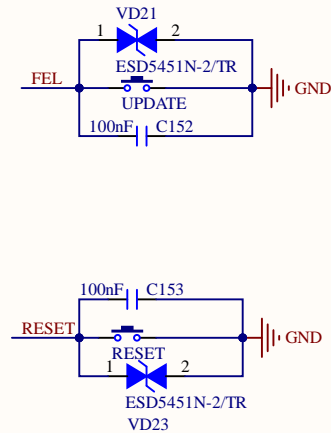
LED



USER-KEY



UPDATE&RESET



EXT IO

