Post Lab Exercise

- a) From the input characteristics of BJT, estimate dynamic input impedance of the BJT.
- b) Estimate β and Early voltage from the output characteristics.
- c) Refer the datasheet of the transistor (BC547) and see if the values match.
- d) You are encouraged to write the net list for common base configuation.
- e) The Fig shows transistor used in CE configuation as an inverter.
 - i. Sketch the output waveform if the input is driven by a square wave signal of 5V, 10kHz.
 - ii. Write the netlist for the circuit shown in Fig. and simulate it. Verify your answer in the previous question.
- iii. Try to measure the switching times.