

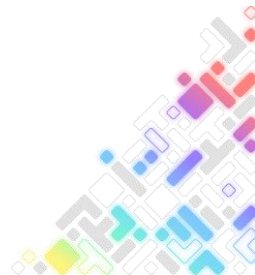
Userspace in Zephyr: Overview and Practicality

Daniel Leung



Topics

- Overview of User Space in Zephyr
- Sharing Data using Memory Domains
- Syscalls
- Why and Why Not User Space?
- Q&A



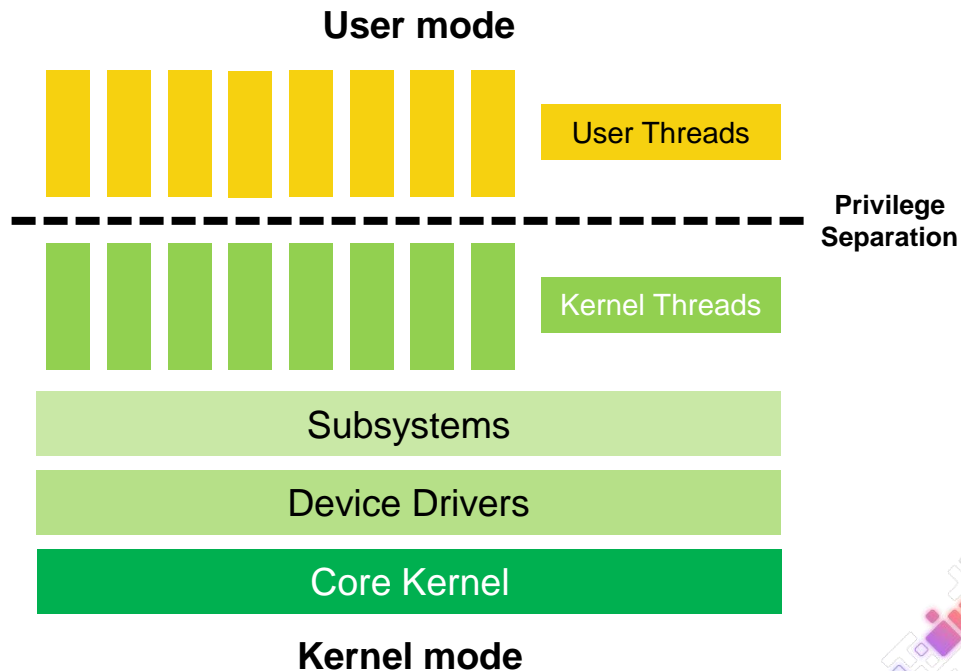
Overview of User Space in Zephyr

- User mode

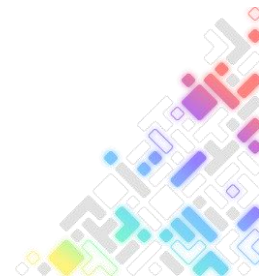
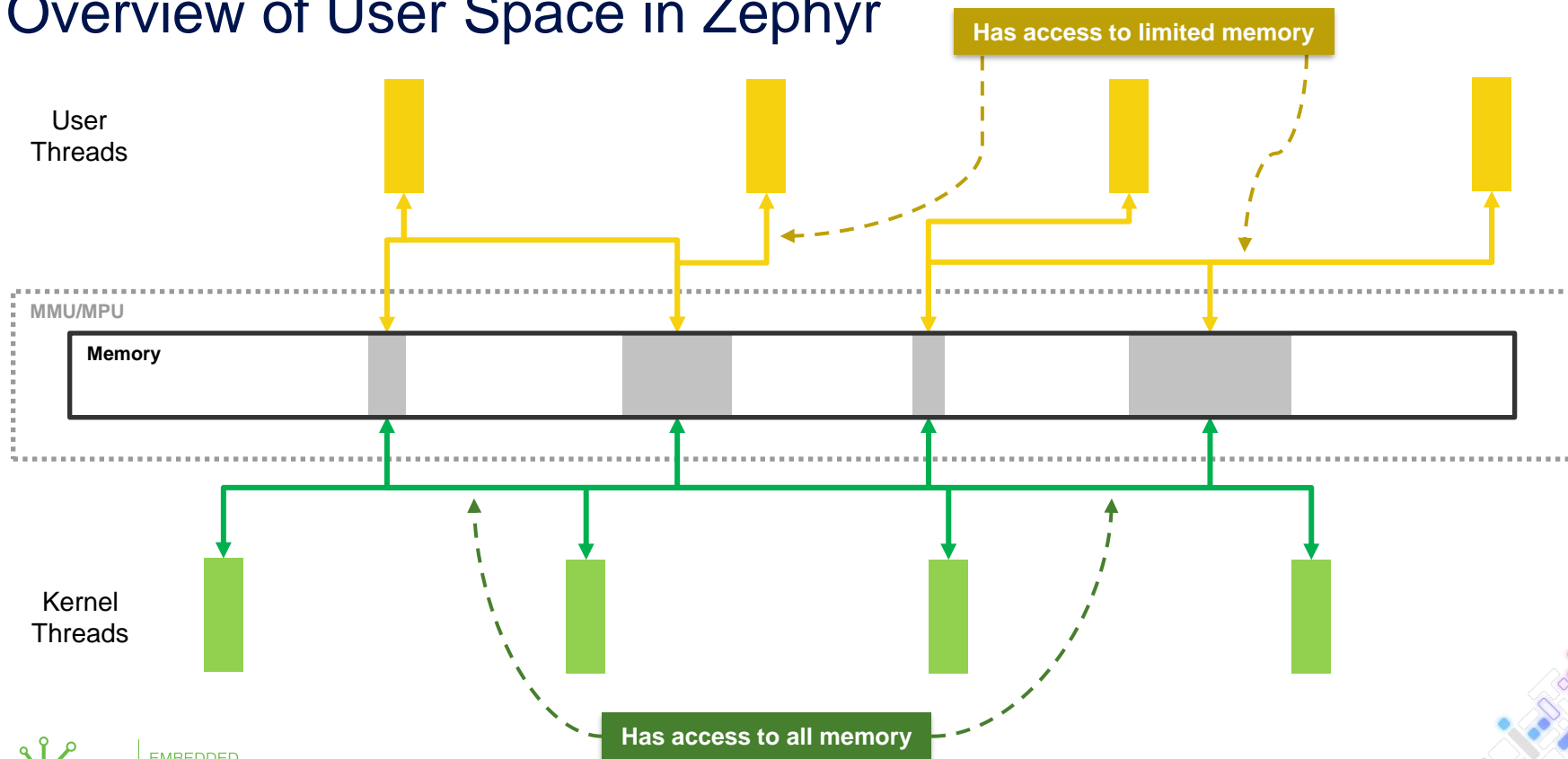
- a.k.a. Unprivileged Mode
- User threads

- Kernel mode

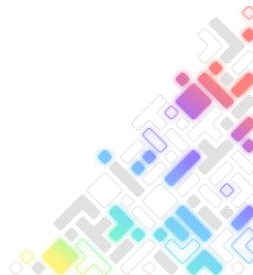
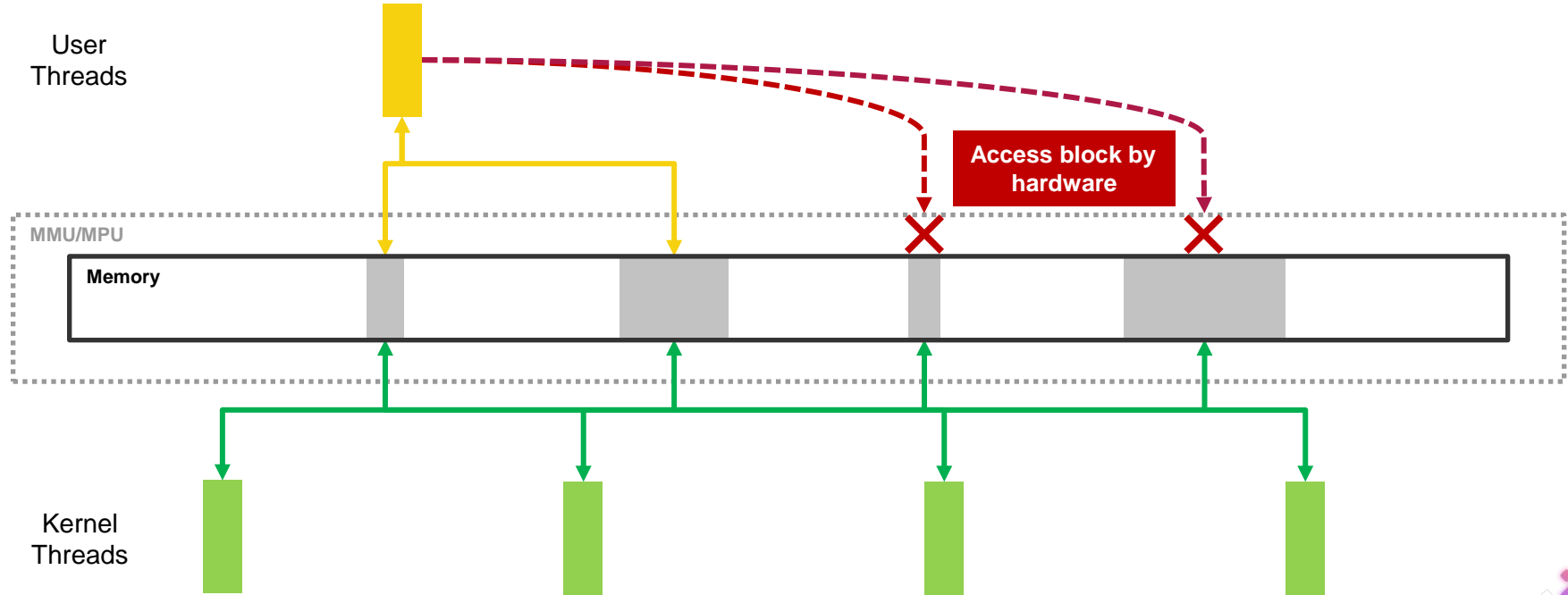
- a.k.a. Supervisor or Privileged Mode
- Core Kernel
- Kernel threads
- Subsystems
- Device Drivers



Overview of User Space in Zephyr

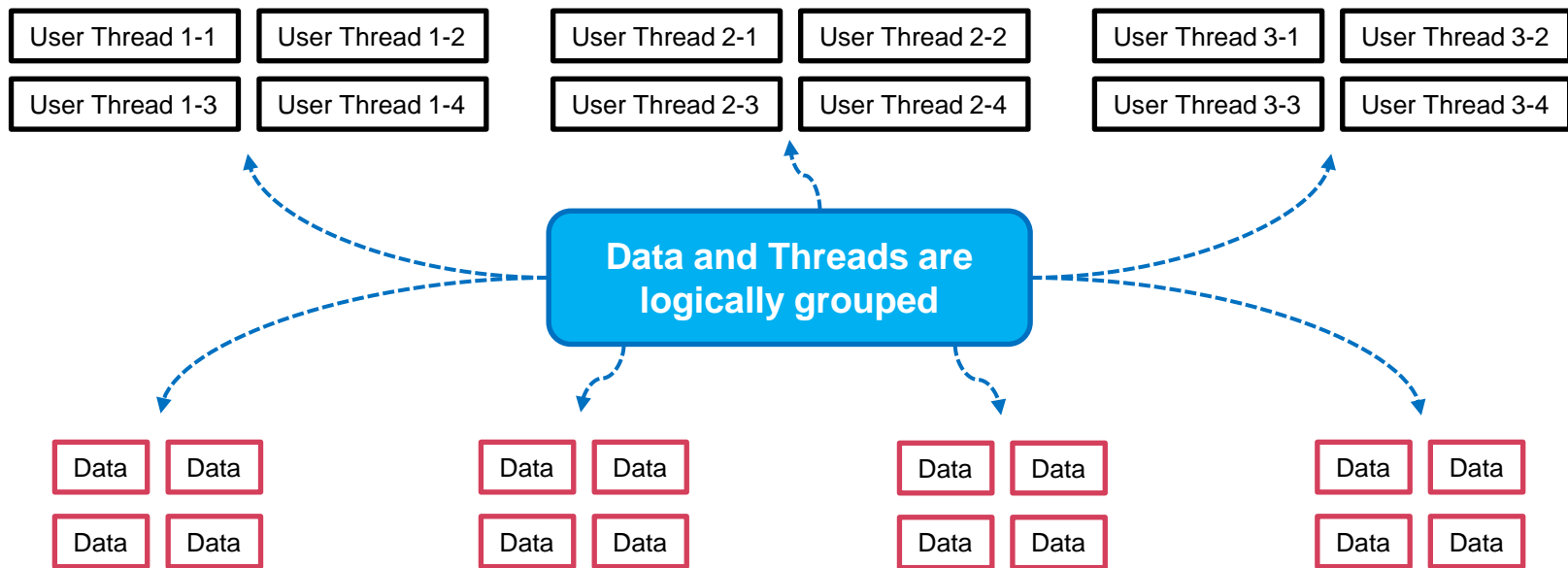


Overview of User Space in Zephyr



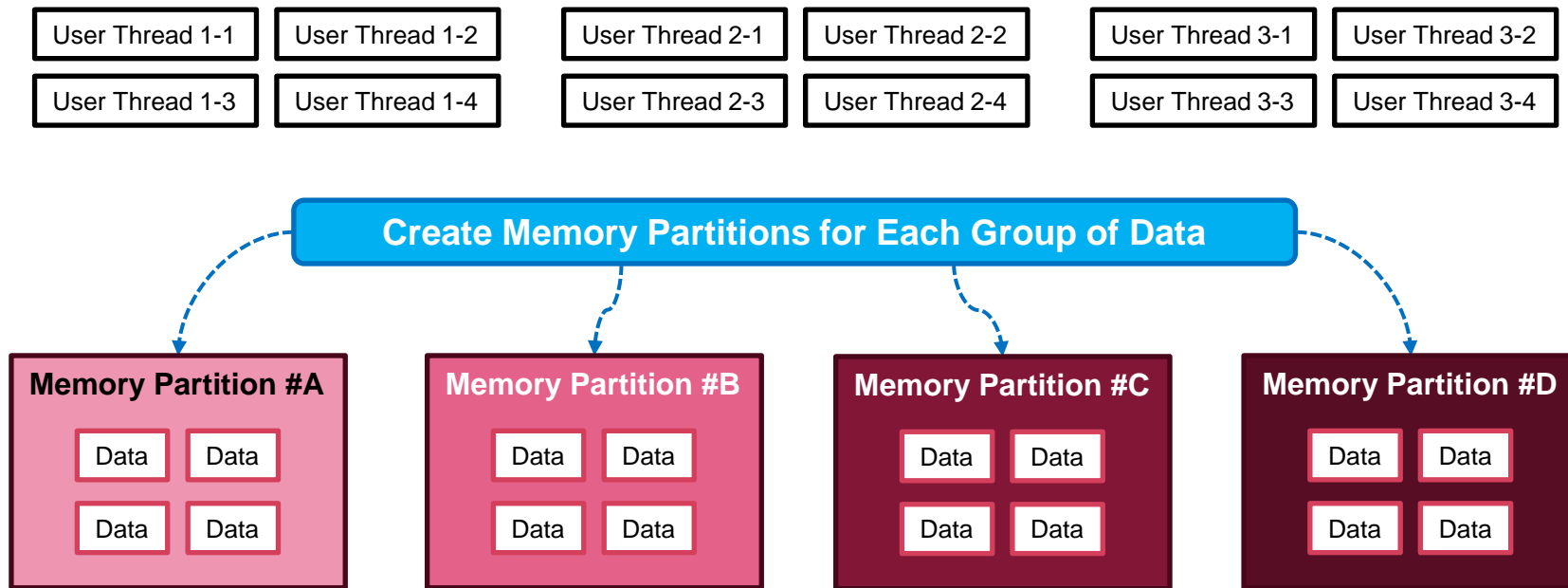
Sharing Data using Memory Domains

... and Memory Partitions



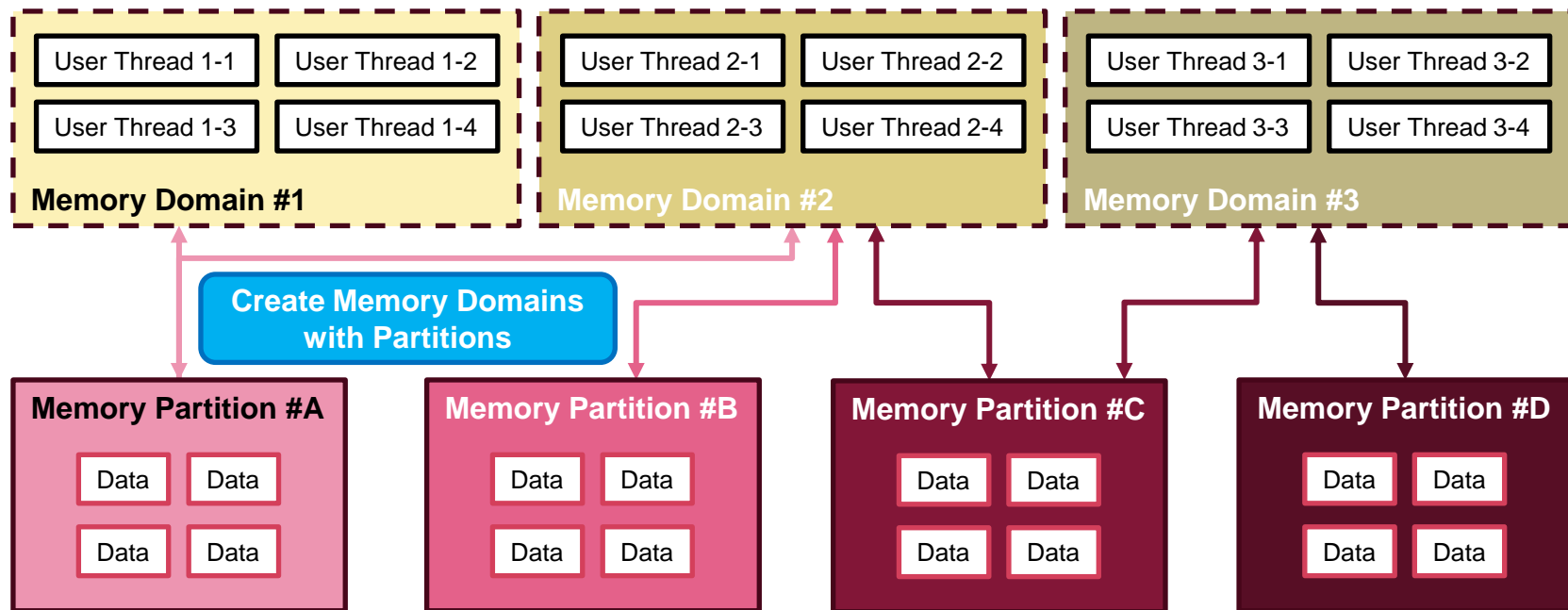
Sharing Data using Memory Domains

... and Memory Partitions



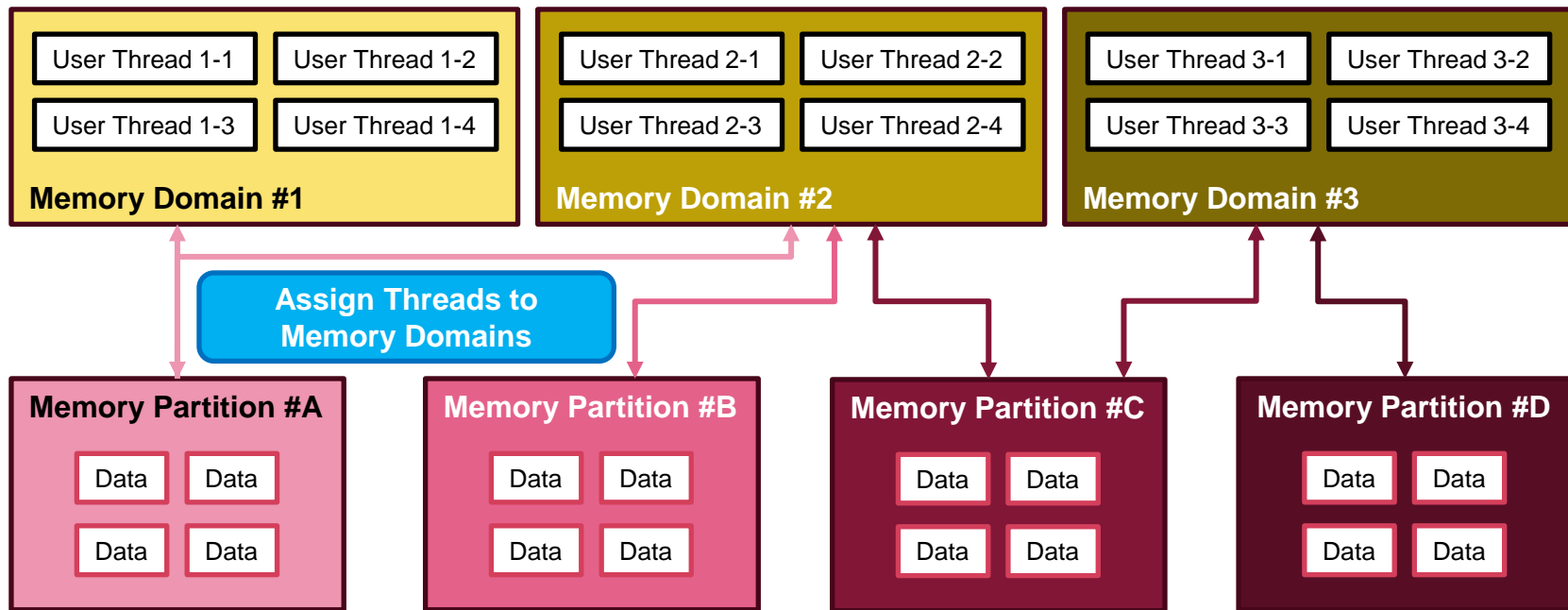
Sharing Data using Memory Domains

... and Memory Partitions



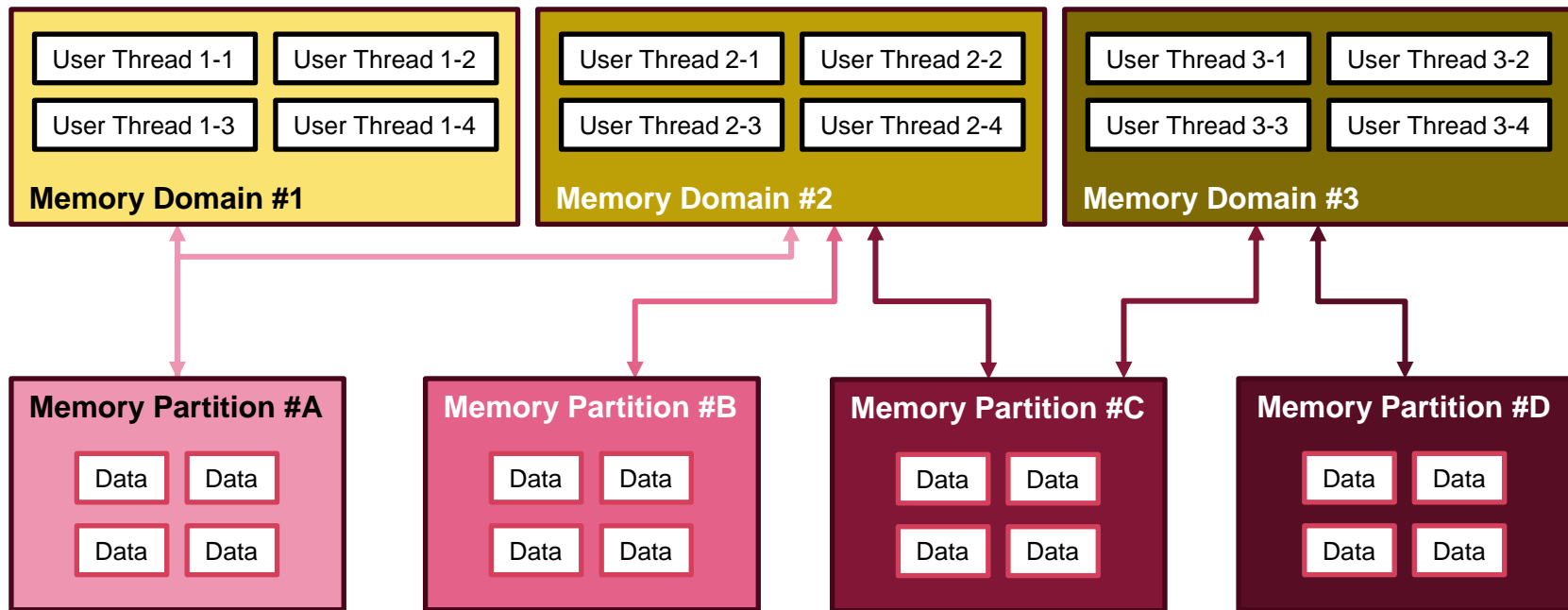
Sharing Data using Memory Domains

... and Memory Partitions



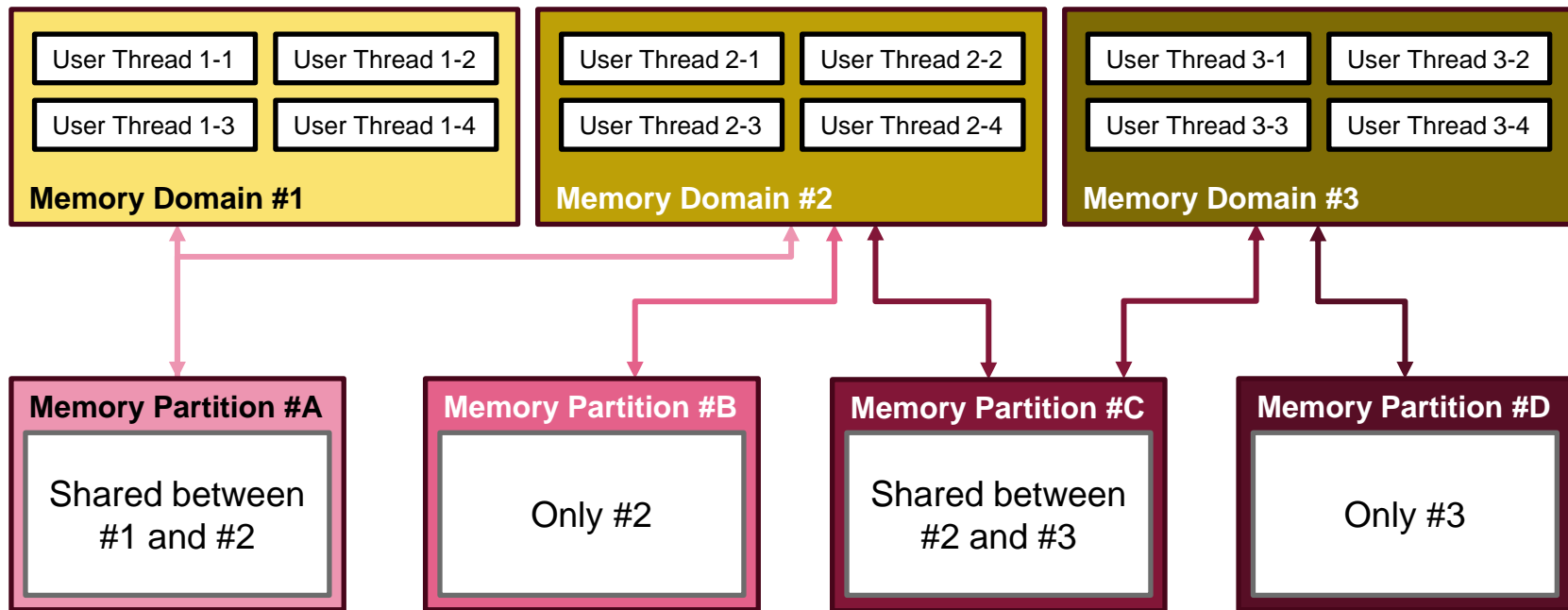
Sharing Data using Memory Domains

... and Memory Partitions



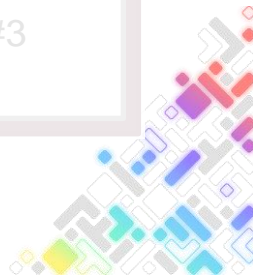
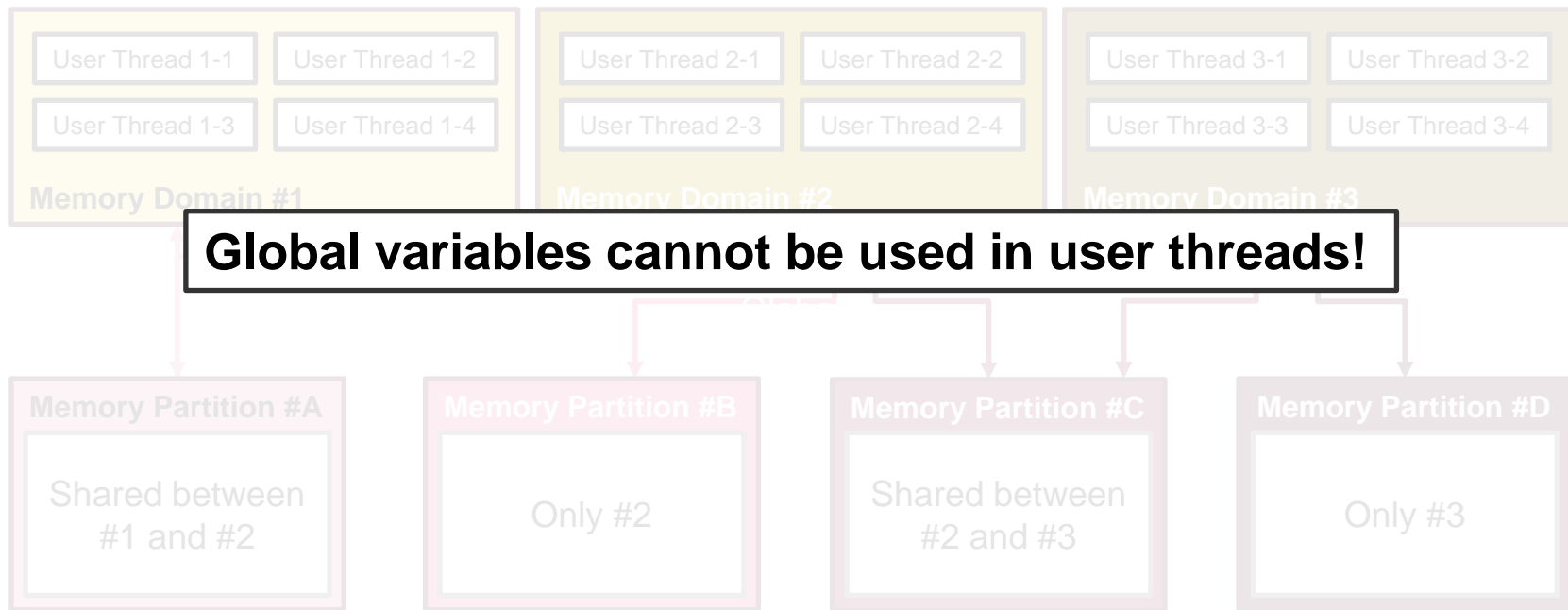
Sharing Data using Memory Domains

... and Memory Partitions



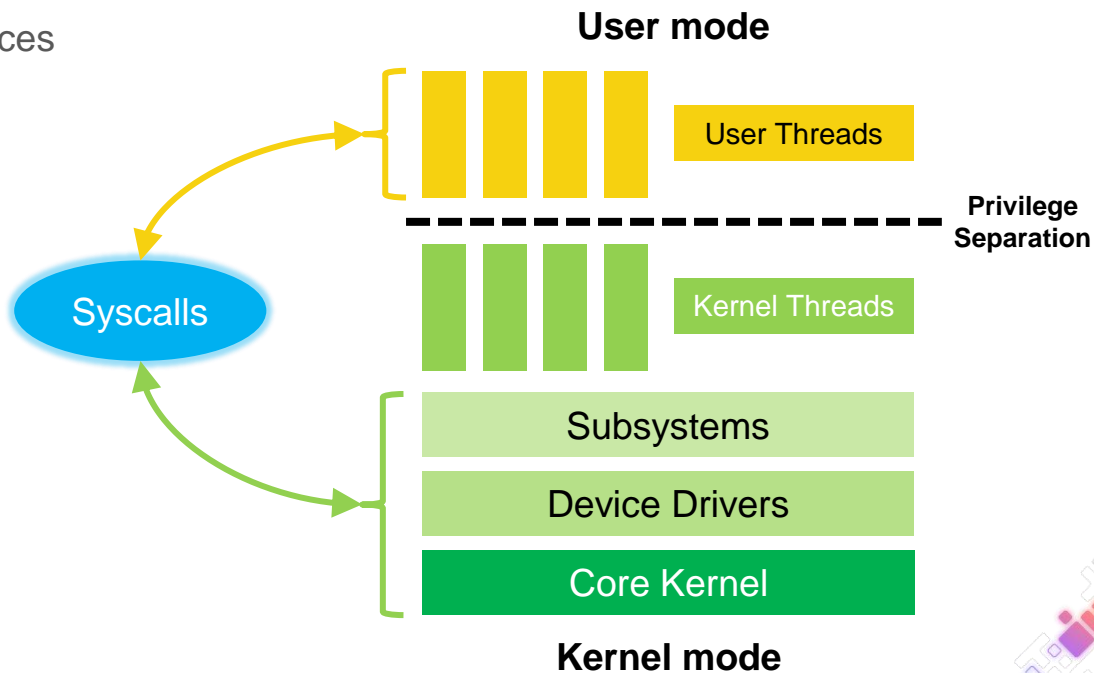
Sharing Data using Memory Domains

... and Memory Partitions



Syscalls

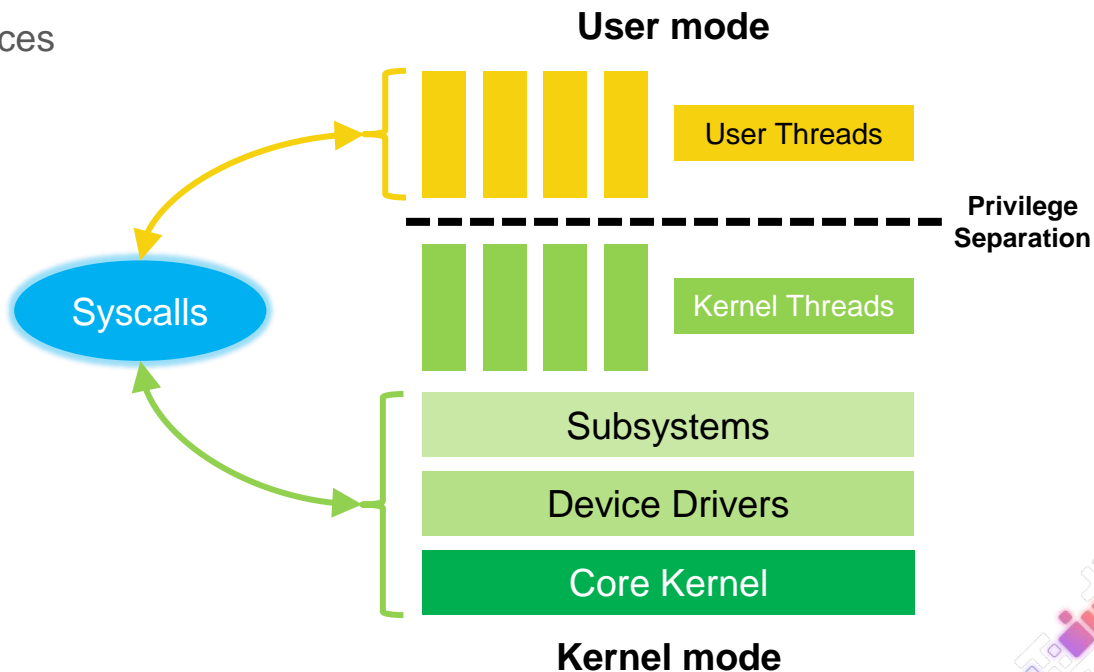
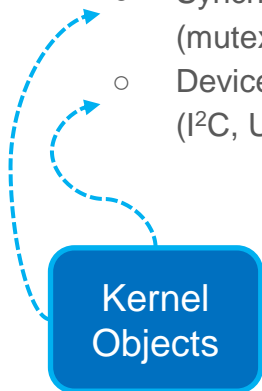
- User threads to access kernel services
 - Synchronization primitives (mutex, semaphore)
 - Device access (I²C, UART)



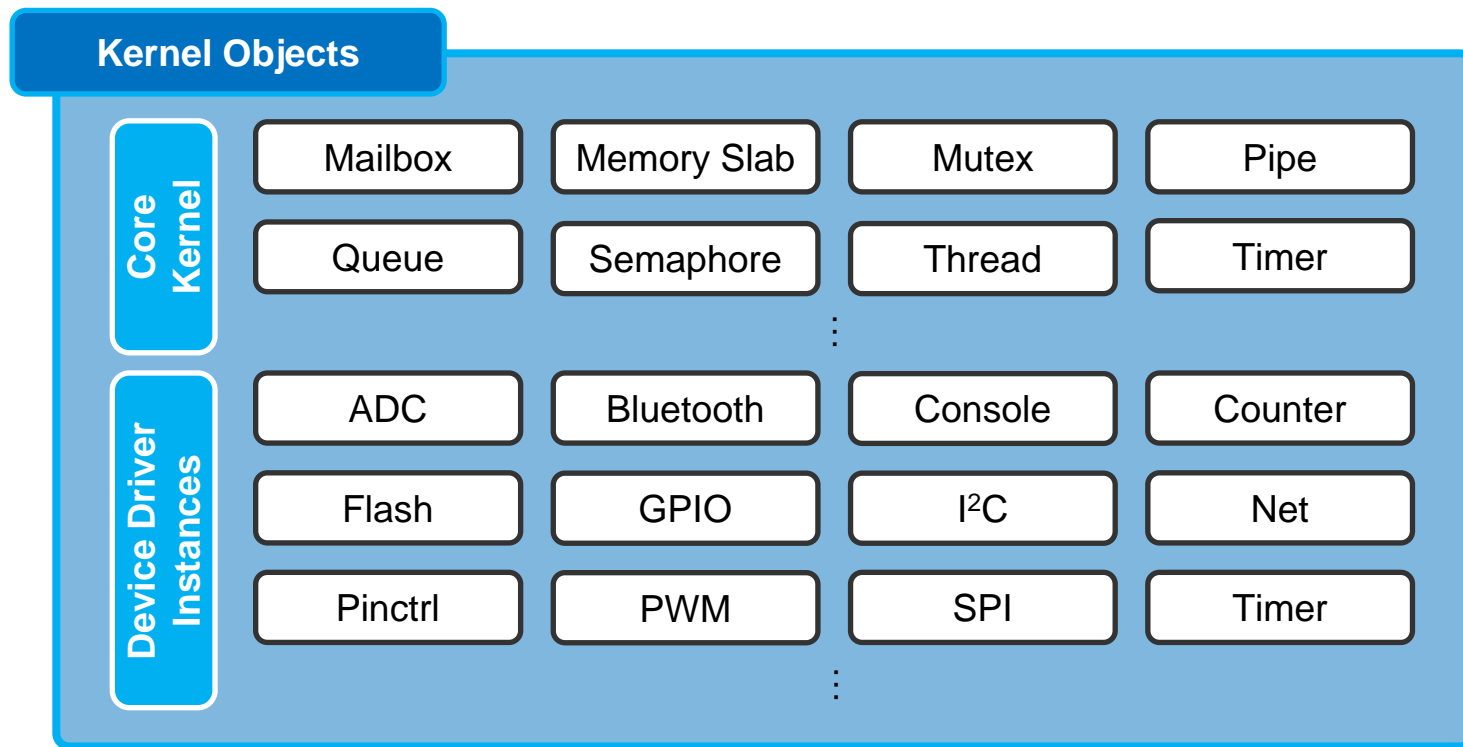
Syscalls

- User threads to access kernel services

- Synchronization primitives (mutex, semaphore)
- Device access (I²C, UART)



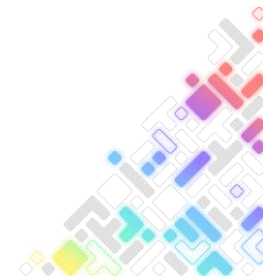
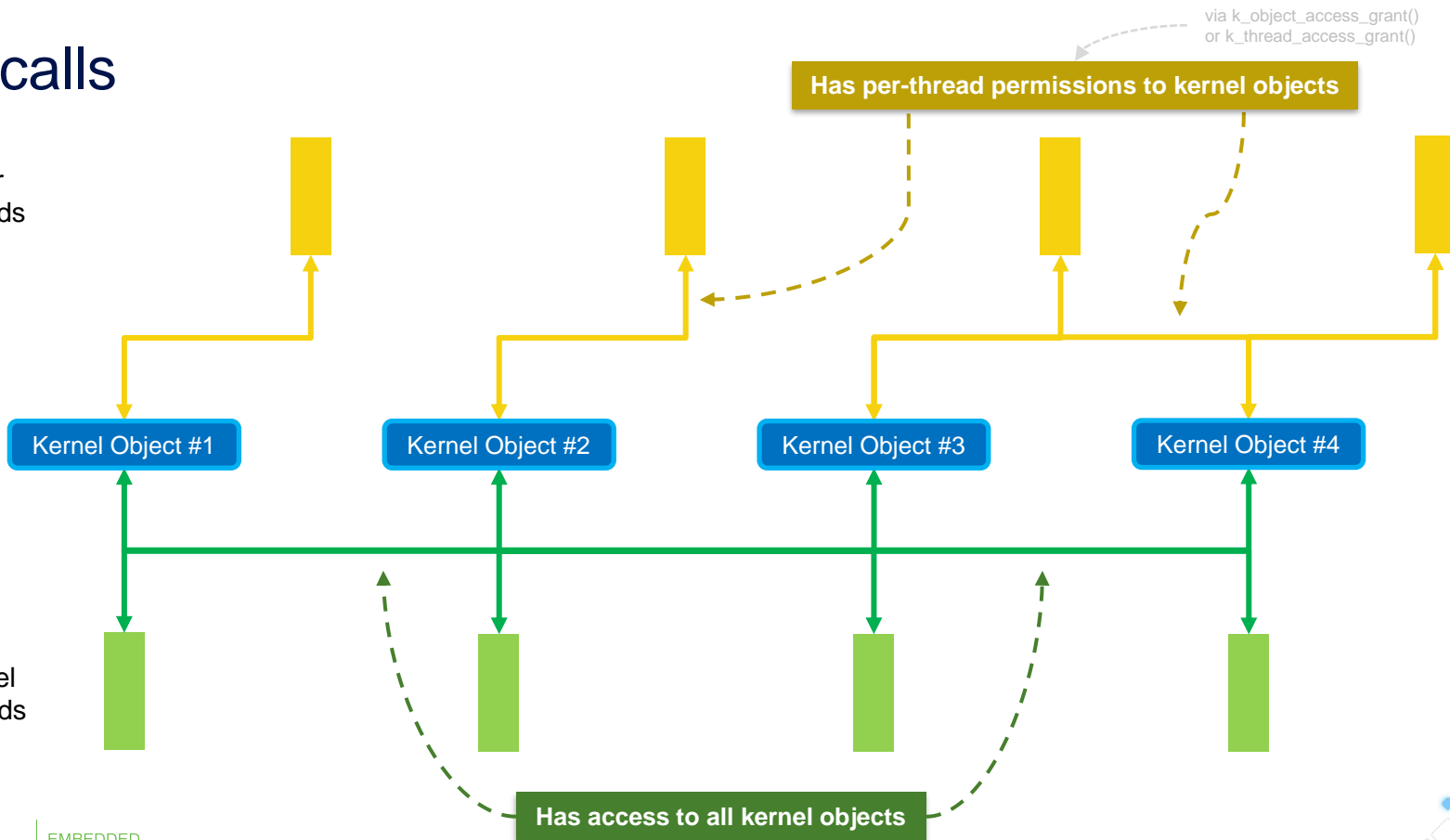
Syscalls



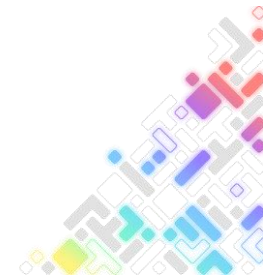
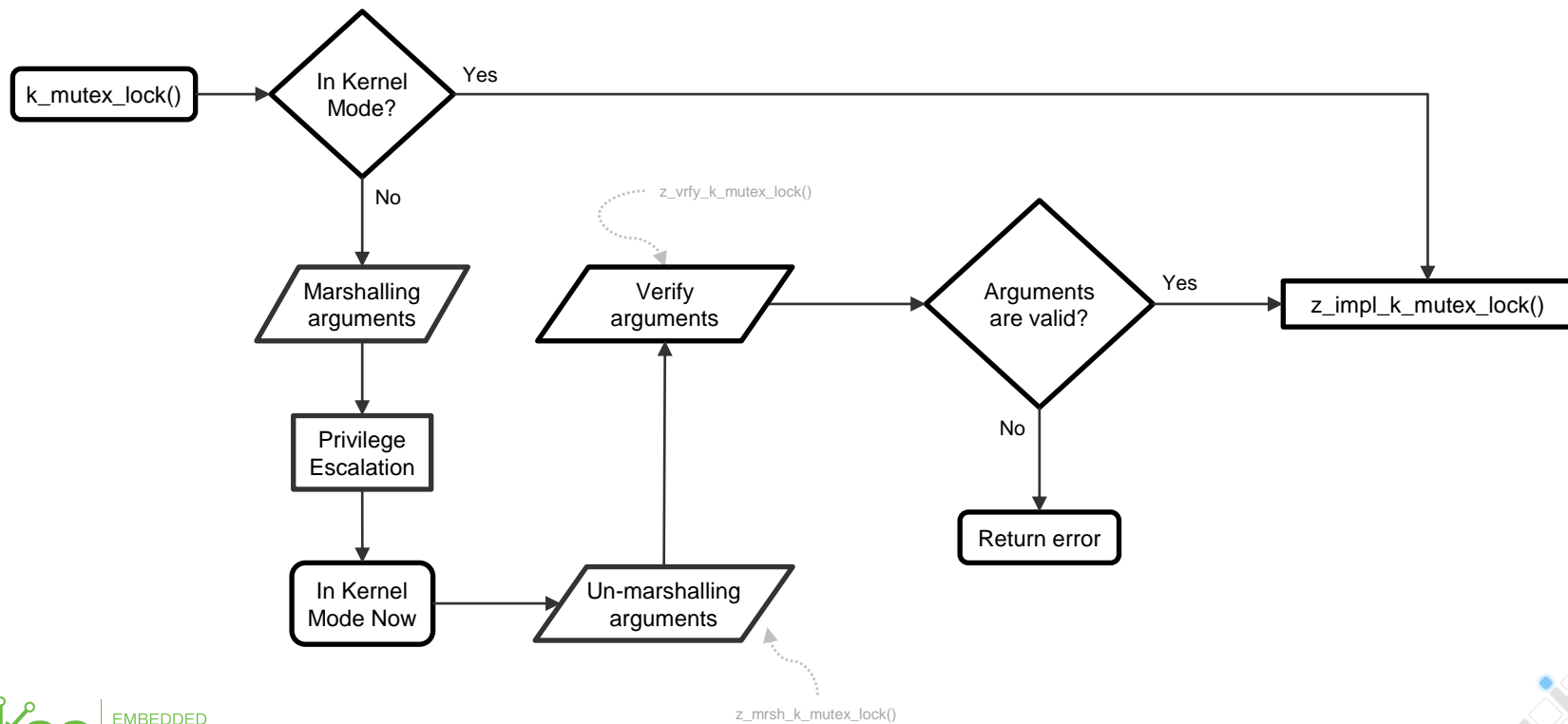
Syscalls

User
Threads

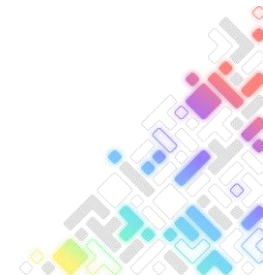
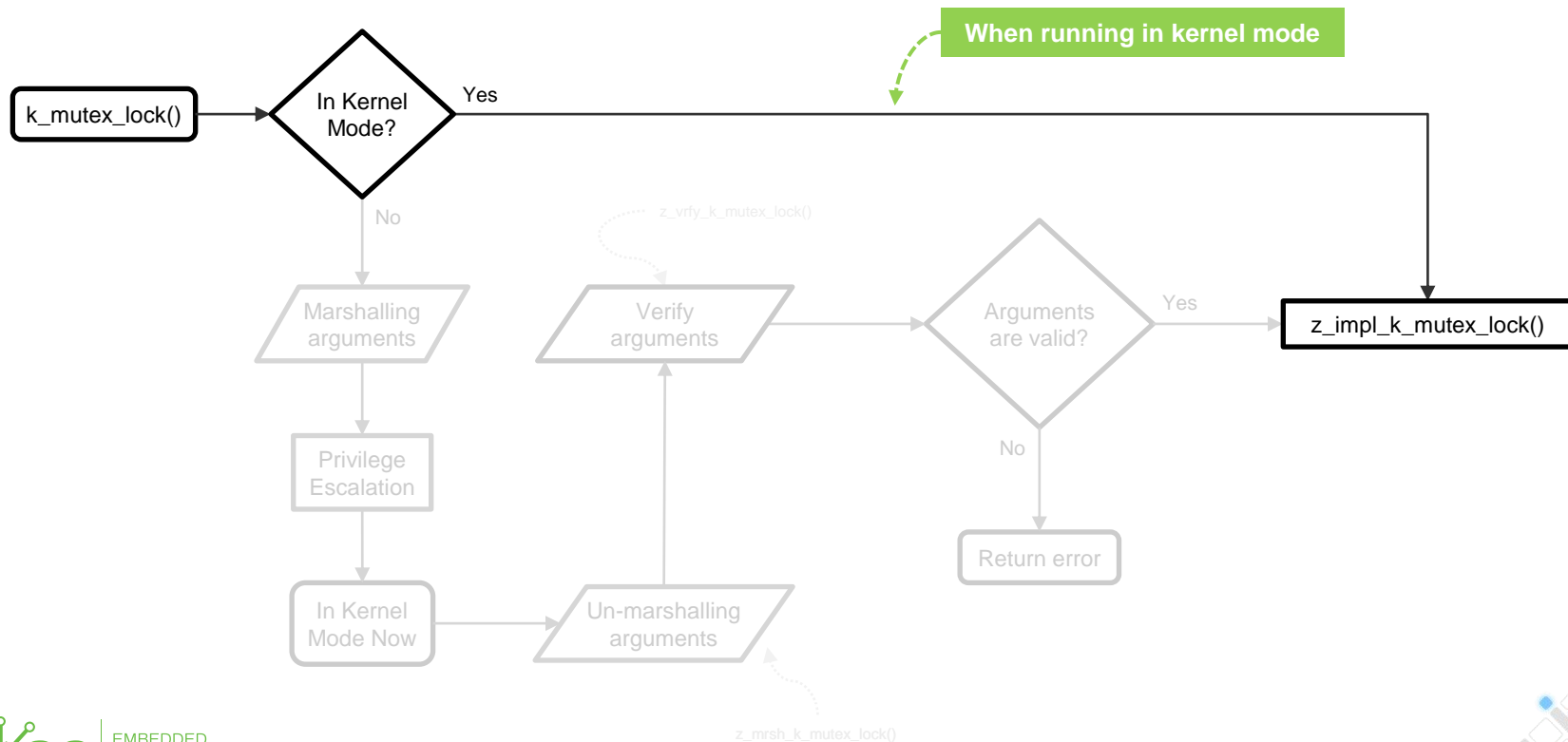
Kernel
Threads



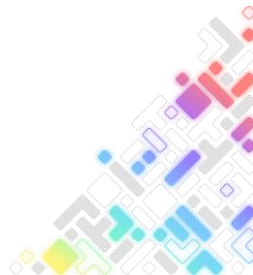
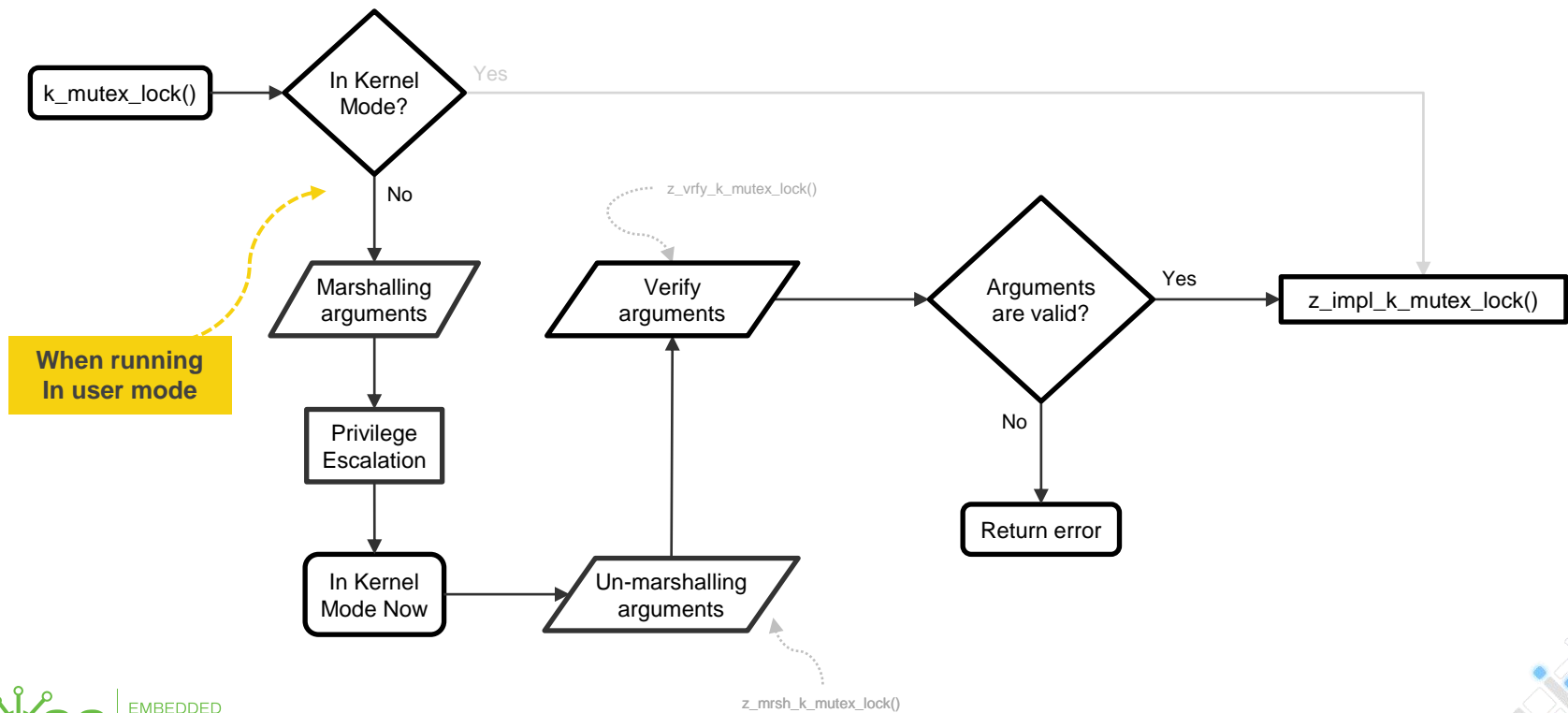
Syscalls



Syscalls



Syscalls



Why and Why Not User Space?

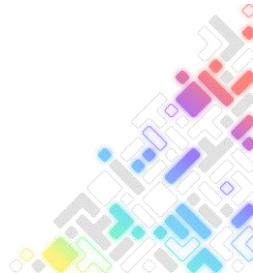
Hardware Requirements

MMU ✓

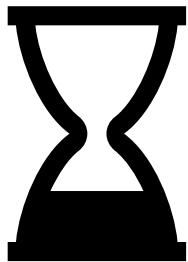
MPU ✓
...or PMP

~~**MMU**
MPU~~

✗



Why and Why Not User Space?



Latency



- Context Switching
- Syscalls

1010
1010

**Memory
Usage**

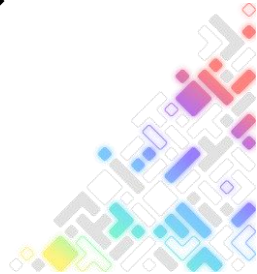
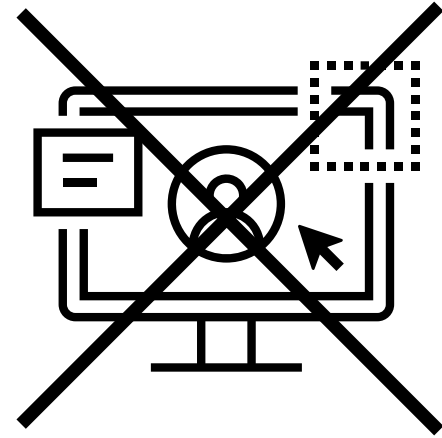


- Storage for Page Tables
- Stack for Privileged Mode
- Page Alignment for Memory Partitions



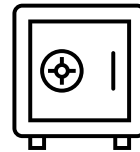
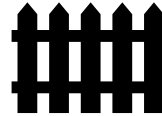
Why and Why Not User Space?

- No interactive users

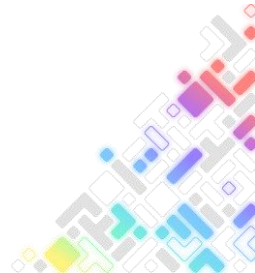


Why and Why Not User Space?

- Catch coding errors
- Isolation between memory domains
- Not for guarding against malicious code
 - User mode code is not trusted

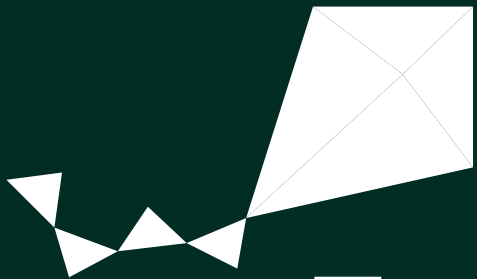


... gets complicated with LLEXT.



Q&A





Zephyr[®] Project

Developer Summit

