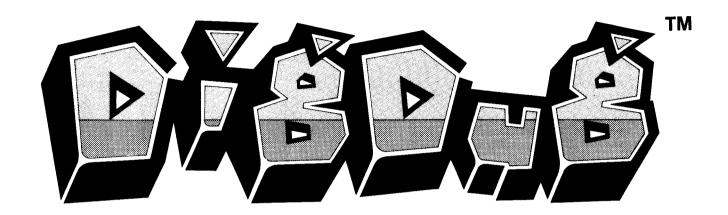
# Schematic Package Supplement to



Operation, Maintenance and Service Manual

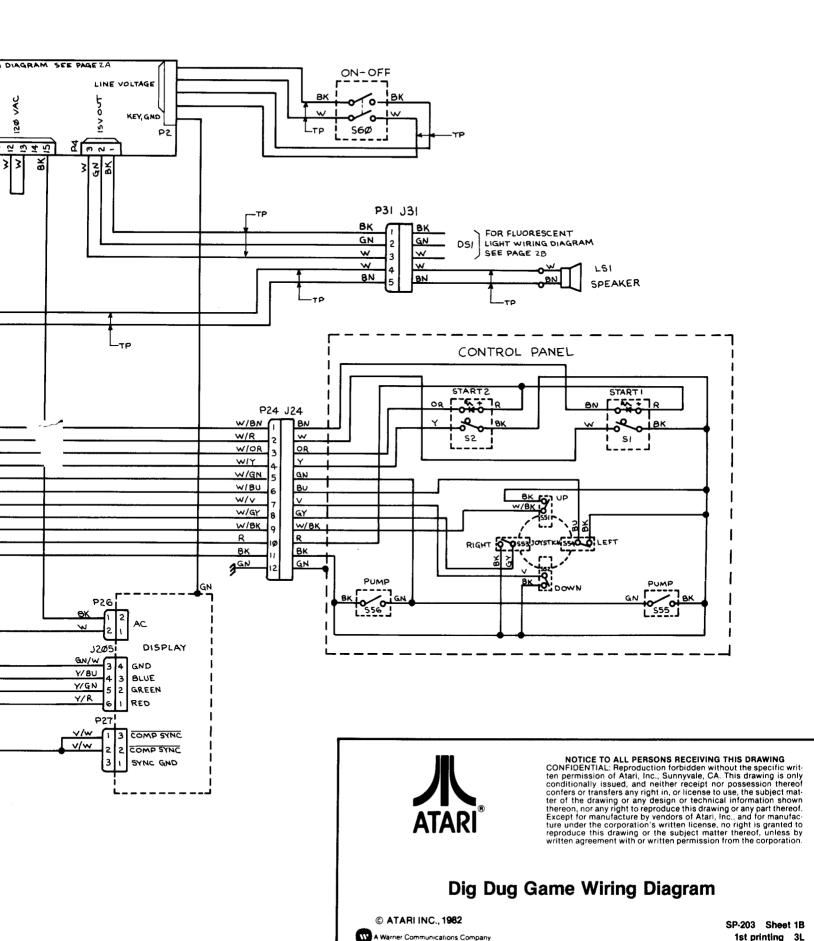


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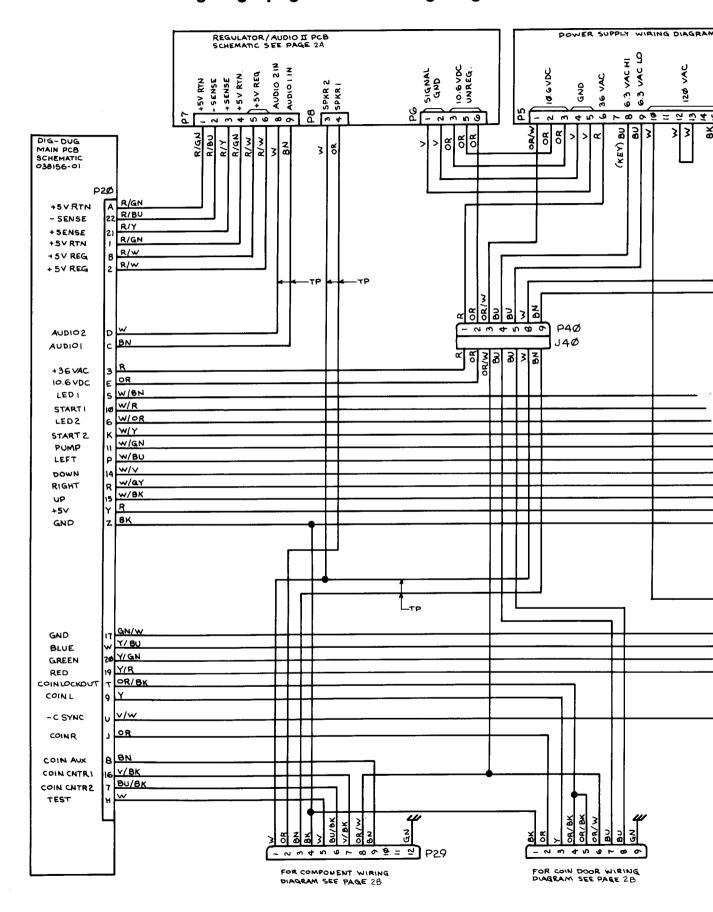
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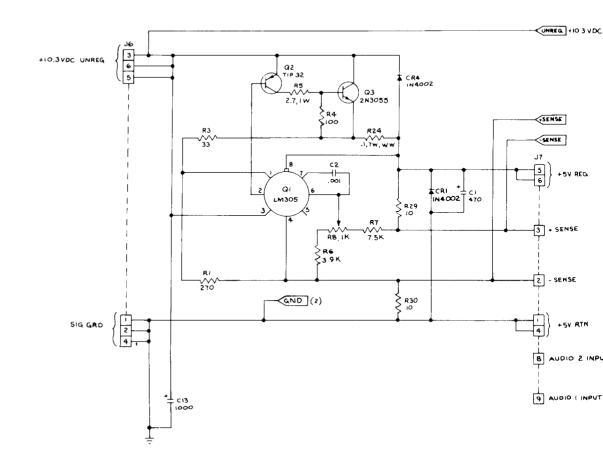
This staple temporarily holds th schematic package together. Ru move the staple before using th

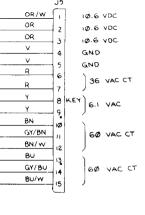


## Dig Dug Upright Game Wiring Diagram



### Regulator/Audio II PCB Schematic Diagram





POWER CORD ASSY MAY HAVE WIRE COLORS AS SHOWN OR WIRE COLORS AS SHOWN OR WIRE (AC), ONE GREEN WIRE (AU) AND ONE WHITE WIRE (AU) AND ONE WHITE WIRE (AU) AND ONE WIRE AND SERVE OF USE AT FI WITH 22ØV & 24ØY (EUROPEAN ONLY)



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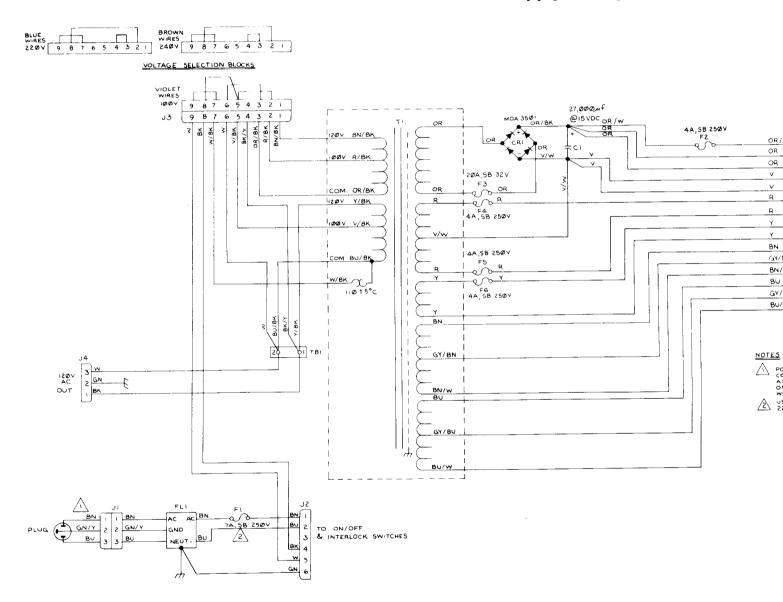
## Dig Dug Power Supply and Reg./Audio II PCB

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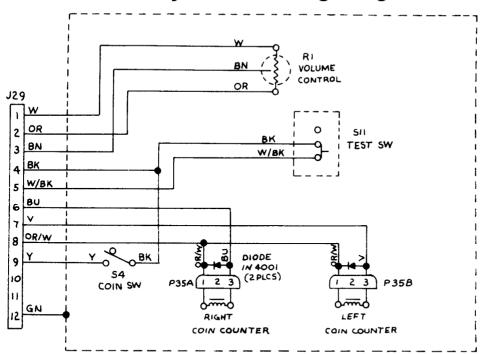
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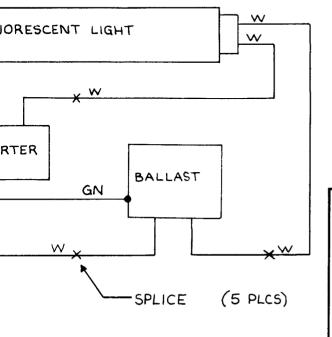
## **Color Raster Power Supply Wiring Diagram**



## **Utility Panel Wiring Diagram**



## **And Speaker Wiring Diagram**





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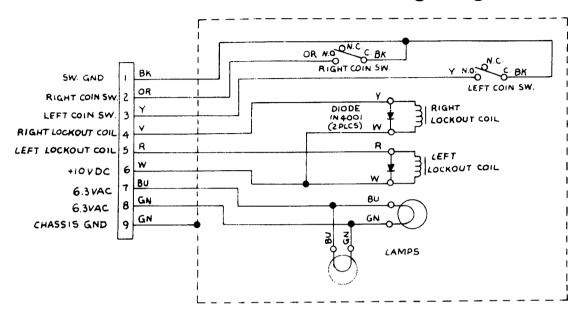
## **Dig Dug Game Wiring Interfaces**

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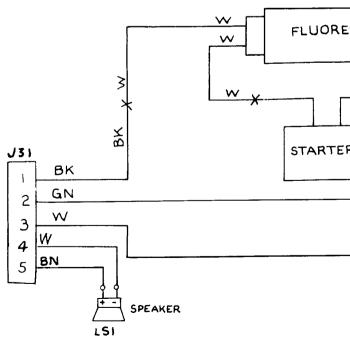
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## **Coin Door Wiring Diagram**



## Fluorescent Light An



### **MEMORY MAP**

HEXA- DECIMAL	R/W				DA	TA			FUNCTION	
ADDRESS		D7	D6	D5	D4	D3	D2	D1	D0	
0000-3FFF	R	D	D	D	D	D	D	D	D	1st Priority Z80 CPU ROM (16K)
0000-1FFF	R	D	D	D	D	D	D	D	D	2nd Priority Z80 CPU ROM (8K)
0000-0FFF	R	D	D	D	D	D	D	D	D	3rd Priority Z80 CPU ROM (4K)
6800-680F 6810-681F	w w			·		D D	D D	D D	D D	Audio Control Audio Control
6820 6821 6822 6823 6825 6826 6827	» » » » »								D D D D D	0 = Reset IRQ1 (Latched) 0 = Reset IRQ2 (Latched) 0 = Enable NMI3 (Latched) 0 = Reset 2nd and 3rd Z80 CPUs (Latched) Custom Chip 53 Mode Control (Latched) Custom Chip 53 Mode Control (Latched) Custom Chip 53 Mode Control (Latched)
6830	w									Watchdog Reset
7000 7100	R/W R/W	D D	D D	D D	D D	D D	D D	D D	D D	Custom Chip 06—Data Custom Chip 06—Command
8000-87FF	R/W	D	D	D	D	D	D	D		2K Playfield RAM
8800-8BFF 9000-93FF 9800-9BFF	R/W R/W R/W	D D	D D D	1K Motion RAM (PIC) 1K Motion RAM (HPOS, VPOS) 1K Motion RAM (FLIP)						
A000 A001 A002 A003 A004 A005 A007	W W W W W								D D D D D	Playfield Select (Latched) Playfield Select (Latched) Playfield Color Select (Latched) Alphanumeric Color Select (Latched) Playfield Color Select (Latched) Playfield Color Select (Latched) Flip Video
B800-B83F B800 B840	W R W	D D	D D	D D	D D	D D D	D D D	D D D	D D D	Write EAROM Address and Data Read EAROM Data Write EAROM Control

	 NMTC
J	
	RES
	 POR
	V RESET
	44 PIN EDGE
	CONNECTOR

HRESET

VBLANK

- 64H\*

- 32H\*

- 16H\*

- 8H\*

- 4H\*

- 4H\*



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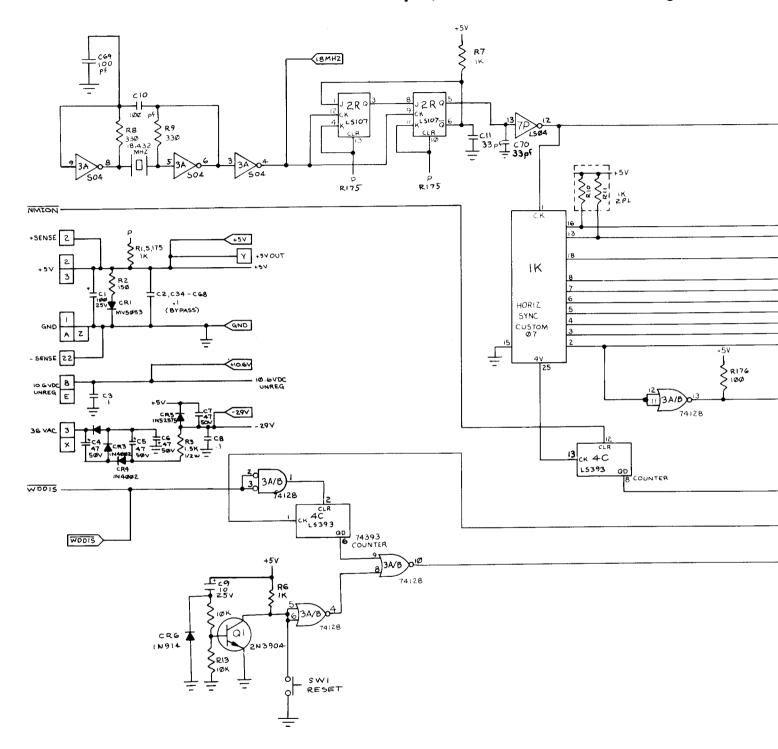
## Dig Dug CPU PCB Schematic Diagram

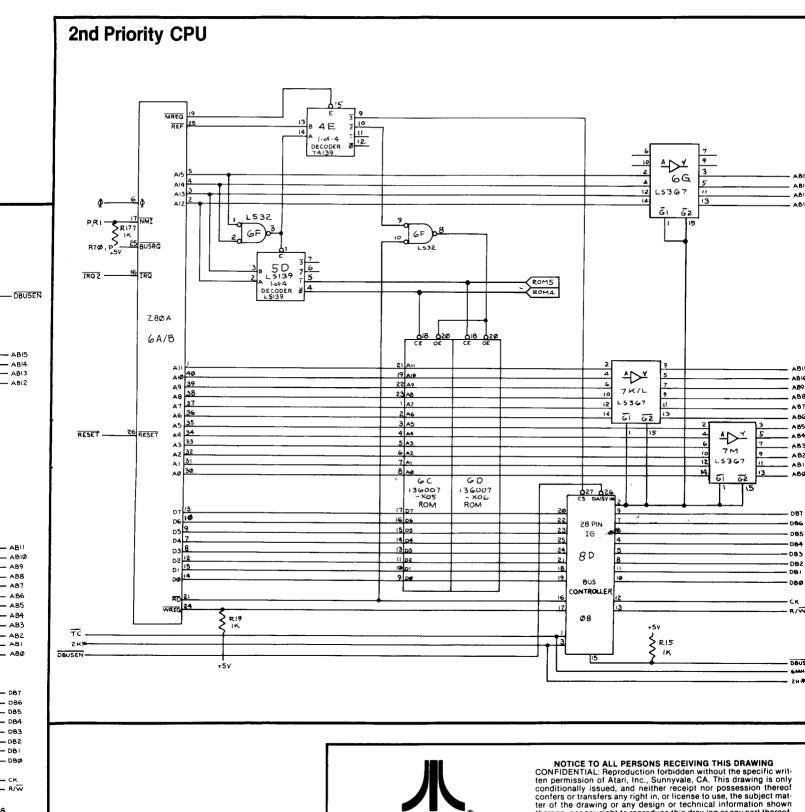
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## CPU PCB Power Input, Clock, NMI, and Watchdog







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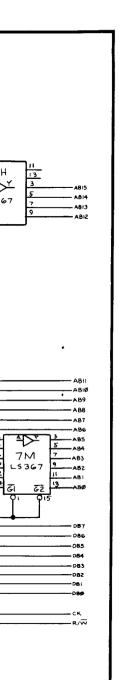
## Dig Dug CPU PCB Schematic Diagram

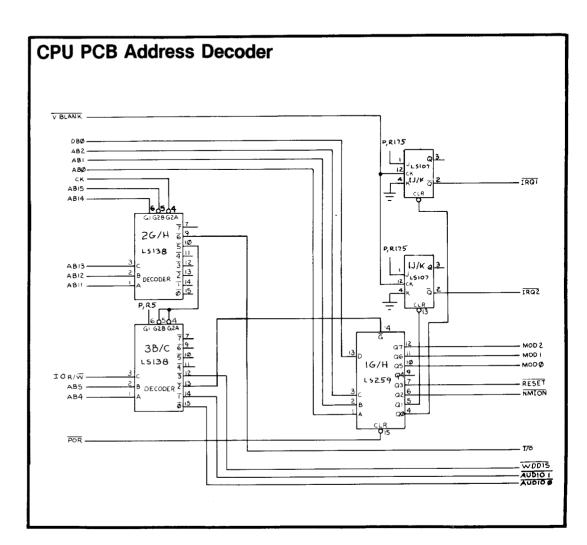
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### 1st Priority CPU MREQ A | of 4 DECODER 74139 12 14 2 7*G* - ABI4 - ABI3 L5367 A14 A13 A12 - AB12 56/H03 ROM3 ROM2 ROM1 IRQI -6J/K 28Ø A 7P L5367 A11 1 A10 40 A9 39 A8 38 A7 37 A6 36 A5 35 A4 34 A3 33 A2 32 A1 31 A0 36 19 AIØ 22 A9 23 AB ABIØ - AB9 - AB8 AB7 - A85 - A84 <u></u> <u> → </u> <u> → </u> 26 RESET POR -- AB3 7J/K - AB2 - AB1 - AB9 L5367 6 M 136007 ROM - DB7 - DB6 - DB5 22 D3 DB3 23 D2 28 PIN DB2 15 D5 14 D4 13 D3 11 D2 10 D1 9 00 05 9 D**B4** DB3 DB4 B D3 21 04 0853 18 07 19 06 8K/L - DB2 D2 - DBI RD RG IK BUS CONTROLLER RI78 R/₩ Ŧć.





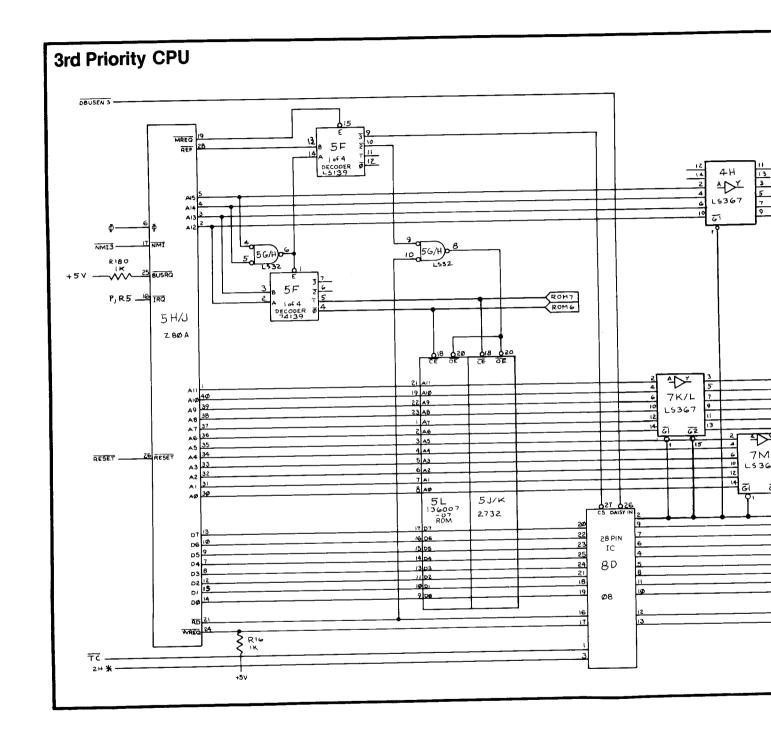


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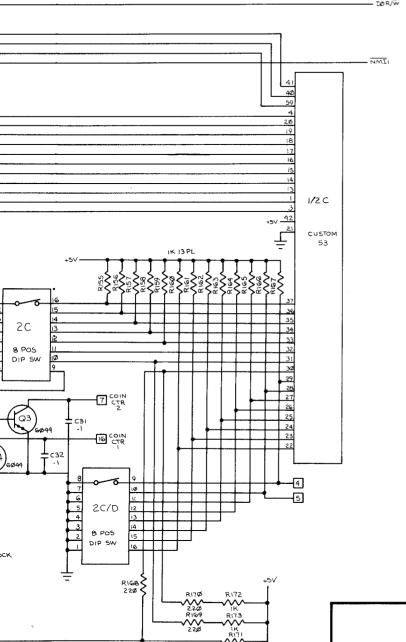
## Dig Dug CPU PCB Schematic Diagram

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## ol Panel Outputs





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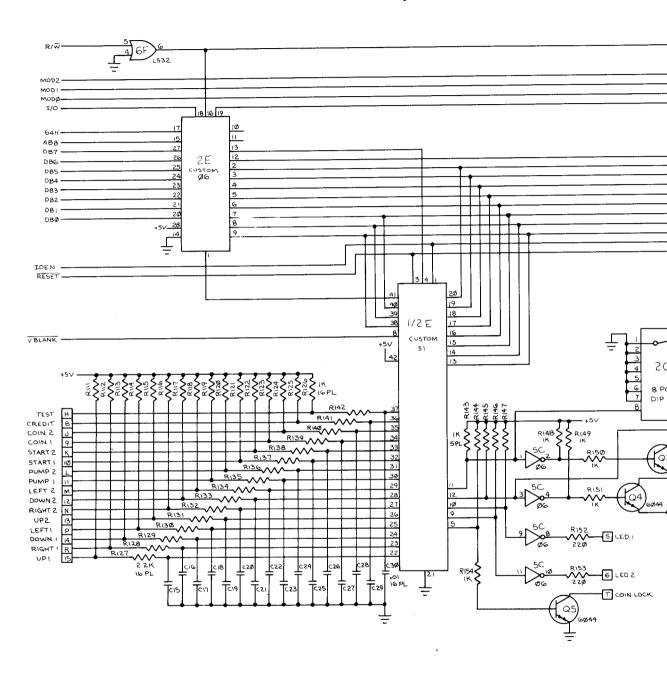
## Dig Dug CPU PCB Schematic Diagram

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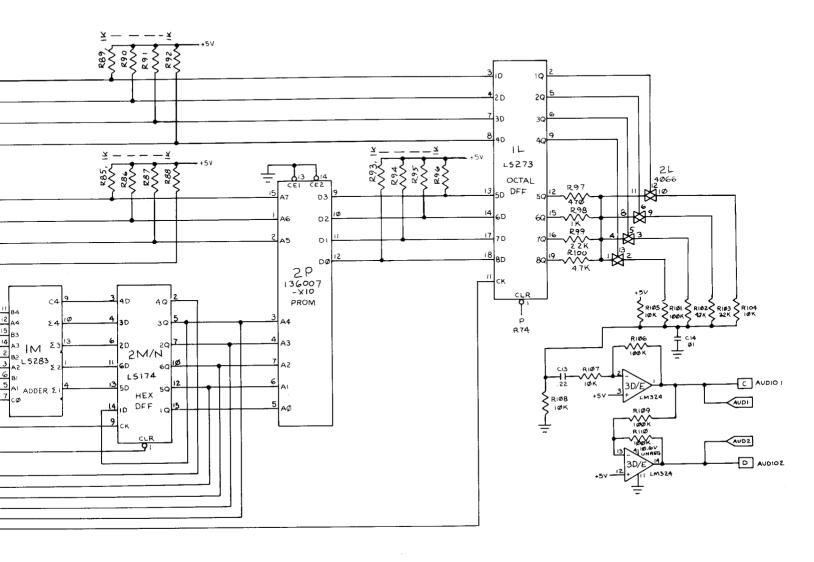
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## Switch Inputs, Coin Door and Control P



COCKTAIL







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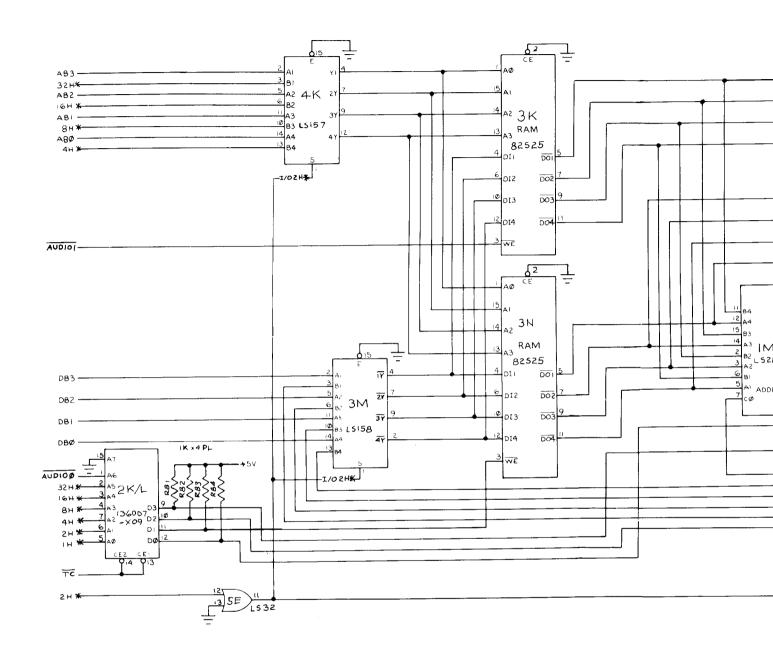
## Dig Dug CPU PCB Schematic Diagram

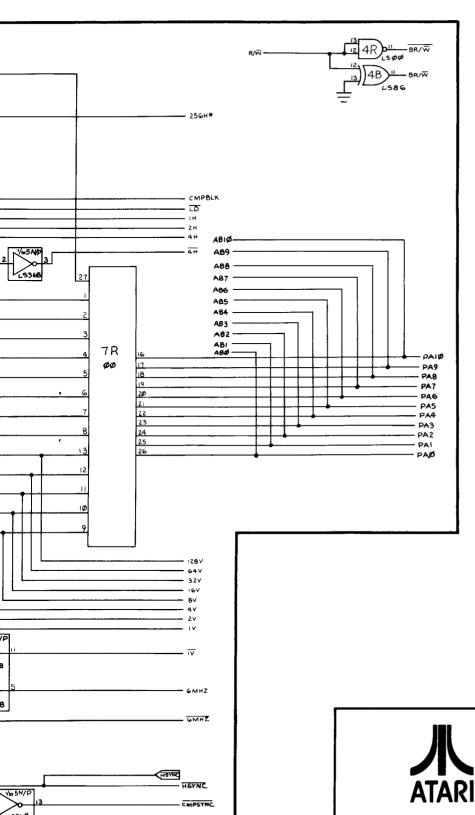
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## Audio







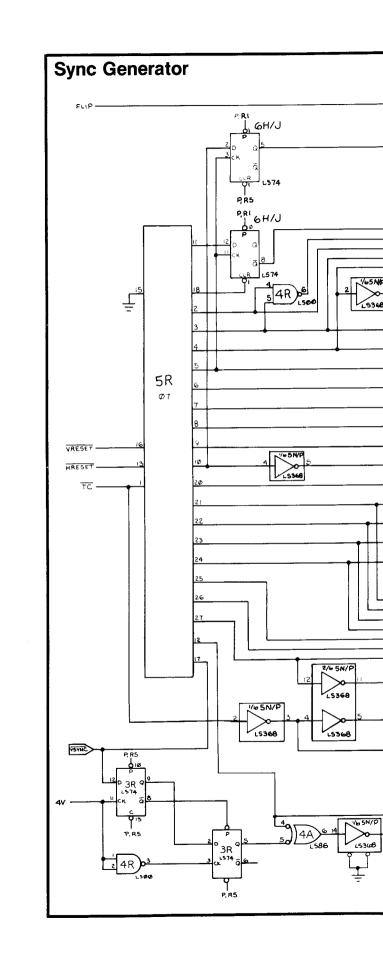
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## Dig Dug Video PCB Schematic Diagram

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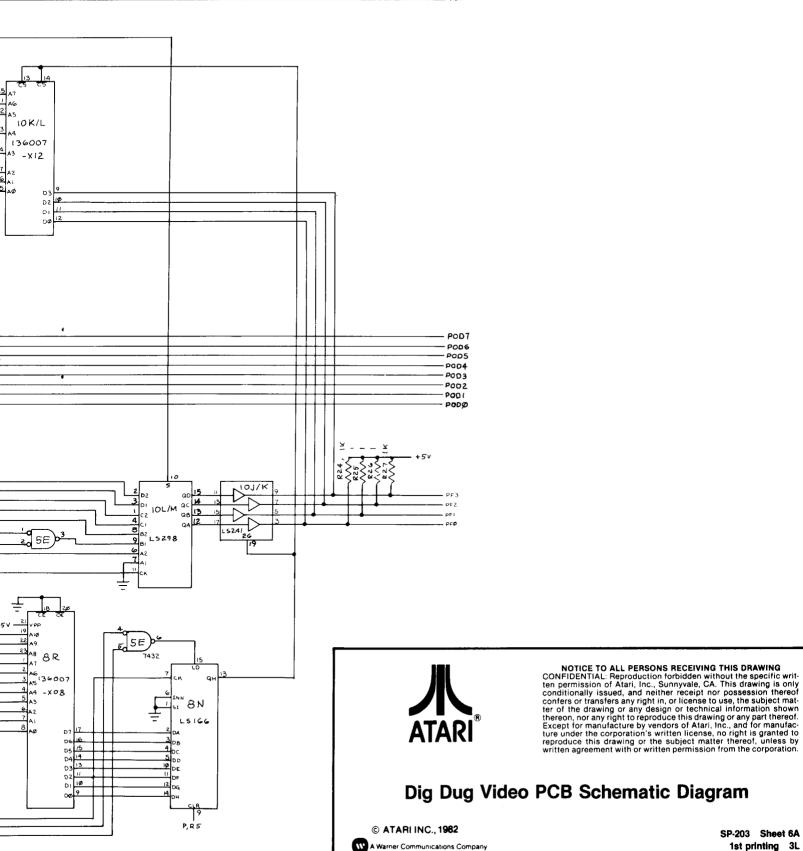
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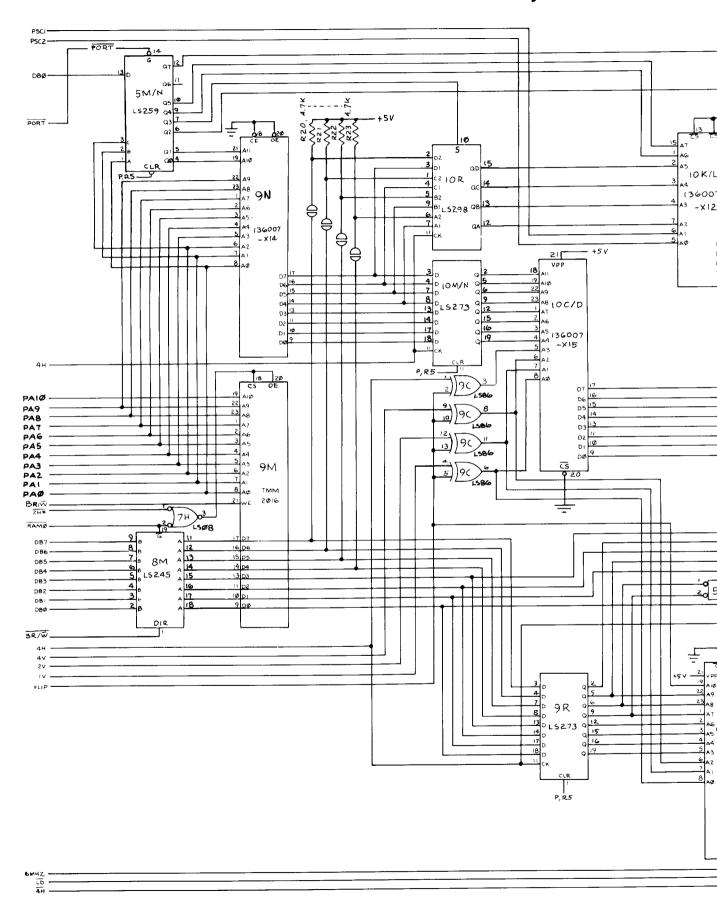


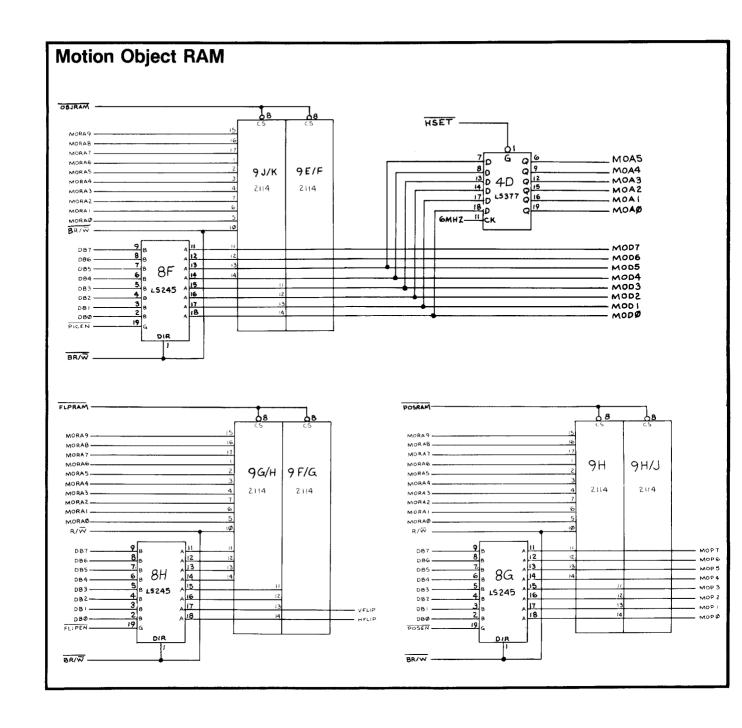
SP-203 Sheet 6A

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## **Playfield Generator**





EAROM

OBJRAM

POSRAN

PICEN

RAMØ POSEN

MORAS

MORAS MORAT

- MORAS - MORA3 - MORA3 - MORA1 - MORAI - VSET - HSET - OBJEN

FERRA

\_ 5 4 V

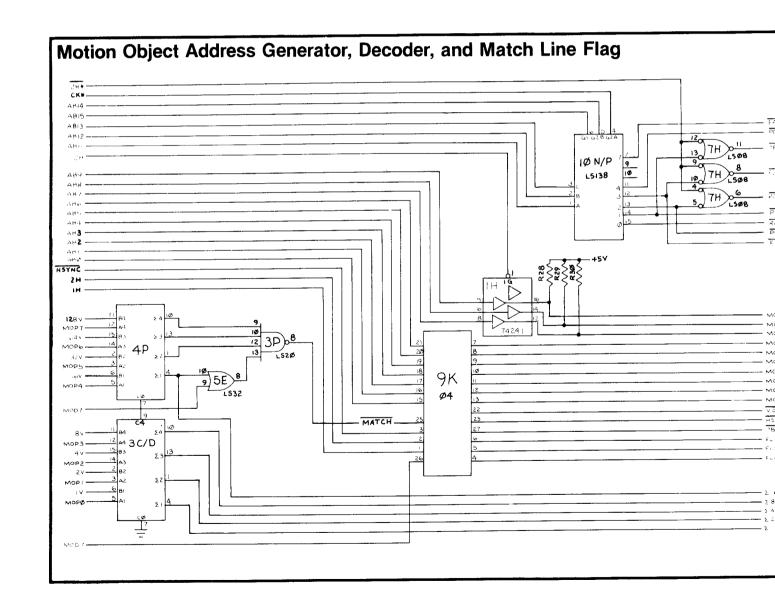


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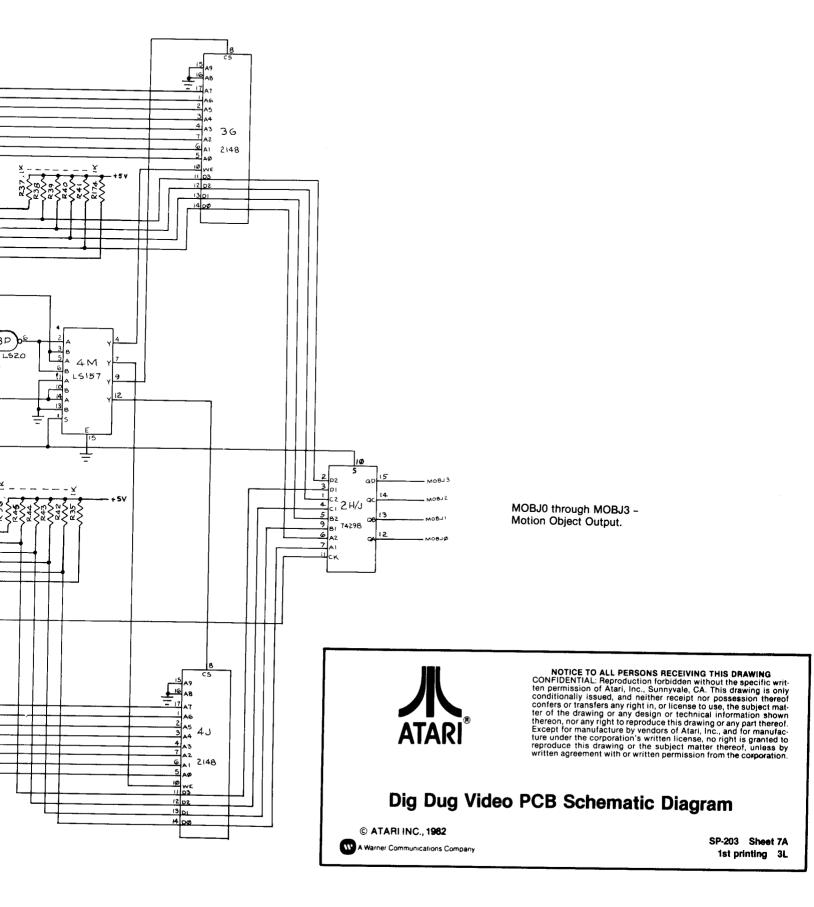
## Dig Dug Video PCB Schematic Diagram

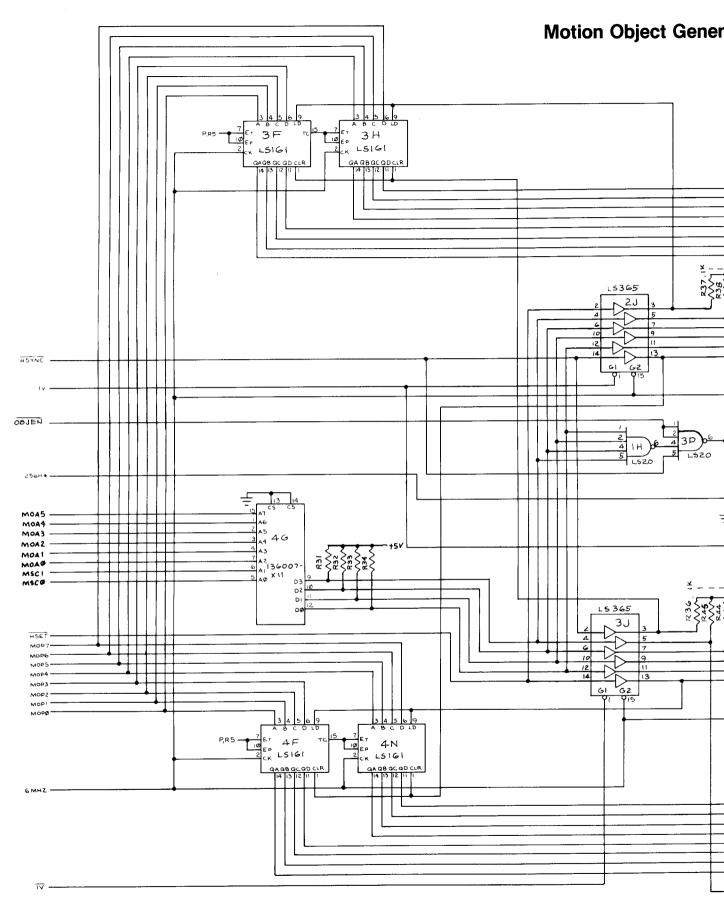
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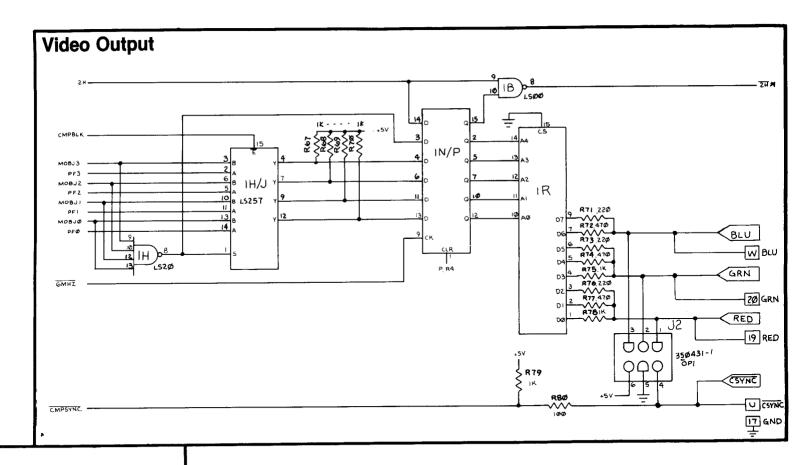


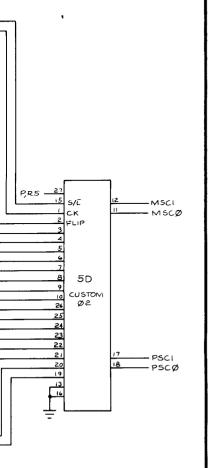
### Generator





MOP0 through MOP7 - Motion Object V and H Position Data. MOBJA0 through MOBJA7 - Motion Object Picture Address.







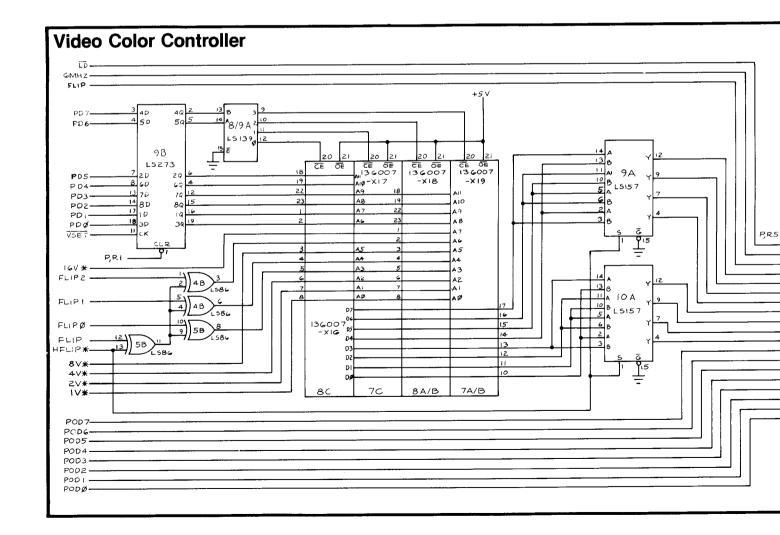
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## Dig Dug Video PCB Schematic Diagram

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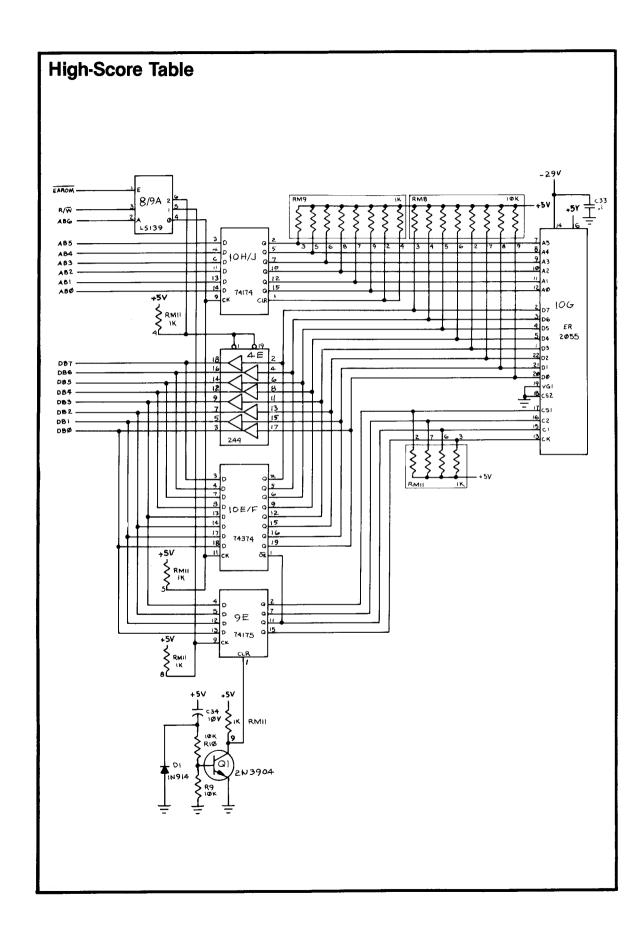


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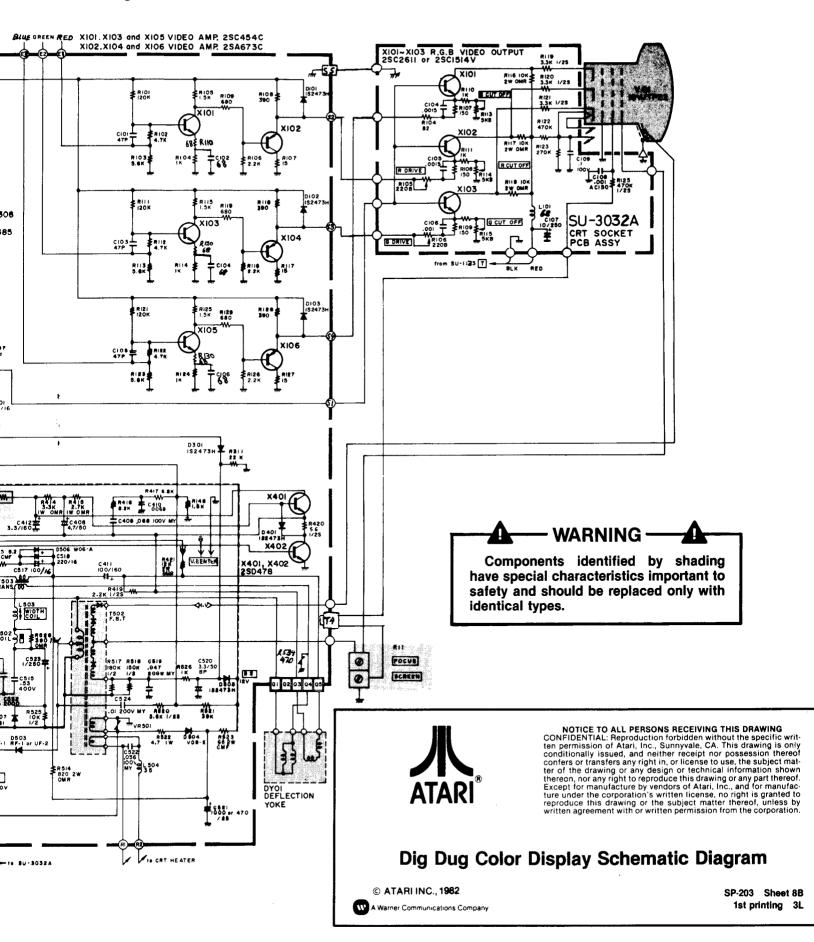
## Dig Dug Video PCB Schematic Diagram

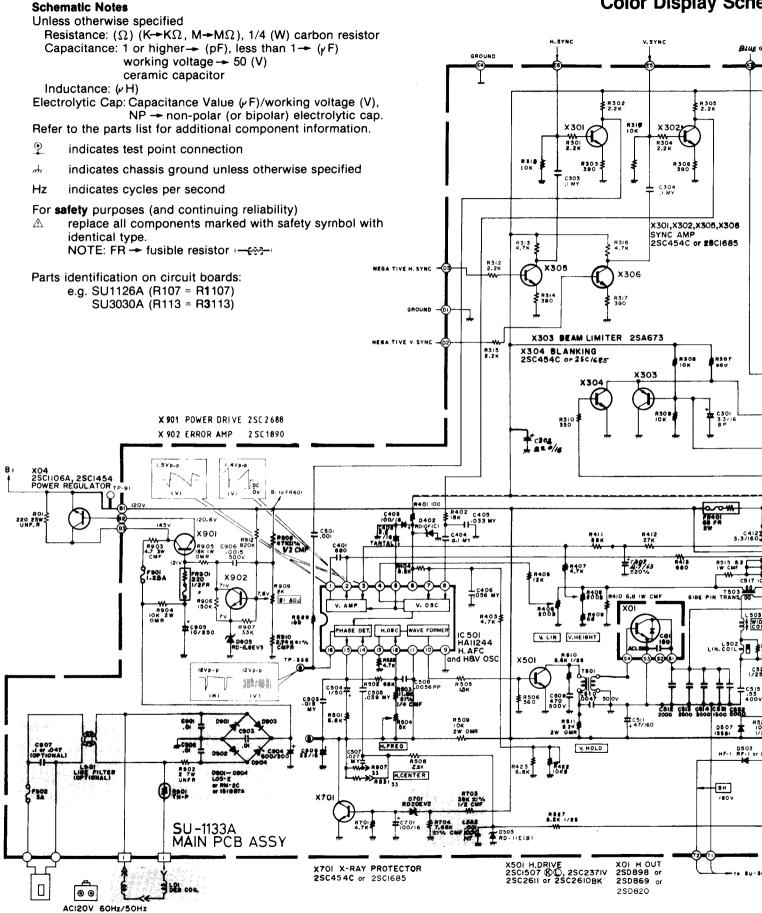
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## Schematic Diagram





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