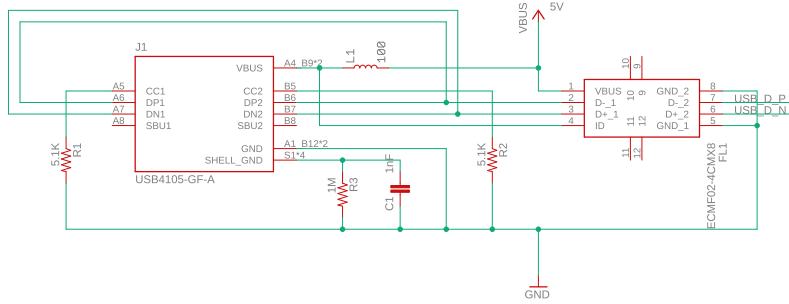


USB Connector



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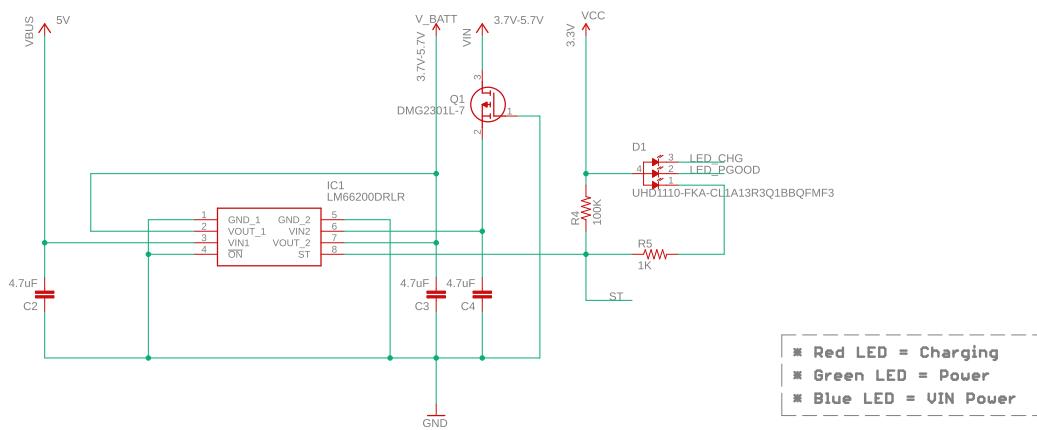
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Ideal Diode



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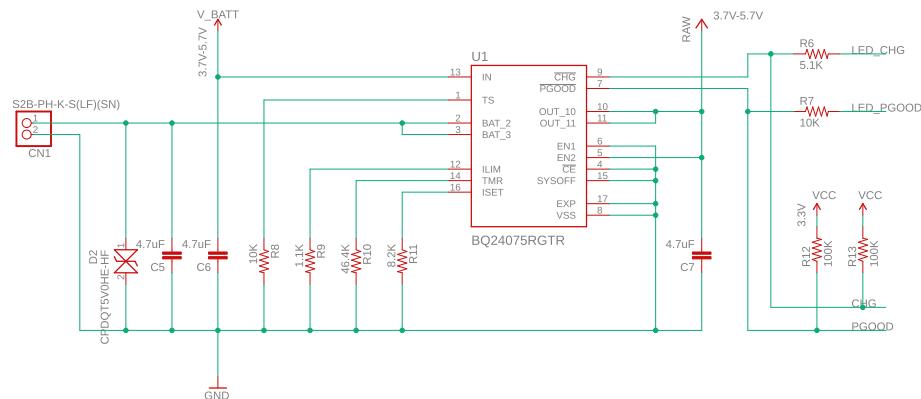
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Battery Charging



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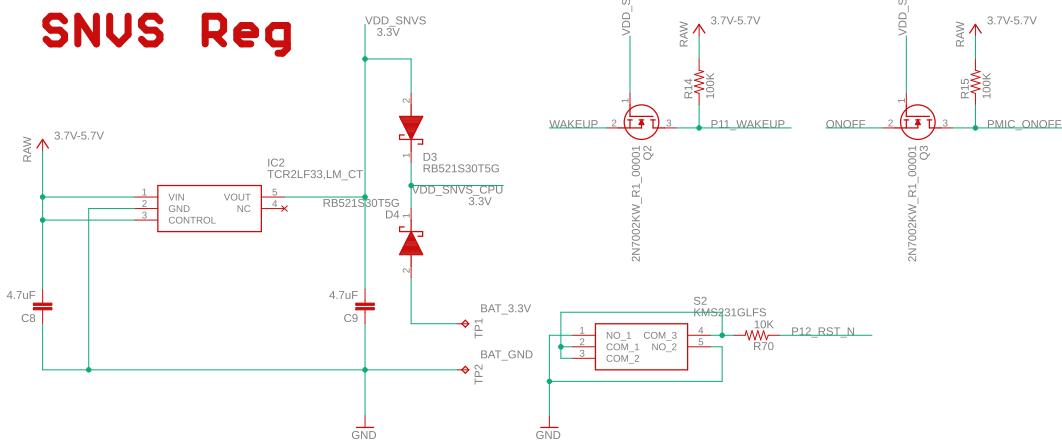
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SNVS Reg



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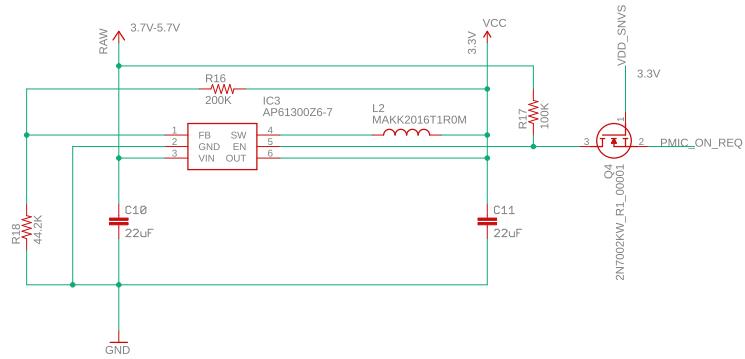
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Main Regulator



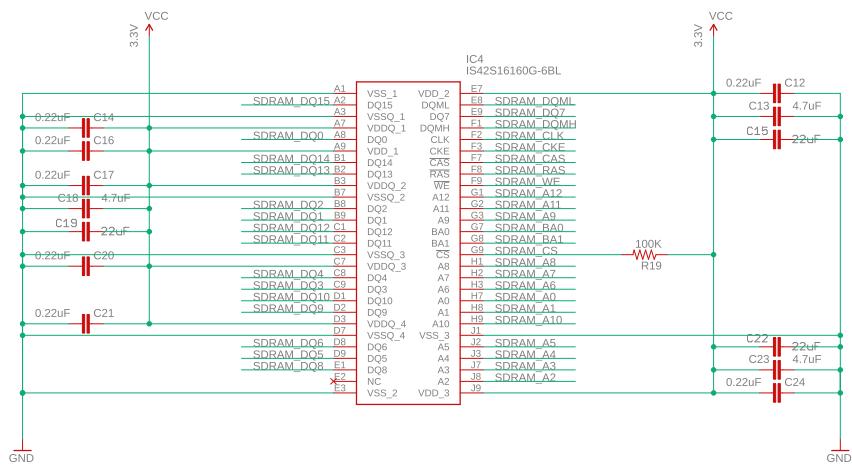
* PMIC_REG_ON is level shifted to ensure regulator goes into PFM mode

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16-BIT SDRAM

* CKE is pulled down by 100K on the RT1062



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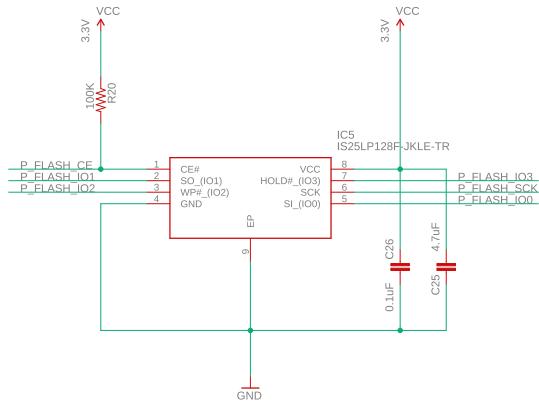
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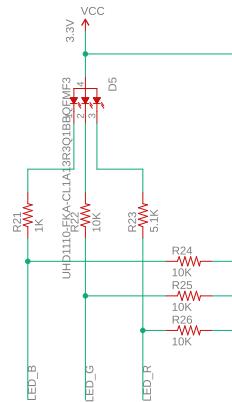
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Program QSPI Flash



RGB LED



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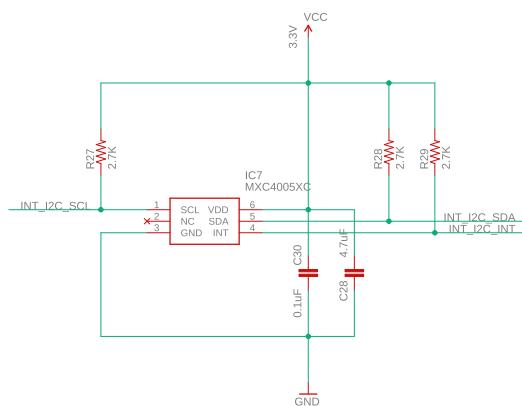
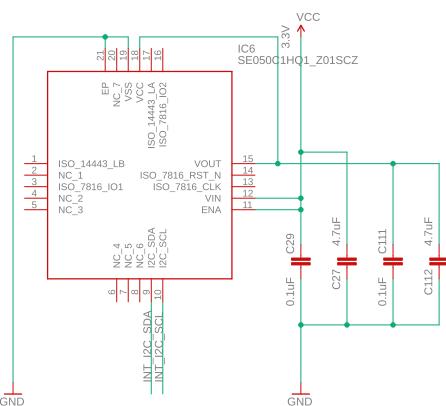
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Security

Accelerometer



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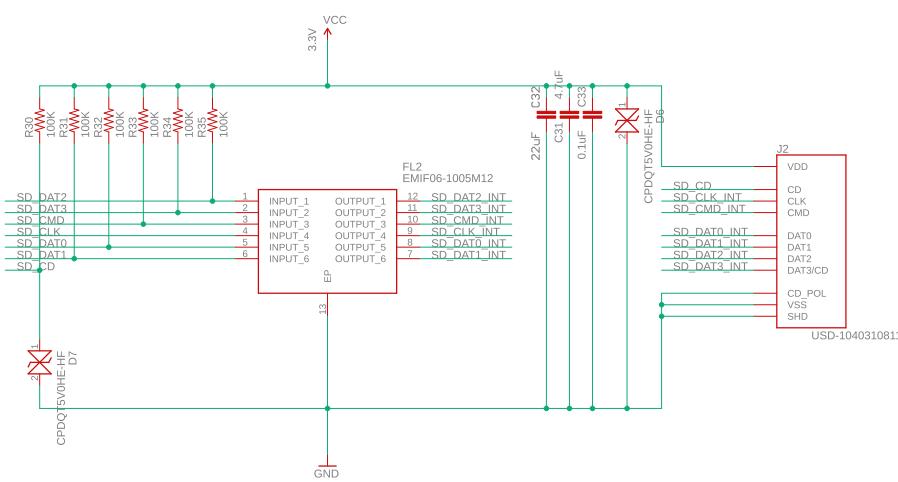
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SD CARD



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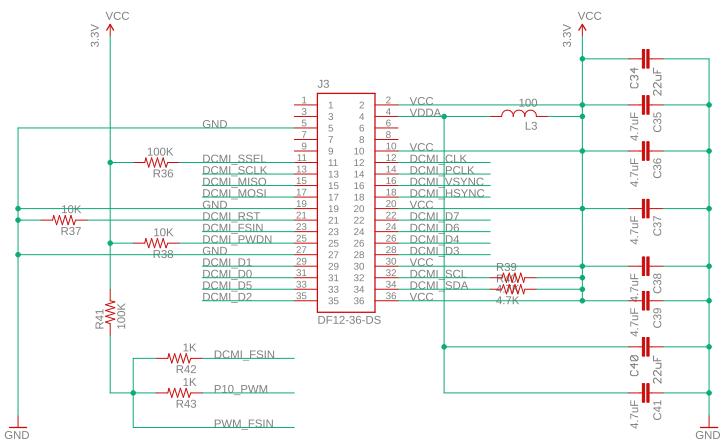
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IMAGE SENSOR CONNECTOR



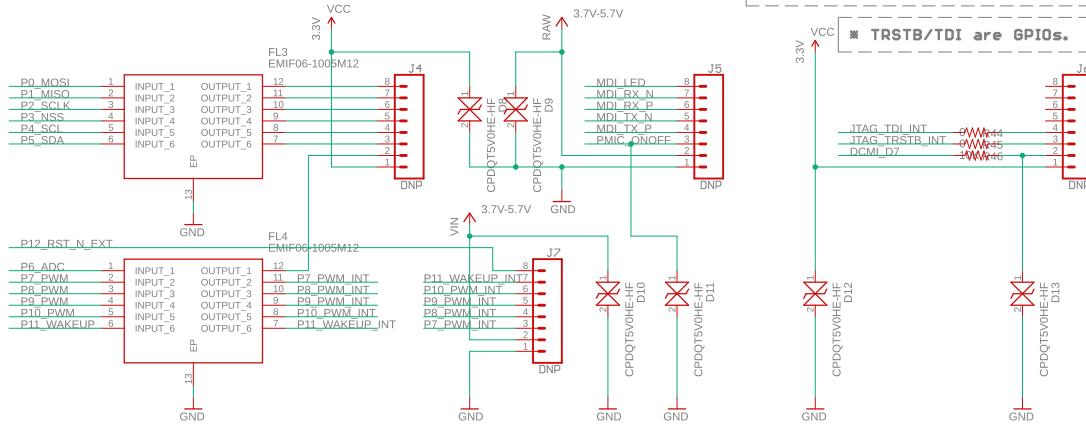
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I/O HEADERS



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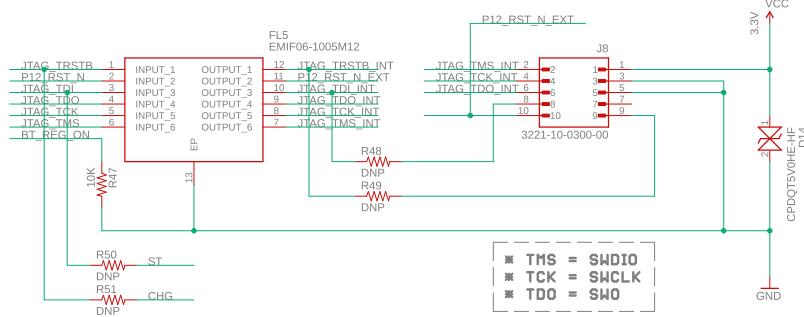
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JTAG

* JTAG_MOD is used for BT_REG_ON - the pin is sampled on the rising edge of reset and is pulled low by 10K



* The JTAG Interface is designed to connect to the SEGGER 9-Pin Cortex-M Adapter.

* Note that JTAG is disabled on the RT1060 by default. Only SWD works.
Given this those I/O pins are routed to the external header.
A fuse must be blown to switch to JTAG which disables SWD.

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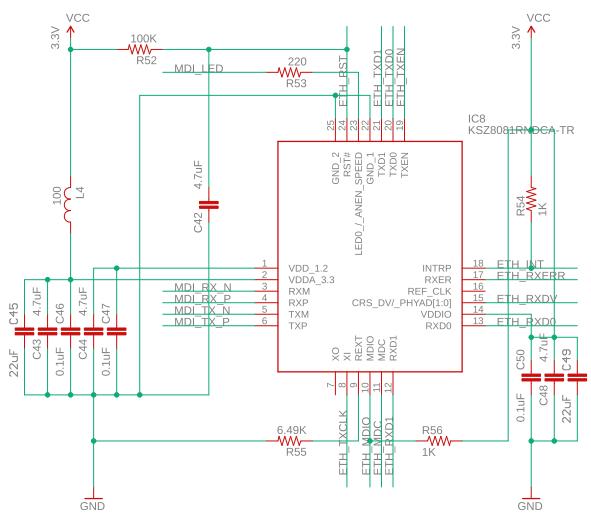
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Ethernet

* 8KV ESD Protection on MDI pins and the LED pin



Mechanical



H1 MOUNT-PAD-ROUND2.8

H2 MOUNT-PAD-ROUND2.8



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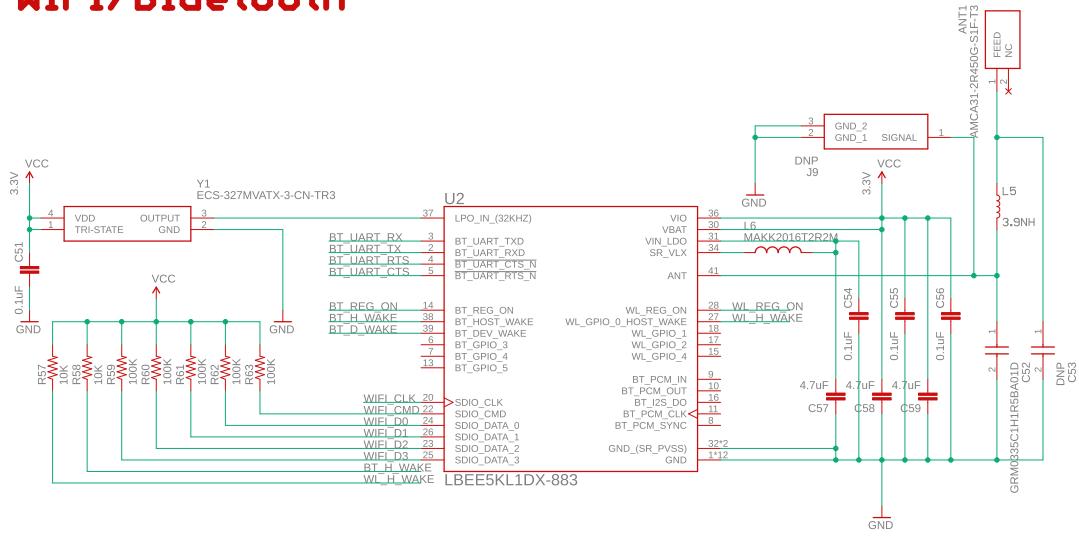
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WiFi/Bluetooth



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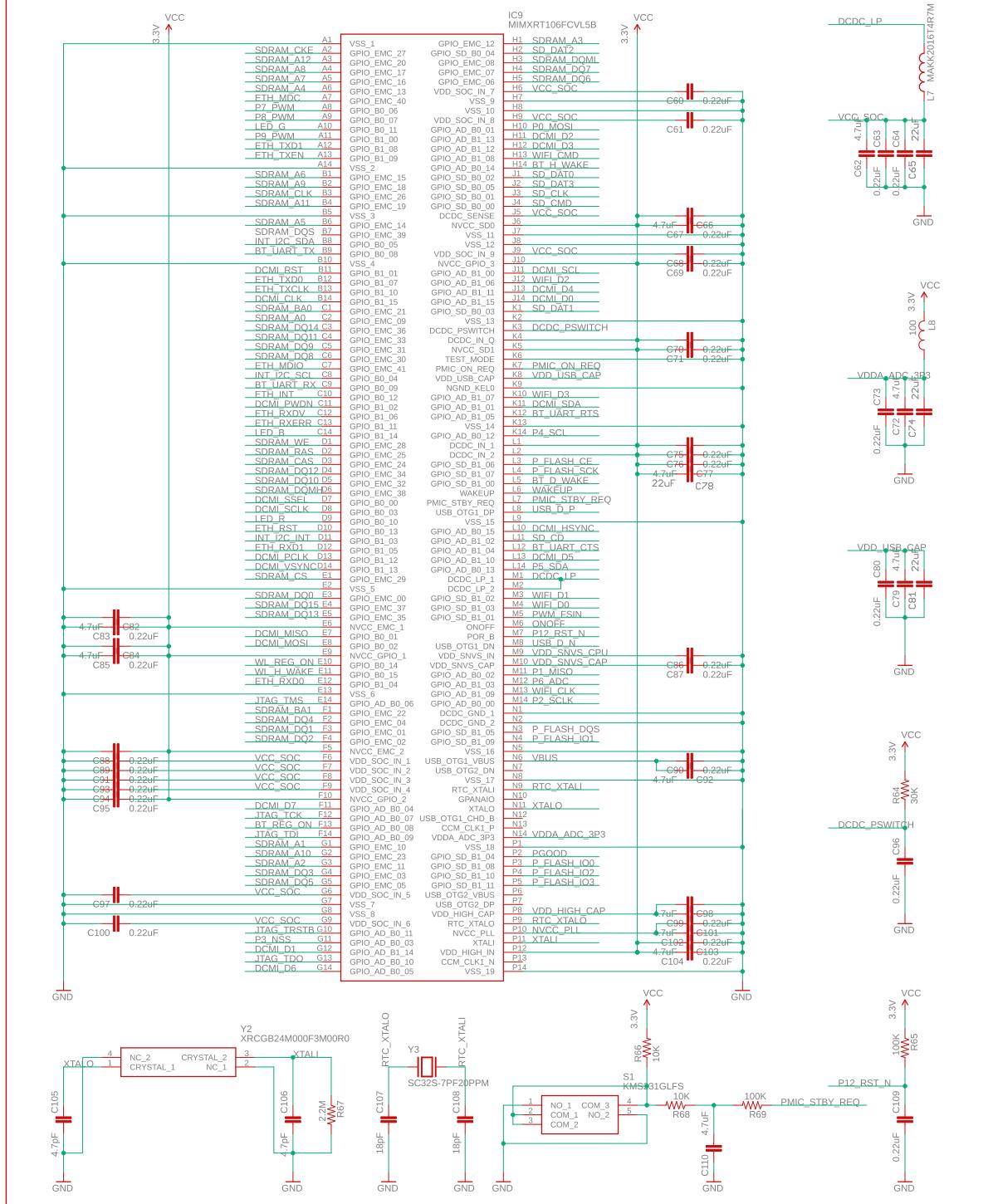
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MIMXRT1062



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