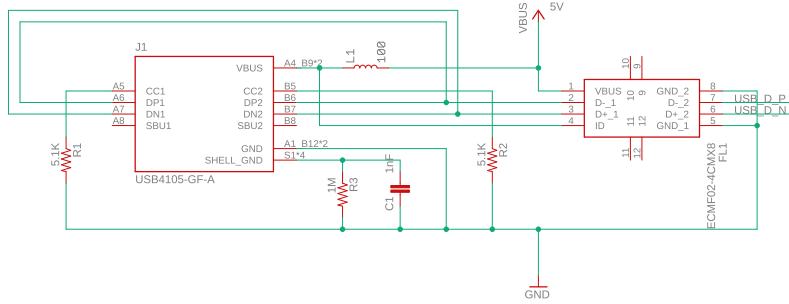


USB Connector



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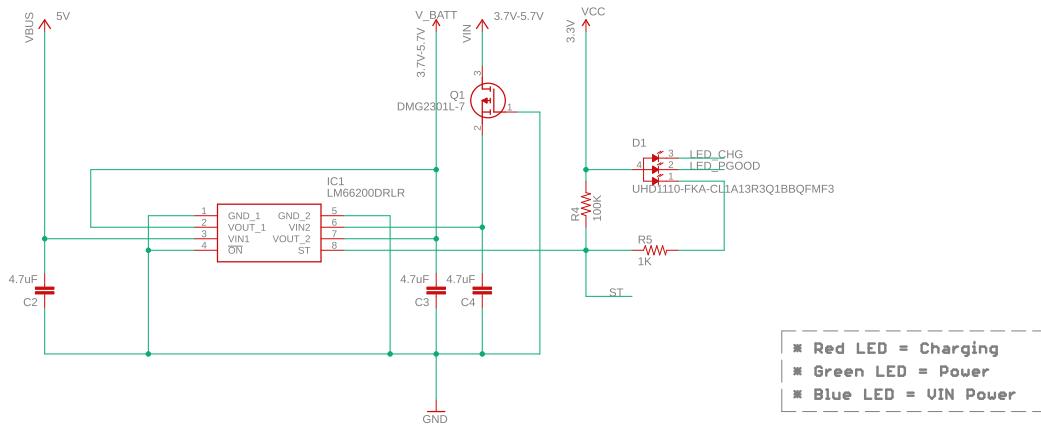
Document Number:

REV:

Date: 6/2/2023 12:05 PM

Sheet: 1/15

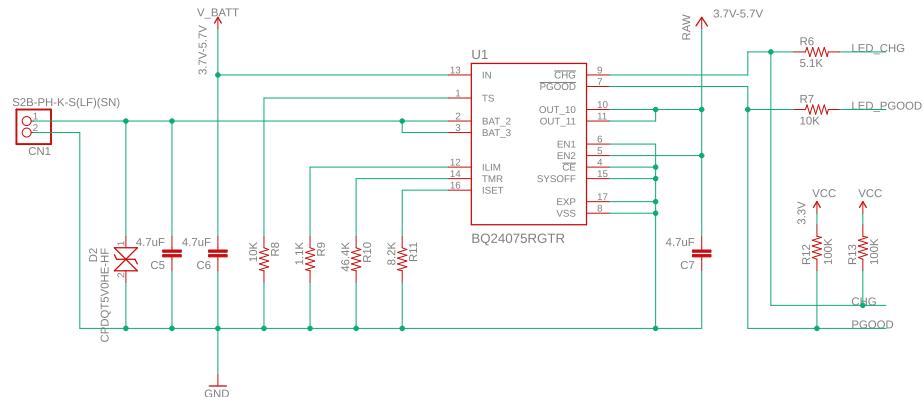
Ideal Diode



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TITLE: rt1060r2	REV:
Document Number:	
Date: 6/2/2023 12:05 PM	Sheet: 2/15

Battery Charging



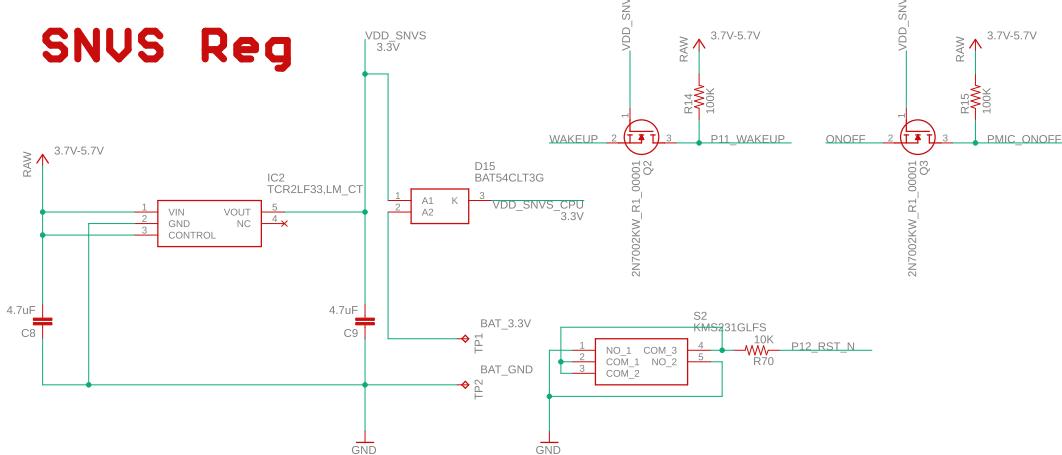
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TITLE: rt1060r2	
Document Number:	REV:
Date: 6/2/2023 12:05 PM	Sheet: 3/15

SNVS Reg



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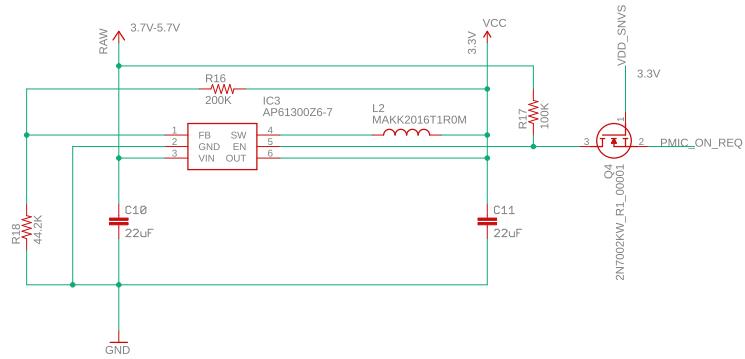
Document Number:

REV:

Date: 6/2/2023 12:05 PM

Sheet: 4/15

Main Regulator



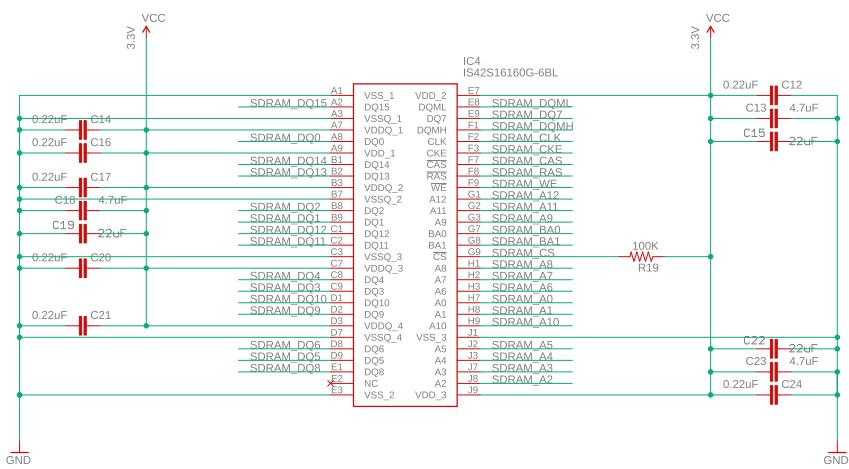
* PMIC_REG_ON is level shifted to ensure regulator goes into PFM mode

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TITLE: rt1060r2	REV:
Document Number:	
Date: 6/2/2023 12:05 PM	Sheet: 5/15

16-BIT SDRAM

* CKE is pulled down by 100K on the RT1062



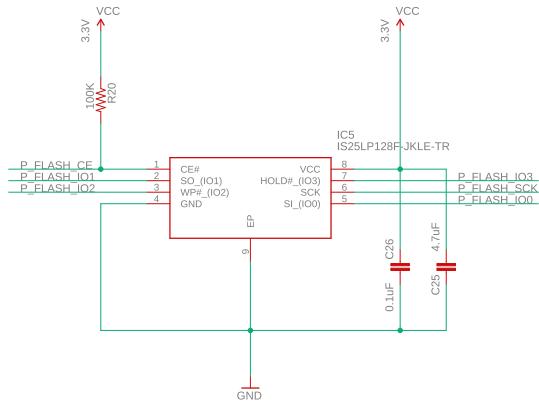
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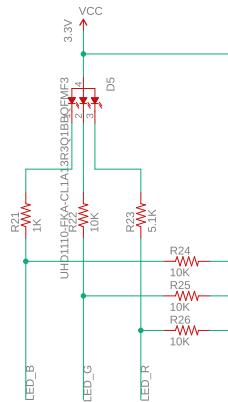
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Document Number:	REV:
Date: 6/2/2023 12:05 PM	Sheet: 6/15

Program QSPI Flash



RGB LED



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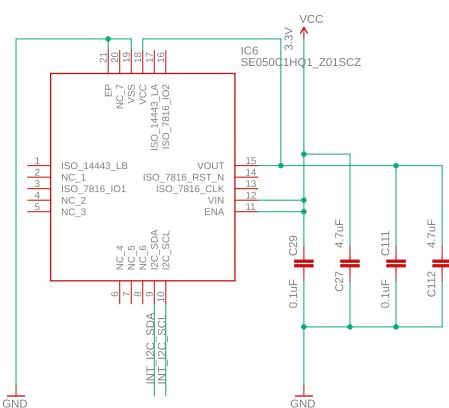
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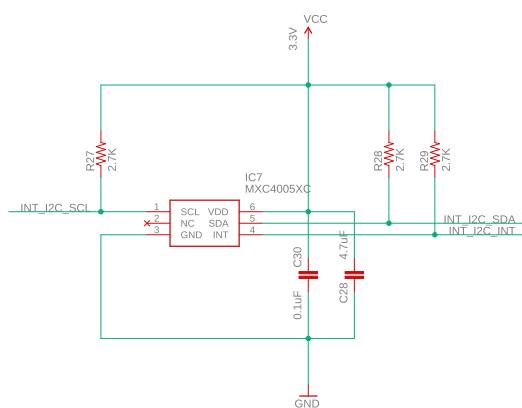
Date: 6/2/2023 12:05 PM

Sheet: 7/15

Security



Accelerometer



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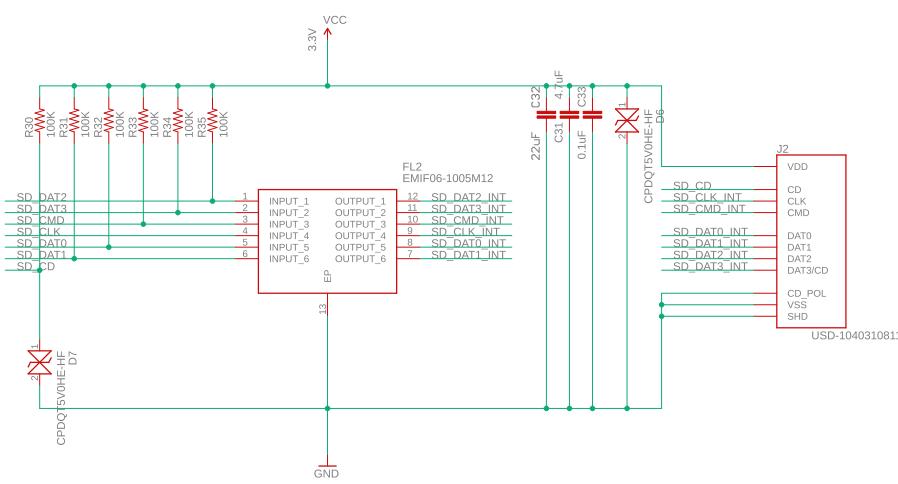
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Document Number:

REV:

Date: 6/2/2023 12:05 PM Sheet: 8/15

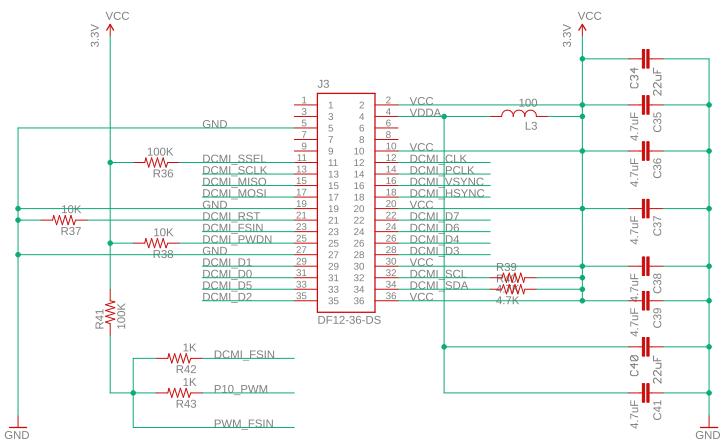
SD CARD



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TITLE: rt1060r2	REV:
Document Number:	
Date: 6/2/2023 12:05 PM	Sheet: 9/15

IMAGE SENSOR CONNECTOR



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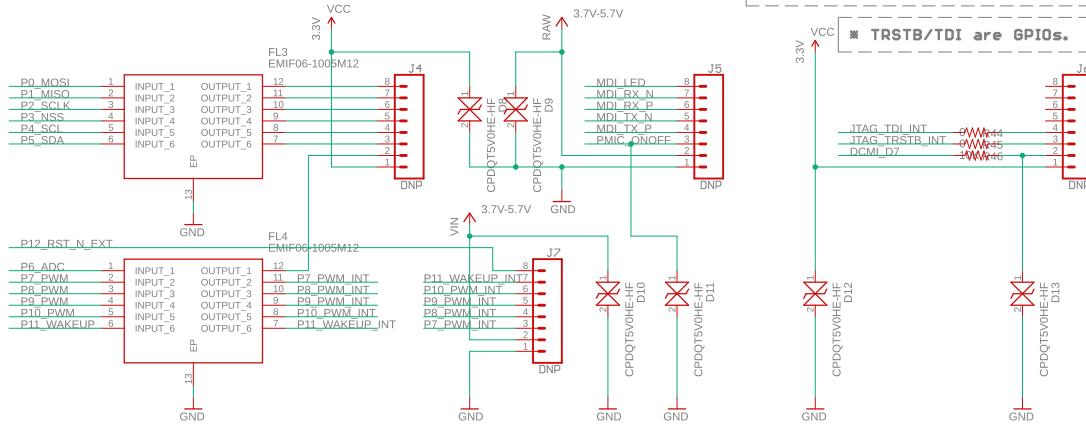
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Document Number: REV:

Date: 6/2/2023 12:05 PM Sheet: 10/15

I/O HEADERS



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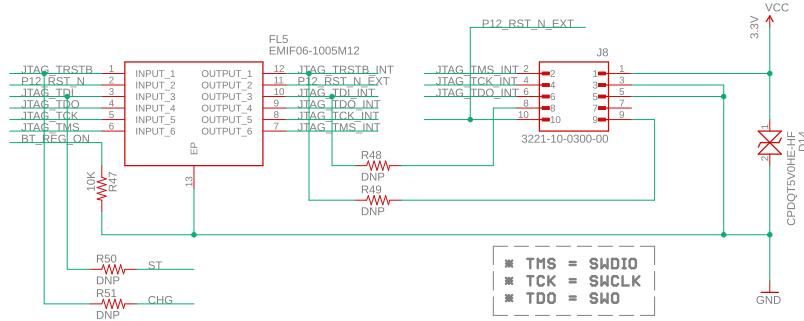
Document Number:

REV:

Date: 6/2/2023 12:05 PM Sheet: 11/15

JTAG

* JTAG_MOD is used for BT_REG_ON - the pin is sampled on the rising edge of reset and is pulled low by 10K



* TMS = SWDIO
* TCK = SWCLK
* TDO = SWO

* The JTAG Interface is designed to connect to the SEGGER 9-Pin Cortex-M Adapter.

* Note that JTAG is disabled on the RT1060 by default. Only SWD works.
Given this those I/O pins are routed to the external header.
A fuse must be blown to switch to JTAG which disables SWD.

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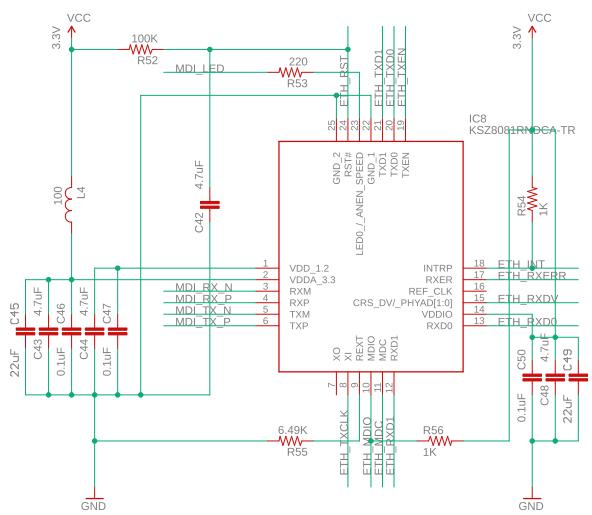
TITLE: rt1060r2

Document Number: REV:

Date: 6/2/2023 12:05 PM Sheet: 12/15

Ethernet

* 8KV ESD Protection on MDI pins and the LED pin



Mechanical



H1 MOUNT-PAD-ROUND2.8

H2 MOUNT-PAD-ROUND2.8



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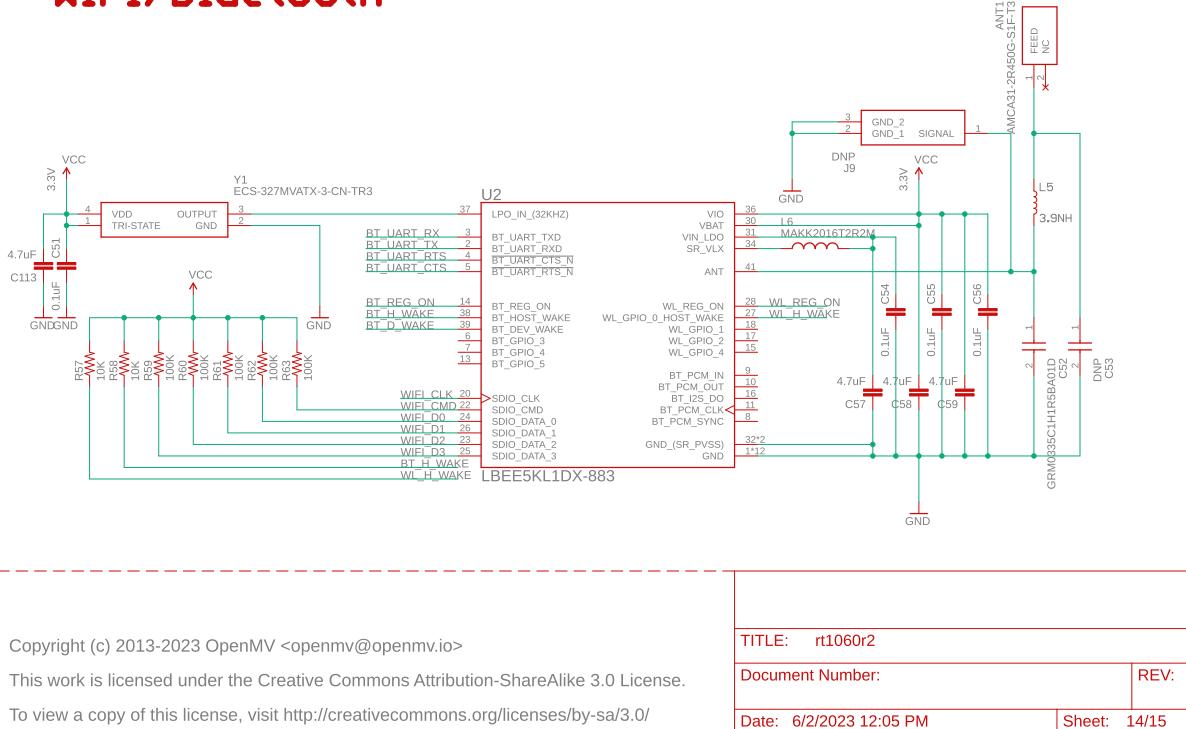
Document Number:

REV:

Date: 6/2/2023 12:05 PM

Sheet: 13/15

WiFi/Bluetooth



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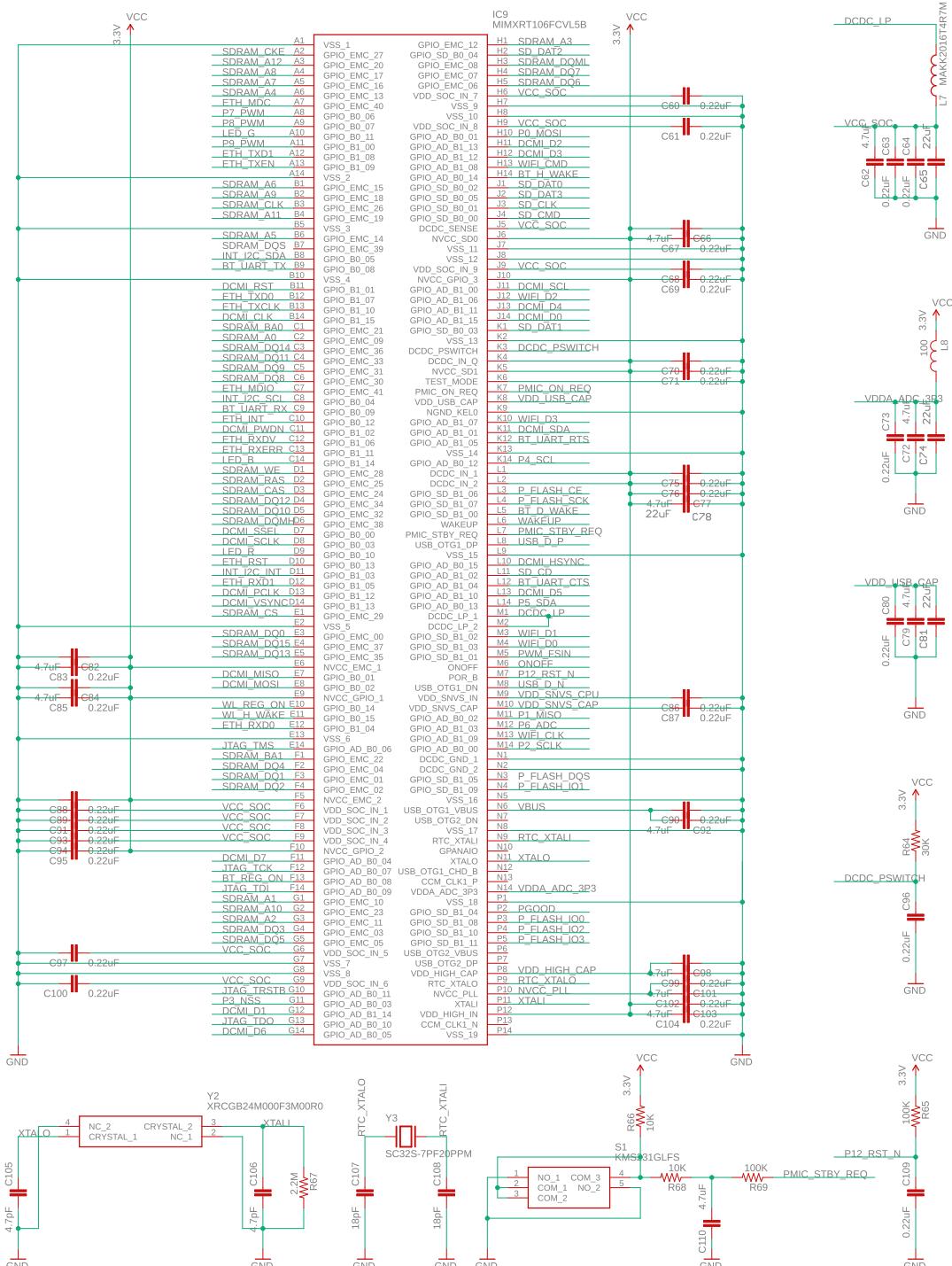
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Date: 6/2/2023 12:05 PM

MIMXRT1062



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Sheet: 15/15