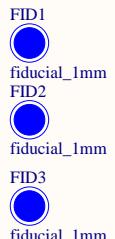
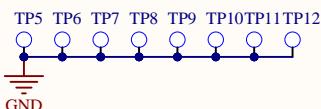
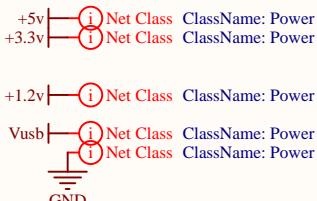
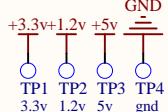


Ground clip pads



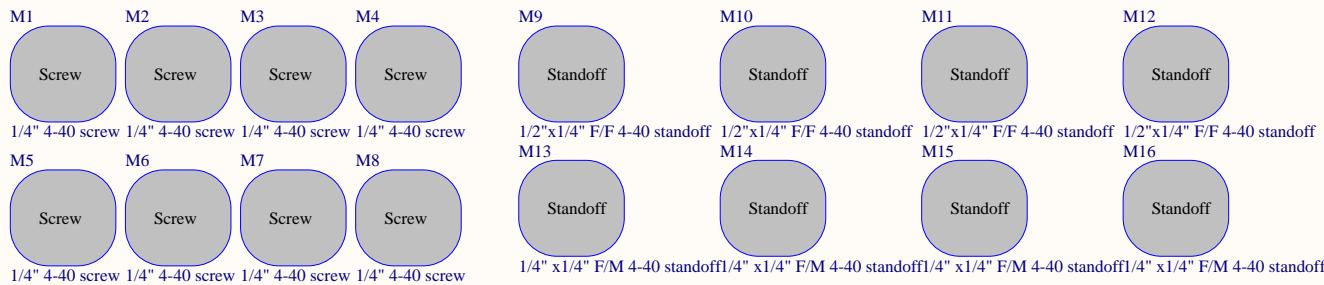
Power supply test points, top side



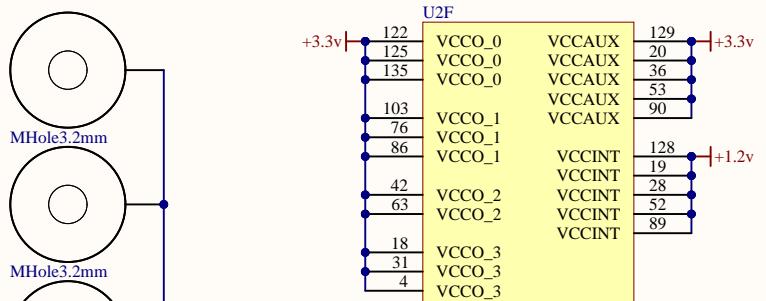
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Title		
Size	Number	Revision
A		
Date:	5/24/2021	Sheet of
File:	C:\Users\...\power.SchDoc	Drawn By:

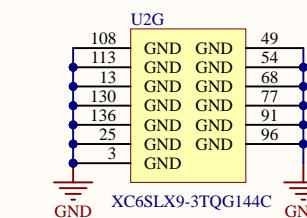
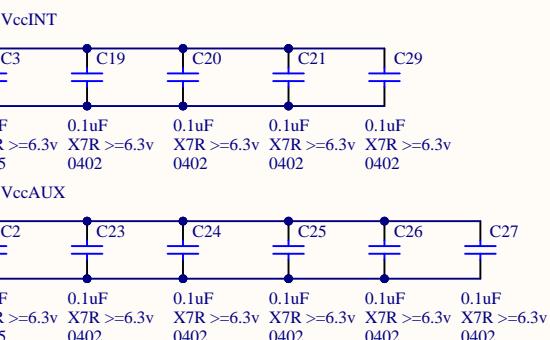
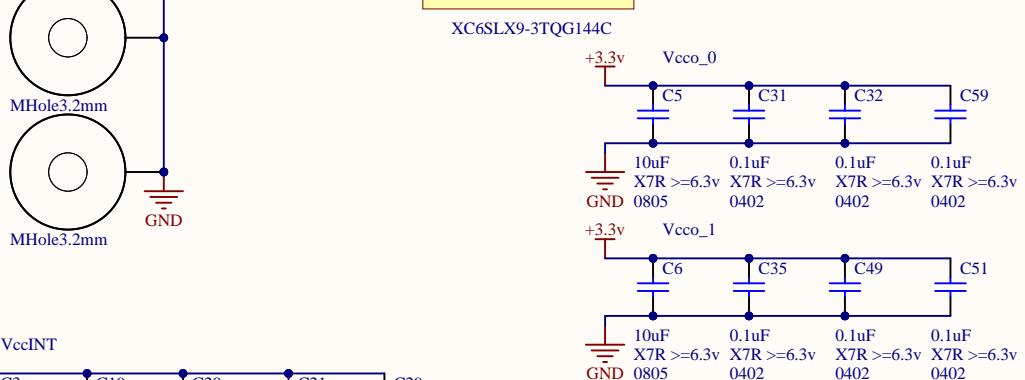
A



B

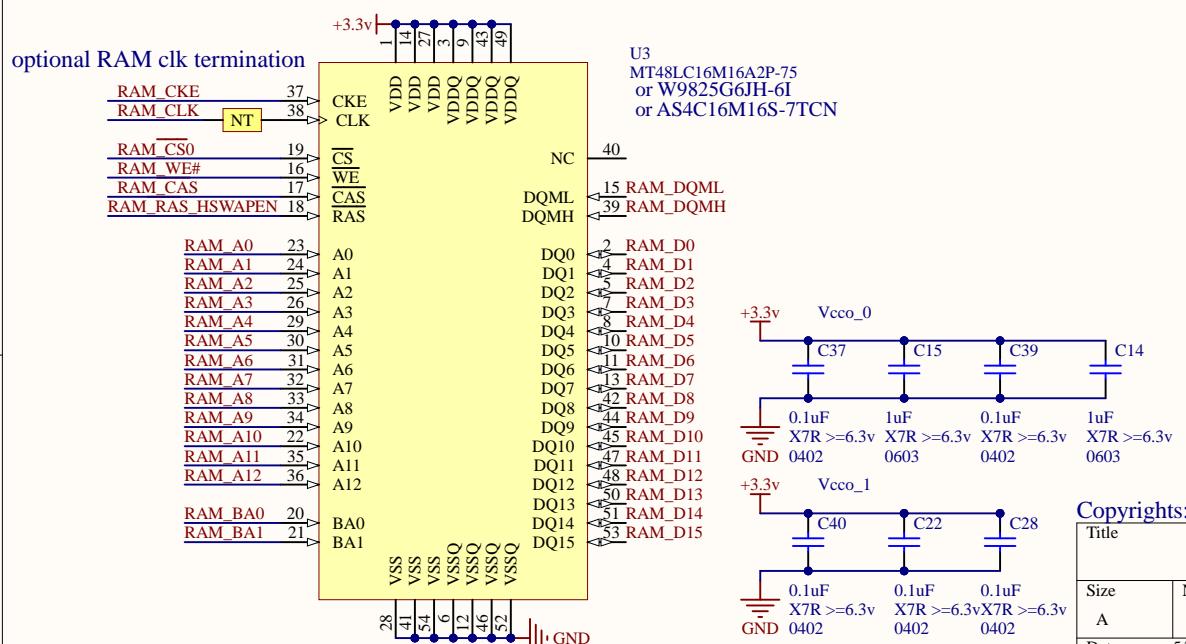
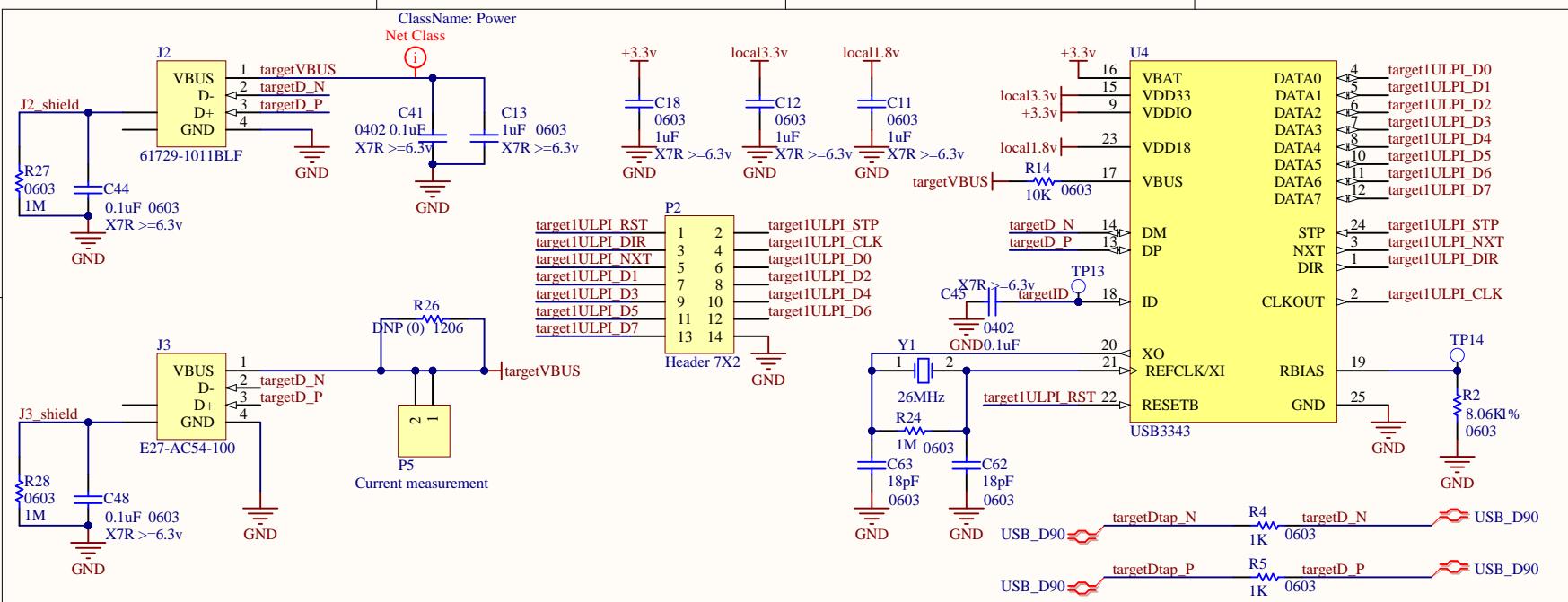


C



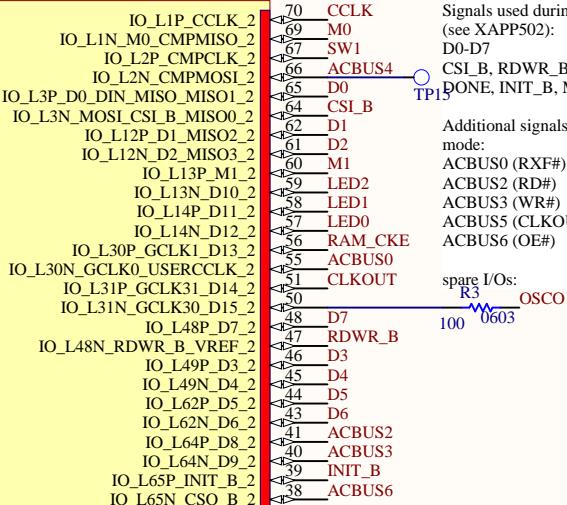
Copyrights: CC-BY-SA 3.0 Patents: Apache 2.0

Title		
Size	Number	Revision
A		
Date:	5/24/2021	Sheet 2 of
File:	C:\Users\...\FPGA power v3.SchDoc	Drawn By:



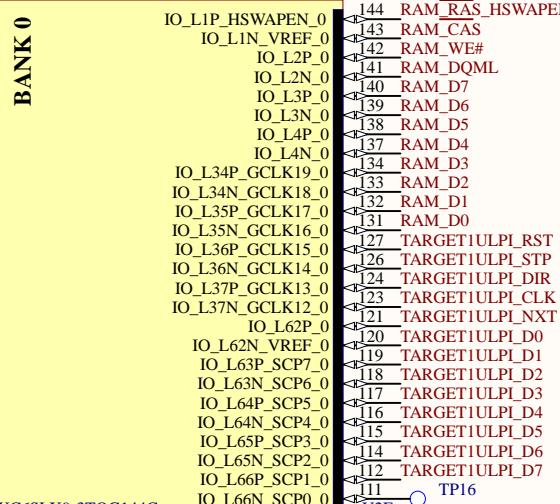
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U2C

BANK 2

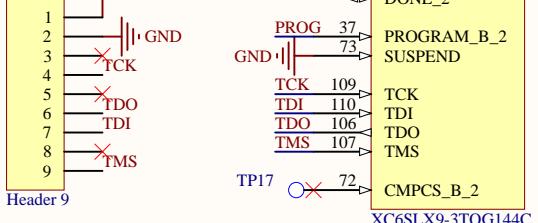
XC6SLX9-3TQG144C

U2A



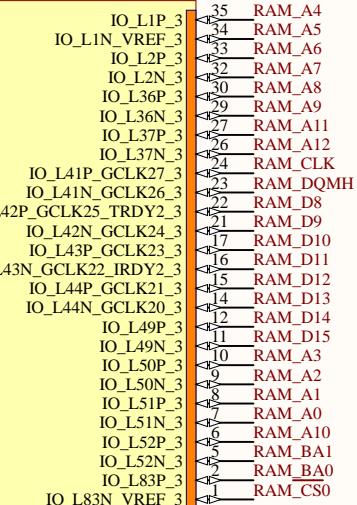
XC6SLX9-3TQG144C

P1

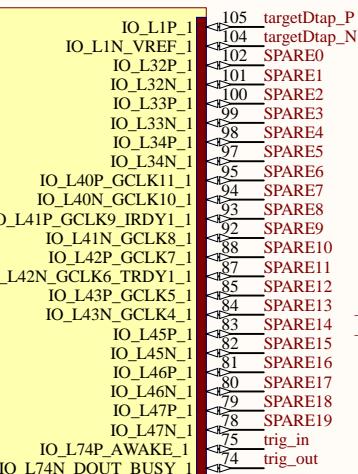


XC6SLX9-3TQG144C

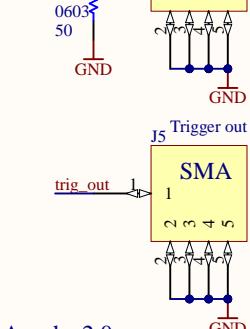
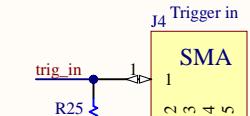
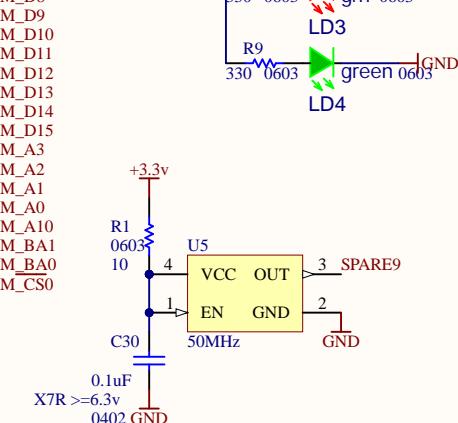
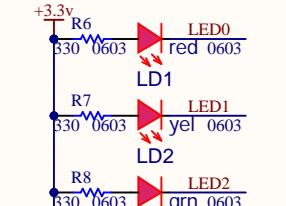
U2D

BANK 3

XC6SLX9-3TQG144C

BANK 1

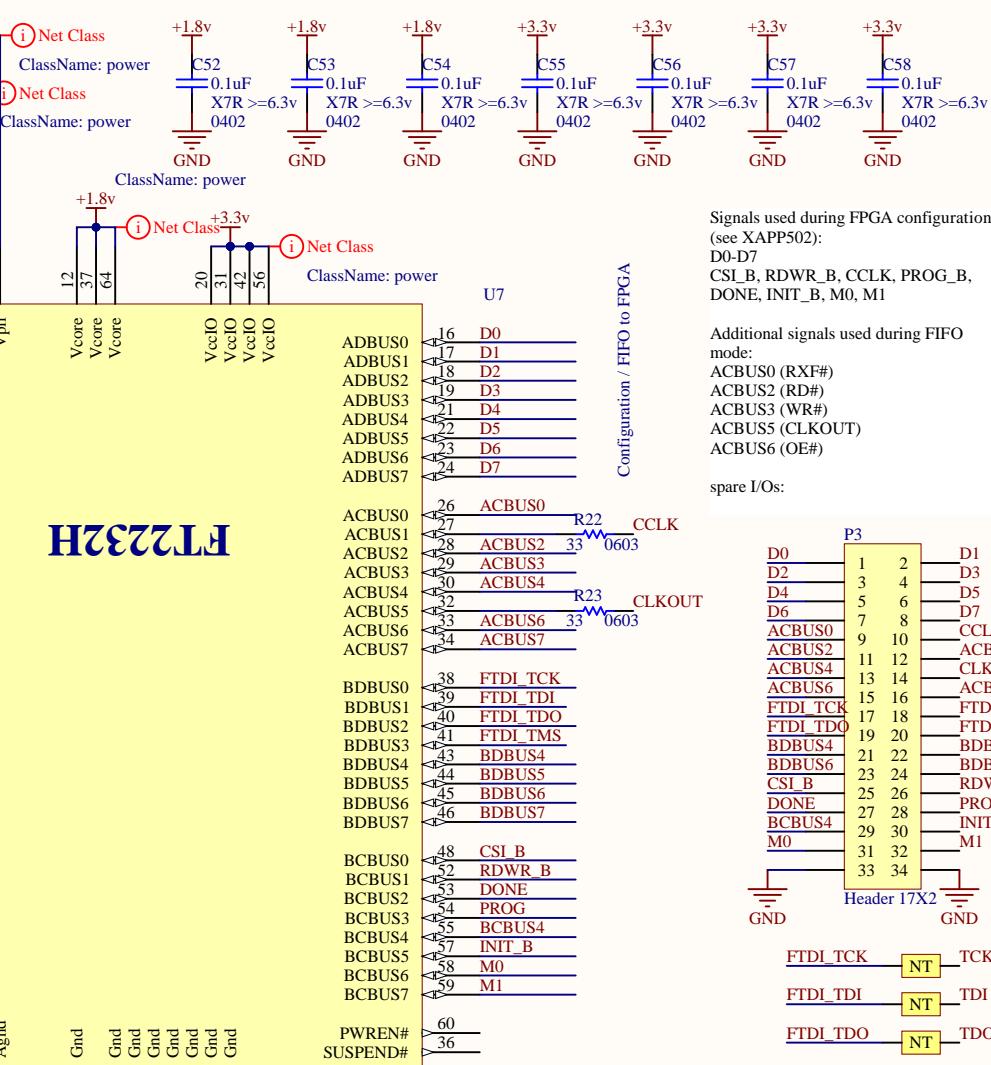
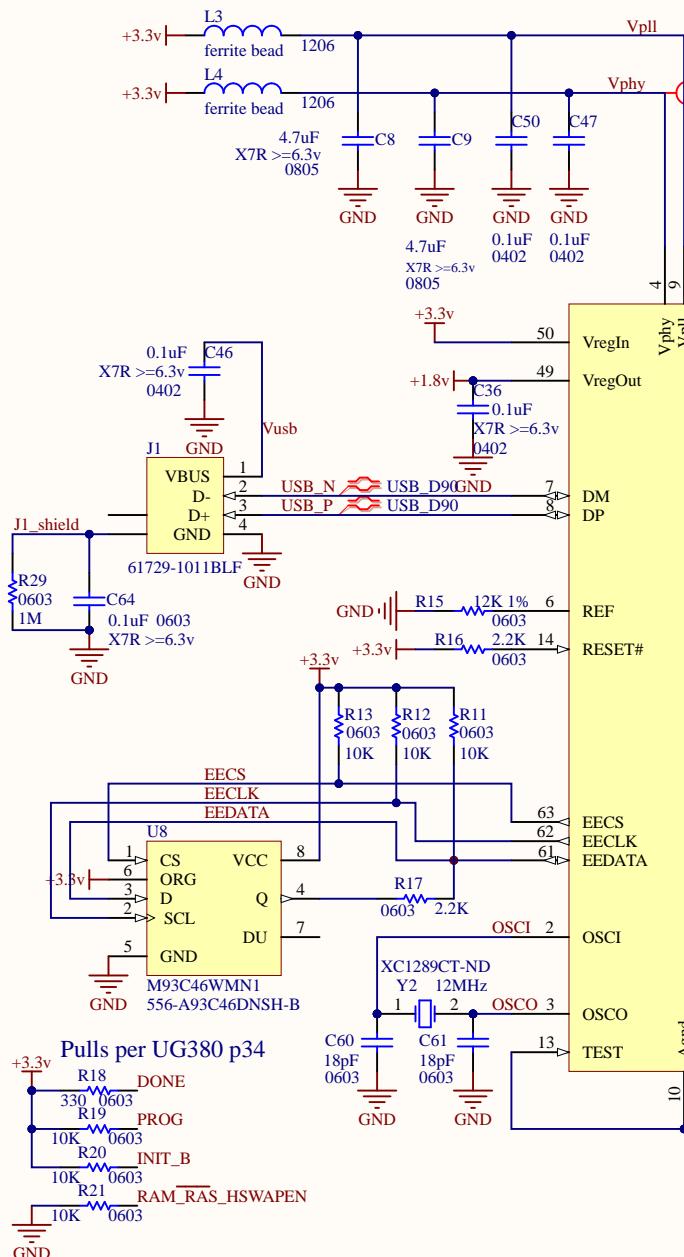
XC6SLX9-3TQG144C



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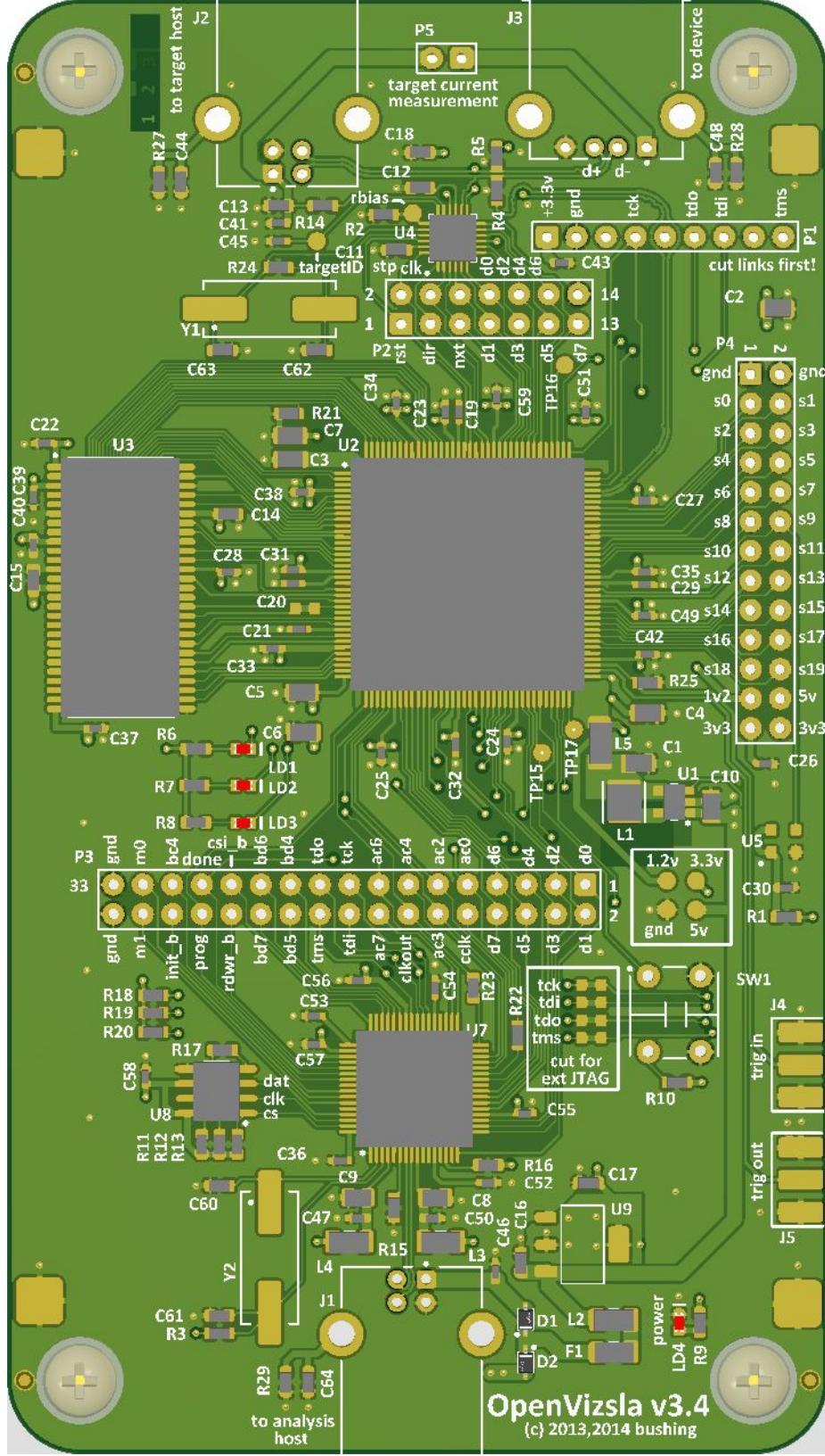
Title		Revision
Size	Number	
A		
Date:	5/24/2021	Sheet of
File:	C:\Users\.\fpga.SchDoc	Drawn By:

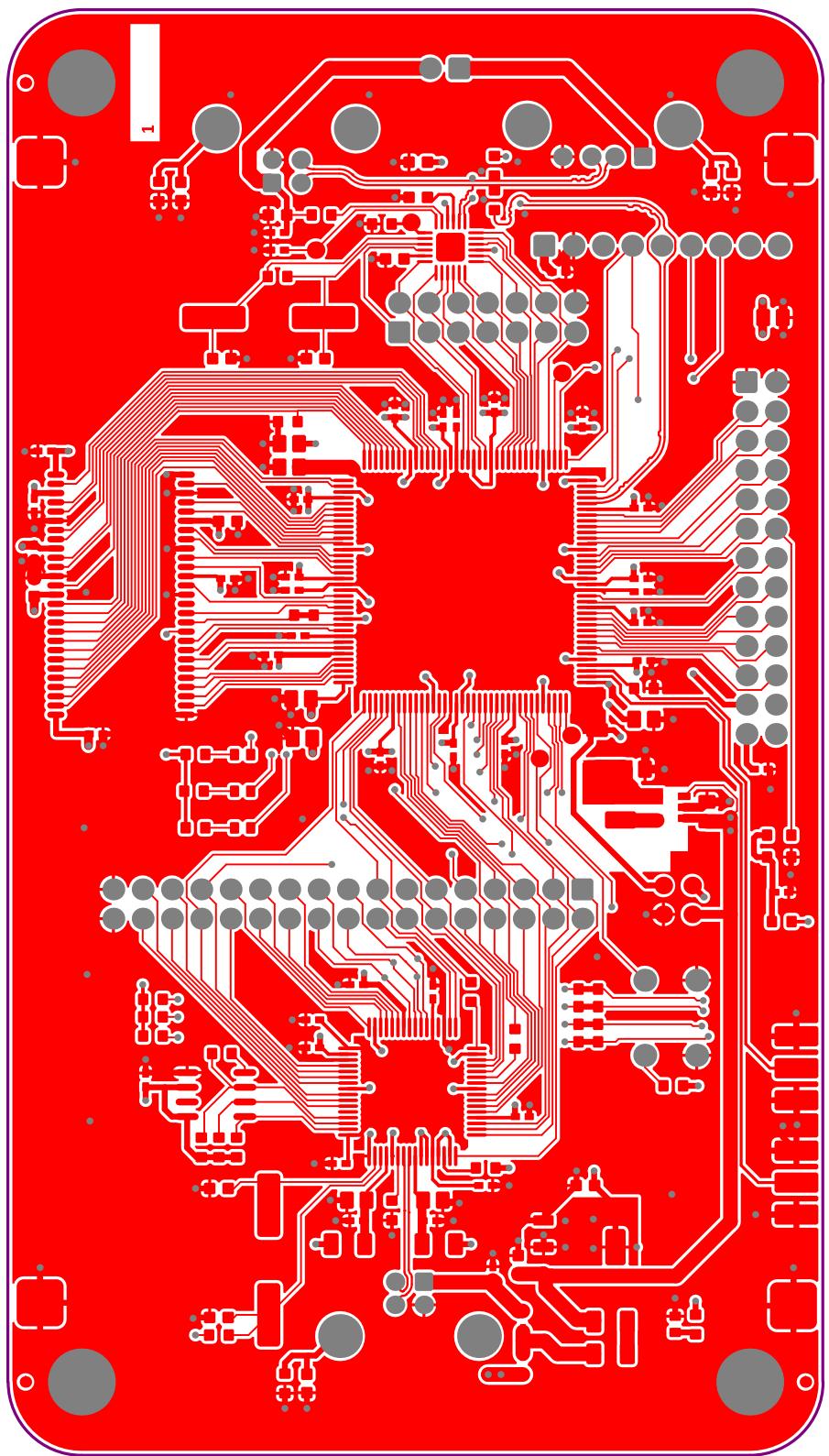
Inspired by http://www.ftdichip.com/Documents/DataSheets/DS_FT2232H_V206.pdf page 47

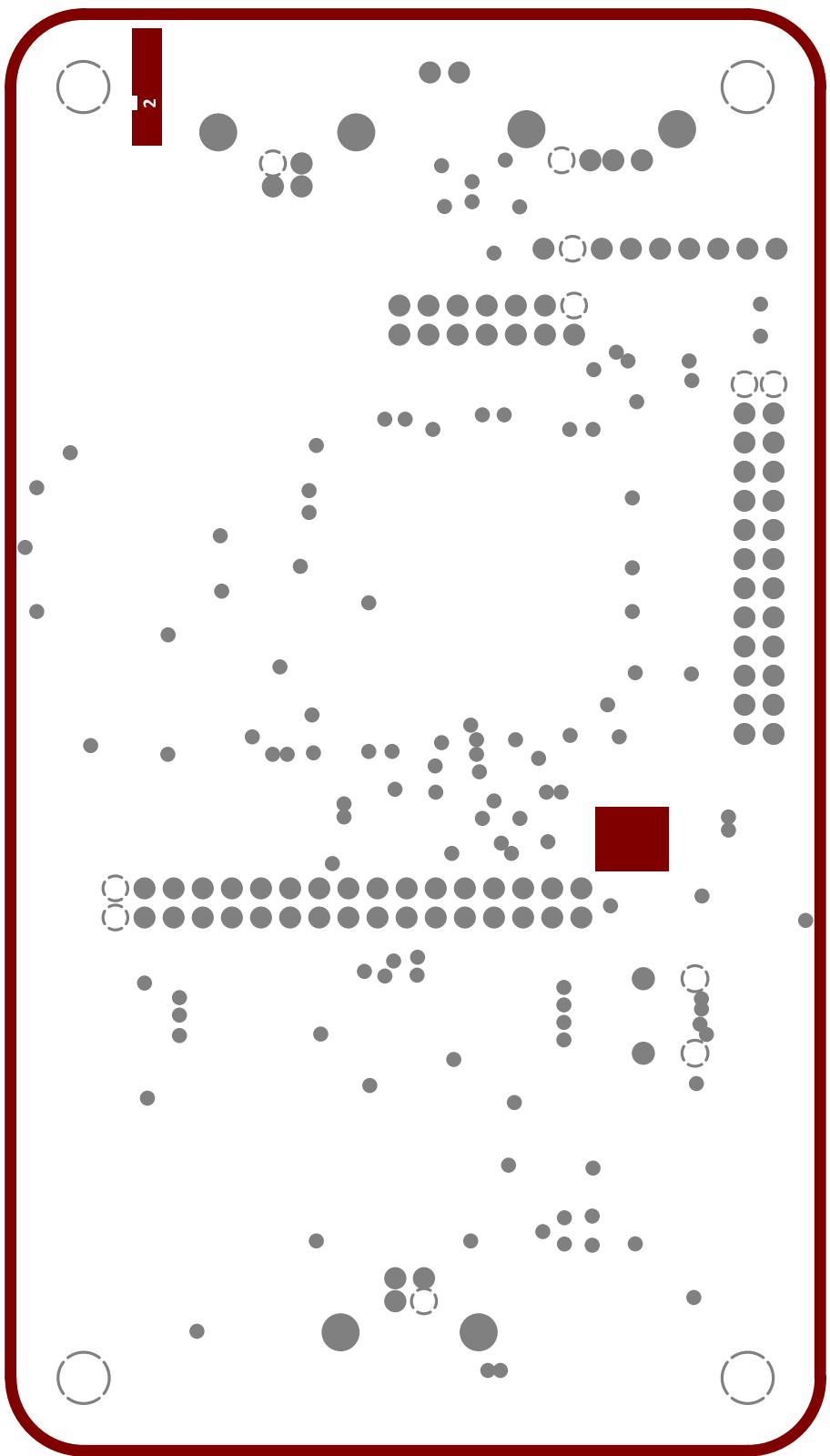


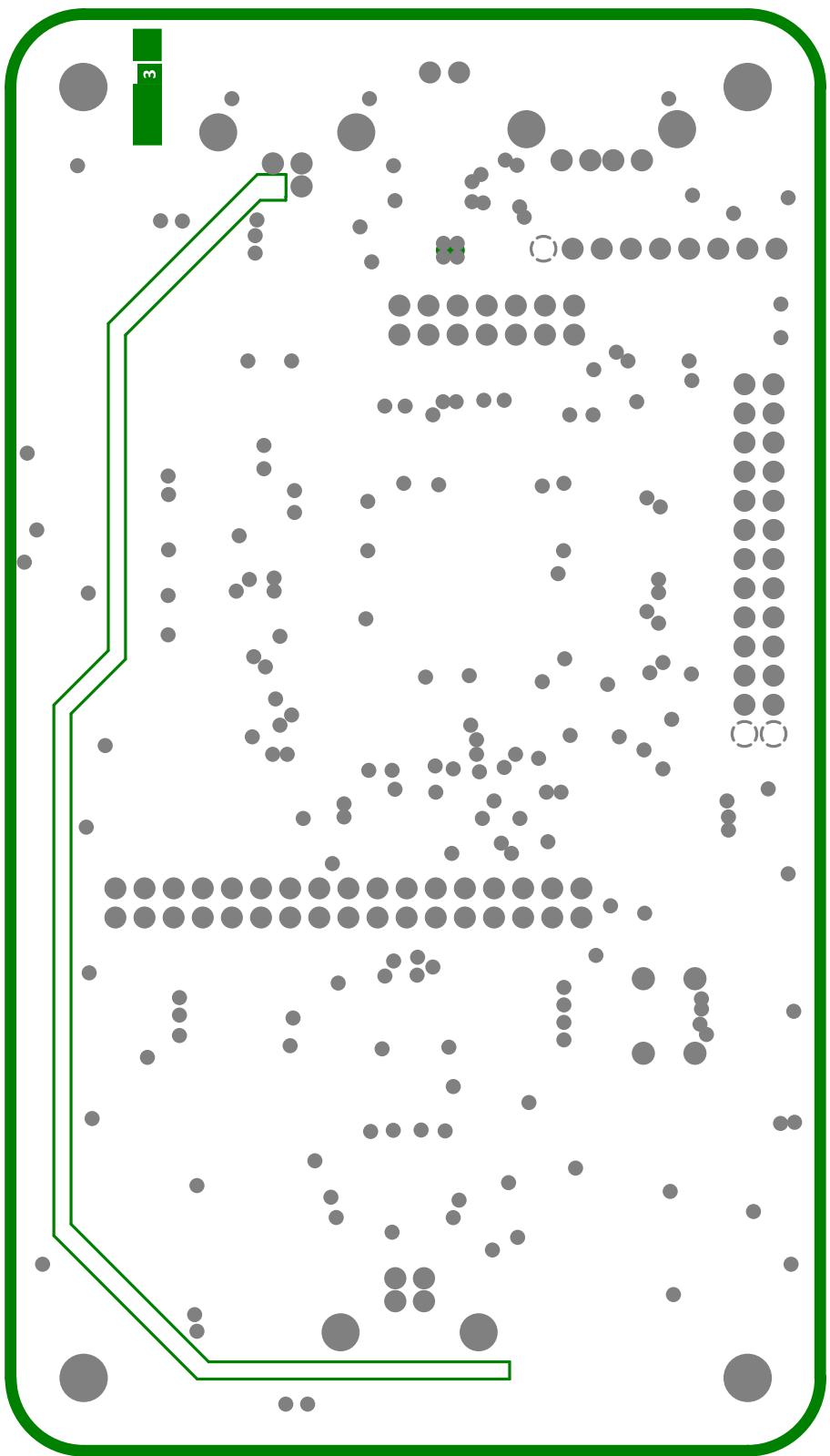
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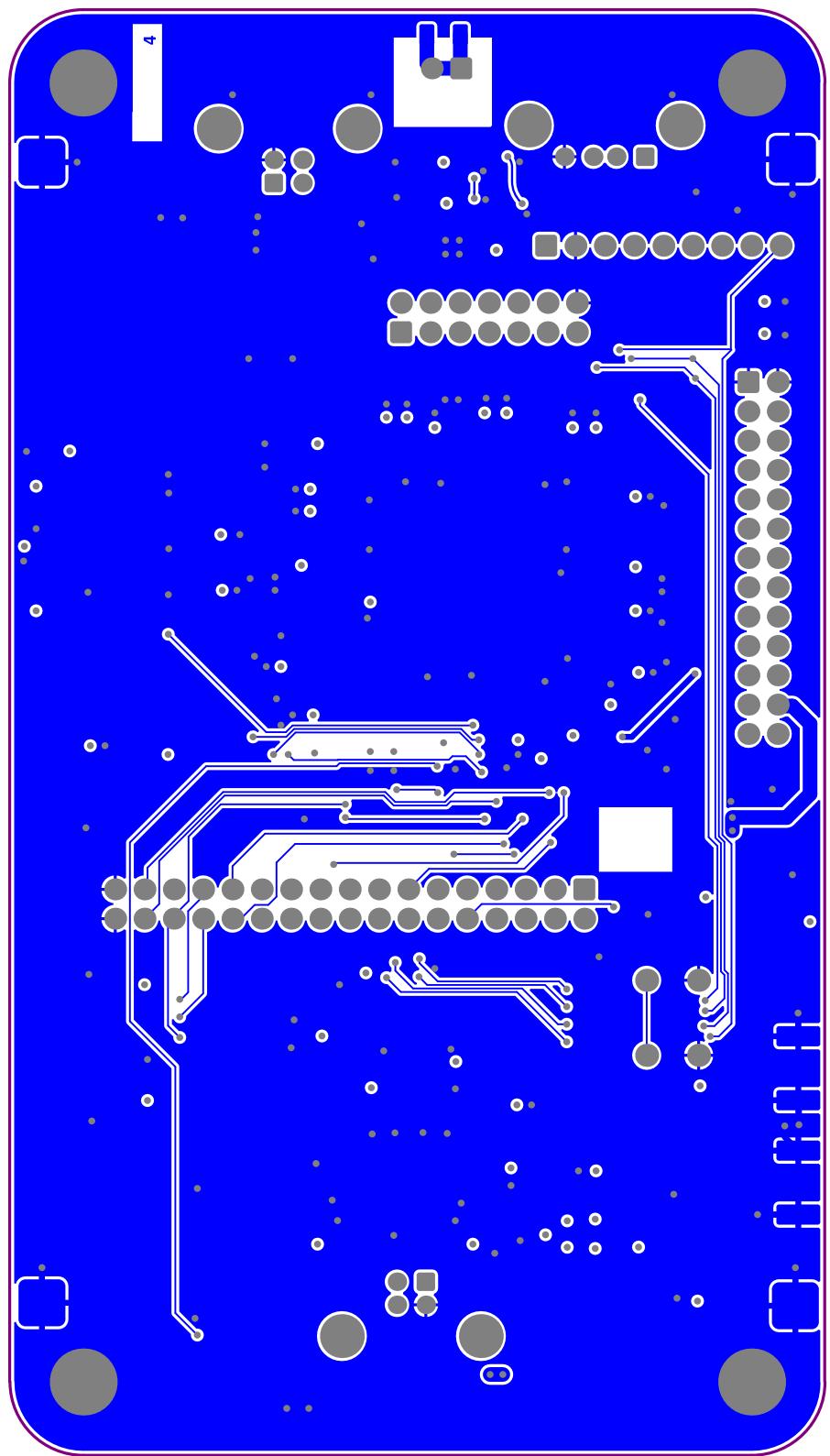
Title		
Size	Number	Revision
A		
Date:	5/24/2021	Sheet of
File:	C:\Users\...\usb.SchDoc	Drawn By:

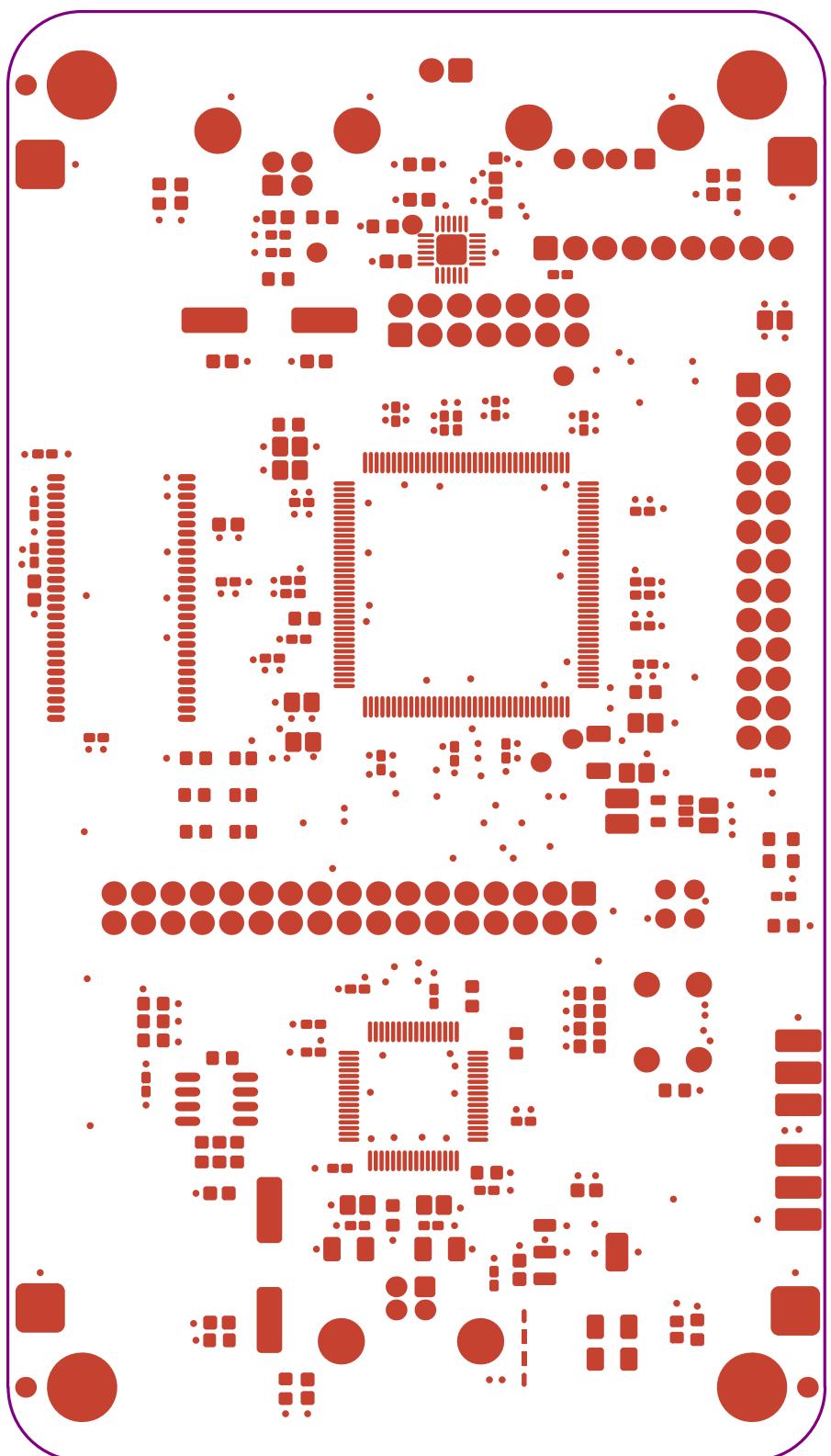


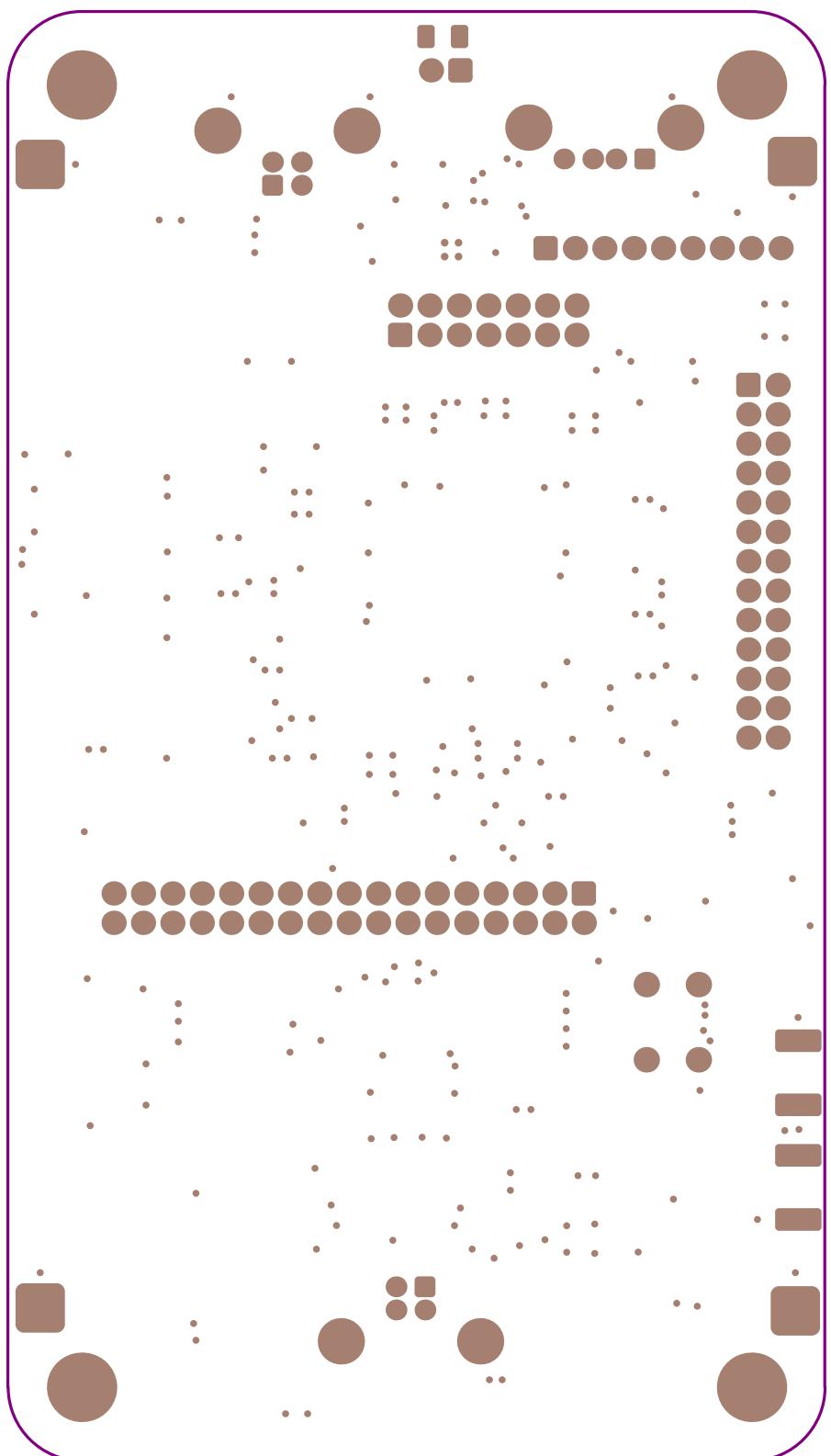


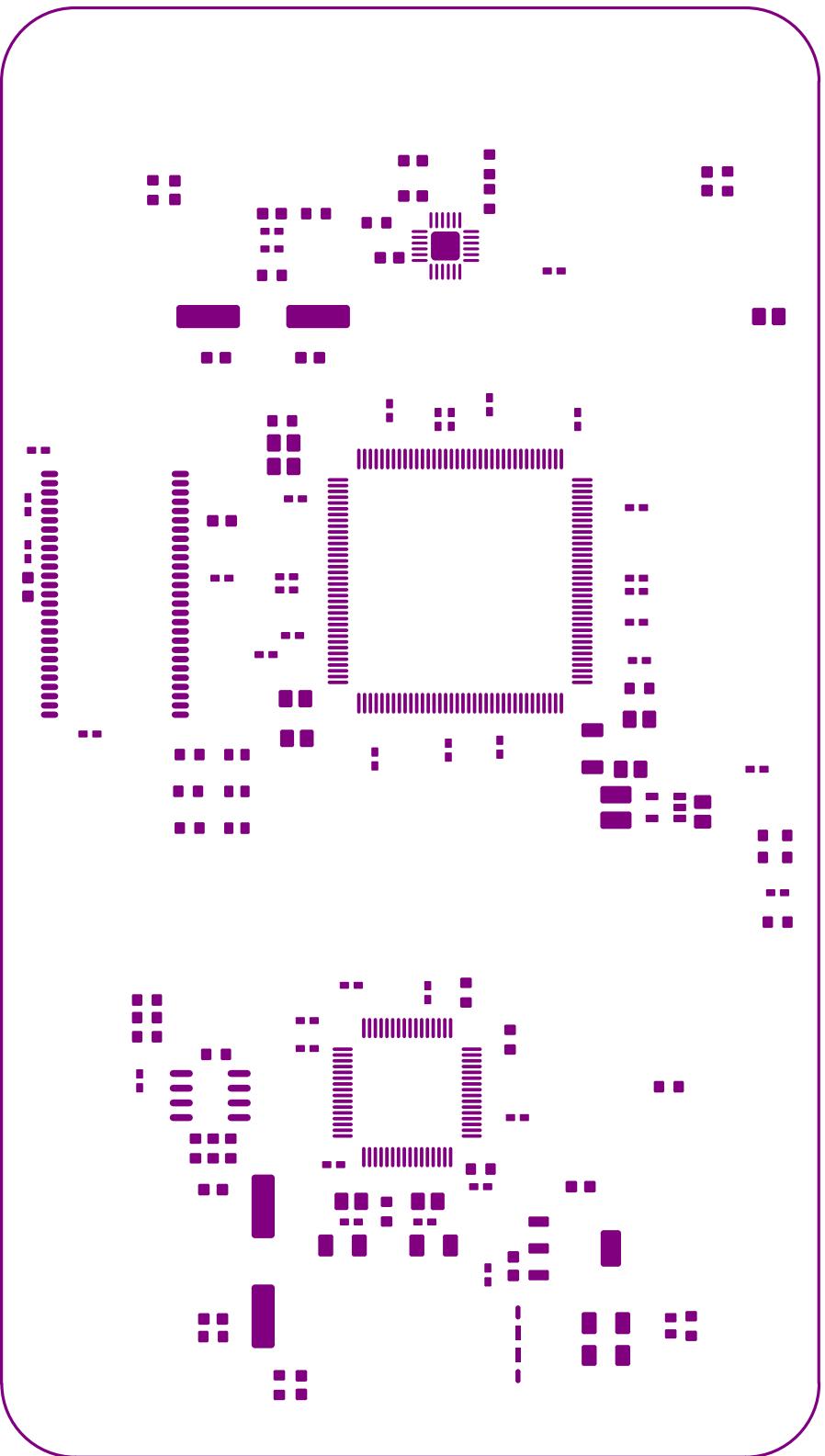


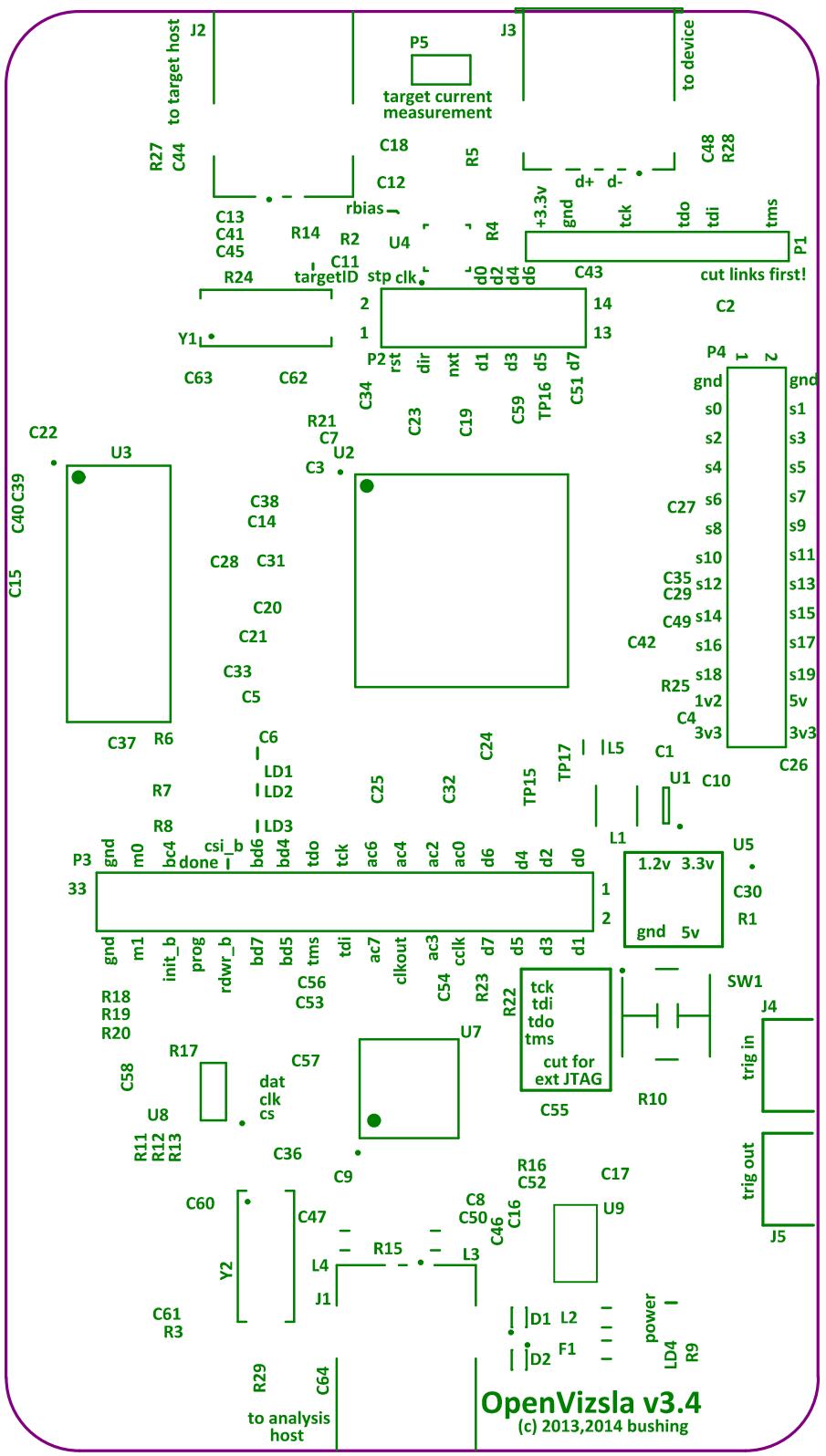












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