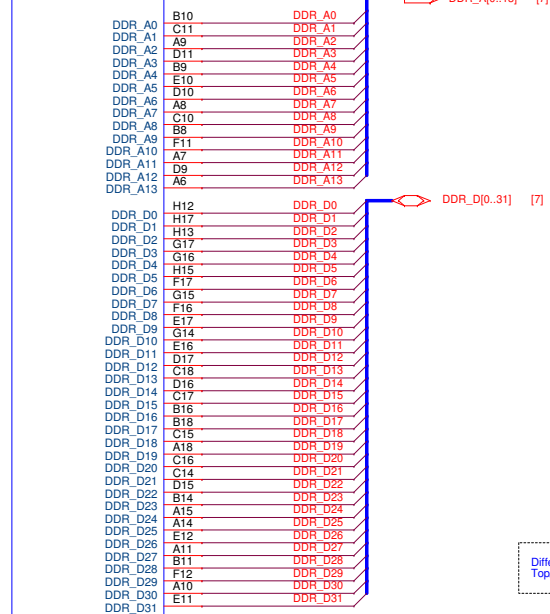


U8F
SAMASD3x



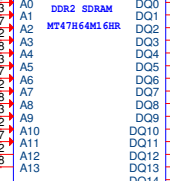
Place R191 right at vias going to pins of U9, end of the line.

Differential 100 Ohms Top/Bottom

Opt MT47H128M16RT

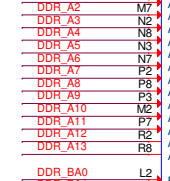
DDR2 SDRAM

U9
DDR2 SDRAM
MT47H64M16HR



Place R192 right at vias going to pins of U10, end of the line.

U10
DDR2 SDRAM
MT47H64M16HR



Opt MT47H128M16RT

Keep nets as short as possible, therefore, DDR2 devices have to be placed close as possible of SAMASD36
The layout EBI DDR2 should use controlled impedance traces of ZO= 50ohm characteristic impedance.

CLK/CLKN should match length within .01" (.25mm)
Address, control and data traces may not exceed 1.3 inches (33.0 mm).
Address, control and data traces must be length-matched to within 0.1 inch (2.54mm).
Data traces must match the data group trace lengths to within 0.05 inches (1.27mm).

Title		
OpenXC Modem, Main - DDR2		
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