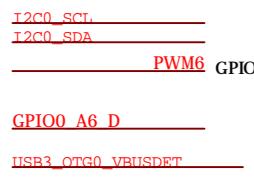


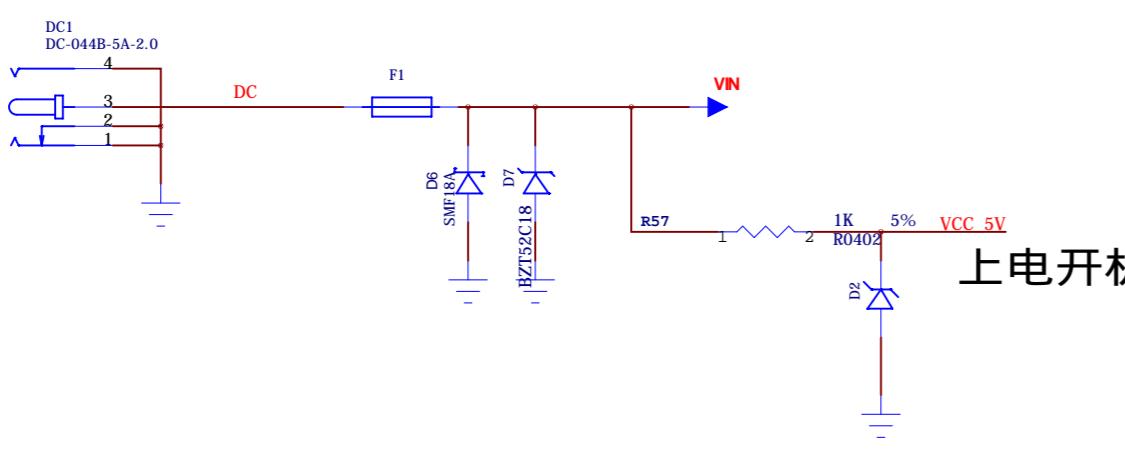
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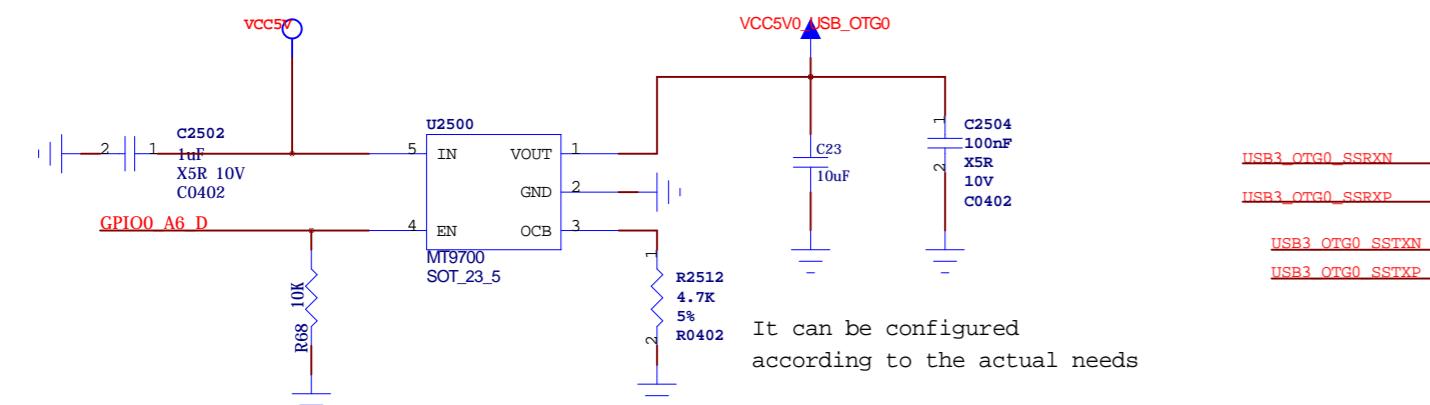


5-18V 输入

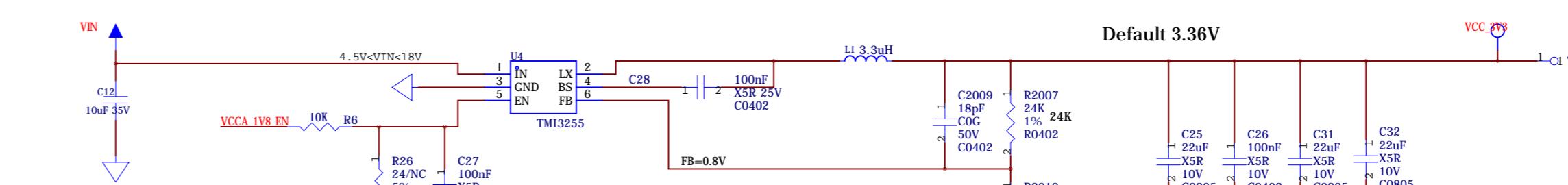
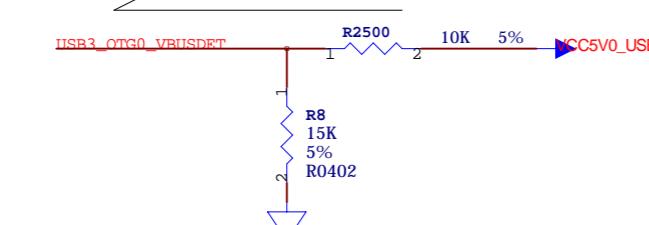


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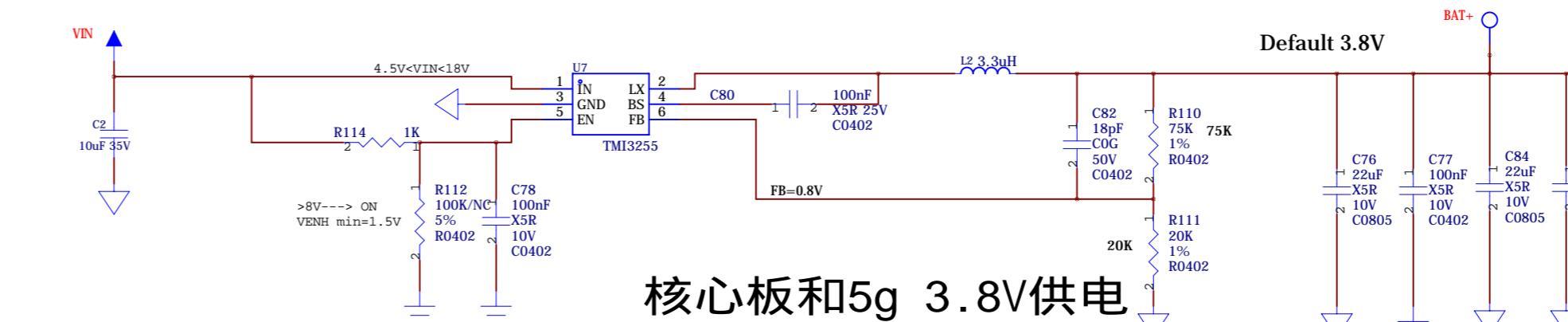
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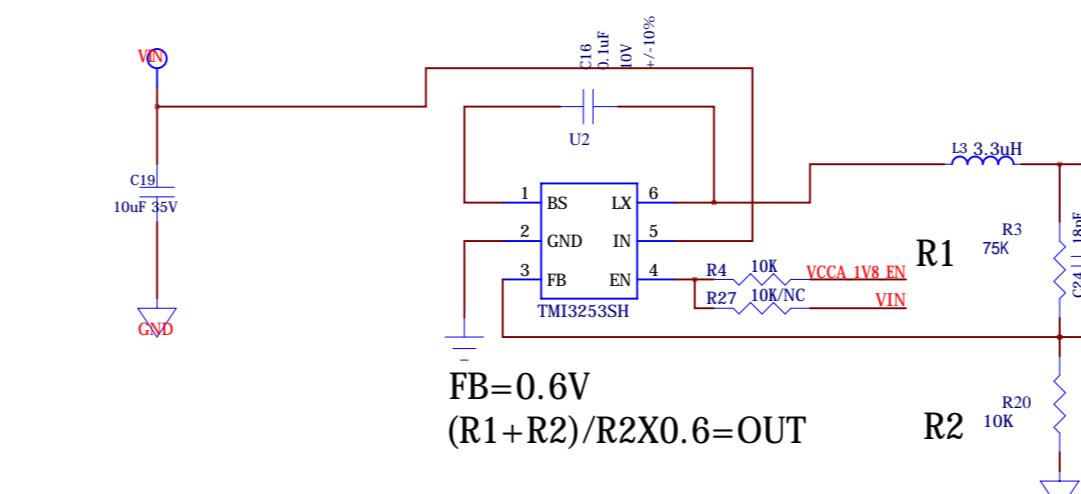
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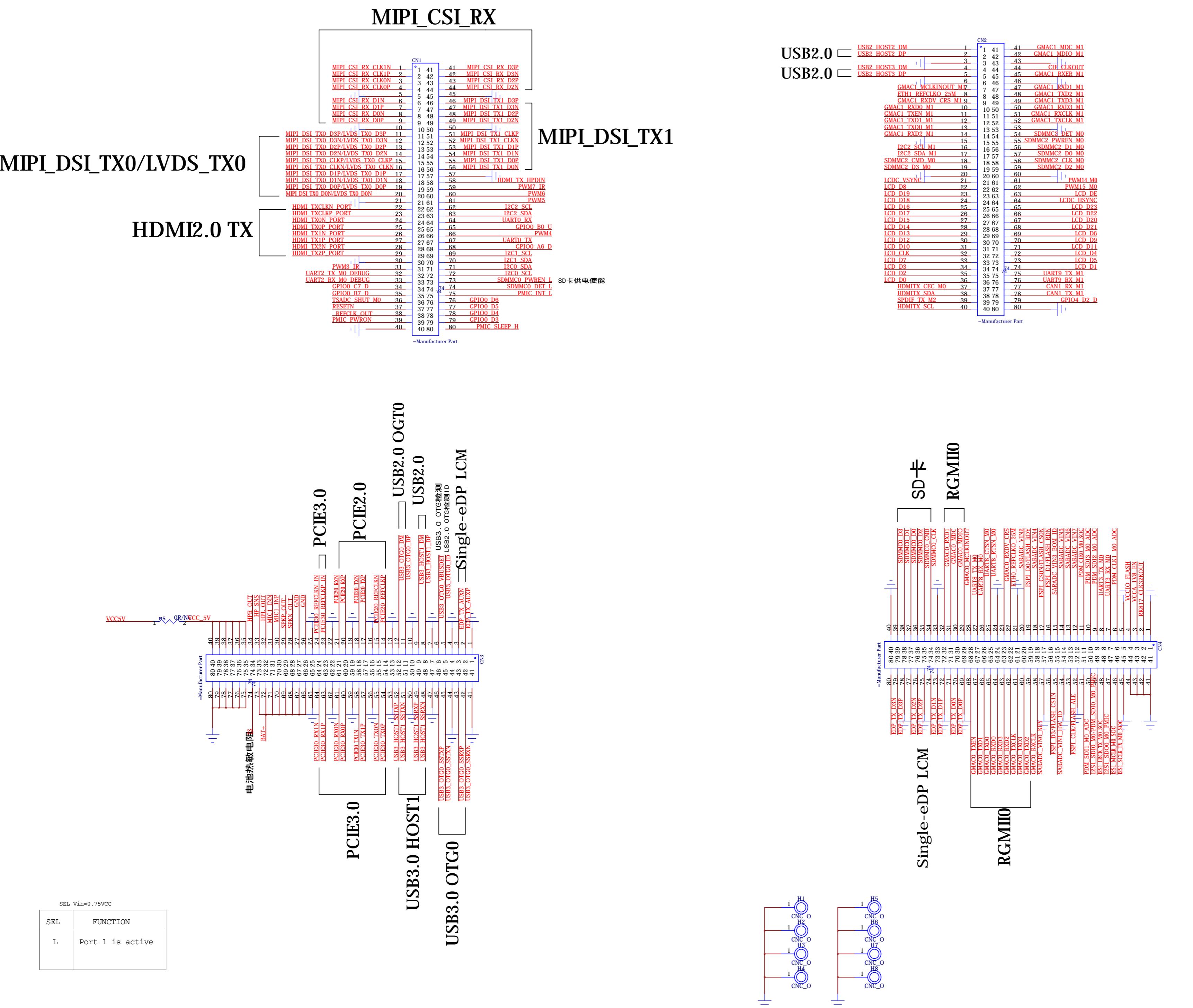
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核心板和5g 3.8V供



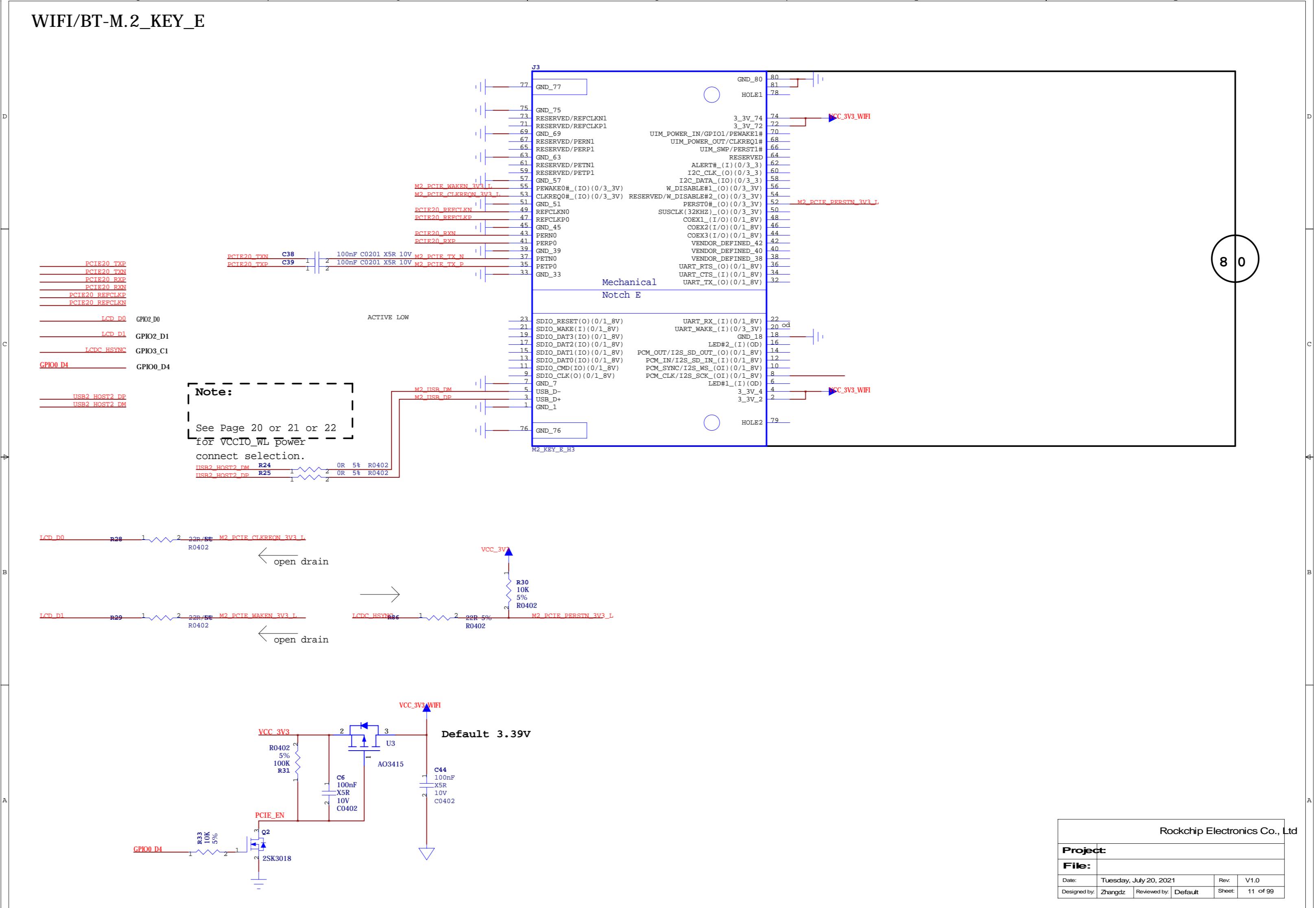
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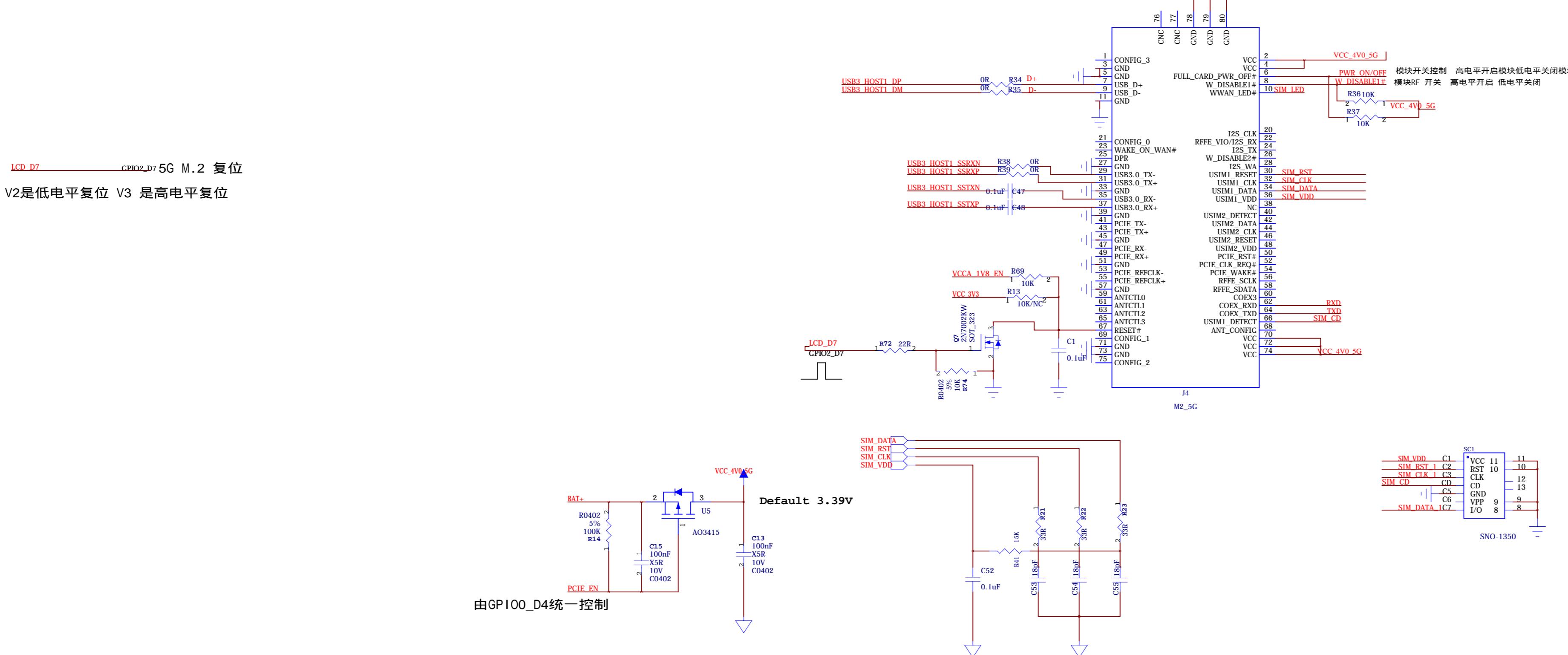
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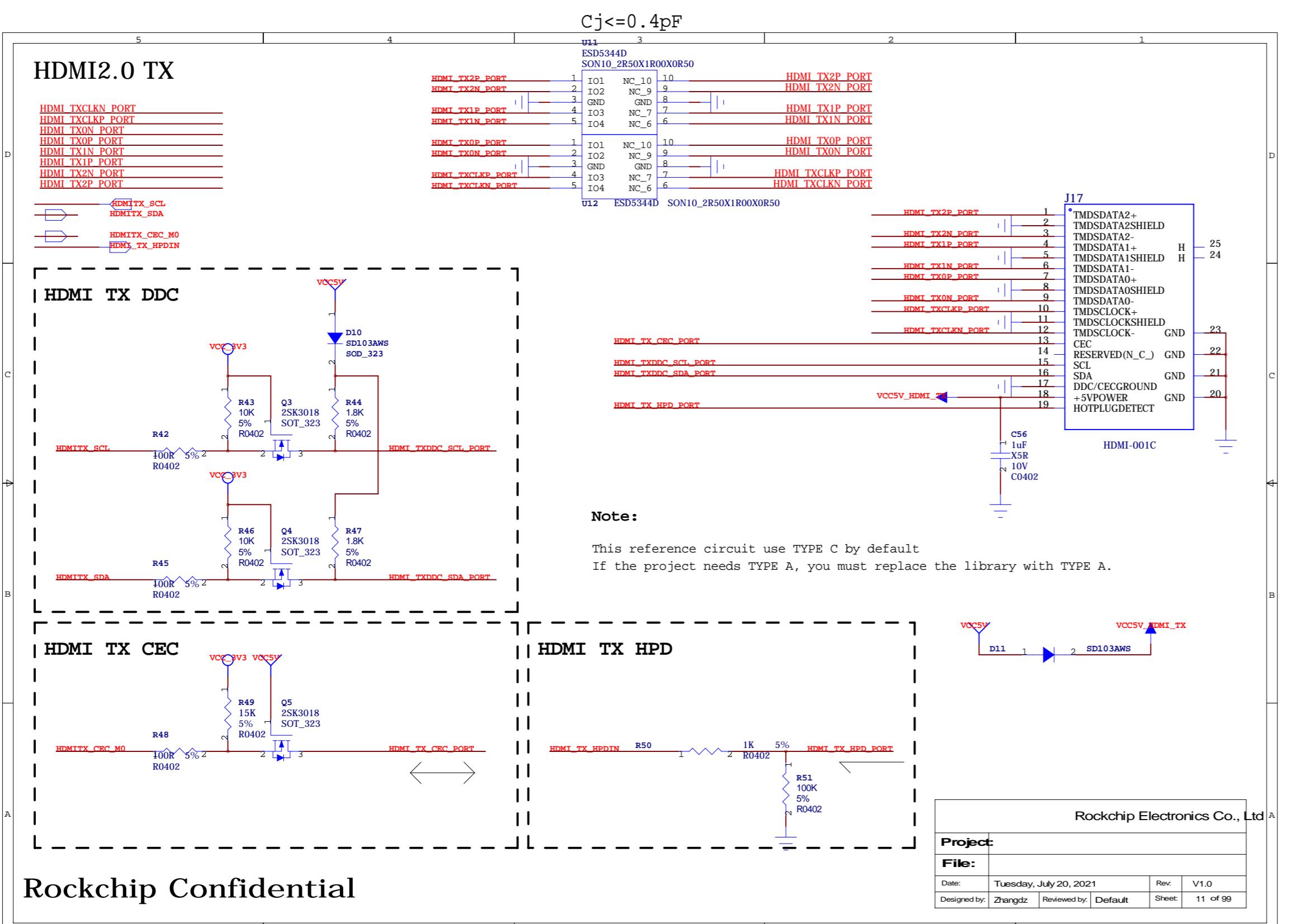
[LCD_D7](#) [GPIO2_D7](#) 5G M.2 复位

V2是低电平复位 V3 是高电平复位

由GP100_D4经

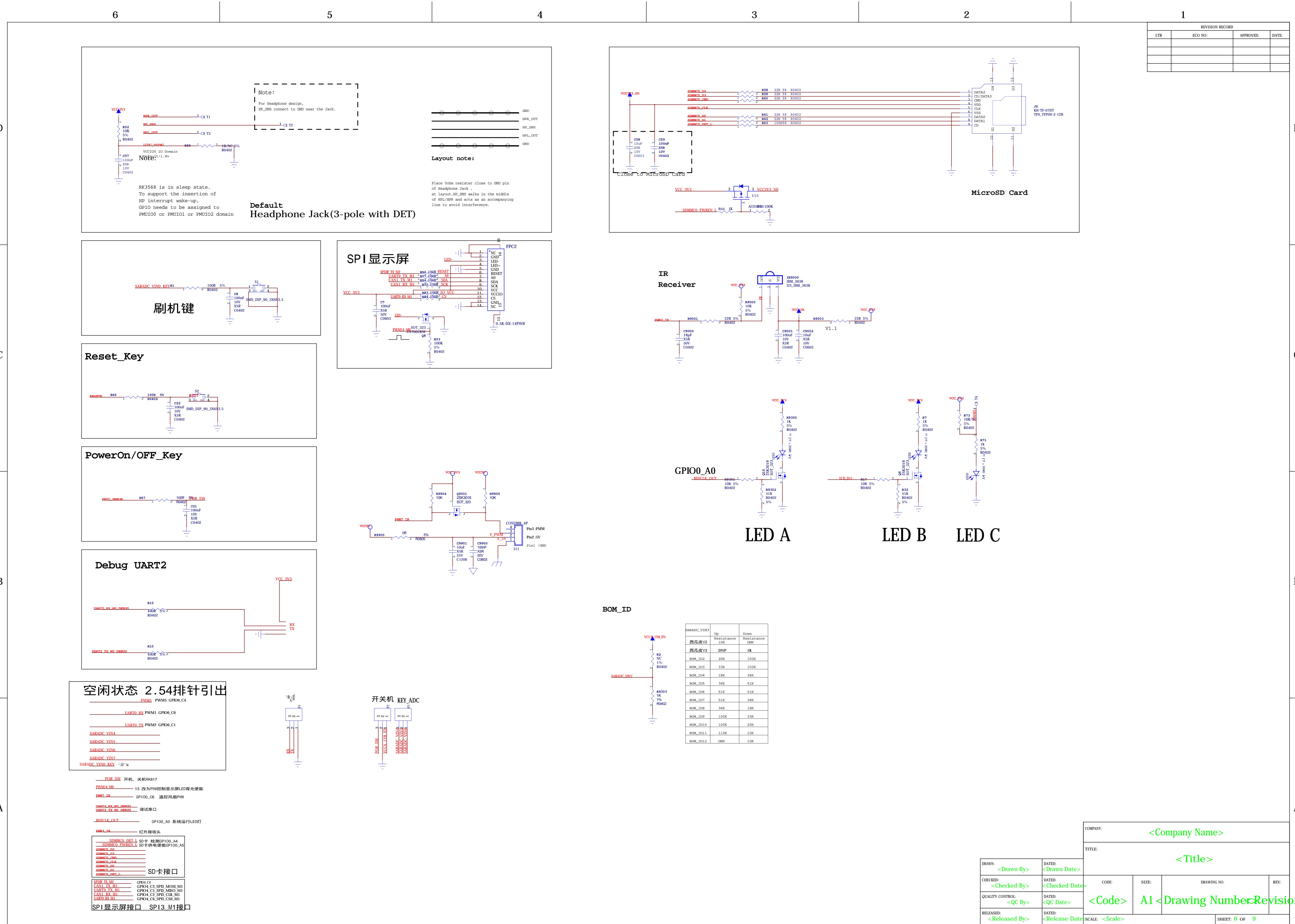
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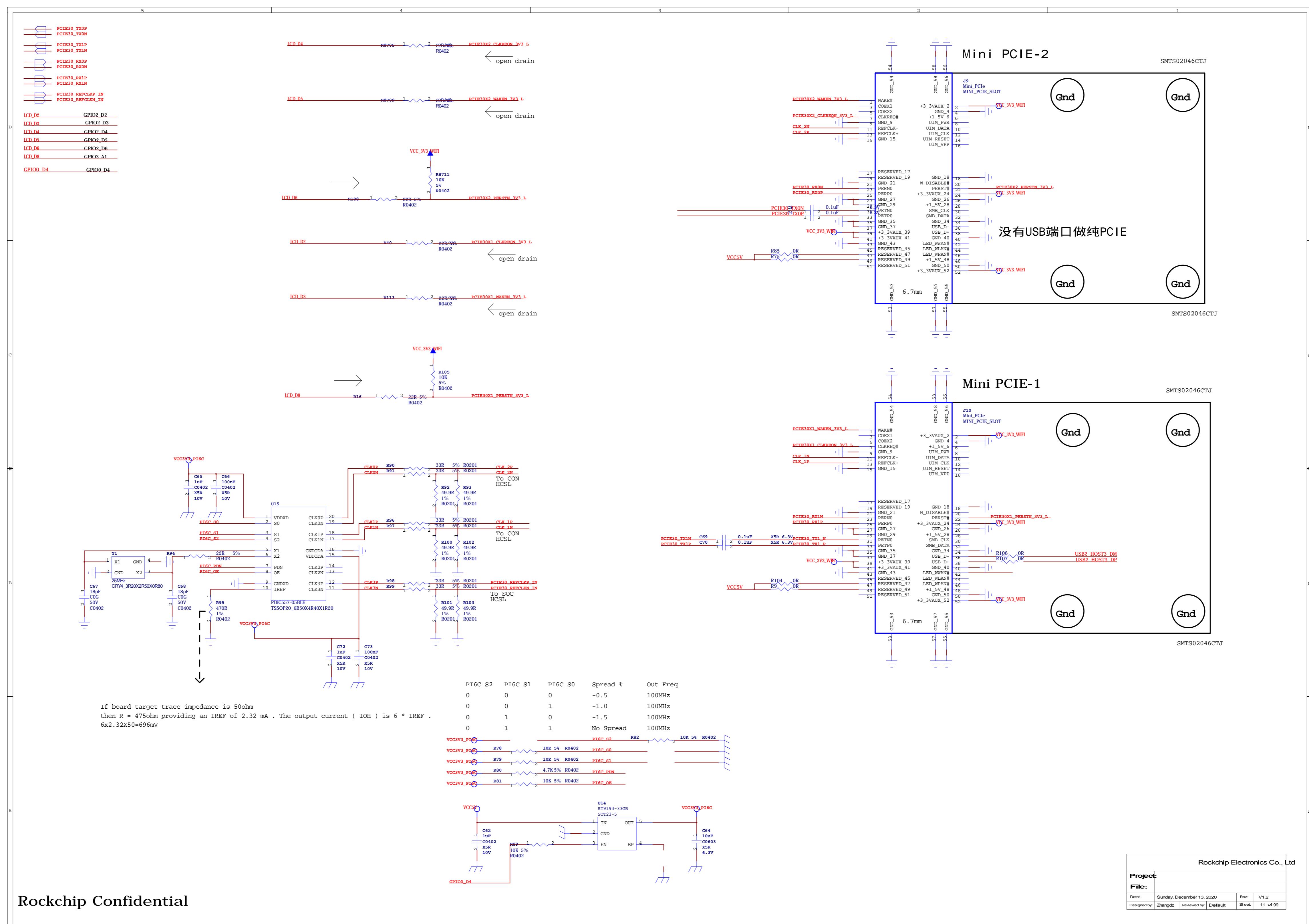


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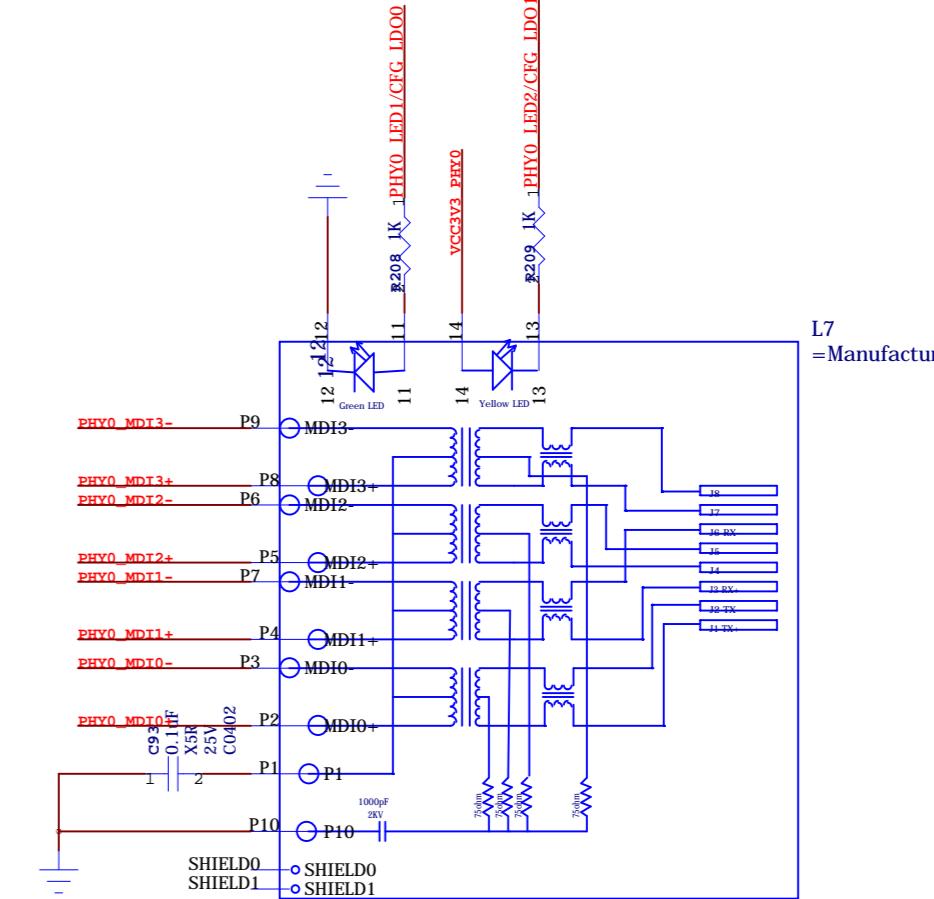
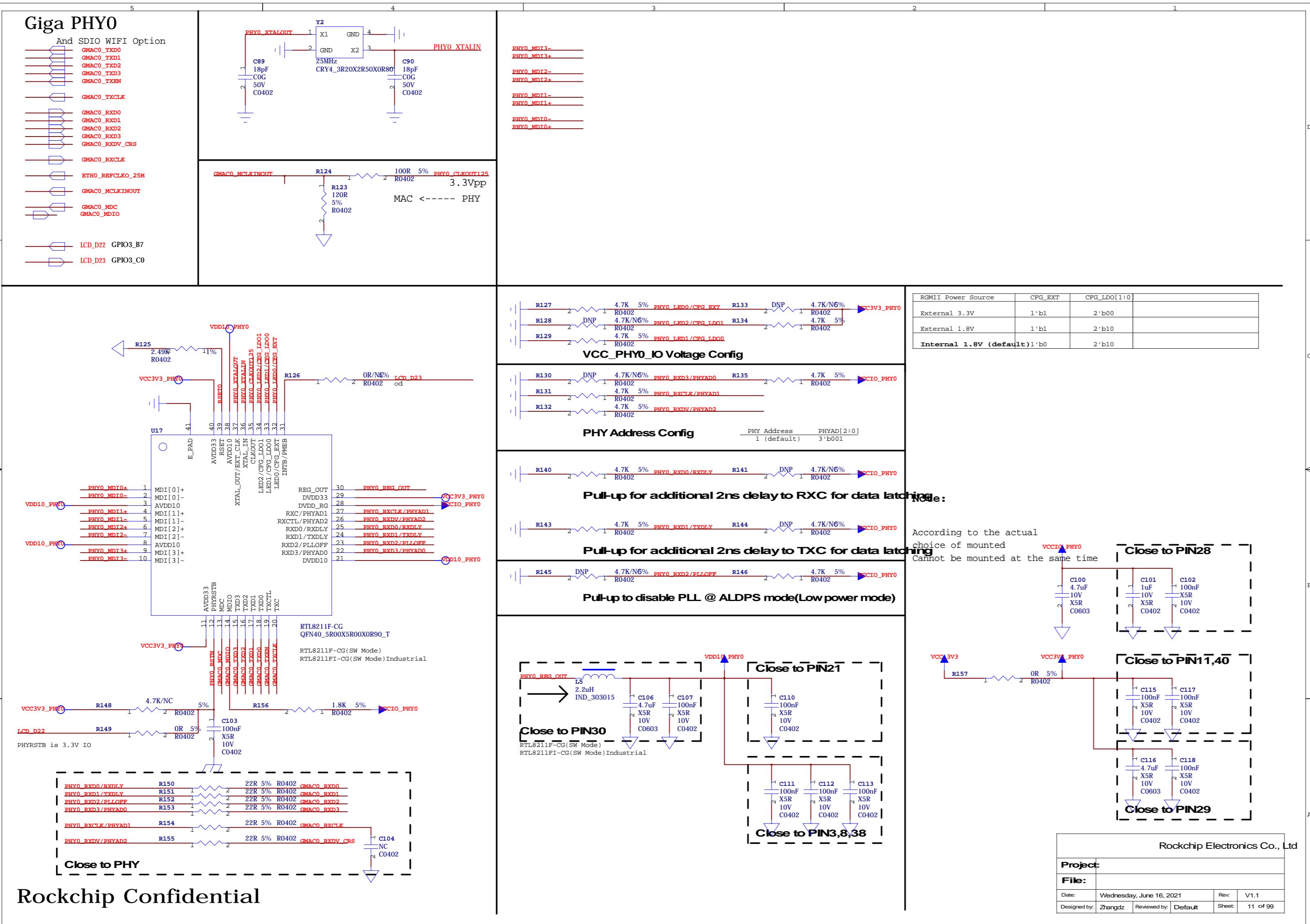


Rockchip Electronics Co., Ltd
Project: _____
File: _____
Date: Sunday, December 13, 2020 Rev: V1.2
Designed by: ZhengZ Reviewer: Default Sheet: 11 of 99

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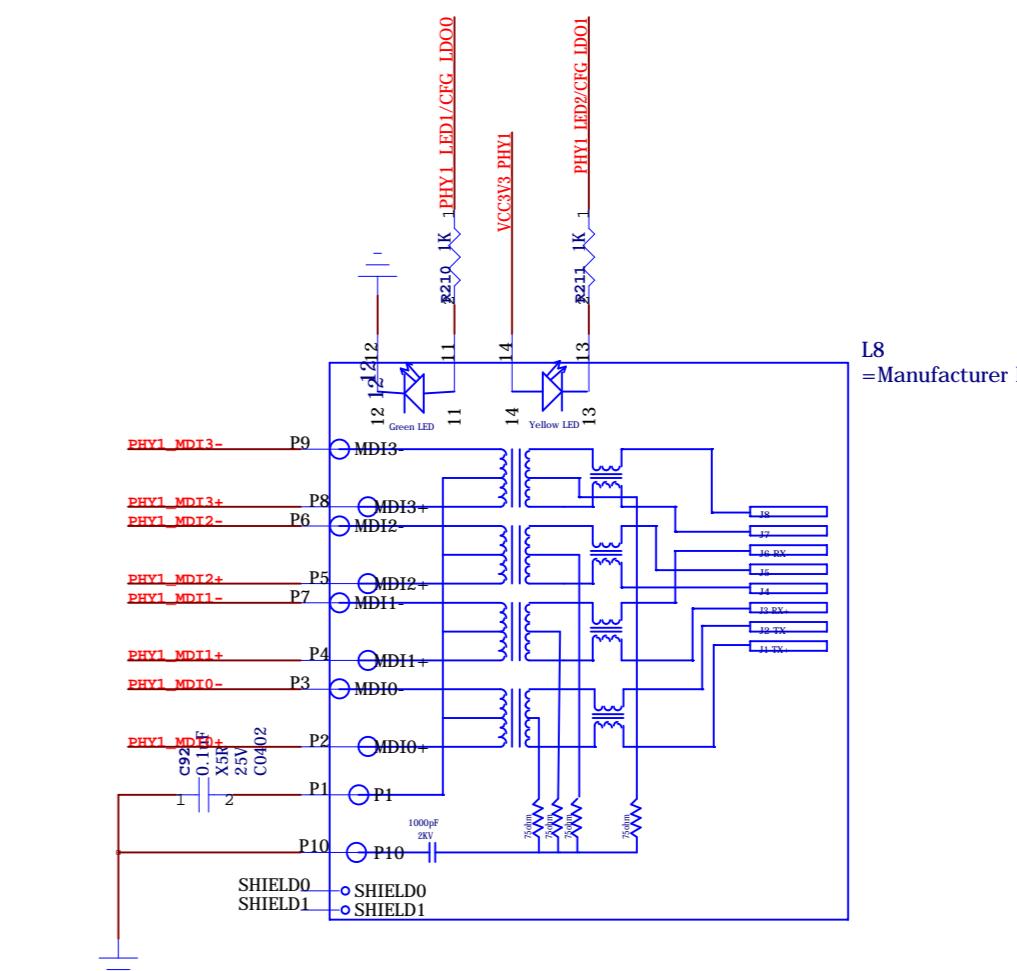
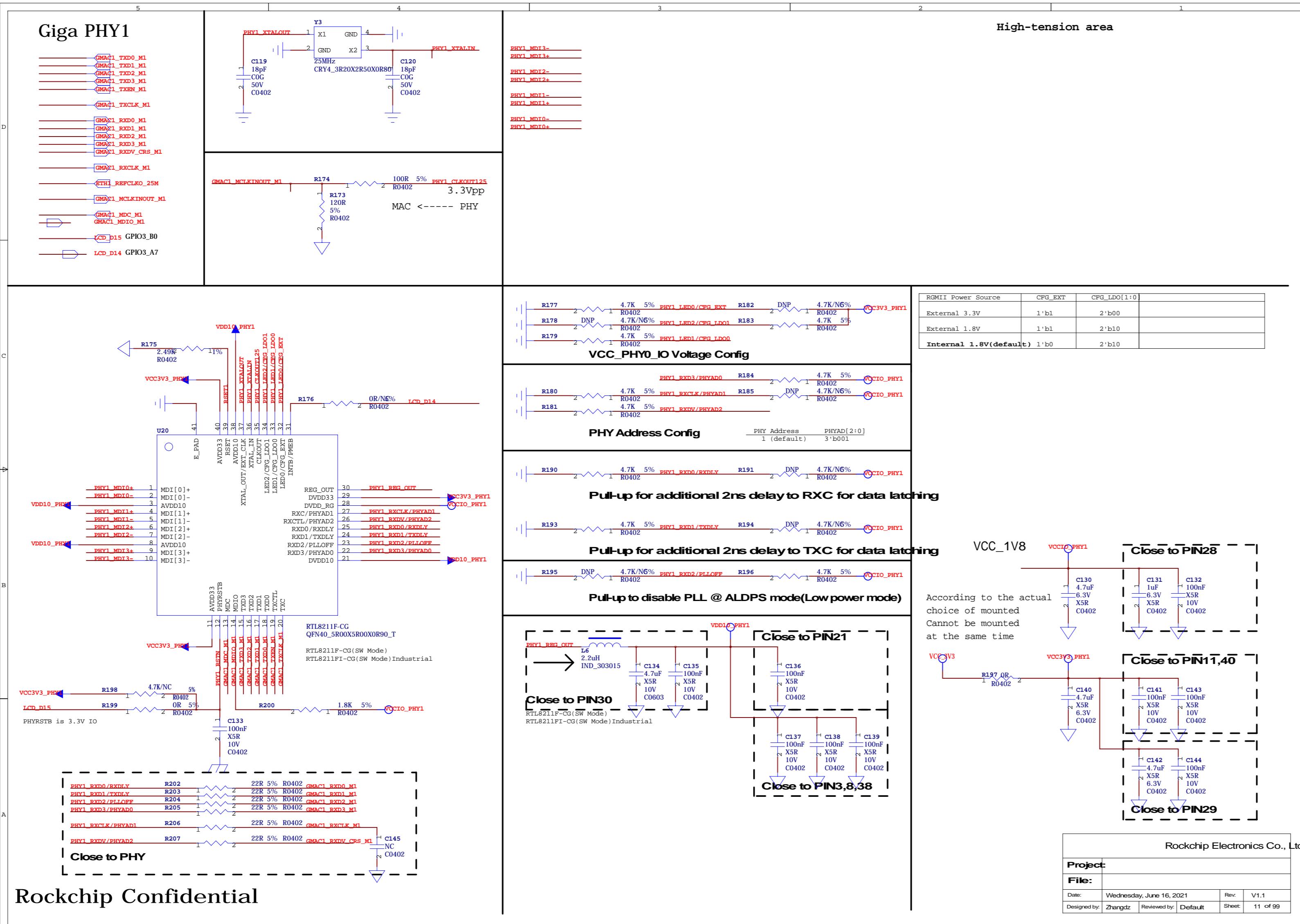


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SHEET: 8 OF 9

REVISION RECORD			
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