Oliver Pranis

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EDUCATION

California Institute of Technology GPA: 3.9 Pasadena, California
Bachelor of Science in Electrical Engineering Expected 07/24

Relevant Coursework: Digital and Embedded Systems Design, Computer Architecture, IC Design, Signal Processing.

SKILLS

Electrical Engineering: VHDL, Analog Design (Cadence), LTspice, PCB Design (Altium), Lab Equipment Usage.
Programming: C, Python, Java, Assembly, MATLAB, Git, Linux, Full Stack Web Development.

PROJECTS

50 Hz - 500 kHz multi-waveform function generator design. March - June 2023
- Voltage-controlled oscillator design, AC to DC power, filter, wave shaping circuitry.
- Implementation of various op-amp circuit blocks.
- SPICE simulation, PCB design, troubleshooting, use of lab equipment.

8-bit CPU Digital Design and Verification Using a Hardware Description Lanuage. January - March 2022
- Design of Control Unit, ALU, Data and Program Memory Access Units.
- Implementation and testing on a CPLD using ABEL hardware description language.

Embedded System Design on an AVR Microcontroller Using Assembly. March - June 2022
- Implementation of the Binario game using AVR assembly programming and debugging.
- Communication protocols, main game loop with interrupts and timers, user interface routines.

WORK EXPERIENCE

Axonics Irvine, California
Electrical Engineering Intern June - August 2022
- Tested custom test fixture systems used in validation and design of Implantable Pulse Generators.
- Designed software, and scripting for test fixture data analysis.

Teaching Assistantships Pasadena, California
Gave lectures, assisted small groups of students, graded works in following areas: September 2021 - ongoing
- Introduction to Digital Logic (digital design, computer architecture, 8-bit CPU design).
- Electronic system prototyping (designing, constructing and testing a system from a schematic to a soldered prototype).
- Introduction to Programming Methods (fundamental data structures and algorithms).

RESEARCH

Design and characterisation of electronic and photonic integrated circuits. June, 2023 – ongoing
Professor Ali Hajimiri's Research Group, California Institute of Technology Pasadena, California
- Helped develop a coherent optical receiver for fiber optic communication applications.
- Used photoelectronic lab equipment to design a test fixture for transimpedance amplifier performance measurements.

Multiwavelength Coupling with Waveguide-Integrated Optical Metasurfaces. July, 2021 – September, 2021
Professor Harry Atwater's Research Group, California Institute of Technology Pasadena, California
- Passive single-wavelength, and switchable multi-wavelength metasurface design.
- Experimental charazterization in FDTD Lumerical simulations.

Fabrication and Assessment of Single CdS Nanowire Photodetectors. September, 2018 – April, 2019
Institute of Solid State Physics, University of Latvia Riga, Latvia
- Synthesis of CdS nanowires using vapour-liquid-solid method.
- Creation of light-sensing detectors using a scanning electron microscope and evaluating their performance.

INTERESTS

- Game development with friends. Game of the Year award. Chosen to represent Latvia internationally.
- 13 years of experience in solo and ensemble accordion performance with awards in international competitions.
- 5 years of experience in choral performance with tours and awards internationally.
- Participation in university badminton and volleyball clubs.