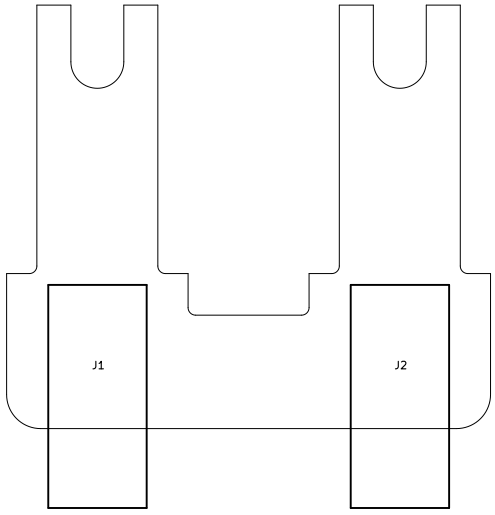
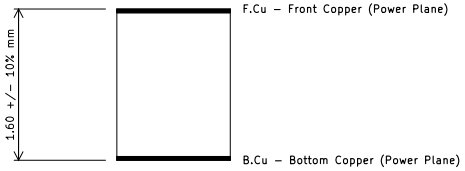
Layer Name: F.Fab
Layer Description: Front Fabrication & Assembly



PCB STACKUP



FABRICATION INSTRUCTIONS:

SUMMARY:	MULTILAYER, THROUGH HOLE
LAYER COUNT:	2 (2 POWER PLANE)
DESIGN RULES:	MIN 0.25MM TRACK / 0.25MM GAP / 0.50MM DRILL
IMPEDANCE CONTROL:	NOT REQUIRED
MATERIAL:	FR4 OR EQUIVALENT, PCB RATED UL94V-0
COPPER WEIGHT:	35um OUTERS
FINISH:	ANY
RESIST COLOUR:	BLUE
SILKSCREEN COLOUR:	WHITE
DIMENSIONS:	64.0MM X 56.0MM
FINISHED THICKNESS:	1.6MM +/- 10% MAX OVER PLATING
EDGE PLATING:	NOT REQUIRED
EDGE CONNECTORS:	NOT REQUIRED
EDGE CHAMFERING:	NOT REQUIRED
THIEVING/VENTING:	SEEK APPROVAL
ROHS/LEAD FREE:	ANY
DRILLING:	SEE DRILL FILE. HOLE SIZES ARE FINISHED.
PLUGGING:	NOT REQUIRED
OTHER NOTES:	NONE

Customer: Internal
Author: Kevin Bibby
Reviewer: Kevin Bibby

Designed by: Optimised Electronic Product Design
www.optimiseddesign.com

Sheet:
File: pt130a_dc-load-banana-plug-adapter.kicad_pcb

Title: DC Load Banana Plug Adapter		**PCB ID: PT130A**
Size: A4	Date: 2022-05-27	**Rev: 1.0**
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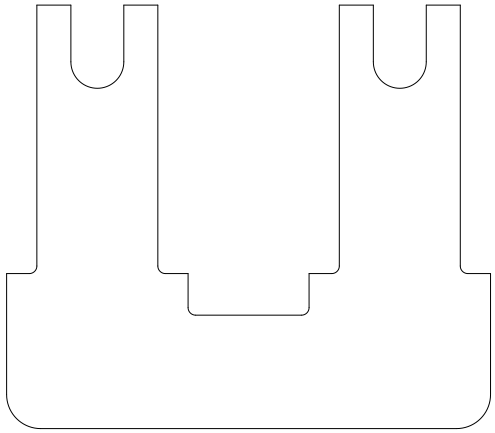

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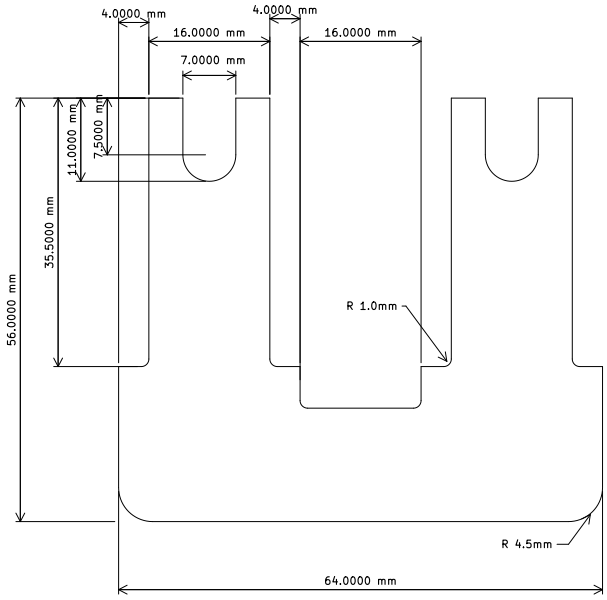
Layer Name: Dwgs.User
Layer Description: User Drawings



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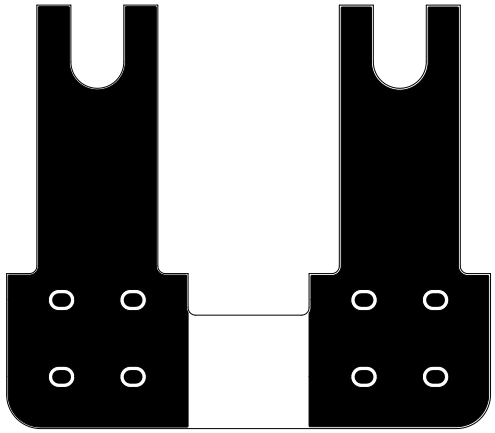
Layer Name: F.Cu
Layer Description: Front Copper



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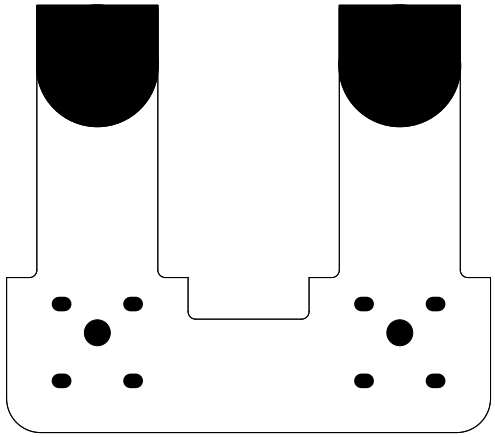
Layer Name: F.Mask
Layer Description: Front Soldermask



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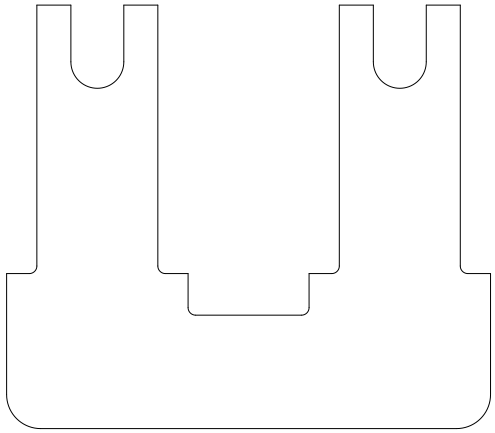
Layer Name: F.Paste
Layer Description: Front Solder Paste



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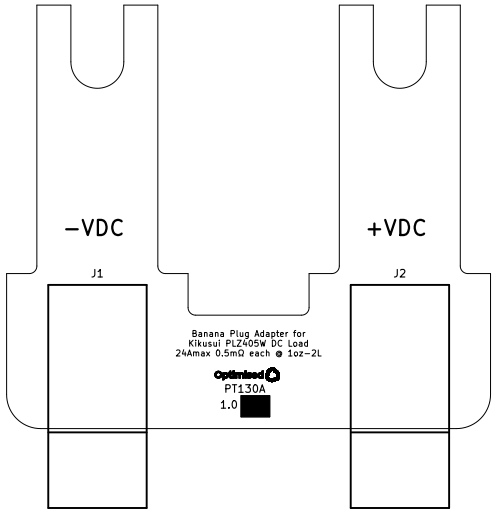
Layer Name: F.SilkS
Layer Description: Front Silkscreen



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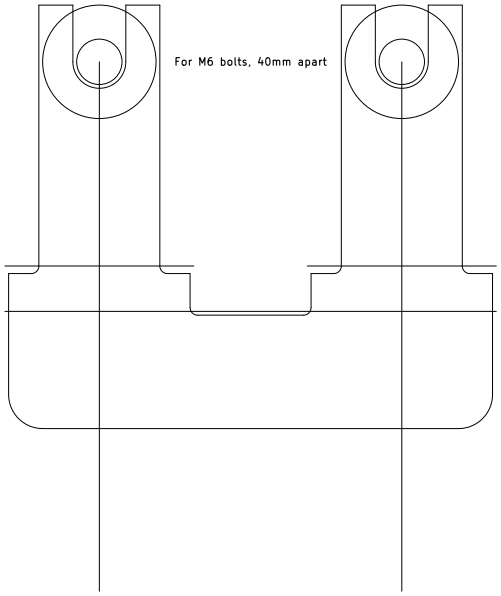
Layer Name: Cmts.User
Layer Description: User Comments



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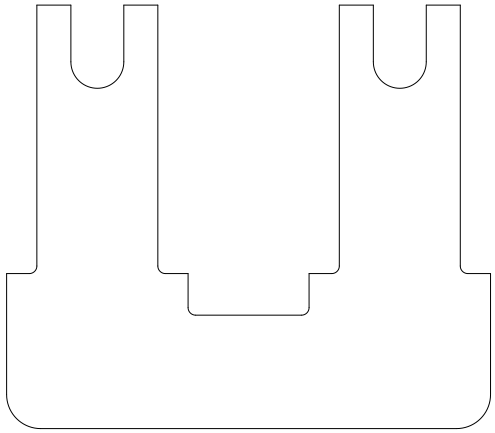
Layer Name: B.Fab
Layer Description: Back Fabrication & Assembly



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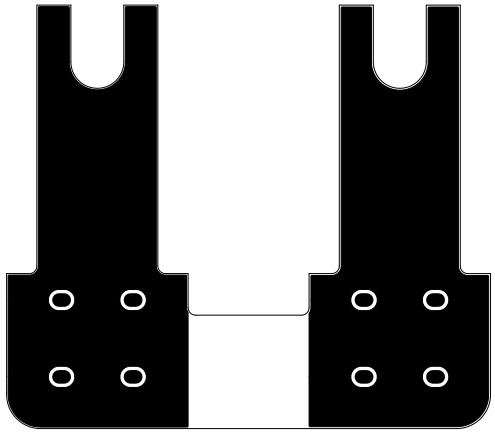
Layer Name: B.Cu
Layer Description: Back Copper



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Layer Name: B.Mask
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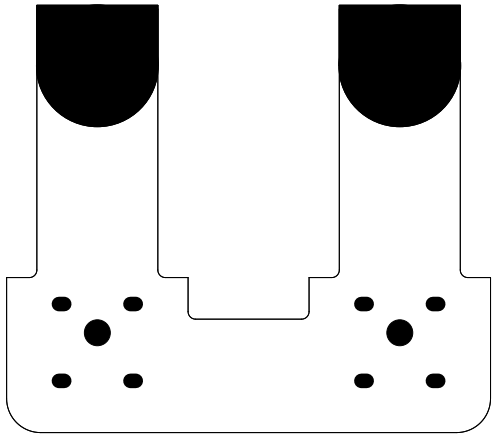

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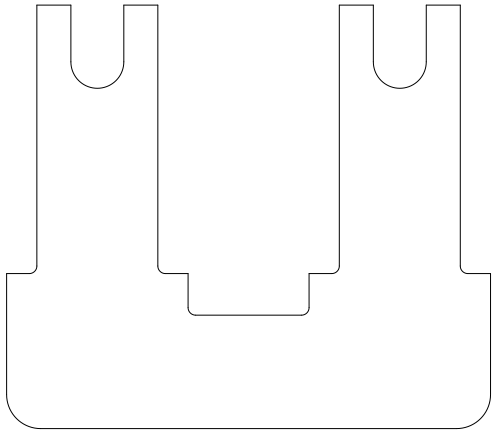
Layer Name: B.Paste
Layer Description: Back Solder Paste



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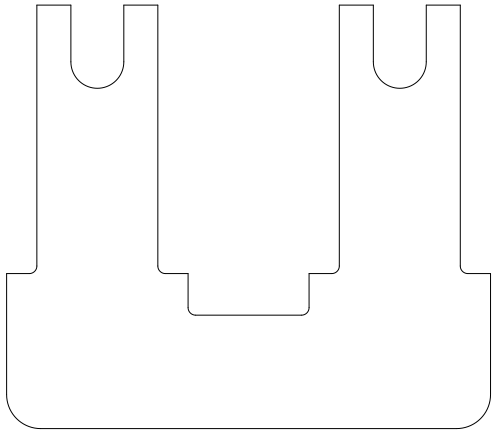
Layer Name: B.SilkS
Layer Description: Back Silkscreen



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