Vehicle Electronics (VEH) System Architecture

sys-veh

Rev: A01

Jinzhi Cai

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1 General Setting

In the OA-II VEH system, each payload module is specilize for a special function. In each module, it have three type of board.

1.1 Main Board

The main board is indecate this board finish the most foundmental function of this module. It is a stand alone board that build the fundation for the other board. It will be the first board in the module.

1.2 Feature Board

The feature board is adding more features to the module. It usually need the main board to function and receive commend from the the main board.

2 Payload Frame (PF)

2.1 Discribtion

2.2 Naming Method

OA2-PF-XXX-YY-Z

XXX indecate how much board it can fit for each module. The first number is for COM, then TAM and PAM.

YY Designer name.

Z Revision number.

example: OA2-PF-333-JC-1

First generation payload frame design by Jinzhi Cai which can contain three COM,TAM,PAM boards.

3 Payload Modules (PM)

3.1 Naming Method

OA2-[COM/PAM/TAM]-XX-YY-Z

XX   The first number indecate the require board for this board to function, zero for stand alone. The second number indecate the role for the board.

YY   Designer name.

Z   Revision number.

example: OA2-COM-01-JC-1

First generation Computing and Operation Module main board design by Jinzhi Cai.

3.2 Computing and Operation Module (COM)

3.3 Telecommunication and Acquisition Module (TAM)

3.4 Power and Actuator Module (PAM)

4 Revision History

Rev#	Editor	Delta	Date
A01	Jinzhi Cai	Initialize	2019-7-2

Table 1: Summary of Revision History