

## CV

### **More than 20 years of experience in Hi-tech companies**

- **Develop R&D workflows and methodologies**
- **New Product Introduction & Launches**
- **New Product Architecture & Specification**
- **Product Refinement & Life Cycle Management**
- **Willing to relocate**

### Personal info:

Name: Moshe Gefen, Tel: 0504039922, Email: gefmos@gmail.com

### Education

1988-1991      BSc. Electrical Eng - Tel Aviv University – Tel Aviv.

Emphasize on: Communication, Computers and Electrophysics

1991-1994      M.Sc Electrical Eng – Ben-Gurion University

Image processing: Markovien Matrix for Synthesis Aperture Radar photos

### Employment History

(2019 - )      online studies:

Python

Machine Learning

App. Development - IONIC

(2015-2018)

Employer:                      NETIC Systems Ltd,

Position:                      Product line manager

NETIC description:              NETIC Systems Ltd. ("NETIC") specializes in multi-disciplinary

Systems (S/W and H/W), taking full responsibility from concept design to mass production stage. IOT projects which includes: wearable technologies, top-notch sensors, embedded systems, meshing communications, applications IOS & Android, Design for regulation Among our customers: BOSCH, RAFAEL, ELBIT, AIRBUS, THALES, DAA, Biosense, NANONICS, MENTOR GRAPHICS...

(2009- 2015)

Employer: Rafael

Position: EDA Manager

Responsibility: Responsible for Systems design tools investments

Develop design flow, methodology, automation and customization

### **Writing scripts in C, C++, Matlab**

(Jan2005-2009)

Employer: Intel Haifa,

Position: Project manager

Responsibility: Matrix management of Merom mobile project

Responsible for the entire life cycle of the project.

From conceptual design till mass-production

Design component of the next generation technology, libraries and macros (e.g. SRAM)

Monitor design levels

Calculate DFX – ( DFR, DFQ, DFT and DFM.)

Analyze risks and suggest containment.

Follow-up design roles implementation and approve violations.

Experience with 90nm, 65nm and 45nm technologies.

(2002-Dec2004)

Employer: Tower Semiconductor LTD

### **Stated as Test Eng: writing test patterns in C, and ASM**

Later: Tower US test site manager - As test site manager responsible for all project test & qualification . Relocation to Austin TX

(1997-2001)

Employer: M-Systems, Tel Aviv

Position: Design Team manager

Responsibility: H/W and embedded S/W design

High speed Board design to support NVM products testing and programming, with the following interfaces: USB, EISA, PCI, PC-Card, and Ethernet.

### **Embedded C, working with NVM controller**

(1995-1997)

Employer: Motorola Semiconductor, Herzelia.

Position: Asic - Logic design

Responsibility: Design modules for Motorola DSP 56300.

Inventor of few patents in Audio protocols.

Knowledge experience: Full custom VLSI design: Verilog, Synthesis (Synopsis DC), Verification (Behavioral, Internal RPG, ASM language), VLSI architecture, Involved in the Layout phase, design first silicon characterization and MP test patterns.

(1991-1995)

Employer: Israeli Air Force

Position: Division commander in the Air Force Intelligence

Responsibility: Maintenance of a SAR (Synthetic Aperture Radar) - air born radar.

Knowledge experience: Image processing – real-time SAR image analysis .