

## Tasks

Name	Begin date	End date
Develop / Code Beaglebone Interface + Basilisk Simulation	4/7/23	5/8/23
Working Basilisk Model	4/7/23	4/21/23
Beaglebone Interface	4/7/23	5/8/23
Communication	4/7/23	5/8/23
Dockerization	5/2/23	5/8/23
2nd Prototype: Dimming Circuit + Sensors	4/7/23	4/28/23
Preliminary Research and Design	4/7/23	4/21/23
Design: PCB Layout	4/7/23	4/17/23
Parts Shipment	4/17/23	4/21/23
Assembly / Verification / Validation	4/18/23	4/28/23
Write Test Code for DAC + Sensors	4/18/23	4/21/23
Assembly: PCB Soldering and Circuit Construction	4/21/23	4/25/23
Verify / Validate System w/ Test Code	4/21/23	4/28/23
Design Documentation	4/7/23	4/28/23
3rd (Final) Prototype: Dimming Circuit + Sensors w/ Spectral Match; Scaling; Final Code	4/28/23	5/30/23
Preliminary Research and Design	4/28/23	5/12/23
Research: Spectral Match, Diffusion Plate	4/28/23	5/5/23
Design: PCB Layout	4/28/23	5/5/23
Parts Shipment	5/5/23	5/12/23
Assembly / Verification / Validation	5/9/23	5/30/23
Finalize Code	5/9/23	5/19/23
Assembly: PCB Soldering and Circuit Construction	5/12/23	5/23/23
Verify / Validate System w/ Final Code	5/23/23	5/30/23
Design: CAD Enclosure (Lower Priority, Purely Aesthetic)	4/28/23	5/30/23
User Manual / Design Documentation	4/28/23	5/30/23
User Manual	4/28/23	5/30/23
Code Documentation	5/9/23	5/30/23

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Final Documentation / Poster	5/26/23	6/16/23
Rough Draft: Final Project Report	5/26/23	6/2/23
Signed Final Report	6/2/23	6/16/23
Poster	6/2/23	6/16/23

Apr 14, 2023

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