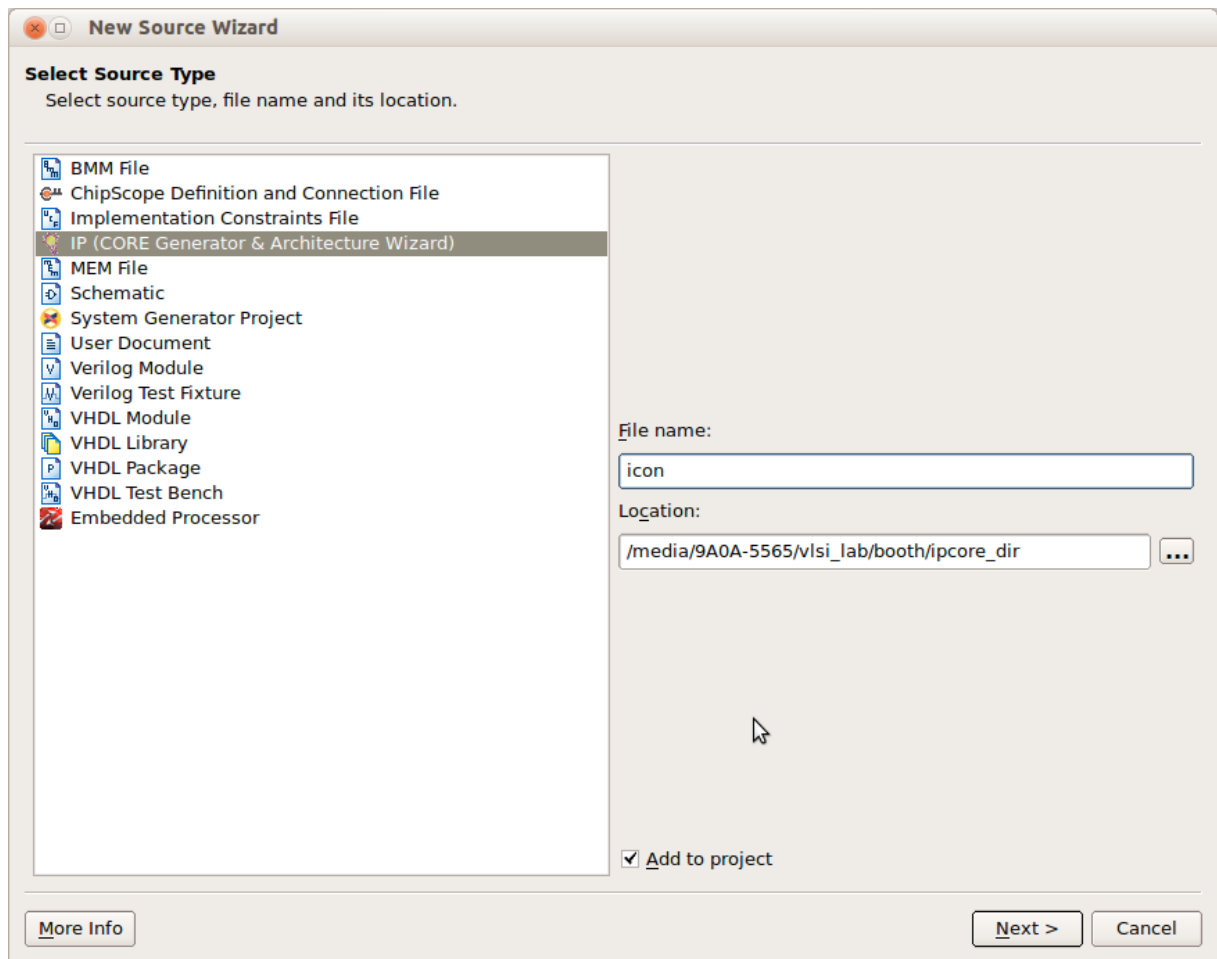


# Booth Multiplier Testing on FPGA board

VLSI Design Lab EE5480

February 17, 2014



## 0.1 Creating Project

Invoke 'ise' and create a new project for the following device details.

1. Family - Virtex 5
2. Device - XC5VLX110T
3. Package- FF1136
4. Speed - -1

## 0.2 Files to add

Add your source files to the project. Add the top.v file given to you. Instantiate your multiplier module in top as indicated in the code. Add the ucf file given to you and change the contents as you need.

## 0.3 Adding Chipscope Ip cores

Now add ICON and VIO IP cores as shown in the following pictures.

## 0.4 Implementing, Programming and testing on board

Implement the top module and generate the bit file. Connect the board and invoke chipscope analyzer. Test the design in chipscope as shown in the class.

