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Project Description

Project title: *Preprocessing for Steiner Tree Problems*

Detailed description:

Introduction:

Given an undirected graph (or a network) $G = (V, E)$ and a node set $T \subseteq V$, a Steiner tree for T in G is a subset (or a subnetwork) $S \subseteq E$ of the edges such that $(V(S), S)$ contains a path from s to t for all $s, t \in T$, where $V(S)$ denotes the set of nodes incident to an edge in S . In other words, a Steiner tree is an edge set S that spans T . The Steiner tree problem is to find a minimal Steiner tree with respect to some given edge costs $c(e)$, $e \in E$. This is a classical NP-hard problem with many important applications in network design in general and VLSI design in particular.

Preprocessing (or reduction):

Preprocessing is a very important algorithmic tool in solving combinatorial and integer programming problems of large scale. The idea, in general, is to detect unnecessary information in the problem description and to reduce the size of the problem by logical implications. For the

Steiner tree problem, many reduction methods are discussed in the literature and have been shown to be very effective for solving large instances.

Here, preprocessing is done using various reduction tests, that suggests removal of certain edges, vertices, etc. based on suitable constraints or bounds. Firstly, I would like to implement efficient realizations of some classical tests, and then implement some new tests, filling some of the gaps the classical tests had left.

Actually each test is specially effective on a certain type of instances, having less (or even no) effect on some other ones, so it is important to have a large collection of tests at one's disposal. Finally, I will integrate these tests into a packet, using some non-trivial techniques described below. It should be emphasized that the most impressive achievements of reductions are mainly due to the interaction of different tests, achieving results which are incomparable to those each single test could achieve on the same instance on its own.

There are two major classes of reduction tests:

- 1) The alternative-based tests use the existence of alternative solutions. For example in case of exclusion tests, it is shown that for any solution containing a certain part of the graph (e.g. a vertex or an edge) there is an alternative solution of no greater cost without this part; the inclusion tests use the converse argument. Some of these tests could be realized in time $O(e + n \log n)$, where $e:=|E|$, $n:=|V|$*
- 2) The bound-based tests use a lower bound for the value of an optimal solution under the assumption that a certain part of the graph is contained (in case of exclusion tests) or is not contained (in case of inclusion tests) in the solution; these tests are successful if such a lower bound exceeds a known upper bound.*

Definitions and notations:

- i) A bottle neck of a path P is a longest edge in P .*
- ii) The bottle neck distance $b(v_i, v_j)$ or b_{ij} between two vertices v_i and v_j in G is the minimum bottle neck length taken over all paths between v_i and v_j in G .*
- iii) The restricted bottleneck distance $rb(v_i, v_j)$ or rb_{ij} between v_i and v_j in G is the bottleneck distance between v_i and v_j in $G - (v_i, v_j)$.*
- iv) An elementary path is a path in which only the endpoints may be terminals. Actually, any path between two vertices can be broken at inner terminals into one or more elementary paths.*
- v) The Steiner distance along a path P between v_i and v_j is the length of a longest elementary path in P .*

- vi) The bottleneck Steiner distance (sometimes also called "special distance") $s(v_i, v_j)$ or s_{ij} between v_i and v_j in G is the minimum Steiner distance taken over all paths between v_i and v_j in G .
- vii) The restricted bottleneck Steiner distance $rs(v_i, v_j)$ or rs_{ij} between v_i and v_j in G is the bottleneck Steiner distance between v_i and v_j in $G - (v_i, v_j)$.
Actually, if $T = V$, then $b_{ij} = s_{ij}$ and $rs_{ij} = rb_{ij}$. In the context of alternative-based reductions, the notion of bottle neck Steiner distances (also called special distances) is often helpful.
- viii) In a (Steiner) tree T , non-terminals of degree at least 3 (in T) and terminals are considered as key nodes.
- ix) A key path in T is a path in which (only) the endpoints are key nodes.
- x) The unique path in T between two nodes v_i and v_j is called the fundamental path between v_i and v_j .
- xi) A tree bottleneck between two nodes v_i and v_j in T is a longest subpath on the fundamental path between v_i and v_j in which only the endpoints may be key nodes.
- xii) For each terminal z , one can define a neighborhood $N(z)$ as the set of vertices which are not closer to any other terminal. If $v \in N(z)$, we call z the base of v (written $\text{base}(v)$). we call $N(z)$ the Voronoi region of z .

I will implement modules to get the above mentioned parameters for a graph, tree, path, edge or a vertex, for those that aren't there in OGDF library, like a method for finding the bottleneck steiner distance between two vertices, etc.

Consider two terminals z_i and z_j as neighbors if there is an edge (v_k, v_l) with $v_k \in N(z_i)$ and $v_l \in N(z_j)$. Given G and T , the Voronoi regions can be computed in time $O(e + n \log n)$. Using them, a minimum spanning tree for the corresponding distance network $DG(R)$ (also denoted by $TD(R)$) can be computed in the same time.

Reduction tests:

A separate module for each of these tests would be implemented, with classes having different parameters to costs, steiner distances, etc. for an edge or for a vertex.

1) Degree-Test I:

The following tests summarized under the name degree-test I are easy to check:

- (i) A nonterminal node of degree one can be removed.
- (ii) If a nonterminal node v is of degree two, with the two incident edges $[u, v]$ and $[v, w]$, then those two edges can be replaced by an edge connecting u and w , $[u, w]$ of cost $c[u, w] = c[u, v] + c[v, w]$.
- (iii) An edge incident to a terminal node of degree one is always in an optimal solution.
- (iv) If an edge e is of minimal cost among the edges incident to a terminal node, and

the other end node is also a terminal, then e is choosable in any optimal solution.

2) Special-Distance-Test

This test computes for each pair of nodes a number (called the special distance) which can be exploited to remove some edges. The special distance can be computed by a modified shortest path algorithm. Given the values $s(u,v)$ for all $u, v \in V$, there is an easy and very effective test for deleting edges. An optimal solution S^* of a Steiner tree problem $ST(G, T, c)$ cannot contain any edge $[u, v] \in E$ with $s(u,v) < c(u,v)$. Concerning implementation, it should be noted that certain special cases of this test can be implemented more efficiently. However, one can also resort to a well-performing approximation of the special-distance-test that runs in $O(|V| \log |V| + |E| + (|T|^2))$

3) Bottleneck Degree m Test:

This might create many new edges, but, in general, most of these can be eliminated by the special-distance-test. The running time for this test is $O((2^m)g)$, where g denotes the time for computing a minimal spanning tree. Due to the exponential behavior, we perform this test only for $m < 4$.

4) Terminal-Distance-Test

In this test, we consider a connected subgraph $H = (W, F)$ of G with some vertices common to T and W , but not all. Let e and f be a shortest and a second shortest edge of the cut induced by W . Edge $e = [u,v]$ with $u \in W$ and $v \in V \setminus W$ is part of some optimal solution of $ST(G, T, c)$ and can thus be contracted, if $c(f) \geq d_u + c(e) + d_v$ with $d_u = \min \{ d(t, u) \mid t \text{ is common to both } T \text{ and } W \}$ and $d_v = \min \{ d(t, v) \mid t \in T \setminus W \}$.

Initially, I would like to mention a rough pseudocode of how the above mentioned tests would be implemented:

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Degree-Test I --> Special-Distance-Test --> Degree-Test I --> Terminal-Distance-Test --> Special-Distance-Test --> Degree-Test I --> Special-Distance-Test --> Degree-Test I --> Return
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Note that the bottleneck degree m test is not included in my default strategy. For some difficult instances, however, it pays to use the bottleneck degree m test and iterate all four tests as long as there is some reduction possible.

Alternative-based reductions:

5) PTm (paths with many terminals) and related tests

Every edge (v_i, v_j) with $c(v_i, v_j) > b(v_i, v_j)$ can be removed from G .

The PTm test is one of the most effective classical exclusion tests, but it is too time consuming for large instances in its original form. I will implement a modified

version of the PTm test.

For two terminals z_i and z_j , one readily observes that the bottleneck Steiner distance $b(z_i, z_j)$ can be computed by determining a bottleneck on the fundamental path between z_i and z_j in the spanning tree $TD(T)$, which can be constructed in time $O(e + n \log n)$. Each such bottleneck can be trivially computed in time $O(r)$, leading to a total time $O(mr)$ for m inquiries ($m \in O(\min\{e, (r^2)\})$). Observing that one actually has a static-tree variant of the bottleneck problem, I'll use a strategy based on depth-first search to achieve time $O(r^2)$ for all inquiries.

I do not need to precompute the b -values, because very often not all the combinations have to be checked; for example if the test condition turns out to be already satisfied during the computation (or, of course, if v_i or v_j were a terminal).

It is observed that if B' be the length of the longest edge in $TD(T)$. Every edge (v_i, v_j) with $c(v_i, v_j) > B'$ can be removed from the network.

Notice that using this test, one can eliminate some edges which could not be eliminated by the PTm test (even in its original form).

It must be mentioned that this observation has a greater impact than one would assume, because in some cases the reduction process is blocked in face of many alternatives with equal weights and can be reactivated only with a measure like this.

6) NTDk (non-terminals of degree k) test:

A non-terminal v_i has degree at most 2 in at least one Steiner minimal tree if for each set A , $|A| \geq 3$, of vertices adjacent to v_i holds: The sum of the lengths of the edges between v_i and vertices in A is not less than the weight of a minimum spanning tree for the network $(A, A \times A, b)$.

If this condition is satisfied, one can remove v_i and incident edges, introducing for each two vertices v_j and v_k adjacent to v_i an edge (v_j, v_k) with length $c_{ij} + c_{ik}$ (and keeping only the shortest edge between each two vertices).

The special cases with k (degree of v_i) in $\{1, 2\}$ can be implemented with total time $O(n)$ (for examination of all non-terminals). For $k \in \{3, \dots, 7\}$ we use the rb -values instead of the exact bottleneck Steiner distances. Because addition of new edges can be a non-trivial matter and the needed rb -values are already available, it is a good idea to check for each new edge if it could be eliminated using the PTm test, in this case it need not be inserted in the first place.

7) NV (Nearest Vertex) and related tests:

Let z_i be a terminal with degree at least 2, and let (z_i, v_i') and (z_i, v_i'') be the shortest and second shortest edges incident with z_i . The edge (z_i, v_i') belongs to at least one Steiner minimal tree, if there is a terminal z_j , z_j not equal to z_i , with $c(z_i, v_i'') \geq c(z_i, v_i') + d(v_i', z_j)$.

The original version of the test NV requires the computation of distances, which is too time-consuming for large instances. But one can accelerate this test without making it less powerful, using an observation given below. For this purpose, we use Voronoi regions, saving some extra information while computing the regions. Let $\text{distance}(z_i)$ be the length of a shortest path from z_i to another terminal z_j over the edge (z_i, v_i') , computed as follows: Each time an edge (v_i, v_j) with $v_i \in N(z_i)$, $v_j \in N(z_j)$, z_j not equal to z_i is visited, it is checked whether v_i is a successor of v_i' in the shortest paths tree with root z_i (simply done through marking the successors of v_i'). In such a case $\text{distance}(z_i)$ is updated to $\min\{\text{distance}(z_i), d(z_i, v_i) + c(v_i, v_j) + d(v_j, z_j)\}$.

It is observed that the condition of the test NV is satisfied if and only if:
 $c(z_i, v_i'') > c(z_i, v_i') + d(v_i', \text{base}(v_i'))$, if v_i'' is not an element of $N(z_i)$, and
 $c(z_i, v_i'') \geq \text{distance}(z_i)$, if $v_i'' \in N(z_i)$.

Using this observation, I'll perform the test NV for all terminals in time $O(e + n \log n)$. Note that in inclusion tests, each included edge is contracted into a terminal.

The Voronoi regions can also be used to perform a related inclusion test, which is called SL (Short Links)

It is also observed that, if say, z_i be a terminal, and (v_1, v_1') and (v_2, v_2') the shortest and second shortest edges which leave the Voronoi region of z_i ($v_1, v_2 \in N(z_i)$, v_1', v_2' are not elements of $N(z_i)$; these edges are called links). The edge (v_1, v_1') belongs to at least one Steiner minimal tree, if $c(v_2, v_2') \geq d(z_i, v_1) + c(v_1, v_1') + d(v_1', z_j)$, where $z_j = \text{base}(v_1')$.

This test can also be performed for all terminals in total time $O(e + n \log n)$. The classical test SE (Short Edges) is a more powerful inclusion test. It is observed that even this test can be implemented with time $O(e + n \log n)$. But although this test is more effective than NV and SL in a single application, the difference almost vanishes when the reduction tests are iterated. Therefore, I only propose to use the much simpler, empirically faster tests NV and SL in our actual implementations.

8) Path substitution (PS)

It is another alternative-based reduction test that is more general than the previous tests in two ways: The test PS examines several edges along a path, instead of examining elementary graph objects (like single edges and vertices). If the test is successful, some of these edges can be deleted at once. The other more general aspect is a consequence of the first: Searching for alternatives for a path, it is not sufficient anymore to find one alternative, because the edges of the path can be involved in many different ways in a Steiner tree. As a consequence, such a test can only be efficient if it has strong requirements as conditions.

The basic idea is to start with a single edge as the path and then try to find

alternative paths for the vertices adjacent to those on the path. If this is not possible for exactly one adjacent vertex, the path is extended by the edge to this vertex and the search for alternative paths is restarted. Such successive extensions could finally lead to the desired situation. I describe here the observation that leads to the formal specification of the test in a simplified way: I give only the description for deleting one edge of the path and define it only for the special case that the starting vertex v_0 has degree 3. The extensions to deleting many edges on the path and to vertices with degree 2 or 4 are more or less straightforward.

It is observed that if a path $P(v_0, \dots, v_l)$ with $\text{degree}(v_0) = 3$ and $v_i \in V \setminus T$ for all $i \in \{0, \dots, l\}$. We denote by v_{i1}, v_{i2}, \dots the vertices adjacent to each v_i on P which are not contained in P . Let $d_0(v_i, v_j)$ be the length of a shortest path between v_i and v_j that does not contain (v_0, v_1) ; and $d_P(v_i, v_j)$ (for v_i and v_j in P) the length of the subpath of P between v_i and v_j .

The edge (v_0, v_1) can be deleted if for all $i \in \{1, \dots, l\}$ there are functions f_i and g_i such that:

- (I) for all v_{ik} adjacent to v_i and for $k = f_i(k)$: $d_P(v_0, v_i) \geq d_0(v_0, v_{ik})$,
- (II) for all v_{0k} adjacent to v_0 and for $k = g_i(k)$: $d_P(v_0, v_i) \geq d_0(v_0, v_{ik})$, $c(v_0, v_{0k}) \geq c(v_i, v_{ik})$.

Bound-based reductions:

Since one cannot expect to solve all instances of an NP-hard problem like the Steiner problem only through reduction tests with a (low-order) polynomial worst-case time (like the tests in the mentioned above), the computation of (sharp) lower bounds is a common part of the standard algorithms for the exact solution of such a problem. The information gained during such computations can be used to reduce the instance further. Besides, by using fast heuristics for generating bounds small worst-case running times can be guaranteed even for this kind of tests

9) Reduction using Voronoi regions:

The Voronoi regions can be used to determine a lower bound for the value of an optimal solution under some additional assumptions (for example, that the solution contains a certain non-terminal). For any terminal z , if I define $\text{radius}(z)$ as the length of a shortest path from z leaving its Voronoi region $N(z)$. These values can be easily determined while computing the Voronoi regions. For convenience, we assume here that the terminals are numbered according to non-decreasing radius-values. For each non-terminal v_i , let z_{i1}, z_{i2} and z_{i3} be the three next terminals to v_i , then I use the following observation to eliminate a non-terminal.

It is observed that if T is a Steiner minimal tree and assuming that v_i is a Steiner node in T . Then $d(v_i, z_{i1}) + d(v_i, z_{i2}) + \sum \text{radius}(z_t)$ (for $t=1$ to $(r-2)$) is a lower bound for the weight of T .

A non-terminal v_i can be eliminated if this lower bound exceeds a known upper

bound. This method can be extended for eliminating edges, by the following observation:

It is also observed that if T is a Steiner minimal tree and assuming that T contains an edge (v_i, v_j) . Then $c(v_i, v_j) + d(v_i, z_{i1}) + d(v_j, z_{j1}) + \sum_{t=1}^{r-2} \text{radius}(z_t)$ is a lower bound for the weight of T .

There is also a test defined performing the same actions as NTDk when it is successful, using the following observation:

It is observed that if T is a Steiner minimal tree and assuming that v_i is a Steiner node whose degree in T is at least three. Then $d(v_i, z_{i1}) + d(v_i, z_{i2}) + d(v_i, z_{i3}) + \sum_{t=1}^{r-3} \text{radius}(z_t)$ is a lower bound for the weight of T .

Intuitively, one expects that a better lower bound should be achievable through this line of argument, because the paths between the terminals in a Steiner tree not only leave the corresponding Voronoi regions, but also span all terminals. Indeed, I would like to use the following idea:

It is observed that, suppose if I consider the auxiliary network $G' = (T, E', d)$, in which two terminals are adjacent if and only if they are neighbors in the original network; defining:

$d'(z_i, z_j) := \min\{\min\{d(z_i, v_i), d(z_j, v_j)\} + c(v_i, v_j) \mid v_i \in N(z_i), v_j \in N(z_j)\}$. The weight of a minimum spanning tree for G is a lower bound for the weight of any Steiner tree for the original instance (G, T) .

This observation can be extended to a test condition, for example, for any non-terminal v_i , the weight of such a spanning tree minus the length of its longest edge plus $d(v_i, z_{i1}) + d(v_i, z_{i2})$ is a lower bound for the weight of any Steiner minimal tree that contains v_i . The resulting test is very fast: The network G can be determined without much extra work while computing the Voronoi regions, and a minimum spanning tree for it can be computed in time $O(|E| + |T|\log|T|)$. For computing upper bounds in this context, we use a modified path heuristic with time $O(e + n \log n)$. So, all these tests can be performed in time $O(e + n \log n)$, this combined test is called as VR (Voronoi Regions).

With a heuristic solution available, all these tests can be easily extended to the case of equality of lower and upper bound. As the intuition suggests, the VR test is most effective for sparse networks with relatively few terminals; in this sense, it is a nice complement to the alternative-based tests, which are often specially successful if the proportion of terminals to all vertices is high.

Integration and implementation of tests:

I would like to implement through a direct control of loops (through parentheses), their termination criteria, switching of parameters, etc. It is observed that the (alternative-based) tests are not very sensitive to the order in which they are executed. On the other hand, the ordering has often an impact on the total time for reductions, (usually the fast version of the PTm test is performed first).

Again, for the implementation of the graph, I have chosen a kind of adjacency-list representation of networks (with all edges in a single array), but depending on the context, maybe, I'm thinking to sometimes switch to other auxiliary representations (all linear in the number of edges) for certain operations. For each test, I'm planning to perform all actions in a single pass (and do not, for example, delete an edge and start the test from scratch).

I would like to stress here that all actions following each test can be realized in a time dominated by the worst-case time $O(e + n \log n)$ of the fast tests. With the additional postulation that in each loop of the selected tests a constant proportion (say 5%) of vertices and edges must be eliminated and that instances of trivially small size are solved directly (by enumeration), one gets the same asymptotical time bound for the whole reduction process as for the first iteration ($O(e + n \log n)$, if one confines oneself to the fast tests).

Another technical aspect is the efficient reconstruction of a solution for the original instance out of a solution for the reduced instance (which often consists of a single terminal). Saving appropriate information during the reduction process, this can be done in time $O(e)$. I'll always perform such a transformation after each run of the program, checking the feasibility and value of the solution in the original instance.

Issues related to implementation:

1) Precomputing Steiner distances:

A crucial issue for the implementation of the test is the calculation of bottleneck Steiner distances. An exact calculation of all s_{ij} would make the test impractical even for medium-size instances. So I'm using a good approximation of these distances and some appropriate data structures for retrieving them.

2) Tree bottle necks:

I'm going to use the tree bottleneck test, because every distance between tree nodes calculated for a minimum spanning tree or a Steiner minimal tree computation can be tested against the tree bottleneck; and in many of the cases where a tree can be ruled out, already an intermediate bottleneck test can rule out this tree, leading to a shortcut in the computation.

3) For the bound-based tests:

An efficient method for generating the dual feasible solution needed for the bound-based test is to use something called the DUAL-ASCENT algorithm. I plan to

improve the test by calculating a lower bound and reduced costs for different roots.

References:

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- [3] T. Koch, A. Martin: Solving Steiner tree problems in graphs to optimality, Networks 32(3), pp. 207-232, 1998
- [4] Tobias Polzin, Siavash Vahdati: Extending Reduction Techniques for the Steiner Tree Problem: A combination of Alternative and Bound-Based Approaches MPI-I-2001-1-007, Dec. 2001
- [5] Eduardo Uchoa, Marcus Poggi de Aragao, Celso Ribeiro: Preprocessing Steiner Problems from VLSI Layout, PUC-Rio.Inf.MCC 32/99, Dec. 1999.

What have you done so far?

As part of the GSoC application process, I've done the programming exercise specified for this project topic using OGDF, in which I've implemented three of the special cases of "Bottleneck Degree m Tests", of which the first two are also called "Degree-Test 1".

How do you plan to realize your project?

I would like to describe my plan to implement various preprocessing heuristics that are mentioned above, during the GSoC.

Bottleneck Degree m tests are partially done already as part of the application process.

Week 1 (19th May to 25th May):

I would like to continue with the Bottleneck Degree m test and complete it, with options to implement each of the special case, individually.

Week 2 (26th May to 1st June):

I'll implement the special-distance test and terminals-distance test, again with options to implement each of the special case, individually.

Week 3 (2nd June to 8th June):

Combine these tests into one packet, and implement them, with and without bottleneck degree m test.

Week 4 (9th June to 15th June):

Make snippets for various quantities, mentioned in the definitions part of the above

project description. These are going to be used in the following algorithms.

Week 5 (16th June to 22nd June):

Start with the alternative-based reductions – the PTm and related tests, that are based on the above mentioned observations.

Mid-term Evaluations:

Week 6 (23th June to 29th June):

Implement NTDk test, and various observations based on it.

Week 7 (30th June to 6th July):

Implement NV test, and various observations based on it.

Week 8 (7th July to 13th July):

Implement SL test and possibly the SE test too.

Week 9 (14th July to 20th July):

Path substitution is implemented and also the observations based on it are also implemented.

Week 10 (21st July to 27th July):

Now, I'll go to the bound-based reductions, starting with the reduction using Voronoi regions (VR). Here, I'll use the snippets that would be made during week 4 for finding Voronoi region.

Week 11 (28th July to 3rd Aug):

Based on observations from the VR, various other tests mentioned above are implemented.

Week 12 (4th Aug to 10th Aug):

I'll find the optimum sequence of the above tests to be performed, so that the instances are significantly reduced, i.e graphs are reduced significantly.

Pencils down:

Week 13 (11th Aug to 17th Aug):

I'll compile all the modules together and provide it as a single/multiple library file (depending on the necessity), with options given to choose between the above reductions techniques. I'll make sure it compiles with the OGDF source code.

Final Deadline:

Week 14 (18th Aug to 22nd Aug):

I'll debug and make sure there is no error in the code, and also make sure that the preprocessing is significantly reducing the instances.

If time permits, I'm planning to implement the algorithms for finding upper bound – namely, the path heuristics and heuristic reductions, which uses the the above mentioned VR (reductions based on Voronoi regions). Otherwise, I would like to contribute to OGDF after GSoC. Also, after GSoC, I would like to work on the reductions based on DUAL-ASCENT algorithms.

Anticipated challenges:

1) I'm not sure if all the observations for each of the reduction tests, mentioned in the reference papers could be implemented during GSoC. I plan to give more importance to being stuck to the schedule and implement the various tests and implement as many observations as possible in a week.

2) I want make sure, that I combine all the reduction tests and compile it with the OGDF source code, in an elegant manner.

Programming Experience

Programming languages:

C – Good

C++ - Good

Python – Good

Java – Medium

Matlab - Medium

Operating systems:

Linux, Windows.

Libraries:

1) I've used many python-based programming libraries like – SymPy, SciPy, NumPy, etc.

2) I worked on an orbit propagator code (a set of libraries) (SGP4) in Matlab, that was used in a student satellite project.

3) I also worked on microcontrollers for various applications using C, like getting serial port data from a sensor into a microcontroller and then wirelessly from microcontroller to PC, etc. So, I had to use a lot of inbuilt libraries that are specific to the microcontroller model to implement various functions.

4) I developed a previously available code for mathematically simulating ray intersection on pipes of different shapes using visual C++, along with a GUI interface to it (it was for a project related to ultrasonics).

Open source software experience:

I have used a wide range of open source softwares – from LibreOffice to Mozilla Firefox, etc. One significant usage was as follows:

As part of my, 3 months summer internship last year (at Ittiam Systems Pvt. Ltd.), I had to demonstrate the improvement in ASR's (Automatic Speech Recognizer's) recognition accuracy of real-time speech signals over the beam-formed (a signal processing technique) signals using CMU's Sphinx - open source Java-based ASR. But, later realized that the Java-based version was not meant to be accurate but to be more reliable. So, I had to shift to their C++ version of CMU's Sphinx, which was more accurate and relevant to my project.

So, I wish if I could contribute my part to the open source community, starting from GSoC 2014.

I actually wanted to contribute to CMU's sphinx itself, but since 2012, they haven't participated in GSoC.

Academic Experience

Academic Performance:

My current CGPA (Cumulative Grade Point Average) after 7 semesters is 7.7/10

I've done a basic course on C programming language (theory and lab). I have also done a course on computer simulations where we had to use C and Python for simulating various real-life problems in electrical engineering.

Do you have any experience with graph algorithm or graph drawing (theory or applications)?

I've done a course on graph theory, where I studied about trees, connectivity of graphs, euler tours and hamiltonian cycles, matchings, edge colourings, directed graphs and various algorithms related to each of those topics with their proofs.

I'm currently doing a project on quantum walks (or quantum random walks) - a computational tool in quantum computing related algorithms. It is based on lot of graph algorithms.

Please describe your knowledge in algorithms and data structures:

I'm currently doing a course on data structures and algorithms, where I'm studying about stacks, queues, binary search trees, trees, graphs, etc. for various applications. I am also studying about and working on algorithms like branch and bound, back tracking, breadth-first search, depth-first search, binary sort, topological sort, etc.

Why OGDF?

Have you already used OGDF before GSoC?

No, this is my first time.

Why are you interested in graph algorithms / graph drawing / your particular project?

One reason is that, I would like to implement algorithms used in steiner tree problems to quantum walks, i.e. trying to find the quantum version of those algorithms, so that it would be useful in quantum computing too.

The other reason is that, since I'm doing my master's in VLSI design, where steiner tree problems are crucial, I would like to learn more about those algorithms as part of GSoC 2014.

After GSoC, you envision your involvement with OGDF will be:

I would like to contribute various libraries for OGDF later too, especially implementing some of the latest improvised graph algorithms, say for shortest path, longest path, travelling salesman, steiner tree and for other such NP-hard problems. I really liked the responses that I got from the mentors of OGDF for my queries. So, I would like to work with them and contribute to the open source community through OGDF.

Additional Notes**Anything else you want to tell us?**

My semester gets over by 10th of May. So, I'll be free for the rest of the summer, i.e. May, June and July. Though my next semester starts in August, since I'll be in my final year, I'll just have two courses and no exams till September. So, I'm quite comfortable with coding the entire summer. In fact, I'm looking forward to it, so that I'll be sure that I'm occupied.