

GPIO Current Protection

nRF52
High Drive ($\geq 2.7\text{ V}$) \rightarrow 6-15 mA sink & drive
Std Drive ($\geq 1.7\text{ V}$) \rightarrow 1-4 mA sink & drive

MSP430FR59x
diode current @ pin max $\pm 2\text{ mA}$
 $I_{OHmax} = -6\text{ mA}$ @ 3V
 $I_{OLmax} = 6\text{ mA}$ @ 3V
"8.12.5.3 Typical Characteristics, Digital Outputs" show 0.5 V drop / increase @ 10 mA

74LVC2T45GT
has $\pm 24\text{ mA}$ drive & sink current

consequence #NAME? limit to 8-10 mA @ 4V with 470 R or 220 R

Target-Features

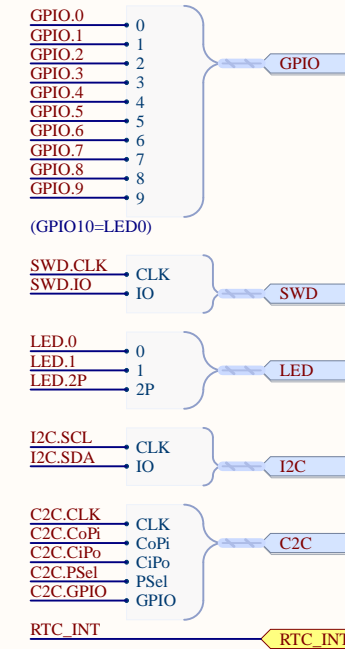
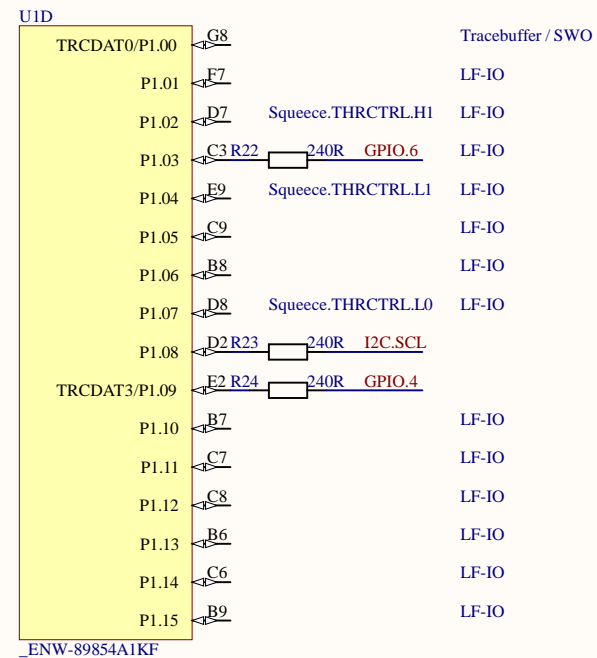
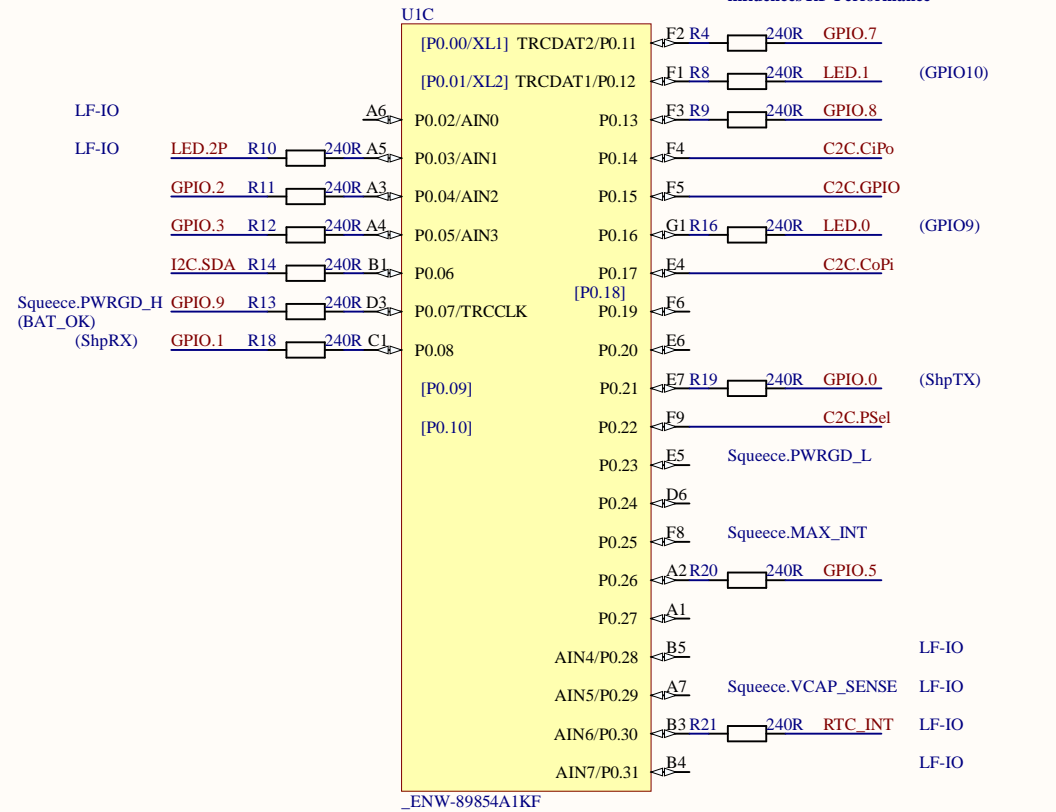
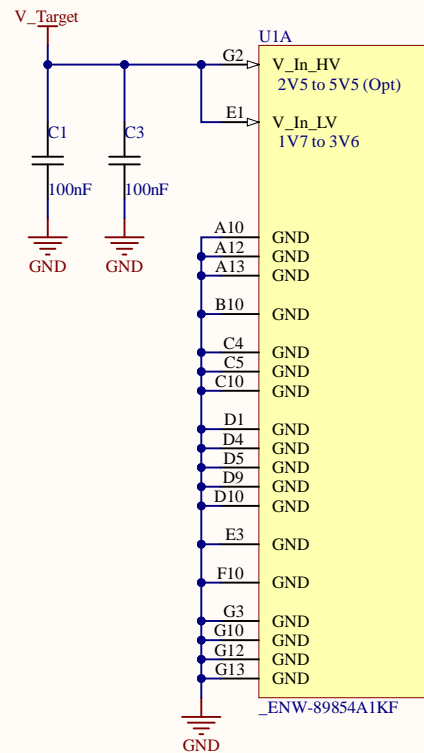
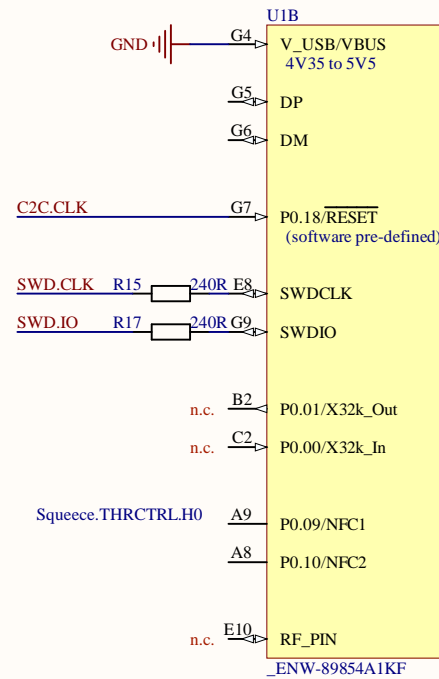
- over-voltage protection for V_LV (max 3.9V)
- two debug LEDs with separate supply
- one self-powered LED to "burn" energy
- io pins not interfering with RF (nRF PS v1.6 page 578)
- LEDs / UART similar to Riotee
- LEDs have minimal impact on pwr-budget

Host-PinHeader-Variants

- SSQ-109-02-G-D-RA \rightarrow right angled socket (default)
- SSQ-109-02-G-D \rightarrow straight socket
- 2x9-header allows to use ribbon-cable to connect to shepherd-cape

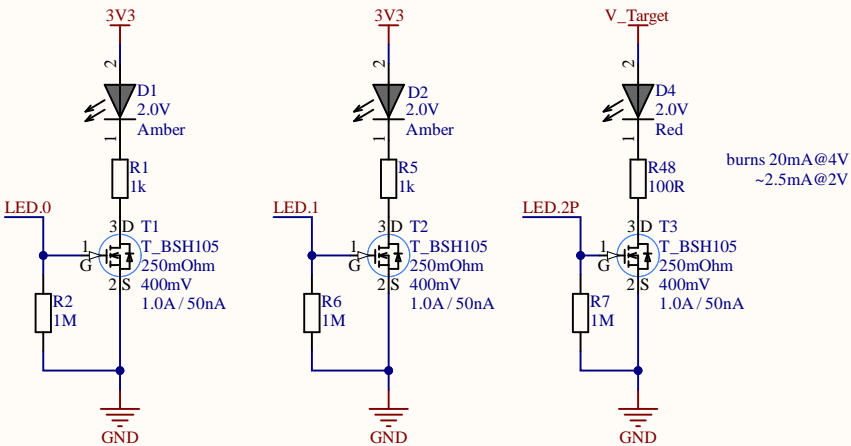
Title Target-Board Overview NES Lab / TU Dresden		
Size A4	Number	Revision
Date: 4.12.2023	Sheet of nRF_FRAM_Target.PrjPcb	
File: C:\Users\...\overview.SchDoc	Drawn By: Ingmar	

nRF52-Module

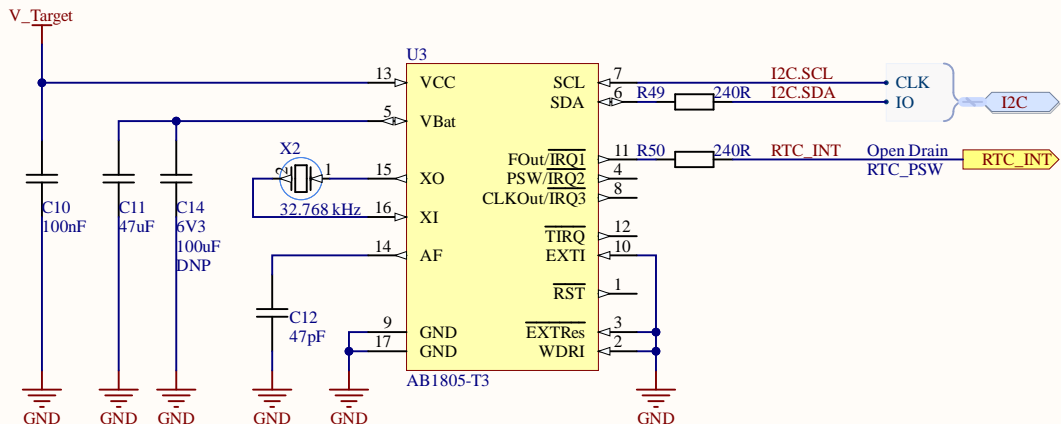


Title Target nRF52840 NES Lab / TU Dresden		
Size A3	Number	Revision
Date:	4.12.2023	Sheet of nRF_FRAM_Target.PrjPcb
File:	C:\Users\...\nRF52.SchDoc	Drawn By: Ingmar

Debug-LEDs



burns 20mA@4V
~2.5mA@2V

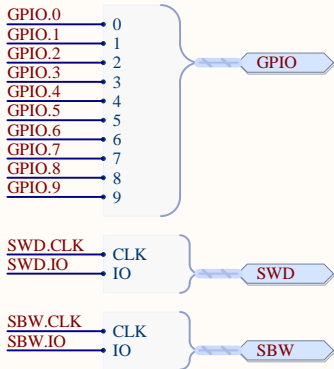
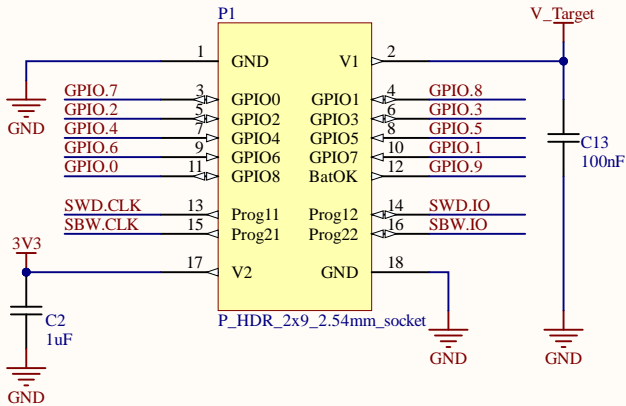


Title RTC & LEDs NES Lab / TU Dresden		
Size A4	Number	Revision
Date: 4.12.2023	Sheet of nRF_FRAM_Target.PrjPcb	
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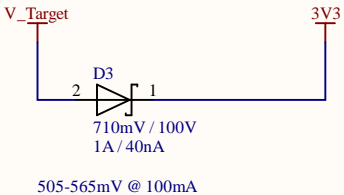
Cape-Port

signal-direction is from Host point of view (Target is Guest)

Switchable Directions:
Group A = GPIO 0:3
Group B = GPIO 8

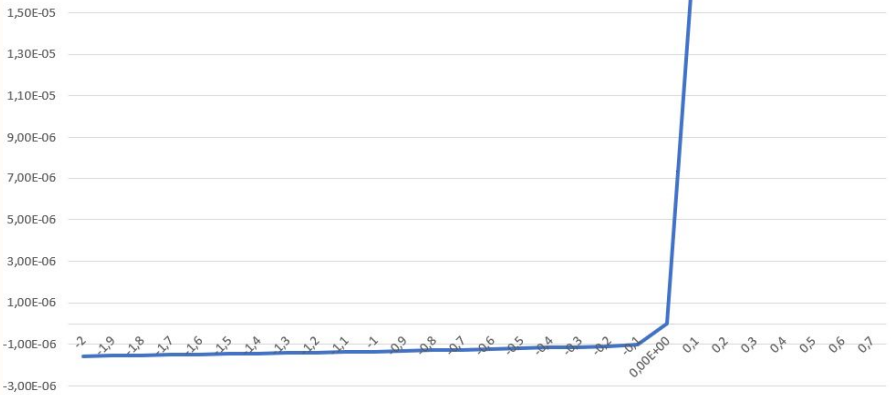


OVP

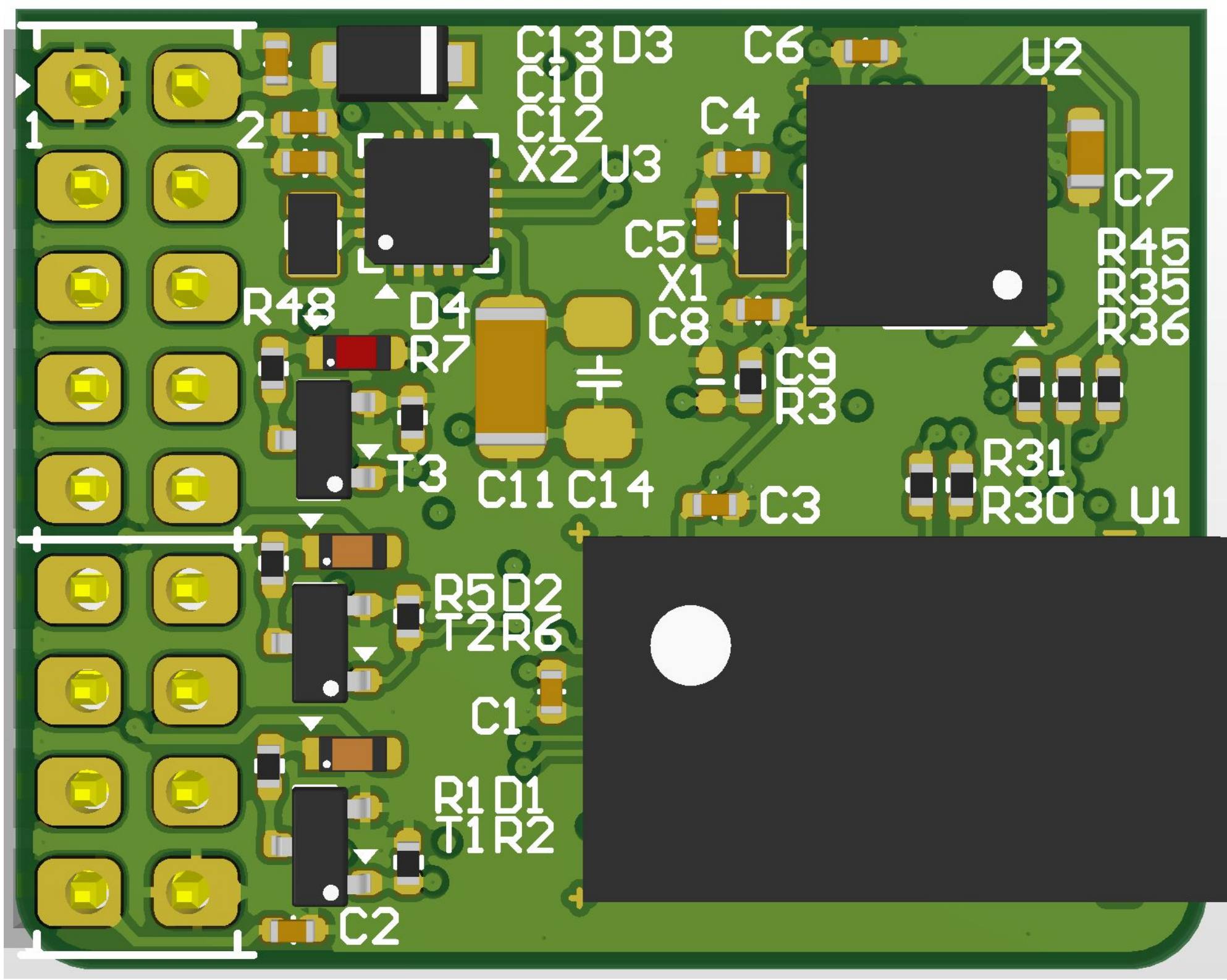


Abs Max Ratings:		One Diode:	
nRF52	3.9 V	+ 0.0 V ->	6 pA (noise)
RTC	3.8 V	+ 0.1 V ->	47 nA
MSP430	4.1 V	+ 0.2 V ->	2.3 uA
		+ 0.3 V ->	120 uA
		+ 0.4 V ->	4.83 mA

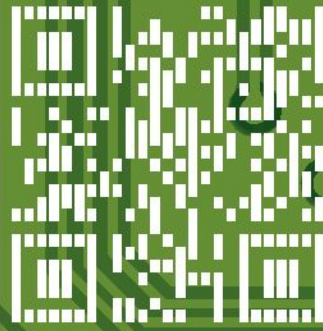
PMEG10010ELRX V3 [mA]



Title Board-Connector NES Lab / TU Dresden		
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File:	C:\Users\...\board_connector.SchDoc	Drawn By: Ingmar



NES Lab
nRF52-MSP
TGT v1.0r0



38x240R

