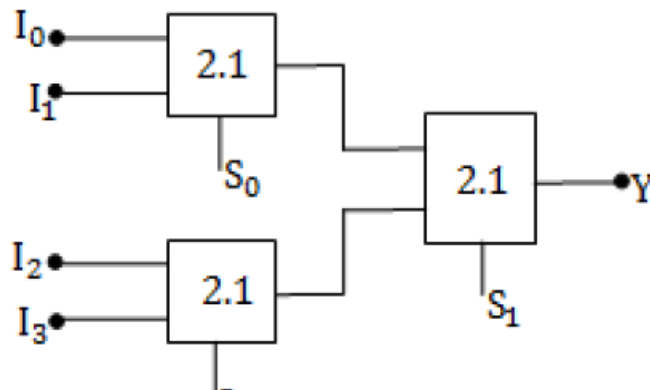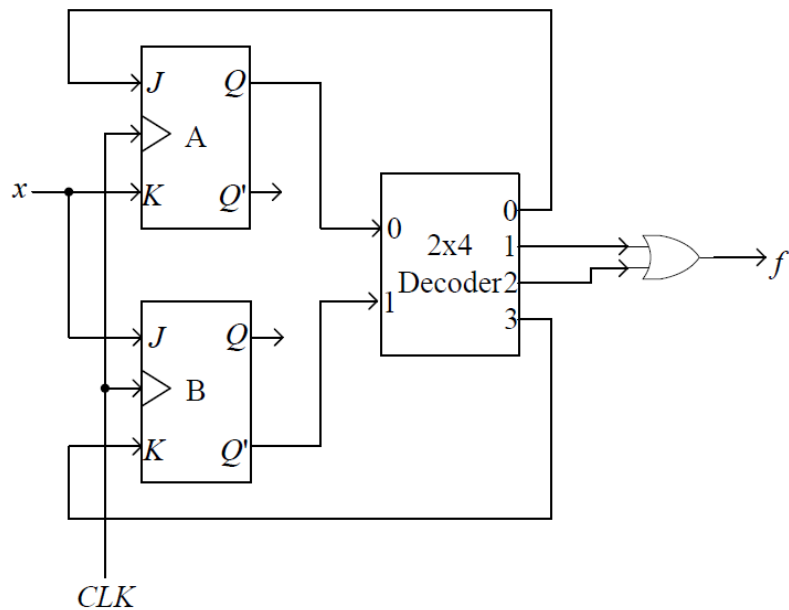# HOMEWORK II

1) Implement this design by using VHDL. Make its simulation in ISIM simulator. Input of the multiplexers should be 2-bit values. Hint: "2.1" stands for 2-to-1 Multiplexer.

2) Implement this design by using VHDL.

3) Implement this design by using VHDL.