

led. . N O MAKEUP TO +20dB POWER III MANGLER 22R->LUS V. -15VIII TO PANEL MOUNTED CONTROL SECTION . SSLK.PCB 0 SIDECHAIN (e) Me ter 5 OR 2×18V TL074 ט ט ט 'n 1000/35 470R (BYPASS)
SOLDERED ON WIRE WIRES TO BYPASS SW OR LINK ON PCB (DEFEAT MAKEUP WHEN OUT) - 15K 100K TL072 لاسط 180K **→ | |**-100F - 56K T.1 жΦ LEFT VCA 120R 7812* +157 2180-CIRCUIT OR DBX202C - 15 V VCA'S 1000/35 жΦ RIGHT 120R (2 t) • п п + 100p 2180-CIRCUI OR DBX2020 THIS IS COMPONENT SIDE -22 K- INPUT 151 1 100P **→ ⊢** 22P - 6WE 950K 1 M L ••••R 0 0 CONTROL PCB HRESHOLD ** = METER SENSITIVITY: 2K=20dB 1K=10dB FULL SCALE AT 1mA METER -20 TO +20dB ALL DIODES 1N4148 .1 = 100nF POLY 47K* (CONTROL BOARD)USE FOR REDUCING THRESHOLD SENS. 100R* OUTPUT RESISTOR. MAY BE SMALLER WITH OUTPUT TX 27K* CHANGED FROM 15K T0 27K - UNITY GAIN GYRAF AUDIO REVISION HISTORY / FIXED:
REV#5: L IN + SHORTED TO GND
REV#6: 220V TRAFO: FARNELL 976-635 OR RS 209-538
REV#7: CAP VALUES AT TL074 PADS W14-94
REV#8: PAD/TRACK SPACINO A/R spacing
REV#9: PAHANGED TO THAT2181 VCA'S - UNITY OAIN 27K'S
(COMPONENT OVERLAY CHANGES ONLY)
REV#10:12V reg after 15V reg
REV#11: CRC and HPF. 3K9>5K1 100K>120K +2x22R +2x1000uF +7812+22u
REV#12 Minor change - track at 22R/-15V 11 Date Rev no# SSL MIXBUS COMPRESSOR CLONE CONTROL SECT: SSLK.PCB Filename: SSL.PCB Size:100×160mm Mount

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