Orlando Mota Pires

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ACADEMIC

- Degree in Computer Engineering at SENAI/CIMATEC **2021 2025**
- High school at Colégio Cândido Portinari, Salvador, BA 2014 2020

EXPERIENCE

Academic monitor at Senai CIMATEC

Period: February 2022 - December 2022

Description: Monitor of the Algorithms and computational thinking disciplines at Senai Cimatec.

Senai CIMATEC Programming Club (co-founder)

Period: September 2021 - March 2024

Description: Teaching algorithms and computer logic to students from computer engineering and other courses and organizing competitive programming marathons like OBI and SBC, as well as managed the organization and maintenance of the Student Initiative.

Scientific initiation at Senai CIMATEC in the HPC Center.

Period: July 2022 - December 2022

Objective: Demonstrate the applicability of the DPC++ language in the development of scientific computational codes in academic and industrial contexts. **(The final paper can be found on the publication 1.)**

Scientific initiation at Senai CIMATEC in the HPC Center.

Period: January 2023 - December 2023

Objective: Creation of introductory python notebooks for learning basic neural networks and artificial intelligence concepts. **Project Github Repository Link**

Scientific initiation at Senai CIMATEC

Period: March 2024 - Currently

Objective: Modeling time series extracted from brain activity with Python and Identifying with autocorrelation techniques, pre-ictal or pre-seizure states in epileptic patients.

Intern Student at Senai CIMATEC

Period: March 2024 - Currently

Description: Intern Student at LAQCC (Latino America Quantum Computing Center) at Senai

CIMATEC

OTHERS

Intel Student ambassador

Period: March 2023 - Currently

Description: Acting as a student ambassador for Intel, I leverage Intel technologies within the university environment to enhance students' understanding and utilization of these advanced tools and technologies.

KNOWLEDGES

Programming Skills

- C++, Python, Java
- Git/Github, Docker, Linux
- DPCPP, CUDA, SYCL, openMPI, HPC and parallell programming

Languages

- Portuguese C1
- English B2
- German *A1*

PUBLICATIONS

Tuning a CPU-Based Stencil computation in a DPC++ Multi-Device Environment CONCEIÇÃO, T.; RODRIGUES, A.H, PIRES, O., SOARES, L. Tuning a CPU-Computation in a DPC++ Multi-Device Environment. Journal of Bioengineering, Technologies and Health, v. 10, p. 1-13, 2018.

OneAPI: an Approach for Developer-Centered HeterogeneousComputing

CONCEIÇÃO, T.; RODRIGUES,A.H, PIRES, O., SOARES, L.OneAPI: an Developer-CenteredApproach for HeterogeneousComputing.WSCAD22,v.10, p. 1-13, 2018.

Statistical Study of Eco-Efficiency in Compact and Average Cars (Chevrolet, Ford, VW, Fiat, Renault) in Brazil Based on the Metro Table in 2019

WENCESLAU, A; WIDMER, A; DUNKEL, G; SAMUEL, J; PIRES, O; NASCIMENTO, Aloisio. Statistical Study of Eco-Efficiency in Compact and Average Cars (Chevrolet, Ford, VW, Fiat, Renault) in Brazil Based on the Metro Table in 2019. Journal of Bioengineering, Technologies and Health, p. 1-7, 2019.