Portable Executable and Common Object File Format Specification with Extensions

Abstract

This specification describes the structure of executable (image) files and object files under the Windows® family of operating systems. These files are referred to as Portable Executable (PE) and Common Object File Format (COFF) files, respectively.

Note

This document is provided to aid in the development of tools and applications for Windows but is not guaranteed to be a complete specification in all respects. Microsoft reserves the right to alter this document without notice.

This revision of the Microsoft Portable Executable and Common Object File Format Specification replaces all previous revisions of this specification.

For the latest information, see:  
 <https://docs.microsoft.com/en-us/windows/win32/debug/pe-format>

Legal Notice

Microsoft Portable Executable and Common Object File Format Specification  
Microsoft Corporation  
Revision 10

Note: This specification is provided to aid in the development of certain development tools for the Microsoft Windows platform. However, Microsoft does not guarantee that it is a complete specification in all respects, and cannot guarantee the accuracy of any information presented after the date of publication. Microsoft reserves the right to alter this specification without notice.

Microsoft will grant a royalty-free license, under reasonable and non-discriminatory terms and conditions, to any Microsoft patent claims (if any exist) that Microsoft deems necessary for the limited purpose of implementing and complying with the required portions of this specification only in the software development tools known as compilers, linkers, and assemblers targeting Microsoft Windows.

Complying with all applicable copyright laws is the responsibility of the user. Without limiting the rights under copyright, no part of this specification may be reproduced, stored in or introduced into a retrieval system, modified or used in a derivative work, or transmitted in any form or by any means (electronic, mechanical, photocopying, recording, or otherwise), or for any purpose, without the express written permission of Microsoft.

Microsoft may have intellectual property rights covering subject matter in this specification. Except as expressly provided in any written license agreement from Microsoft, the furnishing of this specification does not give you any license to any intellectual property rights, and no other rights are granted by implication, estoppel, or otherwise.

© 2016 Microsoft Corporation. All rights reserved.

This specification is provided “AS IS.” Microsoft makes no representations or warranties, express, implied, or statutory, as (1) to the information in this specification, including any warranties of merchantability, fitness for a particular purpose, non-infringement, or title; (2) that the contents of this specification are suitable for any purpose; nor (3) that the implementation of such contents will not infringe any third party patents, copyrights, trademarks, or other rights.

Microsoft will not be liable for any direct, indirect, special, incidental, or consequential damages arising out of or relating to any use or distribution of this specification.

Microsoft, MS-DOS, MSDN, Visual Studio, Visual C++, Win32, Windows, Windows NT, Windows Server, and Windows Vista are either registered trademarks or trademarks of Microsoft Corporation in the United States and/or other countries. Other product and company names mentioned herein may be the trademarks of their respective owners.

The foregoing names and trademarks may not be used in any manner, including advertising or publicity pertaining to this specification or its contents without specific, written prior permission from the respective owners.

Contents

[1. General Concepts 7](#_Toc117863093)

[2. Overview 8](#_Toc117863094)

[3. File Headers 8](#_Toc117863095)

[3.1. MS‑DOS Stub (Image Only) 9](#_Toc117863096)

[3.2. Signature (Image Only) 9](#_Toc117863097)

[3.3. COFF File Header (Object and Image) 9](#_Toc117863098)

[3.3.1. Machine Types 9](#_Toc117863099)

[3.3.2. Characteristics 10](#_Toc117863100)

[3.4. Optional Header (Image Only) 10](#_Toc117863101)

[3.4.1. Optional Header Standard Fields (Image Only) 11](#_Toc117863102)

[3.4.2. Optional Header Windows-Specific Fields (Image Only) 11](#_Toc117863103)

[3.4.3. Optional Header Data Directories (Image Only) 13](#_Toc117863104)

[4. Section Table (Section Headers) 14](#_Toc117863105)

[4.1. Section Flags 15](#_Toc117863106)

[4.2. Grouped Sections (Object Only) 16](#_Toc117863107)

[5. Other Contents of the File 16](#_Toc117863108)

[5.1. Section Data 17](#_Toc117863109)

[5.2. COFF Relocations (Object Only) 17](#_Toc117863110)

[5.2.1. Type Indicators 17](#_Toc117863111)

[5.3. COFF Line Numbers (Deprecated) 24](#_Toc117863112)

[5.4. COFF Symbol Table 24](#_Toc117863113)

[5.4.1. Symbol Name Representation 25](#_Toc117863114)

[5.4.2. Section Number Values 25](#_Toc117863115)

[5.4.3. Type Representation 25](#_Toc117863116)

[5.4.4. Storage Class 26](#_Toc117863117)

[5.5. Auxiliary Symbol Records 27](#_Toc117863118)

[5.5.1. Auxiliary Format 1: Function Definitions 27](#_Toc117863119)

[5.5.2. Auxiliary Format 2: .bf and .ef Symbols 28](#_Toc117863120)

[5.5.3. Auxiliary Format 3: Weak Externals 28](#_Toc117863121)

[5.5.4. Auxiliary Format 4: Files 28](#_Toc117863122)

[5.5.5. Auxiliary Format 5: Section Definitions 29](#_Toc117863123)

[5.5.6. COMDAT Sections (Object Only) 29](#_Toc117863124)

[5.5.7. CLR Token Definition (Object Only) 30](#_Toc117863125)

[5.6. COFF String Table 30](#_Toc117863126)

[5.7. The Attribute Certificate Table (Image Only) 30](#_Toc117863127)

[5.7.1. Certificate Data 31](#_Toc117863128)

[5.8. Delay-Load Import Tables (Image Only) 32](#_Toc117863129)

[5.8.1. The Delay-Load Directory Table 32](#_Toc117863130)

[5.8.2. Attributes 32](#_Toc117863131)

[5.8.3. Name 32](#_Toc117863132)

[5.8.4. Module Handle 32](#_Toc117863133)

[5.8.5. Delay Import Address Table 32](#_Toc117863134)

[5.8.6. Delay Import Name Table 33](#_Toc117863135)

[5.8.7. Delay Bound Import Address Table and Time Stamp 33](#_Toc117863136)

[5.8.8. Delay Unload Import Address Table 33](#_Toc117863137)

[6. Special Sections 33](#_Toc117863138)

[6.1. The .debug Section 34](#_Toc117863139)

[6.1.1. Debug Directory (Image Only) 34](#_Toc117863140)

[6.1.2. Debug Type 35](#_Toc117863141)

[6.1.3. .debug$F (Object Only) 36](#_Toc117863142)

[6.1.4. .debug$S (Object Only) 36](#_Toc117863143)

[6.1.5. .debug$P (Object Only) 36](#_Toc117863144)

[6.1.6. .debug$T (Object Only) 36](#_Toc117863145)

[6.1.7. Linker Support for Microsoft Debug Information 36](#_Toc117863146)

[6.2. The .drectve Section (Object Only) 36](#_Toc117863147)

[6.3. The .edata Section (Image Only) 36](#_Toc117863148)

[6.3.1. Export Directory Table 37](#_Toc117863149)

[6.3.2. Export Address Table 37](#_Toc117863150)

[6.3.3. Export Name Pointer Table 38](#_Toc117863151)

[6.3.4. Export Ordinal Table 38](#_Toc117863152)

[6.3.5. Export Name Table 38](#_Toc117863153)

[6.4. The .idata Section 39](#_Toc117863154)

[6.4.1. Import Directory Table 39](#_Toc117863155)

[6.4.2. Import Lookup Table 39](#_Toc117863156)

[6.4.3. Hint/Name Table 40](#_Toc117863157)

[6.4.4. Import Address Table 40](#_Toc117863158)

[6.5. The .pdata Section 40](#_Toc117863159)

[6.6. The .reloc Section (Image Only) 40](#_Toc117863160)

[6.6.1. Base Relocation Block 41](#_Toc117863161)

[6.6.2. Base Relocation Types 41](#_Toc117863162)

[6.7. The .tls Section 43](#_Toc117863163)

[6.7.1. The TLS Directory 43](#_Toc117863164)

[6.7.2. TLS Callback Functions 44](#_Toc117863165)

[6.8. The Load Configuration Structure (Image Only) 44](#_Toc117863166)

[6.8.1. Load Configuration Directory 44](#_Toc117863167)

[6.8.2. Load Configuration Layout 45](#_Toc117863168)

[6.9. The .rsrc Section 46](#_Toc117863169)

[6.9.1. Resource Directory Table 47](#_Toc117863170)

[6.9.2. Resource Directory Entries 47](#_Toc117863171)

[6.9.3. Resource Directory String 47](#_Toc117863172)

[6.9.4. Resource Data Entry 47](#_Toc117863173)

[6.10. The .cormeta Section (Object Only) 48](#_Toc117863174)

[6.11. The .sxdata Section 48](#_Toc117863175)

[7. Archive (Library) File Format 48](#_Toc117863176)

[7.1. Archive File Signature 49](#_Toc117863177)

[7.2. Archive Member Headers 49](#_Toc117863178)

[7.3. First Linker Member 50](#_Toc117863179)

[7.4. Second Linker Member 50](#_Toc117863180)

[7.5. Longnames Member 51](#_Toc117863181)

[8. Import Library Format 51](#_Toc117863182)

[8.1. Import Header 51](#_Toc117863183)

[8.2. Import Type 52](#_Toc117863184)

[8.3. Import Name Type 52](#_Toc117863185)

[9. TD32 52](#_Toc117863186)

[9.1. TD32FileSignature 52](#_Toc117863187)

[9.2. TD32DirectoryEntry 52](#_Toc117863188)

[9.3. TD32DirectoryHeader 52](#_Toc117863189)

[10. Symbol and Type Information 54](#_Toc117863190)

[10.1. Logical Segments 54](#_Toc117863191)

[10.2. Lexical Scope Linkage 54](#_Toc117863192)

[10.3. Numeric Leaves 55](#_Toc117863193)

[10.4. Types Indices 55](#_Toc117863194)

[10.5. $$SYMBOLS and $$TYPES Definitions 55](#_Toc117863195)

[10.5.1. $$TYPES Definition 55](#_Toc117863196)

[10.5.2. $$SYMBOLS Definition 55](#_Toc117863197)

[11. Symbols 56](#_Toc117863198)

[11.1. General 56](#_Toc117863199)

[Format of Symbol Records 56](#_Toc117863200)

[Symbol Indices 57](#_Toc117863201)

[11.2. Non-modal Symbols 60](#_Toc117863202)

[(0x0001) Compile Flag 60](#_Toc117863203)

[(0x0002) Register 60](#_Toc117863204)

[(0x0003) Constant 60](#_Toc117863205)

[(0x0004) User-defined Type 60](#_Toc117863206)

[(0x0005) Start Search 60](#_Toc117863207)

[(0x0006) End of Block 60](#_Toc117863208)

[(0x0007) Skip Record 60](#_Toc117863209)

[(0x0008) Microsoft Debugger Internal 60](#_Toc117863210)

[(0x0009) Object File Name 60](#_Toc117863211)

[(0x000a) End of Arguments 60](#_Toc117863212)

[(0x000b) COBOL User-defined Type 60](#_Toc117863213)

[(0x000c) Many Registers 60](#_Toc117863214)

[(0x000d) Function Return 60](#_Toc117863215)

[(0x000e) this at Method Entry 60](#_Toc117863216)

[11.3. Symbols for 16:16 Segmented Architectures 60](#_Toc117863217)

[(0x0100) BP Relative 16:16 60](#_Toc117863218)

[(0x0101) Local Data 16:16 60](#_Toc117863219)

[(0x0102) Global Data Symbol 16:16 60](#_Toc117863220)

[(0x0103) Public Symbol 16:16 60](#_Toc117863221)

[(0x0104) Local Start 16:16 61](#_Toc117863222)

[(0x0105) Global Procedure Start 16:16 61](#_Toc117863223)

[(0x0106) Thunk Start 16:16 61](#_Toc117863224)

[(0x0107) Block Start 16:16 61](#_Toc117863225)

[(0x0108) With Start 16:16 61](#_Toc117863226)

[(0x0109) Code Label 16:16 61](#_Toc117863227)

[(0x010a) Change Execution Model 16:16 61](#_Toc117863228)

[(0x010b) Virtual Function Table Path 16:16 61](#_Toc117863229)

[(0x010c) Register Relative 16:16 61](#_Toc117863230)

[11.4. Symbols for 16:32 Segmented Architectures 61](#_Toc117863231)

[(0x0200) BP Relative 16:32 61](#_Toc117863232)

[(0x0201) Local Data 16:32 61](#_Toc117863233)

[(0x0202) Global Data Symbol 16:32 61](#_Toc117863234)

[(0x0203) Public 16:32 61](#_Toc117863235)

[(0x0204) Local Procedure Start 16:32 61](#_Toc117863236)

[(0x0205) Global Procedure Start 16:32 61](#_Toc117863237)

[(0x0206) Thunk Start 16:32 61](#_Toc117863238)

[(0x0207) Block Start 16:32 61](#_Toc117863239)

[(0x0208) With Start 16:32 61](#_Toc117863240)

[(0x0209) Code Label 16:32 61](#_Toc117863241)

[(0x020a) Change Execution Model 16:32 61](#_Toc117863242)

[(0x020b) Virtual Function Table Path 16:32 61](#_Toc117863243)

[(0x020c) Register Relative 16:32 61](#_Toc117863244)

[(0x020d) Local Thread Storage 16:32 61](#_Toc117863245)

[(0x020e) Global Thread Storage 16:32 61](#_Toc117863246)

[11.5. Symbols for MIPS Architectures 61](#_Toc117863247)

[(0x0300) Local Procedure Start MIPS 61](#_Toc117863248)

[(0x0301) Global Procedure Start MIPS 61](#_Toc117863249)

[11.6. Symbols for CVPACK Optimization 62](#_Toc117863250)

[(0x0400) Procedure Reference 62](#_Toc117863251)

[(0x0401) Data Reference 62](#_Toc117863252)

[(0x0402) Symbol Page Alignment 62](#_Toc117863253)

[12. Types Definition Segment ($$TYPES) 62](#_Toc117863254)

[12.1. Type Record 62](#_Toc117863255)

[12.2. Type String 62](#_Toc117863256)

[Member Attribute Field 64](#_Toc117863257)

[12.3. Leaf Indices Referenced from Symbols 65](#_Toc117863258)

[12.4. Type Records Referenced from Type Records 65](#_Toc117863259)

[12.5. Subfields of Complex Lists 65](#_Toc117863260)

[13. Numeric Leaves 65](#_Toc117863261)

[14. Predefined Primitive Types 65](#_Toc117863262)

[14.1. Format of Reserved Types 65](#_Toc117863263)

[14.2. Primitive Type Listing 65](#_Toc117863264)

[15. Register Enumerations 65](#_Toc117863265)

[15.1. Intel 80x86/80x87 Architectures 65](#_Toc117863266)

[15.2. Motorola 68000 Architectures 65](#_Toc117863267)

[15.3. MIPS Architectures 65](#_Toc117863268)

[16. Symbol and Type Format for Microsoft Executables 65](#_Toc117863269)

[16.1. Introduction 65](#_Toc117863270)

[16.2. Debug Information Format 65](#_Toc117863271)

[OMF 65](#_Toc117863272)

[PE Format 66](#_Toc117863273)

[16.3. Subsection Directory 66](#_Toc117863274)

[16.4. SubSection Types (sst...) 68](#_Toc117863275)

[(0x0120) sstModule 68](#_Toc117863276)

[(0x0121) sstTypes 69](#_Toc117863277)

[(0x0122) sstPublic 69](#_Toc117863278)

[(0x0123) sstPublicSym 69](#_Toc117863279)

[(0x0124) sstSymbols 69](#_Toc117863280)

[(0x0125) sstAlignSym 69](#_Toc117863281)

[(0x0126) sstSrcLnSeg 69](#_Toc117863282)

[(0x0127) sstSrcModule 69](#_Toc117863283)

[(0x0128) sstLibraries 71](#_Toc117863284)

[(0x0129) sstGlobalSym 71](#_Toc117863285)

[(0x012a) sstGlobalPub 71](#_Toc117863286)

[(0x012b) sstGlobalTypes 71](#_Toc117863287)

[(0x012c) sstMPC 72](#_Toc117863288)

[(0x012d) sstSegMap 72](#_Toc117863289)

[(0x012e) sstSegName 73](#_Toc117863290)

[(0x012f) sstPreComp 73](#_Toc117863291)

[(0x0133) sstFileIndex 73](#_Toc117863292)

[(0x0134) sstStaticSym 74](#_Toc117863293)

[16.5. Hash table and sort table descriptions 74](#_Toc117863294)

[Appendix A: Calculating Authenticode PE Image Hash 76](#_Toc117863295)

[A.1 What is an Authenticode PE Image Hash? 76](#_Toc117863296)

[A.2 What is Covered in an Authenticode PE Image Hash? 76](#_Toc117863297)

[Appendix B: SizeOf 77](#_Toc117863298)

[B.1 Delphi SizeOf 77](#_Toc117863299)

[B.2 C++ SizeOf 78](#_Toc117863300)

[References 79](#_Toc117863301)

# General Concepts

This document specifies the structure of executable (image) files and object files under the Microsoft® Windows® family of operating systems. These files are referred to as Portable Executable (PE) and Common Object File Format (COFF) files, respectively. The name “Portable Executable” refers to the fact that the format is not architecture specific.

Certain concepts that appear throughout this specification are described in the following table:

| **Name** | **Description** |
| --- | --- |
| attribute certificate | A certificate that is used to associate verifiable statements with an image. A number of different verifiable statements can be associated with a file; one of the most useful ones is a statement by a software manufacturer that indicates what the message digest of the image is expected to be. A message digest is similar to a checksum except that it is extremely difficult to forge. Therefore, it is very difficult to modify a file to have the same message digest as the original file. The statement can be verified as being made by the manufacturer by using public or private key cryptography schemes. This document describes details about attribute certificates other than to allow for their insertion into image files. |
| date/time stamp | A stamp that is used for different purposes in several places in a PE or COFF file. In most cases, the format of each stamp is the same as that used by the time functions in the C run-time library. For exceptions, see the descripton of **IMAGE\_DEBUG\_TYPE\_REPRO** (section 6.1.2). If the stamp value is 0 or 0xFFFFFFFF, it does not represent a real or meaningful date/time stamp. |
| file pointer | The location of an item within the file itself, before being processed by the linker (in the case of object files) or the loader (in the case of image files). In other words, this is a position within the file as stored on disk. |
| linker | A reference to the linker that is provided with Microsoft Visual Studio®. |
| object file | A file that is given as input to the linker. The linker produces an image file, which in turn is used as input by the loader. The term “object file” does not necessarily imply any connection to object-oriented programming. |
| reserved, must be 0 | A description of a field that indicates that the value of the field must be zero for generators and consumers must ignore the field. |
| RVA | Relative virtual address. In an image file, the address of an item after it is loaded into memory, with the base address of the image file subtracted from it. The RVA of an item almost always differs from its position within the file on disk (file pointer).  In an object file, an RVA is less meaningful because memory locations are not assigned. In this case, an RVA would be an address within a section (described later in this table), to which a relocation is later applied during linking. For simplicity, a compiler should just set the first RVA in each section to zero. |
| section | The basic unit of code or data within a PE or COFF file. For example, all code in an object file can be combined within a single section or (depending on compiler behavior) each function can occupy its own section. With more sections, there is more file overhead, but the linker is able to link in code more selectively. A section is similar to a segment in Intel 8086 architecture. All the raw data in a section must be loaded contiguously. In addition, an image file can contain a number of sections, such as .tls or .reloc, which have special purposes. |
| VA | virtual address. Same as RVA, except that the base address of the image file is not subtracted. The address is called a “VA” because Windows creates a distinct VA space for each process, independent of physical memory. For almost all purposes, a VA should be considered just an address. A VA is not as predictable as an RVA because the loader might not load the image at its preferred location. |

# Overview

Figure 1 illustrates the Microsoft PE executable format.

|  |  |  |
| --- | --- | --- |
| MS-DOS 2.0 Compatible  EXE Header |  | Base of Image Header |
| unused |  |  |
| OEM Identifier  OEM Information  Offset to PE Header |  | MS‑DOS 2.0 Section  (for MS­‑DOS compatibility only) |
| MS‑DOS 2.0 Stub Program  and  Relocation Table |  |  |
| unused |  |  |
| PE Header  (aligned on 8-byte boundary) |  |  |
| Section Headers |  |  |
| Image Pages:  import info  export info  base relocations  resource info |  |  |

Figure 1. Typical Portable EXE File Layout

Figure 2 illustrates the Microsoft COFF object-module format:

|  |  |  |
| --- | --- | --- |
| Microsoft COFF Header |  |  |
| Section Headers |  |  |
| Raw Data:  code  data  debug info  relocations |  |  |

Figure 2. Typical COFF Object Module Layout

# File Headers

The PE file header consists of a Microsoft MS‑DOS® stub, the PE signature, the COFF file header, and an optional header. A COFF object file header consists of a COFF file header and an optional header. In both cases, the file headers are followed immediately by section headers.

## MS‑DOS Stub (Image Only)

The MS‑DOS stub is a valid application that runs under MS‑DOS. It is placed at the front of the EXE image. The linker places a default stub here, which prints out the message “This program cannot be run in DOS mode” when the image is run in MS‑DOS. The user can specify a different stub by using the /STUB linker option.

At location 0x3c, the stub has the file offset to the PE signature. This information enables Windows to properly execute the image file, even though it has an MS‑DOS stub. This file offset is placed at location 0x3c during linking.

## Signature (Image Only)

After the MS‑DOS stub, at the file offset specified at offset 0x3c, is a 4-byte signature that identifies the file as a PE format image file. This signature is “PE\0\0” (the letters “P” and “E” followed by two null bytes).

## COFF File Header (Object and Image)

At the beginning of an object file, or immediately after the signature of an image file, is a standard COFF file header in the following format. Note that the Windows loader limits the number of sections to 96.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | Machine | The number that identifies the type of target machine. For more information, see section 3.3.1, “Machine Types.” |
| 2 | 2 | NumberOfSections | The number of sections. This indicates the size of the section table, which immediately follows the headers. |
| 4 | 4 | TimeDateStamp | The low 32 bits of the number of seconds since 00:00 January 1, 1970 (a C run-time time\_t value), that indicates when the file was created. |
| 8 | 4 | PointerToSymbolTable | The file offset of the COFF symbol table, or zero if no COFF symbol table is present. This value should be zero for an image because COFF debugging information is deprecated. |
| 12 | 4 | NumberOfSymbols | The number of entries in the symbol table. This data can be used to locate the string table, which immediately follows the symbol table. This value should be zero for an image because COFF debugging information is deprecated. |
| 16 | 2 | SizeOfOptionalHeader | The size of the optional header, which is required for executable files but not for object files. This value should be zero for an object file. For a description of the header format, see section 3.4, “Optional Header (Image Only).” |
| 18 | 2 | Characteristics | The flags that indicate the attributes of the file. For specific flag values, see section 3.3.2, “Characteristics.” |

### Machine Types

The Machine field has one of the following values that specifies its CPU type. An image file can be run only on the specified machine or on a system that emulates the specified machine.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_FILE\_MACHINE\_UNKNOWN | 0x0 | The contents of this field are assumed to be applicable to any machine type |
| IMAGE\_FILE\_MACHINE\_AM33 | 0x1d3 | Matsushita AM33 |
| IMAGE\_FILE\_MACHINE\_AMD64 | 0x8664 | x64 |
| IMAGE\_FILE\_MACHINE\_ARM | 0x1c0 | ARM little endian |
| IMAGE\_FILE\_MACHINE\_ARM64 | 0xaa64 | ARM64 little endian |
| IMAGE\_FILE\_MACHINE\_ARMNT | 0x1c4 | ARM Thumb-2 little endian |
| IMAGE\_FILE\_MACHINE\_EBC | 0xebc | EFI byte code |
| IMAGE\_FILE\_MACHINE\_I386 | 0x14c | Intel 386 or later processors and compatible processors |
| IMAGE\_FILE\_MACHINE\_IA64 | 0x200 | Intel Itanium processor family |
| IMAGE\_FILE\_MACHINE\_M32R | 0x9041 | Mitsubishi M32R little endian |
| IMAGE\_FILE\_MACHINE\_MIPS16 | 0x266 | MIPS16 |
| IMAGE\_FILE\_MACHINE\_MIPSFPU | 0x366 | MIPS with FPU |
| IMAGE\_FILE\_MACHINE\_MIPSFPU16 | 0x466 | MIPS16 with FPU |
| IMAGE\_FILE\_MACHINE\_POWERPC | 0x1f0 | Power PC little endian |
| IMAGE\_FILE\_MACHINE\_POWERPCFP | 0x1f1 | Power PC with floating point support |
| IMAGE\_FILE\_MACHINE\_R4000 | 0x166 | MIPS little endian |
| IMAGE\_FILE\_MACHINE\_RISCV32 | 0x5032 | RISC-V 32-bit address space |
| IMAGE\_FILE\_MACHINE\_RISCV64 | 0x5064 | RISC-V 64-bit address space |
| IMAGE\_FILE\_MACHINE\_RISCV128 | 0x5128 | RISC-V 128-bit address space |
| IMAGE\_FILE\_MACHINE\_SH3 | 0x1a2 | Hitachi SH3 |
| IMAGE\_FILE\_MACHINE\_SH3DSP | 0x1a3 | Hitachi SH3 DSP |
| IMAGE\_FILE\_MACHINE\_SH4 | 0x1a6 | Hitachi SH4 |
| IMAGE\_FILE\_MACHINE\_SH5 | 0x1a8 | Hitachi SH5 |
| IMAGE\_FILE\_MACHINE\_THUMB | 0x1c2 | Thumb |
| IMAGE\_FILE\_MACHINE\_WCEMIPSV2 | 0x169 | MIPS little-endian WCE v2 |

### Characteristics

The Characteristics field contains flags that indicate attributes of the object or image file. The following flags are currently defined:

| **Flag** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_FILE\_RELOCS\_STRIPPED | 0x0001 | Image only, Windows CE, and Microsoft Windows NT® and later. This indicates that the file does not contain base relocations and must therefore be loaded at its preferred base address. If the base address is not available, the loader reports an error. The default behavior of the linker is to strip base relocations from executable (EXE) files. |
| IMAGE\_FILE\_EXECUTABLE\_IMAGE | 0x0002 | Image only. This indicates that the image file is valid and can be run. If this flag is not set, it indicates a linker error. |
| IMAGE\_FILE\_LINE\_NUMS\_STRIPPED | 0x0004 | COFF line numbers have been removed. This flag is deprecated and should be zero. |
| IMAGE\_FILE\_LOCAL\_SYMS\_STRIPPED | 0x0008 | COFF symbol table entries for local symbols have been removed. This flag is deprecated and should be zero. |
| IMAGE\_FILE\_AGGRESSIVE\_WS\_TRIM | 0x0010 | Obsolete. Aggressively trim working set. This flag is deprecated for Windows 2000 and later and must be zero. |
| IMAGE\_FILE\_LARGE\_ADDRESS\_ AWARE | 0x0020 | Application can handle > 2‑GB addresses. |
|  | 0x0040 | This flag is reserved for future use. |
| IMAGE\_FILE\_BYTES\_REVERSED\_LO | 0x0080 | Little endian: the least significant bit (LSB) precedes the most significant bit (MSB) in memory. This flag is deprecated and should be zero. |
| IMAGE\_FILE\_32BIT\_MACHINE | 0x0100 | Machine is based on a 32-bit-word architecture. |
| IMAGE\_FILE\_DEBUG\_STRIPPED | 0x0200 | Debugging information is removed from the image file. |
| IMAGE\_FILE\_REMOVABLE\_RUN\_ FROM\_SWAP | 0x0400 | If the image is on removable media, fully load it and copy it to the swap file. |
| IMAGE\_FILE\_NET\_RUN\_FROM\_SWAP | 0x0800 | If the image is on network media, fully load it and copy it to the swap file. |
| IMAGE\_FILE\_SYSTEM | 0x1000 | The image file is a system file, not a user program. |
| IMAGE\_FILE\_DLL | 0x2000 | The image file is a dynamic-link library (DLL). Such files are considered executable files for almost all purposes, although they cannot be directly run. |
| IMAGE\_FILE\_UP\_SYSTEM\_ONLY | 0x4000 | The file should be run only on a uniprocessor machine. |
| IMAGE\_FILE\_BYTES\_REVERSED\_HI | 0x8000 | Big endian: the MSB precedes the LSB in memory. This flag is deprecated and should be zero. |

## Optional Header (Image Only)

Every image file has an optional header that provides information to the loader. This header is optional in the sense that some files (specifically, object files) do not have it. For image files, this header is required. An object file can have an optional header, but generally this header has no function in an object file except to increase its size.

Note that the size of the optional header is not fixed. The SizeOfOptionalHeader field in the COFF header must be used to validate that a probe into the file for a particular data directory does not go beyond SizeOfOptionalHeader. For more information, see section 3.3, “COFF File Header (Object and Image).”

The NumberOfRvaAndSizes field of the optional header should also be used to ensure that no probe for a particular data directory entry goes beyond the optional header. In addition, it is important to validate the optional header magic number for format compatibility.

The optional header magic number determines whether an image is a PE32 or PE32+ executable.

| **Magic number** | **PE format** |
| --- | --- |
| 0x10b | PE32 |
| 0x20b | PE32+ |

PE32+ images allow for a 64-bit address space while limiting the image size to 2 gigabytes. Other PE32+ modifications are addressed in their respective sections.

The optional header itself has three major parts.

| **Offset (PE32/PE32+)** | **Size (PE32/PE32+)** | **Header part** | **Description** |
| --- | --- | --- | --- |
| 0 | 28/24 | Standard fields | Fields that are defined for all implementations of COFF, including UNIX. |
| 28/24 | 68/88 | Windows-specific fields | Additional fields to support specific features of Windows (for example, subsystems). |
| 96/112 | Variable | Data directories | Address/size pairs for special tables that are found in the image file and are used by the operating system (for example, the import table and the export table). |

### Optional Header Standard Fields (Image Only)

The first eight fields of the optional header are standard fields that are defined for every implementation of COFF. These fields contain general information that is useful for loading and running an executable file. They are unchanged for the PE32+ format.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | Magic | The unsigned integer that identifies the state of the image file. The most common number is 0x10B, which identifies it as a normal executable file. 0x107 identifies it as a ROM image, and 0x20B identifies it as a PE32+ executable. |
| 2 | 1 | MajorLinkerVersion | The linker major version number. |
| 3 | 1 | MinorLinkerVersion | The linker minor version number. |
| 4 | 4 | SizeOfCode | The size of the code (text) section, or the sum of all code sections if there are multiple sections. |
| 8 | 4 | SizeOfInitializedData | The size of the initialized data section, or the sum of all such sections if there are multiple data sections. |
| 12 | 4 | SizeOfUninitializedData | The size of the uninitialized data section (BSS), or the sum of all such sections if there are multiple BSS sections. |
| 16 | 4 | AddressOfEntryPoint | The address of the entry point relative to the image base when the executable file is loaded into memory. For program images, this is the starting address. For device drivers, this is the address of the initialization function. An entry point is optional for DLLs. When no entry point is present, this field must be zero. |
| 20 | 4 | BaseOfCode | The address that is relative to the image base of the beginning-of-code section when it is loaded into memory. |

PE32 contains this additional field, which is absent in PE32+, following BaseOfCode.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 24 | 4 | BaseOfData | The address that is relative to the image base of the beginning-of-data section when it is loaded into memory. |

### Optional Header Windows-Specific Fields (Image Only)

The next 21 fields are an extension to the COFF optional header format. They contain additional information that is required by the linker and loader in Windows.

| **Offset (PE32/ PE32+)** | **Size (PE32/ PE32+)** | **Field** | **Description** |
| --- | --- | --- | --- |
| 28/24 | 4/8 | ImageBase | The preferred address of the first byte of image when loaded into memory; must be a multiple of 64 K. The default for DLLs is 0x10000000. The default for Windows CE EXEs is 0x00010000. The default for Windows NT, Windows 2000, Windows XP, Windows 95, Windows 98, and Windows Me is 0x00400000. |
| 32/32 | 4 | SectionAlignment | The alignment (in bytes) of sections when they are loaded into memory. It must be greater than or equal to FileAlignment. The default is the page size for the architecture. |
| 36/36 | 4 | FileAlignment | The alignment factor (in bytes) that is used to align the raw data of sections in the image file. The value should be a power of 2 between 512 and 64 K, inclusive. The default is 512. If the SectionAlignment is less than the architecture’s page size, then FileAlignment must match SectionAlignment. |
| 40/40 | 2 | MajorOperatingSystemVersion | The major version number of the required operating system. |
| 42/42 | 2 | MinorOperatingSystemVersion | The minor version number of the required operating system. |
| 44/44 | 2 | MajorImageVersion | The major version number of the image. |
| 46/46 | 2 | MinorImageVersion | The minor version number of the image. |
| 48/48 | 2 | MajorSubsystemVersion | The major version number of the subsystem. |
| 50/50 | 2 | MinorSubsystemVersion | The minor version number of the subsystem. |
| 52/52 | 4 | Win32VersionValue | Reserved, must be zero. |
| 56/56 | 4 | SizeOfImage | The size (in bytes) of the image, including all headers, as the image is loaded in memory. It must be a multiple of SectionAlignment. |
| 60/60 | 4 | SizeOfHeaders | The combined size of an MS‑DOS stub, PE header, and section headers rounded up to a multiple of FileAlignment. |
| 64/64 | 4 | CheckSum | The image file checksum. The algorithm for computing the checksum is incorporated into IMAGHELP.DLL. The following are checked for validation at load time: all drivers, any DLL loaded at boot time, and any DLL that is loaded into a critical Windows process. |
| 68/68 | 2 | Subsystem | The subsystem that is required to run this image. For more information, see “Windows Subsystem” later in this specification. |
| 70/70 | 2 | DllCharacteristics | For more information, see “DLL Characteristics” later in this specification. |
| 72/72 | 4/8 | SizeOfStackReserve | The size of the stack to reserve. Only SizeOfStackCommit is committed; the rest is made available one page at a time until the reserve size is reached. |
| 76/80 | 4/8 | SizeOfStackCommit | The size of the stack to commit. |
| 80/88 | 4/8 | SizeOfHeapReserve | The size of the local heap space to reserve. Only SizeOfHeapCommit is committed; the rest is made available one page at a time until the reserve size is reached. |
| 84/96 | 4/8 | SizeOfHeapCommit | The size of the local heap space to commit. |
| 88/104 | 4 | LoaderFlags | Reserved, must be zero. |
| 92/108 | 4 | NumberOfRvaAndSizes | The number of data-directory entries in the remainder of the optional header. Each describes a location and size. |

#### Windows Subsystem

The following values defined for the Subsystem field of the optional header determine which Windows subsystem (if any) is required to run the image.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_SUBSYSTEM\_UNKNOWN | 0 | An unknown subsystem |
| IMAGE\_SUBSYSTEM\_NATIVE | 1 | Device drivers and native Windows processes |
| IMAGE\_SUBSYSTEM\_WINDOWS\_GUI | 2 | The Windows graphical user interface (GUI) subsystem |
| IMAGE\_SUBSYSTEM\_WINDOWS\_CUI | 3 | The Windows character subsystem |
| IMAGE\_SUBSYSTEM\_OS2\_CUI | 5 | The OS/2 character subsystem |
| IMAGE\_SUBSYSTEM\_POSIX\_CUI | 7 | The Posix character subsystem |
| IMAGE\_SUBSYSTEM\_NATIVE\_WINDOWS | 8 | Native Win9x driver |
| IMAGE\_SUBSYSTEM\_WINDOWS\_CE\_GUI | 9 | Windows CE |
| IMAGE\_SUBSYSTEM\_EFI\_APPLICATION | 10 | An Extensible Firmware Interface (EFI) application |
| IMAGE\_SUBSYSTEM\_EFI\_BOOT\_ SERVICE\_DRIVER | 11 | An EFI driver with boot services |
| IMAGE\_SUBSYSTEM\_EFI\_RUNTIME\_ DRIVER | 12 | An EFI driver with run-time services |
| IMAGE\_SUBSYSTEM\_EFI\_ROM | 13 | An EFI ROM image |
| IMAGE\_SUBSYSTEM\_XBOX | 14 | XBOX |
| IMAGE\_SUBSYSTEM\_WINDOWS\_BOOT\_APPLICATION | 16 | Windows boot application. |

#### DLL Characteristics

The following values are defined for the DllCharacteristics field of the optional header.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
|  | 0x0001 | Reserved, must be zero. |
|  | 0x0002 | Reserved, must be zero. |
|  | 0x0004 | Reserved, must be zero. |
|  | 0x0008 | Reserved, must be zero. |
| IMAGE\_DLLCHARACTERISTICS\_HIGH\_ENTROPY\_VA | 0x0020 | Image can handle a high entropy 64-bit virtual address space. |
| IMAGE\_DLLCHARACTERISTICS\_  DYNAMIC\_BASE | 0x0040 | DLL can be relocated at load time. |
| IMAGE\_DLLCHARACTERISTICS\_  FORCE\_INTEGRITY | 0x0080 | Code Integrity checks are enforced. |
| IMAGE\_DLLCHARACTERISTICS\_  NX\_COMPAT | 0x0100 | Image is NX compatible. |
| IMAGE\_DLLCHARACTERISTICS\_ NO\_ISOLATION | 0x0200 | Isolation aware, but do not isolate the image. |
| IMAGE\_DLLCHARACTERISTICS\_ NO\_SEH | 0x0400 | Does not use structured exception (SE) handling. No SE handler may be called in this image. |
| IMAGE\_DLLCHARACTERISTICS\_ NO\_BIND | 0x0800 | Do not bind the image. |
| IMAGE\_DLLCHARACTERISTICS\_APPCONTAINER | 0x1000 | Image must execute in an AppContainer. |
| IMAGE\_DLLCHARACTERISTICS\_ WDM\_DRIVER | 0x2000 | A WDM driver. |
| IMAGE\_DLLCHARACTERISTICS\_GUARD\_CF | 0x4000 | Image supports Control Flow Guard. |
| IMAGE\_DLLCHARACTERISTICS\_ TERMINAL\_SERVER\_AWARE | 0x8000 | Terminal Server aware. |

### Optional Header Data Directories (Image Only)

Each data directory gives the address and size of a table or string that Windows uses. These data directory entries are all loaded into memory so that the system can use them at run time. A data directory is an 8‑byte field that has the following declaration:

typedef struct \_IMAGE\_DATA\_DIRECTORY {

DWORD VirtualAddress;

DWORD Size;

} IMAGE\_DATA\_DIRECTORY, \*PIMAGE\_DATA\_DIRECTORY;

The first field, VirtualAddress, is actually the RVA of the table. The RVA is the address of the table relative to the base address of the image when the table is loaded. The second field gives the size in bytes. The data directories, which form the last part of the optional header, are listed in the following table.

Note that the number of directories is not fixed. Before looking for a specific directory, check the NumberOfRvaAndSizes field in the optional header.

Also, do not assume that the RVAs in this table point to the beginning of a section or that the sections that contain specific tables have specific names.

| **Offset**  **(PE/PE32+)** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 96/112 | 8 | Export Table | The export table address and size. For more information see section 6.3, “The .edata Section (Image Only).” |
| 104/120 | 8 | Import Table | The import table address and size. For more information, see section 6.4, “The .idata Section.” |
| 112/128 | 8 | Resource Table | The resource table address and size. For more information, see section 6.9, “The .rsrc Section.” |
| 120/136 | 8 | Exception Table | The exception table address and size. For more information, see section 6.5, “The .pdata Section.” |
| 128/144 | 8 | Certificate Table | The attribute certificate table address and size. For more information, see section 5.7, “The Attribute Certificate Table (Image Only).” |
| 136/152 | 8 | Base Relocation Table | The base relocation table address and size. For more information, see section 6.6, "The .reloc Section (Image Only)." |
| 144/160 | 8 | Debug | The debug data starting address and size. For more information, see section 6.1, “The .debug Section.” |
| 152/168 | 8 | Architecture | Reserved, must be 0 |
| 160/176 | 8 | Global Ptr | The RVA of the value to be stored in the global pointer register. The size member of this structure must be set to zero. |
| 168/184 | 8 | TLS Table | The thread local storage (TLS) table address and size. For more information, see section 6.7, “The .tls Section.” |
| 176/192 | 8 | Load Config Table | The load configuration table address and size. For more information, see section 6.8, “The Load Configuration Structure (Image Only).” |
| 184/200 | 8 | Bound Import | The bound import table address and size. |
| 192/208 | 8 | IAT | The import address table address and size. For more information, see section 6.4.4, “Import Address Table.” |
| 200/216 | 8 | Delay Import Descriptor | The delay import descriptor address and size. For more information, see section 5.8, “Delay-Load Import Tables (Image Only).” |
| 208/224 | 8 | CLR Runtime Header | The CLR runtime header address and size. For more information, see section 6.10, “The .cormeta Section (Object Only).” |
| 216/232 | 8 | Reserved, must be zero | |

The Certificate Table entry points to a table of attribute certificates. These certificates are not loaded into memory as part of the image. As such, the first field of this entry, which is normally an RVA, is a file pointer instead.

# Section Table (Section Headers)

Each row of the section table is, in effect, a section header. This table immediately follows the optional header, if any. This positioning is required because the file header does not contain a direct pointer to the section table. Instead, the location of the section table is determined by calculating the location of the first byte after the headers. Make sure to use the size of the optional header as specified in the file header.

The number of entries in the section table is given by the NumberOfSections field in the file header. Entries in the section table are numbered starting from one (1). The code and data memory section entries are in the order chosen by the linker.

In an image file, the VAs for sections must be assigned by the linker so that they are in ascending order and adjacent, and they must be a multiple of the SectionAlignment value in the optional header.

Each section header (section table entry) has the following format, for a total of 40 bytes per entry.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 8 | Name | An 8-byte, null-padded UTF-8 encoded string. If the string is exactly 8 characters long, there is no terminating null. For longer names, this field contains a slash (/) that is followed by an ASCII representation of a decimal number that is an offset into the string table. Executable images do not use a string table and do not support section names longer than 8 characters. Long names in object files are truncated if they are emitted to an executable file. |
| 8 | 4 | VirtualSize | The total size of the section when loaded into memory. If this value is greater than SizeOfRawData, the section is zero-padded. This field is valid only for executable images and should be set to zero for object files. |
| 12 | 4 | VirtualAddress | For executable images, the address of the first byte of the section relative to the image base when the section is loaded into memory. For object files, this field is the address of the first byte before relocation is applied; for simplicity, compilers should set this to zero. Otherwise, it is an arbitrary value that is subtracted from offsets during relocation. |
| 16 | 4 | SizeOfRawData | The size of the section (for object files) or the size of the initialized data on disk (for image files). For executable images, this must be a multiple of FileAlignment from the optional header. If this is less than VirtualSize, the remainder of the section is zero-filled. Because the SizeOfRawData field is rounded but the VirtualSize field is not, it is possible for SizeOfRawData to be greater than VirtualSize as well. When a section contains only uninitialized data, this field should be zero. |
| 20 | 4 | PointerToRawData | The file pointer to the first page of the section within the COFF file. For executable images, this must be a multiple of FileAlignment from the optional header. For object files, the value should be aligned on a 4‑byte boundary for best performance. When a section contains only uninitialized data, this field should be zero. |
| 24 | 4 | PointerToRelocations | The file pointer to the beginning of relocation entries for the section. This is set to zero for executable images or if there are no relocations. |
| 28 | 4 | PointerToLinenumbers | The file pointer to the beginning of line-number entries for the section. This is set to zero if there are no COFF line numbers. This value should be zero for an image because COFF debugging information is deprecated. |
| 32 | 2 | NumberOfRelocations | The number of relocation entries for the section. This is set to zero for executable images. |
| 34 | 2 | NumberOfLinenumbers | The number of line-number entries for the section. This value should be zero for an image because COFF debugging information is deprecated. |
| 36 | 4 | Characteristics | The flags that describe the characteristics of the section. For more information, see section 4.1, “Section Flags.” |

## Section Flags

The section flags in the Characteristics field of the section header indicate characteristics of the section.

| **Flag** | **Value** | **Description** |
| --- | --- | --- |
|  | 0x00000000 | Reserved for future use. |
|  | 0x00000001 | Reserved for future use. |
|  | 0x00000002 | Reserved for future use. |
|  | 0x00000004 | Reserved for future use. |
| IMAGE\_SCN\_TYPE\_NO\_PAD | 0x00000008 | The section should not be padded to the next boundary. This flag is obsolete and is replaced by IMAGE\_SCN\_ALIGN\_1BYTES. This is valid only for object files. |
|  | 0x00000010 | Reserved for future use. |
| IMAGE\_SCN\_CNT\_CODE | 0x00000020 | The section contains executable code. |
| IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | 0x00000040 | The section contains initialized data. |
| IMAGE\_SCN\_CNT\_UNINITIALIZED\_ DATA | 0x00000080 | The section contains uninitialized data. |
| IMAGE\_SCN\_LNK\_OTHER | 0x00000100 | Reserved for future use. |
| IMAGE\_SCN\_LNK\_INFO | 0x00000200 | The section contains comments or other information. The .drectve section has this type. This is valid for object files only. |
|  | 0x00000400 | Reserved for future use. |
| IMAGE\_SCN\_LNK\_REMOVE | 0x00000800 | The section will not become part of the image. This is valid only for object files. |
| IMAGE\_SCN\_LNK\_COMDAT | 0x00001000 | The section contains COMDAT data. For more information, see section 5.5.6, “COMDAT Sections (Object Only).” This is valid only for object files. |
| IMAGE\_SCN\_GPREL | 0x00008000 | The section contains data referenced through the global pointer (GP). |
| IMAGE\_SCN\_MEM\_PURGEABLE | 0x00020000 | Reserved for future use. |
| IMAGE\_SCN\_MEM\_16BIT | 0x00020000 | Reserved for future use. |
| IMAGE\_SCN\_MEM\_LOCKED | 0x00040000 | Reserved for future use. |
| IMAGE\_SCN\_MEM\_PRELOAD | 0x00080000 | Reserved for future use. |
| IMAGE\_SCN\_ALIGN\_1BYTES | 0x00100000 | Align data on a 1-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_2BYTES | 0x00200000 | Align data on a 2-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_4BYTES | 0x00300000 | Align data on a 4-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_8BYTES | 0x00400000 | Align data on an 8-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_16BYTES | 0x00500000 | Align data on a 16-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_32BYTES | 0x00600000 | Align data on a 32-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_64BYTES | 0x00700000 | Align data on a 64-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_128BYTES | 0x00800000 | Align data on a 128-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_256BYTES | 0x00900000 | Align data on a 256-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_512BYTES | 0x00A00000 | Align data on a 512-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_1024BYTES | 0x00B00000 | Align data on a 1024-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_2048BYTES | 0x00C00000 | Align data on a 2048-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_4096BYTES | 0x00D00000 | Align data on a 4096-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_ALIGN\_8192BYTES | 0x00E00000 | Align data on an 8192-byte boundary. Valid only for object files. |
| IMAGE\_SCN\_LNK\_NRELOC\_OVFL | 0x01000000 | The section contains extended relocations. |
| IMAGE\_SCN\_MEM\_DISCARDABLE | 0x02000000 | The section can be discarded as needed. |
| IMAGE\_SCN\_MEM\_NOT\_CACHED | 0x04000000 | The section cannot be cached. |
| IMAGE\_SCN\_MEM\_NOT\_PAGED | 0x08000000 | The section is not pageable. |
| IMAGE\_SCN\_MEM\_SHARED | 0x10000000 | The section can be shared in memory. |
| IMAGE\_SCN\_MEM\_EXECUTE | 0x20000000 | The section can be executed as code. |
| IMAGE\_SCN\_MEM\_READ | 0x40000000 | The section can be read. |
| IMAGE\_SCN\_MEM\_WRITE | 0x80000000 | The section can be written to. |

IMAGE\_SCN\_LNK\_NRELOC\_OVFL indicates that the count of relocations for the section exceeds the 16 bits that are reserved for it in the section header. If the bit is set and the NumberOfRelocations field in the section header is 0xffff, the actual relocation count is stored in the 32-bit VirtualAddress field of the first relocation. It is an error if IMAGE\_SCN\_LNK\_NRELOC\_OVFL is set and there are fewer than 0xffff relocations in the section.

## Grouped Sections (Object Only)

The “$” character (dollar sign) has a special interpretation in section names in object files.

When determining the image section that will contain the contents of an object section, the linker discards the “$” and all characters that follow it. Thus, an object section named **.text$X** actually contributes to the **.text** section in the image.

However, the characters following the “$” determine the ordering of the contributions to the image section. All contributions with the same object-section name are allocated contiguously in the image, and the blocks of contributions are sorted in lexical order by object-section name. Therefore, everything in object files with section name **.text$X** ends up together, after the **.text$W** contributions and before the **.text$Y** contributions.

The section name in an image file never contains a “$” character.

# Other Contents of the File

The data structures that were described so far, up to and including the optional header, are all located at a fixed offset from the beginning of the file (or from the PE header if the file is an image that contains an MS‑DOS stub).

The remainder of a COFF object or image file contains blocks of data that are not necessarily at any specific file offset. Instead, the locations are defined by pointers in the optional header or a section header.

An exception is for images with a SectionAlignment value of less than the page size of the architecture (4 K for Intel x86 and for MIPS, and 8 K for Itanium). For a description of SectionAlignment, see section 3.4, "Optional Header (Image Only)." In this case, there are constraints on the file offset of the section data, as described in section 5.1, "Section Data." Another exception is that attribute certificate and debug information must be placed at the very end of an image file, with the attribute certificate table immediately preceding the debug section, because the loader does not map these into memory. The rule about attribute certificate and debug information does not apply to object files, however.

## Section Data

Initialized data for a section consists of simple blocks of bytes. However, for sections that contain all zeros, the section data need not be included.

The data for each section is located at the file offset that was given by the PointerToRawData field in the section header. The size of this data in the file is indicated by the SizeOfRawData field. If SizeOfRawData is less than VirtualSize, the remainder is padded with zeros.

In an image file, the section data must be aligned on a boundary as specified by the FileAlignment field in the optional header. Section data must appear in order of the RVA values for the corresponding sections (as do the individual section headers in the section table).

There are additional restrictions on image files if the SectionAlignment value in the optional header is less than the page size of the architecture. For such files, the location of section data in the file must match its location in memory when the image is loaded, so that the physical offset for section data is the same as the RVA.

## COFF Relocations (Object Only)

Object files contain COFF relocations, which specify how the section data should be modified when placed in the image file and subsequently loaded into memory.

Image files do not contain COFF relocations, because all referenced symbols have already been assigned addresses in a flat address space. An image contains relocation information in the form of base relocations in the **.reloc** section (unless the image has the IMAGE\_FILE\_RELOCS\_STRIPPED attribute). For more information, see section 6.6, "The .reloc Section (Image Only)."

For each section in an object file, an array of fixed-length records holds the section’s COFF relocations. The position and length of the array are specified in the section header. Each element of the array has the following format.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | VirtualAddress | The address of the item to which relocation is applied. This is the offset from the beginning of the section, plus the value of the section’s RVA/Offset field. See section 4, “Section Table (Section Headers).” For example, if the first byte of the section has an address of 0x10, the third byte has an address of 0x12. |
| 4 | 4 | SymbolTableIndex | A zero-based index into the symbol table. This symbol gives the address that is to be used for the relocation. If the specified symbol has section storage class, then the symbol’s address is the address with the first section of the same name. |
| 8 | 2 | Type | A value that indicates the kind of relocation that should be performed. Valid relocation types depend on machine type. See section 5.2.1, “Type Indicators.” |

If the symbol referred to by the SymbolTableIndex field has the storage class IMAGE\_SYM\_CLASS\_SECTION, the symbol’s address is the beginning of the section. The section is usually in the same file, except when the object file is part of an archive (library). In that case, the section can be found in any other object file in the archive that has the same archive-member name as the current object file. (The relationship with the archive-member name is used in the linking of import tables, that is, the **.idata** section.)

### Type Indicators

The Type field of the relocation record indicates what kind of relocation should be performed. Different relocation types are defined for each type of machine.

#### x64 Processors

The following relocation type indicators are defined for x64 and compatible processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_AMD64\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_AMD64\_ADDR64 | 0x0001 | The 64-bit VA of the relocation target. |
| IMAGE\_REL\_AMD64\_ADDR32 | 0x0002 | The 32-bit VA of the relocation target. |
| IMAGE\_REL\_AMD64\_ADDR32NB | 0x0003 | The 32-bit address without an image base (RVA). |
| IMAGE\_REL\_AMD64\_REL32 | 0x0004 | The 32-bit relative address from the byte following the relocation. |
| IMAGE\_REL\_AMD64\_REL32\_1 | 0x0005 | The 32-bit address relative to byte distance 1 from the relocation. |
| IMAGE\_REL\_AMD64\_REL32\_2 | 0x0006 | The 32-bit address relative to byte distance 2 from the relocation. |
| IMAGE\_REL\_AMD64\_REL32\_3 | 0x0007 | The 32-bit address relative to byte distance 3 from the relocation. |
| IMAGE\_REL\_AMD64\_REL32\_4 | 0x0008 | The 32-bit address relative to byte distance 4 from the relocation. |
| IMAGE\_REL\_AMD64\_REL32\_5 | 0x0009 | The 32-bit address relative to byte distance 5 from the relocation. |
| IMAGE\_REL\_AMD64\_SECTION | 0x000A | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_AMD64\_SECREL | 0x000B | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_AMD64\_SECREL7 | 0x000C | A 7-bit unsigned offset from the base of the section that contains the target. |
| IMAGE\_REL\_AMD64\_TOKEN | 0x000D | CLR tokens. |
| IMAGE\_REL\_AMD64\_SREL32 | 0x000E | A 32-bit signed span-dependent value emitted into the object. |
| IMAGE\_REL\_AMD64\_PAIR | 0x000F | A pair that must immediately follow every span-dependent value. |
| IMAGE\_REL\_AMD64\_SSPAN32 | 0x0010 | A 32-bit signed span-dependent value that is applied at link time. |

#### ARM Processors

The following relocation type indicators are defined for ARM processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_ARM\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_ARM\_ADDR32 | 0x0001 | The 32-bit VA of the target. |
| IMAGE\_REL\_ARM\_ADDR32NB | 0x0002 | The 32-bit RVA of the target. |
| IMAGE\_REL\_ARM\_BRANCH24 | 0x0003 | The 24-bit relative displacement to the target. |
| IMAGE\_REL\_ARM\_BRANCH11 | 0x0004 | The reference to a subroutine call. The reference consists of two 16-bit instructions with 11-bit offsets. |
| IMAGE\_REL\_ARM\_SECTION | 0x000E | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_ARM\_SECREL | 0x000F | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_ARM\_MOV32 | 0x0010 | The 32-bit VA of the target. This relocation is applied using a MOVW instruction for the low 16 bits followed by a MOVT for the high 16 bits. |
| IMAGE\_REL\_THUMB\_MOV32 | 0x0011 | The 32-bit VA of the target. This relocation is applied using a MOVW instruction for the low 16 bits followed by a MOVT for the high 16 bits. |
| IMAGE\_REL\_THUMB\_BRANCH20 | 0x0012 | The instruction is fixed up with the 21-bit relative displacement to the 2-byte aligned target. The least significant bit of the displacement is always zero and is not stored. This relocation corresponds to a Thumb-2 32-bit conditional B instruction. |
| Unused | 0x0013 |  |
| IMAGE\_REL\_THUMB\_BRANCH24 | 0x0014 | The instruction is fixed up with the 25-bit relative displacement to the 2-byte aligned target. The least significant bit of the displacement is zero and is not stored.This relocation corresponds to a Thumb-2 B instruction. |
| IMAGE\_REL\_THUMB\_BLX23 | 0x0015 | The instruction is fixed up with the 25-bit relative displacement to the 4-byte aligned target. The low 2 bits of the displacement are zero and are not stored.  This relocation corresponds to a Thumb-2 BLX instruction. |
| IMAGE\_REL\_ARM\_PAIR | 0x0016 | The relocation is valid only when it immediately follows a ARM\_REFHI or THUMB\_REFHI. Its SymbolTableIndex contains a displacement and not an index into the symbol table. |

#### ARM64 Processors

The following relocation type indicators are defined for ARM64 processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_ARM64\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_ARM64\_ADDR32 | 0x0001 | The 32-bit VA of the target. |
| IMAGE\_REL\_ARM64\_ADDR32NB | 0x0002 | The 32-bit RVA of the target. |
| IMAGE\_REL\_ARM64\_BRANCH26 | 0x0003 | The 26-bit relative displacement to the target, for B and BL instructions. |
| IMAGE\_REL\_ARM64\_PAGEBASE\_REL21 | 0x0004 | The page base of the target, for ADRP instruction. |
| IMAGE\_REL\_ARM64\_REL21 | 0x0005 | The 12-bit relative displacement to the target, for instruction ADR |
| IMAGE\_REL\_ARM64\_PAGEOFFSET\_12A | 0x0006 | The 12-bit page offset of the target, for instructions ADD/ADDS (immediate) with zero shift. |
| IMAGE\_REL\_ARM64\_PAGEOFFSET\_12L | 0x0007 | The 12-bit page offset of the target, for instruction LDR (indexed, unsigned immediate). |
| IMAGE\_REL\_ARM64\_SECREL | 0x0008 | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_ARM64\_SECREL\_LOW12A | 0x0009 | Bit 0:11 of section offset of the target, for instructions ADD/ADDS (immediate) with zero shift. |
| IMAGE\_REL\_ARM64\_SECREL\_HIGH12A | 0x000A | Bit 12:23 of section offset of the target, for instructions ADD/ADDS (immediate) with zero shift. |
| IMAGE\_REL\_ARM64\_SECREL\_LOW12L | 0x000B | Bit 0:11 of section offset of the target, for instruction LDR (indexed, unsigned immediate). |
| IMAGE\_REL\_ARM64\_TOKEN | 0x000C | CLR token. |
| IMAGE\_REL\_ARM64\_SECTION | 0x000D | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_ARM64\_ADDR64 | 0x000E | The 64-bit VA of the relocation target. |
| IMAGE\_REL\_ARM64\_BRANCH19 | 0x000F | The 19-bit offset to the relocation target, for conditional B instruction. |
| IMAGE\_REL\_ARM64\_BRANCH14 | 0x0010 | The 14-bit offset to the relocation target, for instructions TBZ and TBNZ. |

#### Hitachi SuperH Processors

The following relocation type indicators are defined for SH3 and SH4 processors. SH5-specific relocations are noted as SHM (SH Media).

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_SH3\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_SH3\_DIRECT16 | 0x0001 | A reference to the 16-bit location that contains the VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT32 | 0x0002 | The 32-bit VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT8 | 0x0003 | A reference to the 8-bit location that contains the VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT8\_WORD | 0x0004 | A reference to the 8-bit instruction that contains the effective 16-bit VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT8\_LONG | 0x0005 | A reference to the 8-bit instruction that contains the effective 32-bit VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT4 | 0x0006 | A reference to the 8-bit location whose low 4 bits contain the VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT4\_WORD | 0x0007 | A reference to the 8-bit instruction whose low 4 bits contain the effective 16-bit VA of the target symbol. |
| IMAGE\_REL\_SH3\_DIRECT4\_LONG | 0x0008 | A reference to the 8-bit instruction whose low 4 bits contain the effective 32-bit VA of the target symbol. |
| IMAGE\_REL\_SH3\_PCREL8\_WORD | 0x0009 | A reference to the 8-bit instruction that contains the effective 16-bit relative offset of the target symbol. |
| IMAGE\_REL\_SH3\_PCREL8\_LONG | 0x000A | A reference to the 8-bit instruction that contains the effective 32-bit relative offset of the target symbol. |
| IMAGE\_REL\_SH3\_PCREL12\_WORD | 0x000B | A reference to the 16-bit instruction whose low 12 bits contain the effective 16-bit relative offset of the target symbol. |
| IMAGE\_REL\_SH3\_STARTOF\_SECTION | 0x000C | A reference to a 32-bit location that is the VA of the section that contains the target symbol. |
| IMAGE\_REL\_SH3\_SIZEOF\_SECTION | 0x000D | A reference to the 32-bit location that is the size of the section that contains the target symbol. |
| IMAGE\_REL\_SH3\_SECTION | 0x000E | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_SH3\_SECREL | 0x000F | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_SH3\_DIRECT32\_NB | 0x0010 | The 32-bit RVA of the target symbol. |
| IMAGE\_REL\_SH3\_GPREL4\_LONG | 0x0011 | GP relative. |
| IMAGE\_REL\_SH3\_TOKEN | 0x0012 | CLR token. |
| IMAGE\_REL\_SHM\_PCRELPT | 0x0013 | The offset from the current instruction in longwords. If the NOMODE bit is not set, insert the inverse of the low bit at bit 32 to select PTA or PTB. |
| IMAGE\_REL\_SHM\_REFLO | 0x0014 | The low 16 bits of the 32-bit address. |
| IMAGE\_REL\_SHM\_REFHALF | 0x0015 | The high 16 bits of the 32-bit address. |
| IMAGE\_REL\_SHM\_RELLO | 0x0016 | The low 16 bits of the relative address. |
| IMAGE\_REL\_SHM\_RELHALF | 0x0017 | The high 16 bits of the relative address. |
| IMAGE\_REL\_SHM\_PAIR | 0x0018 | The relocation is valid only when it immediately follows a REFHALF, RELHALF, or RELLO relocation. The SymbolTableIndex field of the relocation contains a displacement and not an index into the symbol table. |
| IMAGE\_REL\_SHM\_NOMODE | 0x8000 | The relocation ignores section mode. |

#### IBM PowerPC Processors

The following relocation type indicators are defined for PowerPC processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_PPC\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_PPC\_ADDR64 | 0x0001 | The 64-bit VA of the target. |
| IMAGE\_REL\_PPC\_ADDR32 | 0x0002 | The 32-bit VA of the target. |
| IMAGE\_REL\_PPC\_ADDR24 | 0x0003 | The low 24 bits of the VA of the target. This is valid only when the target symbol is absolute and can be sign-extended to its original value. |
| IMAGE\_REL\_PPC\_ADDR16 | 0x0004 | The low 16 bits of the target’s VA. |
| IMAGE\_REL\_PPC\_ADDR14 | 0x0005 | The low 14 bits of the target’s VA. This is valid only when the target symbol is absolute and can be sign-extended to its original value. |
| IMAGE\_REL\_PPC\_REL24 | 0x0006 | A 24-bit PC-relative offset to the symbol’s location. |
| IMAGE\_REL\_PPC\_REL14 | 0x0007 | A 14-bit PC-relative offset to the symbol’s location. |
| IMAGE\_REL\_PPC\_ADDR32NB | 0x000A | The 32-bit RVA of the target. |
| IMAGE\_REL\_PPC\_SECREL | 0x000B | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_PPC\_SECTION | 0x000C | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_PPC\_SECREL16 | 0x000F | The 16-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_PPC\_REFHI | 0x0010 | The high 16 bits of the target’s 32-bit VA. This is used for the first instruction in a two-instruction sequence that loads a full address. This relocation must be immediately followed by a PAIR relocation whose SymbolTableIndex contains a signed 16-bit displacement that is added to the upper 16 bits that was taken from the location that is being relocated. |
| IMAGE\_REL\_PPC\_REFLO | 0x0011 | The low 16 bits of the target’s VA. |
| IMAGE\_REL\_PPC\_PAIR | 0x0012 | A relocation that is valid only when it immediately follows a REFHI or SECRELHI relocation. Its SymbolTableIndex contains a displacement and not an index into the symbol table. |
| IMAGE\_REL\_PPC\_SECRELLO | 0x0013 | The low 16 bits of the 32-bit offset of the target from the beginning of its section. |
| IMAGE\_REL\_PPC\_GPREL | 0x0015 | The 16-bit signed displacement of the target relative to the GP register. |
| IMAGE\_REL\_PPC\_TOKEN | 0x0016 | The CLR token. |

#### Intel 386 Processors

The following relocation type indicators are defined for Intel 386 and compatible processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_I386\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_I386\_DIR16 | 0x0001 | Not supported. |
| IMAGE\_REL\_I386\_REL16 | 0x0002 | Not supported. |
| IMAGE\_REL\_I386\_DIR32 | 0x0006 | The target’s 32-bit VA. |
| IMAGE\_REL\_I386\_DIR32NB | 0x0007 | The target’s 32-bit RVA. |
| IMAGE\_REL\_I386\_SEG12 | 0x0009 | Not supported. |
| IMAGE\_REL\_I386\_SECTION | 0x000A | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_I386\_SECREL | 0x000B | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_I386\_TOKEN | 0x000C | The CLR token. |
| IMAGE\_REL\_I386\_SECREL7 | 0x000D | A 7-bit offset from the base of the section that contains the target. |
| IMAGE\_REL\_I386\_REL32 | 0x0014 | The 32-bit relative displacement to the target. This supports the x86 relative branch and call instructions. |

#### Intel Itanium Processor Family (IPF)

The following relocation type indicators are defined for the Intel Itanium processor family and compatible processors. Note that relocations on *instructions* use the bundle’s offset and slot number for the relocation offset.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_IA64\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_IA64\_IMM14 | 0x0001 | The instruction relocation can be followed by an ADDEND relocation whose value is added to the target address before it is inserted into the specified slot in the IMM14 bundle. The relocation target must be absolute or the image must be fixed. |
| IMAGE\_REL\_IA64\_IMM22 | 0x0002 | The instruction relocation can be followed by an ADDEND relocation whose value is added to the target address before it is inserted into the specified slot in the IMM22 bundle. The relocation target must be absolute or the image must be fixed. |
| IMAGE\_REL\_IA64\_IMM64 | 0x0003 | The slot number of this relocation must be one (1). The relocation can be followed by an ADDEND relocation whose value is added to the target address before it is stored in all three slots of the IMM64 bundle. |
| IMAGE\_REL\_IA64\_DIR32 | 0x0004 | The target’s 32-bit VA. This is supported only for /LARGEADDRESSAWARE:NO images. |
| IMAGE\_REL\_IA64\_DIR64 | 0x0005 | The target’s 64-bit VA. |
| IMAGE\_REL\_IA64\_PCREL21B | 0x0006 | The instruction is fixed up with the 25-bit relative displacement to the 16-bit aligned target. The low 4 bits of the displacement are zero and are not stored. |
| IMAGE\_REL\_IA64\_PCREL21M | 0x0007 | The instruction is fixed up with the 25-bit relative displacement to the 16-bit aligned target. The low 4 bits of the displacement, which are zero, are not stored. |
| IMAGE\_REL\_IA64\_PCREL21F | 0x0008 | The LSBs of this relocation’s offset must contain the slot number whereas the rest is the bundle address. The bundle is fixed up with the 25-bit relative displacement to the 16-bit aligned target. The low 4 bits of the displacement are zero and are not stored. |
| IMAGE\_REL\_IA64\_GPREL22 | 0x0009 | The instruction relocation can be followed by an ADDEND relocation whose value is added to the target address and then a 22-bit GP-relative offset that is calculated and applied to the GPREL22 bundle. |
| IMAGE\_REL\_IA64\_LTOFF22 | 0x000A | The instruction is fixed up with the 22-bit GP-relative offset to the target symbol’s literal table entry. The linker creates this literal table entry based on this relocation and the ADDEND relocation that might follow. |
| IMAGE\_REL\_IA64\_SECTION | 0x000B | The 16-bit section index of the section contains the target. This is used to support debugging information. |
| IMAGE\_REL\_IA64\_SECREL22 | 0x000C | The instruction is fixed up with the 22-bit offset of the target from the beginning of its section. This relocation can be followed immediately by an ADDEND relocation, whose Value field contains the 32-bit unsigned offset of the target from the beginning of the section. |
| IMAGE\_REL\_IA64\_SECREL64I | 0x000D | The slot number for this relocation must be one (1). The instruction is fixed up with the 64-bit offset of the target from the beginning of its section. This relocation can be followed immediately by an ADDEND relocation whose Value field contains the 32-bit unsigned offset of the target from the beginning of the section. |
| IMAGE\_REL\_IA64\_SECREL32 | 0x000E | The address of data to be fixed up with the 32-bit offset of the target from the beginning of its section. |
| IMAGE\_REL\_IA64\_DIR32NB | 0x0010 | The target’s 32-bit RVA. |
| IMAGE\_REL\_IA64\_SREL14 | 0x0011 | This is applied to a signed 14-bit immediate that contains the difference between two relocatable targets. This is a declarative field for the linker that indicates that the compiler has already emitted this value. |
| IMAGE\_REL\_IA64\_SREL22 | 0x0012 | This is applied to a signed 22-bit immediate that contains the difference between two relocatable targets. This is a declarative field for the linker that indicates that the compiler has already emitted this value. |
| IMAGE\_REL\_IA64\_SREL32 | 0x0013 | This is applied to a signed 32-bit immediate that contains the difference between two relocatable values. This is a declarative field for the linker that indicates that the compiler has already emitted this value. |
| IMAGE\_REL\_IA64\_UREL32 | 0x0014 | This is applied to an unsigned 32-bit immediate that contains the difference between two relocatable values. This is a declarative field for the linker that indicates that the compiler has already emitted this value. |
| IMAGE\_REL\_IA64\_PCREL60X | 0x0015 | A 60-bit PC-relative fixup that always stays as a BRL instruction of an MLX bundle. |
| IMAGE\_REL\_IA64\_PCREL60B | 0x0016 | A 60-bit PC-relative fixup. If the target displacement fits in a signed 25-bit field, convert the entire bundle to an MBB bundle with NOP.B in slot 1 and a 25-bit BR instruction (with the 4 lowest bits all zero and dropped) in slot 2. |
| IMAGE\_REL\_IA64\_PCREL60F | 0x0017 | A 60-bit PC-relative fixup. If the target displacement fits in a signed 25-bit field, convert the entire bundle to an MFB bundle with NOP.F in slot 1 and a 25-bit (4 lowest bits all zero and dropped) BR instruction in slot 2. |
| IMAGE\_REL\_IA64\_PCREL60I | 0x0018 | A 60-bit PC-relative fixup. If the target displacement fits in a signed 25-bit field, convert the entire bundle to an MIB bundle with NOP.I in slot 1 and a 25-bit (4 lowest bits all zero and dropped) BR instruction in slot 2. |
| IMAGE\_REL\_IA64\_PCREL60M | 0x0019 | A 60-bit PC-relative fixup. If the target displacement fits in a signed 25-bit field, convert the entire bundle to an MMB bundle with NOP.M in slot 1 and a 25-bit (4 lowest bits all zero and dropped) BR instruction in slot 2. |
| IMAGE\_REL\_IA64\_IMMGPREL64 | 0x001a | A 64-bit GP-relative fixup. |
| IMAGE\_REL\_IA64\_TOKEN | 0x001b | A CLR token. |
| IMAGE\_REL\_IA64\_GPREL32 | 0x001c | A 32-bit GP-relative fixup. |
| IMAGE\_REL\_IA64\_ADDEND | 0x001F | The relocation is valid only when it immediately follows one of the following relocations: IMM14, IMM22, IMM64, GPREL22, LTOFF22, LTOFF64, SECREL22, SECREL64I, or SECREL32. Its value contains the addend to apply to instructions within a bundle, not for data. |

#### MIPS Processors

The following relocation type indicators are defined for MIPS processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_MIPS\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_MIPS\_REFHALF | 0x0001 | The high 16 bits of the target’s 32-bit VA. |
| IMAGE\_REL\_MIPS\_REFWORD | 0x0002 | The target’s 32-bit VA. |
| IMAGE\_REL\_MIPS\_JMPADDR | 0x0003 | The low 26 bits of the target’s VA. This supports the MIPS J and JAL instructions. |
| IMAGE\_REL\_MIPS\_REFHI | 0x0004 | The high 16 bits of the target’s 32-bit VA. This is used for the first instruction in a two-instruction sequence that loads a full address. This relocation must be immediately followed by a PAIR relocation whose SymbolTableIndex contains a signed 16-bit displacement that is added to the upper 16 bits that are taken from the location that is being relocated. |
| IMAGE\_REL\_MIPS\_REFLO | 0x0005 | The low 16 bits of the target’s VA. |
| IMAGE\_REL\_MIPS\_GPREL | 0x0006 | A 16-bit signed displacement of the target relative to the GP register. |
| IMAGE\_REL\_MIPS\_LITERAL | 0x0007 | The same as IMAGE\_REL\_MIPS\_GPREL. |
| IMAGE\_REL\_MIPS\_SECTION | 0x000A | The 16-bit section index of the section contains the target. This is used to support debugging information. |
| IMAGE\_REL\_MIPS\_SECREL | 0x000B | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_MIPS\_SECRELLO | 0x000C | The low 16 bits of the 32-bit offset of the target from the beginning of its section. |
| IMAGE\_REL\_MIPS\_SECRELHI | 0x000D | The high 16 bits of the 32-bit offset of the target from the beginning of its section. An IMAGE\_REL\_MIPS\_PAIR relocation must immediately follow this one. The SymbolTableIndex of the PAIR relocation contains a signed 16-bit displacement that is added to the upper 16 bits that are taken from the location that is being relocated. |
| IMAGE\_REL\_MIPS\_JMPADDR16 | 0x0010 | The low 26 bits of the target’s VA. This supports the MIPS16 JAL instruction. |
| IMAGE\_REL\_MIPS\_REFWORDNB | 0x0022 | The target’s 32-bit RVA. |
| IMAGE\_REL\_MIPS\_PAIR | 0x0025 | The relocation is valid only when it immediately follows a REFHI or SECRELHI relocation. Its SymbolTableIndex contains a displacement and not an index into the symbol table. |

#### Mitsubishi M32R

The following relocation type indicators are defined for the Mitsubishi M32R processors.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_M32R\_ABSOLUTE | 0x0000 | The relocation is ignored. |
| IMAGE\_REL\_M32R\_ADDR32 | 0x0001 | The target’s 32-bit VA. |
| IMAGE\_REL\_M32R\_ADDR32NB | 0x0002 | The target’s 32-bit RVA. |
| IMAGE\_REL\_M32R\_ADDR24 | 0x0003 | The target’s 24-bit VA. |
| IMAGE\_REL\_M32R\_GPREL16 | 0x0004 | The target’s 16-bit offset from the GP register. |
| IMAGE\_REL\_M32R\_PCREL24 | 0x0005 | The target’s 24-bit offset from the program counter (PC), shifted left by 2 bits and sign-extended |
| IMAGE\_REL\_M32R\_PCREL16 | 0x0006 | The target’s 16-bit offset from the PC, shifted left by 2 bits and sign-extended |
| IMAGE\_REL\_M32R\_PCREL8 | 0x0007 | The target’s 8-bit offset from the PC, shifted left by 2 bits and sign-extended |
| IMAGE\_REL\_M32R\_REFHALF | 0x0008 | The 16 MSBs of the target VA. |
| IMAGE\_REL\_M32R\_REFHI | 0x0009 | The 16 MSBs of the target VA, adjusted for LSB sign extension. This is used for the first instruction in a two-instruction sequence that loads a full 32-bit address. This relocation must be immediately followed by a PAIR relocation whose SymbolTableIndex contains a signed 16-bit displacement that is added to the upper 16 bits that are taken from the location that is being relocated. |
| IMAGE\_REL\_M32R\_REFLO | 0x000A | The 16 LSBs of the target VA. |
| IMAGE\_REL\_M32R\_PAIR | 0x000B | The relocation must follow the REFHI relocation. Its SymbolTableIndex contains a displacement and not an index into the symbol table. |
| IMAGE\_REL\_M32R\_SECTION | 0x000C | The 16-bit section index of the section that contains the target. This is used to support debugging information. |
| IMAGE\_REL\_M32R\_SECREL | 0x000D | The 32-bit offset of the target from the beginning of its section. This is used to support debugging information and static thread local storage. |
| IMAGE\_REL\_M32R\_TOKEN | 0x000E | The CLR token. |

## COFF Line Numbers (Deprecated)

COFF line numbers are no longer produced and, in the future, will not be consumed.

COFF line numbers indicate the relationship between code and line numbers in source files. The Microsoft format for COFF line numbers is similar to standard COFF, but it has been extended to allow a single section to relate to line numbers in multiple source files.

COFF line numbers consist of an array of fixed-length records. The location (file offset) and size of the array are specified in the section header. Each line-number record is of the following format.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Type (\*) | This is a union of two fields: SymbolTableIndex and VirtualAddress. Whether SymbolTableIndex or RVA is used depends on the value of Linenumber. |
| 4 | 2 | Linenumber | When nonzero, this field specifies a one-based line number. When zero, the Type field is interpreted as a symbol table index for a function. |

The Type field is a union of two 4‑byte fields: SymbolTableIndex and VirtualAddress.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | SymbolTableIndex | Used when Linenumber is zero: index to symbol table entry for a function. This format is used to indicate the function to which a group of line-number records refers. |
| 0 | 4 | VirtualAddress | Used when Linenumber is non-zero: the RVA of the executable code that corresponds to the source line indicated. In an object file, this contains the VA within the section. |

A line-number record can either set the Linenumber field to zero and point to a function definition in the symbol table or it can work as a standard line-number entry by giving a positive integer (line number) and the corresponding address in the object code.

A group of line-number entries always begins with the first format: the index of a function symbol. If this is the first line-number record in the section, then it is also the COMDAT symbol name for the function if the section’s COMDAT flag is set. See section 5.5.6, “COMDAT Sections (Object Only).” The function’s auxiliary record in the symbol table has a pointer to the Linenumber field that points to this same line-number record.

A record that identifies a function is followed by any number of line-number entries that give actual line-number information (that is, entries with Linenumber greater than zero). These entries are one-based, relative to the beginning of the function, and represent every source line in the function except for the first line.

For example, the first line-number record for the following example would specify the ReverseSign function (SymbolTableIndex of ReverseSign and Linenumber set to zero). Then records with Linenumber values of 1, 2, and 3 would follow, corresponding to source lines as shown:

// some code precedes ReverseSign function

int ReverseSign(int i)

1: {

2: return -1 \* i;

3: }

## COFF Symbol Table

The symbol table in this section is inherited from the traditional COFF format. It is distinct from Microsoft Visual C++® debug information. A file can contain both a COFF symbol table and Visual C++ debug information, and the two are kept separate. Some Microsoft tools use the symbol table for limited but important purposes, such as communicating COMDAT information to the linker. Section names and file names, as well as code and data symbols, are listed in the symbol table.

The location of the symbol table is indicated in the COFF header.

The symbol table is an array of records, each 18 bytes long. Each record is either a standard or auxiliary symbol-table record. A standard record defines a symbol or name and has the following format.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 8 | Name (\*) | The name of the symbol, represented by a union of three structures. An array of 8 bytes is used if the name is not more than 8 bytes long. For more information, see section 5.4.1, “Symbol Name Representation.” |
| 8 | 4 | Value | The value that is associated with the symbol. The interpretation of this field depends on SectionNumber and StorageClass. A typical meaning is the relocatable address. |
| 12 | 2 | SectionNumber | The signed integer that identifies the section, using a one-based index into the section table. Some values have special meaning, as defined in section 5.4.2, “Section Number Values.” |
| 14 | 2 | Type | A number that represents type. Microsoft tools set this field to 0x20 (function) or 0x0 (not a function). For more information, see section 5.4.3, “Type Representation.” |
| 16 | 1 | StorageClass | An enumerated value that represents storage class. For more information, see section 5.4.4, “Storage Class.” |
| 17 | 1 | NumberOfAuxSymbols | The number of auxiliary symbol table entries that follow this record. |

Zero or more auxiliary symbol-table records immediately follow each standard symbol-table record. However, typically not more than one auxiliary symbol-table record follows a standard symbol-table record (except for **.file** records with long file names). Each auxiliary record is the same size as a standard symbol-table record (18 bytes), but rather than define a new symbol, the auxiliary record gives additional information on the last symbol defined. The choice of which of several formats to use depends on the StorageClass field. Currently-defined formats for auxiliary symbol table records are shown in section 5.5, “Auxiliary Symbol Records.”

Tools that read COFF symbol tables must ignore auxiliary symbol records whose interpretation is unknown. This allows the symbol table format to be extended to add new auxiliary records, without breaking existing tools.

### Symbol Name Representation

The ShortName field in a symbol table consists of 8 bytes that contain the name itself, if it is not more than 8 bytes long, or the ShortName field gives an offset into the string table. To determine whether the name itself or an offset is given, test the first 4 bytes for equality to zero.

By convention, the names are treated as zero-terminated UTF-8 encoded strings.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 8 | ShortName | An array of 8 bytes. This array is padded with nulls on the right if the name is less than 8 bytes long. |
| 0 | 4 | Zeroes | A field that is set to all zeros if the name is longer than 8 bytes. |
| 4 | 4 | Offset | An offset into the string table. |

### Section Number Values

Normally, the Section Value field in a symbol table entry is a one-based index into the section table. However, this field is a signed integer and can take negative values. The following values, less than one, have special meanings.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_SYM\_UNDEFINED | 0 | The symbol record is not yet assigned a section. A value of zero indicates that a reference to an external symbol is defined elsewhere. A value of non-zero is a common symbol with a size that is specified by the value. |
| IMAGE\_SYM\_ABSOLUTE | -1 | The symbol has an absolute (non-relocatable) value and is not an address. |
| IMAGE\_SYM\_DEBUG | -2 | The symbol provides general type or debugging information but does not correspond to a section. Microsoft tools use this setting along with .file records (storage class FILE). |

### Type Representation

The Type field of a symbol table entry contains 2 bytes, where each byte represents type information. The LSB represents the simple (base) data type, and the MSB represents the complex type, if any:

| **MSB** | **LSB** |
| --- | --- |
| Complex type: none, pointer, function, array. | Base type: integer, floating-point, and so on. |

The following values are defined for base type, although Microsoft tools generally do not use this field and set the LSB to 0. Instead, Visual C++ debug information is used to indicate types. However, the possible COFF values are listed here for completeness.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_SYM\_TYPE\_NULL | 0 | No type information or unknown base type. Microsoft tools use this setting |
| IMAGE\_SYM\_TYPE\_VOID | 1 | No valid type; used with void pointers and functions |
| IMAGE\_SYM\_TYPE\_CHAR | 2 | A character (signed byte) |
| IMAGE\_SYM\_TYPE\_SHORT | 3 | A 2-byte signed integer |
| IMAGE\_SYM\_TYPE\_INT | 4 | A natural integer type (normally 4 bytes in Windows) |
| IMAGE\_SYM\_TYPE\_LONG | 5 | A 4-byte signed integer |
| IMAGE\_SYM\_TYPE\_FLOAT | 6 | A 4-byte floating-point number |
| IMAGE\_SYM\_TYPE\_DOUBLE | 7 | An 8-byte floating-point number |
| IMAGE\_SYM\_TYPE\_STRUCT | 8 | A structure |
| IMAGE\_SYM\_TYPE\_UNION | 9 | A union |
| IMAGE\_SYM\_TYPE\_ENUM | 10 | An enumerated type |
| IMAGE\_SYM\_TYPE\_MOE | 11 | A member of enumeration (a specific value) |
| IMAGE\_SYM\_TYPE\_BYTE | 12 | A byte; unsigned 1-byte integer |
| IMAGE\_SYM\_TYPE\_WORD | 13 | A word; unsigned 2-byte integer |
| IMAGE\_SYM\_TYPE\_UINT | 14 | An unsigned integer of natural size (normally, 4 bytes) |
| IMAGE\_SYM\_TYPE\_DWORD | 15 | An unsigned 4-byte integer |

The most significant byte specifies whether the symbol is a pointer to, function returning, or array of the base type that is specified in the LSB. Microsoft tools use this field only to indicate whether the symbol is a function, so that the only two resulting values are 0x0 and 0x20 for the Type field. However, other tools can use this field to communicate more information.

It is very important to specify the function attribute correctly. This information is required for incremental linking to work correctly. For some architectures, the information may be required for other purposes.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_SYM\_DTYPE\_NULL | 0 | No derived type; the symbol is a simple scalar variable. |
| IMAGE\_SYM\_DTYPE\_POINTER | 1 | The symbol is a pointer to base type. |
| IMAGE\_SYM\_DTYPE\_FUNCTION | 2 | The symbol is a function that returns a base type. |
| IMAGE\_SYM\_DTYPE\_ARRAY | 3 | The symbol is an array of base type. |

### Storage Class

The StorageClass field of the symbol table indicates what kind of definition a symbol represents. The following table shows possible values. Note that the StorageClass field is an unsigned 1-byte integer. The special value -1 should therefore be taken to mean its unsigned equivalent, 0xFF.

Although the traditional COFF format uses many storage-class values, Microsoft tools rely on Visual C++ debug format for most symbolic information and generally use only four storage-class values: EXTERNAL (2), STATIC (3), FUNCTION (101), and STATIC (103). Except in the second column heading below, “Value” should be taken to mean the Value field of the symbol record (whose interpretation depends on the number found as the storage class).

| **Constant** | **Value** | **Description/interpretation of the Value field** |
| --- | --- | --- |
| IMAGE\_SYM\_CLASS\_END\_OF\_FUNCTION | -1 (0xFF) | A special symbol that represents the end of function, for debugging purposes. |
| IMAGE\_SYM\_CLASS\_NULL | 0 | No assigned storage class. |
| IMAGE\_SYM\_CLASS\_AUTOMATIC | 1 | The automatic (stack) variable. The Value field specifies the stack frame offset. |
| IMAGE\_SYM\_CLASS\_EXTERNAL | 2 | A value that Microsoft tools use for external symbols. The Value field indicates the size if the section number is IMAGE\_SYM\_UNDEFINED (0). If the section number is not zero, then the Value field specifies the offset within the section. |
| IMAGE\_SYM\_CLASS\_STATIC | 3 | The offset of the symbol within the section. If the Value field is zero, then the symbol represents a section name. |
| IMAGE\_SYM\_CLASS\_REGISTER | 4 | A register variable. The Value field specifies the register number. |
| IMAGE\_SYM\_CLASS\_EXTERNAL\_DEF | 5 | A symbol that is defined externally. |
| IMAGE\_SYM\_CLASS\_LABEL | 6 | A code label that is defined within the module. The Value field specifies the offset of the symbol within the section. |
| IMAGE\_SYM\_CLASS\_UNDEFINED\_LABEL | 7 | A reference to a code label that is not defined. |
| IMAGE\_SYM\_CLASS\_MEMBER\_OF\_STRUCT | 8 | The structure member. The Value field specifies the nth member. |
| IMAGE\_SYM\_CLASS\_ARGUMENT | 9 | A formal argument (parameter) of a function. The Value field specifies the nth argument. |
| IMAGE\_SYM\_CLASS\_STRUCT\_TAG | 10 | The structure tag-name entry. |
| IMAGE\_SYM\_CLASS\_MEMBER\_OF\_UNION | 11 | A union member. The Value field specifies the nth member. |
| IMAGE\_SYM\_CLASS\_UNION\_TAG | 12 | The Union tag-name entry. |
| IMAGE\_SYM\_CLASS\_TYPE\_DEFINITION | 13 | A Typedef entry. |
| IMAGE\_SYM\_CLASS\_UNDEFINED\_STATIC | 14 | A static data declaration. |
| IMAGE\_SYM\_CLASS\_ENUM\_TAG | 15 | An enumerated type tagname entry. |
| IMAGE\_SYM\_CLASS\_MEMBER\_OF\_ENUM | 16 | A member of an enumeration. The Value field specifies the nth member. |
| IMAGE\_SYM\_CLASS\_REGISTER\_PARAM | 17 | A register parameter. |
| IMAGE\_SYM\_CLASS\_BIT\_FIELD | 18 | A bit-field reference. The Value field specifies the nth bit in the bit field. |
| IMAGE\_SYM\_CLASS\_BLOCK | 100 | A .bb (beginning of block) or .eb (end of block) record. The Value field is the relocatable address of the code location. |
| IMAGE\_SYM\_CLASS\_FUNCTION | 101 | A value that Microsoft tools use for symbol records that define the extent of a function: begin function (.bf), end function (.ef), and lines in function (.lf). For .lf records, the Value field gives the number of source lines in the function. For .ef records, the Value field gives the size of the function code. |
| IMAGE\_SYM\_CLASS\_END\_OF\_STRUCT | 102 | An end-of-structure entry. |
| IMAGE\_SYM\_CLASS\_FILE | 103 | A value that Microsoft tools, as well as traditional COFF format, use for the source-file symbol record. The symbol is followed by auxiliary records that name the file. |
| IMAGE\_SYM\_CLASS\_SECTION | 104 | A definition of a section (Microsoft tools use STATIC storage class instead). |
| IMAGE\_SYM\_CLASS\_WEAK\_EXTERNAL | 105 | A weak external. For more information, see section 5.5.3, “Auxiliary Format 3: Weak Externals.” |
| IMAGE\_SYM\_CLASS\_CLR\_TOKEN | 107 | A CLR token symbol. The name is an ASCII string that consists of the hexadecimal value of the token. For more information, see section 5.5.7, “CLR Token Definition (Object Only).” |

## Auxiliary Symbol Records

Auxiliary symbol table records always follow, and apply to, some standard symbol table record. An auxiliary record can have any format that the tools can recognize, but 18 bytes must be allocated for them so that symbol table is maintained as an array of regular size. Currently, Microsoft tools recognize auxiliary formats for the following kinds of records: function definitions, function begin and end symbols (**.bf** and **.ef**), weak externals, file names, and section definitions.

The traditional COFF design also includes auxiliary-record formats for arrays and structures. Microsoft tools do not use these, but instead place that symbolic information in Visual C++ debug format in the debug sections.

### Auxiliary Format 1: Function Definitions

A symbol table record marks the beginning of a function definition if it has all of the following: a storage class of EXTERNAL (2), a Type value that indicates it is a function (0x20), and a section number that is greater than zero. Note that a symbol table record that has a section number of UNDEFINED (0) does not define the function and does not have an auxiliary record. Function-definition symbol records are followed by an auxiliary record in the format described below:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | TagIndex | The symbol-table index of the corresponding .bf (begin function) symbol record. |
| 4 | 4 | TotalSize | The size of the executable code for the function itself. If the function is in its own section, the SizeOfRawData in the section header is greater or equal to this field, depending on alignment considerations. |
| 8 | 4 | PointerToLinenumber | The file offset of the first COFF line-number entry for the function, or zero if none exists. For more information, see section 5.3, “COFF Line Numbers (Deprecated).” |
| 12 | 4 | PointerToNextFunction | The symbol-table index of the record for the next function. If the function is the last in the symbol table, this field is set to zero. |
| 16 | 2 | Unused | |

### Auxiliary Format 2: .bf and .ef Symbols

For each function definition in the symbol table, three items describe the beginning, ending, and number of lines. Each of these symbols has storage class FUNCTION (101):

* A symbol record named **.bf** (begin function). The Value field is unused.
* A symbol record named **.lf** (lines in function). The Value field gives the number of lines in the function.
* A symbol record named **.ef** (end of function). The Value field has the same number as the Total Size field in the function-definition symbol record.

The **.bf** and **.ef** symbol records (but not **.lf** records) are followed by an auxiliary record with the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Unused | |
| 4 | 2 | Linenumber | The actual ordinal line number (1, 2, 3, and so on) within the source file, corresponding to the .bf or .ef record. |
| 6 | 6 | Unused | |
| 12 | 4 | PointerToNextFunction (.bf only) | The symbol-table index of the next .bf symbol record. If the function is the last in the symbol table, this field is set to zero. It is not used for .ef records. |
| 16 | 2 | Unused | |

### Auxiliary Format 3: Weak Externals

“Weak externals” are a mechanism for object files that allows flexibility at link time. A module can contain an unresolved external symbol (sym1), but it can also include an auxiliary record that indicates that if sym1 is not present at link time, another external symbol (sym2) is used to resolve references instead.

If a definition of sym1 is linked, then an external reference to the symbol is resolved normally. If a definition of sym1 is not linked, then all references to the weak external for sym1 refer to sym2 instead. The external symbol, sym2, must always be linked; typically, it is defined in the module that contains the weak reference to sym1.

Weak externals are represented by a symbol table record with EXTERNAL storage class, UNDEF section number, and a value of zero. The weak-external symbol record is followed by an auxiliary record with the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | TagIndex | The symbol-table index of sym2, the symbol to be linked if sym1 is not found. |
| 4 | 4 | Characteristics | A value of IMAGE\_WEAK\_EXTERN\_SEARCH\_NOLIBRARY indicates that no library search for sym1 should be performed.  A value of IMAGE\_WEAK\_EXTERN\_SEARCH\_LIBRARY indicates that a library search for sym1 should be performed.  A value of IMAGE\_WEAK\_EXTERN\_SEARCH\_ALIAS indicates that sym1 is an alias for sym2. |
| 8 | 10 | Unused | |

Note that the Characteristics field is not defined in WINNT.H; instead, the Total Size field is used.

### Auxiliary Format 4: Files

This format follows a symbol-table record with storage class FILE (103). The symbol name itself should be **.file**, and the auxiliary record that follows it gives the name of a source-code file.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 18 | File Name | An ANSI string that gives the name of the source file. This is padded with nulls if it is less than the maximum length. |

### Auxiliary Format 5: Section Definitions

This format follows a symbol-table record that defines a section. Such a record has a symbol name that is the name of a section (such as **.text** or **.drectve**) and has storage class STATIC (3). The auxiliary record provides information about the section to which it refers. Thus, it duplicates some of the information in the section header.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Length | The size of section data; the same as SizeOfRawData in the section header. |
| 4 | 2 | NumberOfRelocations | The number of relocation entries for the section. |
| 6 | 2 | NumberOfLinenumbers | The number of line-number entries for the section. |
| 8 | 4 | CheckSum | The checksum for communal data. It is applicable if the IMAGE\_SCN\_LNK\_COMDAT flag is set in the section header. For more information, see section 5.5.6, “COMDAT Sections (Object Only).” |
| 12 | 2 | Number | One-based index into the section table for the associated section. This is used when the COMDAT selection setting is 5. |
| 14 | 1 | Selection | The COMDAT selection number. This is applicable if the section is a COMDAT section. |
| 15 | 3 | Unused | |

### COMDAT Sections (Object Only)

The Selection field of the section definition auxiliary format is applicable if the section is a COMDAT section. A COMDAT section is a section that can be defined by more than one object file. (The flag IMAGE\_SCN\_LNK\_COMDAT is set in the Section Flags field of the section header.) The Selection field determines the way in which the linker resolves the multiple definitions of COMDAT sections.

The first symbol that has the section value of the COMDAT section must be the section symbol. This symbol has the name of the section, the Value field equal to zero, the section number of the COMDAT section in question, the Type field equal to IMAGE\_SYM\_TYPE\_NULL, the Class field equal to IMAGE\_SYM\_CLASS\_STATIC, and one auxiliary record. The second symbol is called “the COMDAT symbol” and is used by the linker in conjunction with the Selection field.

The values for the Selection field are shown below.

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_COMDAT\_SELECT\_NODUPLICATES | 1 | If this symbol is already defined, the linker issues a "multiply defined symbol" error. |
| IMAGE\_COMDAT\_SELECT\_ANY | 2 | Any section that defines the same COMDAT symbol can be linked; the rest are removed. |
| IMAGE\_COMDAT\_SELECT\_SAME\_SIZE | 3 | The linker chooses an arbitrary section among the definitions for this symbol. If all definitions are not the same size, a "multiply defined symbol" error is issued. |
| IMAGE\_COMDAT\_SELECT\_EXACT\_MATCH | 4 | The linker chooses an arbitrary section among the definitions for this symbol. If all definitions do not match exactly, a "multiply defined symbol" error is issued. |
| IMAGE\_COMDAT\_SELECT\_ASSOCIATIVE | 5 | The section is linked if a certain other COMDAT section is linked. This other section is indicated by the Number field of the auxiliary symbol record for the section definition. This setting is useful for definitions that have components in multiple sections (for example, code in one and data in another), but where all must be linked or discarded as a set. The other section with which this section is associated must be a COMDAT section; it cannot be another associative COMDAT section (that is, the other section cannot have IMAGE\_COMDAT\_SELECT\_ASSOCIATIVE set). |
| IMAGE\_COMDAT\_SELECT\_LARGEST | 6 | The linker chooses the largest definition from among all of the definitions for this symbol. If multiple definitions have this size, the choice between them is arbitrary. |

### CLR Token Definition (Object Only)

This auxiliary symbol generally follows the IMAGE\_SYM\_CLASS\_CLR\_TOKEN. It is used to associate a token with the COFF symbol table’s namespace.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 1 | bAuxType | Must be IMAGE\_AUX\_SYMBOL\_TYPE\_TOKEN\_DEF (1). |
| 1 | 1 | bReserved | Reserved, must be zero. |
| 2 | 4 | SymbolTableIndex | The symbol index of the COFF symbol to which this CLR token definition refers. |
| 6 | 12 |  | Reserved, must be zero. |

## COFF String Table

Immediately following the COFF symbol table is the COFF string table. The position of this table is found by taking the symbol table address in the COFF header and adding the number of symbols multiplied by the size of a symbol.

At the beginning of the COFF string table are 4 bytes that contain the total size (in bytes) of the rest of the string table. This size includes the size field itself, so that the value in this location would be 4 if no strings were present.

Following the size are null-terminated strings that are pointed to by symbols in the COFF symbol table.

## The Attribute Certificate Table (Image Only)

Attribute certificates can be associated with an image by adding an attribute certificate table. The attribute certificate table is composed of a set of contiguous, quadword-aligned attribute certificate entries. Zero padding is inserted between the original end of the file and the beginning of the attribute certificate table to achieve this alignment. Each attribute certificate entry contains the following fields.

|  |  |  |  |
| --- | --- | --- | --- |
| **Offset** | **Size** | **Field** | **Description** |
| 0 | 4 | dwLength | Specifies the length of the attribute certificate entry. |
| 4 | 2 | wRevision | Contains the certificate version number. For details, see the following text. |
| 6 | 2 | wCertificateType | Specifies the type of content in **bCertificate**. For details, see the following text. |
| 8 | See the following | bCertificate | Contains a certificate, such as an Authenticode signature. For details, see the following text. |

The virtual address value from the Certificate Table entry in the Optional Header Data Directory is a file offset to the first attribute certificate entry. Subsequent entries are accessed by advancing that entry’s dwLength bytes, rounded up to an 8‑byte multiple, from the start of the current attribute certificate entry. This continues until the sum of the rounded dwLength values equals the Size value from the Certificates Table entry in the Optional Header Data Directory. If the sum of the rounded dwLength values does not equal the Size value, then either the attribute certificate table or the Size field is corrupted.

For example, if the Optional Header Data Directory’s Certificate Table Entry contains:

virtual address = 0x5000

size = 0x1000

The first certificate starts at offset 0x5000 from the start of the file on disk. To advance through all the attribute certificate entries:

1. Add the first attribute certificate's dwLength value to the starting offset.

2. Round the value from step 1 up to the nearest 8-byte multiple to find the offset of the second attribute certificate entry.

3. Add the offset value from step 2 to the second attribute certificate entry's dwLength value and round up to the nearest 8-byte multiple to determine the offset of the third attribute certificate entry.

4. Repeat step 3 for each successive certificate until the calculated offset equals 0x6000 (0x5000 start + 0x1000 total size), which indicates that you’ve walked the entire table.

Alternatively, you can enumerate the certificate entries by calling the Win32® **ImageEnumerateCertificates** function in a loop. For a link to the function's reference page, see “References.”

Attribute certificate table entries can contain any certificate type, as long as the entry has the correct dwLength value, a unique wRevision value, and a unique wCertificateType value. The most common type of certificate table entry is a WIN\_CERTIFICATE structure, which is documented in Wintrust.h and discussed in the remainder of this section.

The options for the WIN\_CERTIFICATE **wRevision** member include (but are not limited to) the following.

|  |  |  |
| --- | --- | --- |
| **Value** | **Name** | **Notes** |
| 0x0100 | WIN\_CERT\_REVISION\_1\_0 | Version 1, legacy version of the Win\_Certificate structure. It is supported only for purposes of verifying legacy Authenticode signatures |
| 0x0200 | WIN\_CERT\_REVISION\_2\_0 | Version 2 is the current version of the Win\_Certificate structure. |

The options for the WIN\_CERTIFICATE **wCertificateType** member include (but are not limited to) the items in the following table. Note that some values are not currently supported.

| **Value** | **Name** | **Notes** |
| --- | --- | --- |
| 0x0001 | WIN\_CERT\_TYPE\_X509 | **bCertificate** contains an X.509 Certificate  Not Supported |
| 0x0002 | WIN\_CERT\_TYPE\_PKCS\_SIGNED\_DATA | **bCertificate** contains a PKCS#7 SignedData structure |
| 0x0003 | WIN\_CERT\_TYPE\_RESERVED\_1 | Reserved |
| 0x0004 | WIN\_CERT\_TYPE\_TS\_STACK\_SIGNED | Terminal Server Protocol Stack Certificate signing  Not Supported |

The WIN\_CERTIFICATE structure's **bCertificate** member contains a variable-length byte array with the content type specified by **wCertificateType**. The type supported by Authenticode is WIN\_CERT\_TYPE\_PKCS\_SIGNED\_DATA, a PKCS#7 **SignedData** structure. For details on the Authenticode digital signature format, see “Windows Authenticode Portable Executable Signature Format.”

If the **bCertificate** content does not end on a quadword boundary, the attribute certificate entry is padded with zeros, from the end of **bCertificate** to the next quadword boundary.

The **dwLength** value is the length of the finalized WIN\_CERTIFICATE structure and is computed as:

dwLength = offsetof(WIN\_CERTIFICATE, bCertificate) + (size of the variable-length binary array contained within bCertificate)

This length should include the size of any padding that is used to satisfy the requirement that each WIN\_CERTIFICATE structure is quadword aligned:

dwLength += (8 – (dwLength & 7)) & 7;

The **Certificate Table size**—specified in the **Certificates Table** entry in the **Optional Header Data Directory** (section 3.4.3)—includes the padding.

For more information on using the ImageHlp API to enumerate, add, and remove certificates from PE Files, see “ImageHlp Functions.”

### Certificate Data

As stated in the preceding section, the certificates in the attribute certificate table can contain any certificate type. Certificates that ensure a PE file's integrity may include a PE image hash.

A PE image hash (or file hash) is similar to a file checksum in that the hash algorithm produces a message digest that is related to the integrity of a file. However, a checksum is produced by a simple algorithm and is used primarily to detect whether a block of memory on disk has gone bad and the values stored there have become corrupted. A file hash is similar to a checksum in that it also detects file corruption. However, unlike most checksum algorithms, it is very difficult to modify a file without changing the file hash from its original unmodified value. A file hash can thus be used to detect intentional and even subtle modifications to a file, such as those introduced by viruses, hackers, or Trojan horse programs.

When included in a certificate, the image digest must exclude certain fields in the PE Image, such as the Checksum and Certificate Table entry in Optional Header Data Directories. This is because the act of adding a Certificate changes these fields and would cause a different hash value to be calculated.

The Win32 **ImageGetDigestStream** function provides a data stream from a target PE file with which to hash functions. This data stream remains consistent when certificates are added to or removed from a PE file. Based on the parameters that are passed to **ImageGetDigestStream**, other data from the PE image can be omitted from the hash computation. For a link to the function's reference page, see ”References.”

## Delay-Load Import Tables (Image Only)

These tables were added to the image to support a uniform mechanism for applications to delay the loading of a DLL until the first call into that DLL. The layout of the tables matches that of the traditional import tables that are described in section 6.4, "The .idata Section." Only a few details are discussed here.

### The Delay-Load Directory Table

The delay-load directory table is the counterpart to the import directory table. It can be retrieved through the Delay Import Descriptor entry in the optional header data directories list (offset 200). The table is arranged as follows:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Attributes | Must be zero. |
| 4 | 4 | Name | The RVA of the name of the DLL to be loaded. The name resides in the read-only data section of the image. |
| 8 | 4 | Module Handle | The RVA of the module handle (in the data section of the image) of the DLL to be delay-loaded. It is used for storage by the routine that is supplied to manage delay-loading. |
| 12 | 4 | Delay Import Address Table | The RVA of the delay-load import address table. For more information, see section 5.8.5, "Delay Import Address Table (IAT)." |
| 16 | 4 | Delay Import Name Table | The RVA of the delay-load name table, which contains the names of the imports that might need to be loaded. This matches the layout of the import name table. For more information, see section 6.4.3, "Hint/Name Table." |
| 20 | 4 | Bound Delay Import Table | The RVA of the bound delay-load address table, if it exists. |
| 24 | 4 | Unload Delay Import Table | The RVA of the unload delay-load address table, if it exists. This is an exact copy of the delay import address table. If the caller unloads the DLL, this table should be copied back over the delay import address table so that subsequent calls to the DLL continue to use the thunking mechanism correctly. |
| 28 | 4 | Time Stamp | The timestamp of the DLL to which this image has been bound. |

The tables that are referenced in this data structure are organized and sorted just as their counterparts are for traditional imports. For details, see section 6.4, "The .idata Section."

### Attributes

As yet, no attribute flags are defined. The linker sets this field to zero in the image. This field can be used to extend the record by indicating the presence of new fields, or it can be used to indicate behaviors to the delay or unload helper functions.

### Name

The name of the DLL to be delay-loaded resides in the read-only data section of the image. It is referenced through the szName field.

### Module Handle

The handle of the DLL to be delay-loaded is in the data section of the image. The phmod field points to the handle. The supplied delay-load helper uses this location to store the handle to the loaded DLL.

### Delay Import Address Table

The delay import address table (IAT) is referenced by the delay import descriptor through the pIAT field. The delay-load helper updates these pointers with the real entry points so that the thunks are no longer in the calling loop. The function pointers are accessed by using the expression pINT->u1.Function.

### Delay Import Name Table

The delay import name table (INT) contains the names of the imports that might require loading. They are ordered in the same fashion as the function pointers in the IAT. They consist of the same structures as the standard INT and are accessed by using the expression pINT->u1.AddressOfData->Name[0].

### Delay Bound Import Address Table and Time Stamp

The delay bound import address table (BIAT) is an optional table of IMAGE\_THUNK\_DATA items that is used along with the timestamp field of the delay-load directory table by a post-process binding phase.

### Delay Unload Import Address Table

The delay unload import address table (UIAT) is an optional table of IMAGE\_THUNK\_DATA items that the unload code uses to handle an explicit unload request. It consists of initialized data in the read-only section that is an exact copy of the original IAT that referred the code to the delay-load thunks. On the unload request, the library can be freed, the \*phmod cleared, and the UIAT written over the IAT to restore everything to its preload state.

# Special Sections

Typical COFF sections contain code or data that linkers and Microsoft Win32® loaders process without special knowledge of the section contents. The contents are relevant only to the application that is being linked or executed.

However, some COFF sections have special meanings when found in object files or image files. Tools and loaders recognize these sections because they have special flags set in the section header, because special locations in the image optional header point to them, or because the section name itself indicates a special function of the section. (Even if the section name itself does not indicate a special function of the section, the section name is dictated by convention, so the authors of this specification can refer to a section name in all cases.)

The reserved sections and their attributes are described in the table below, followed by detailed descriptions for the section types that are persisted into executables and the section types that contain metadata for extensions.

| Section Name | Content | Characteristics |
| --- | --- | --- |
| .bss | Uninitialized data (free format) | IMAGE\_SCN\_CNT\_UNINITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE |
| .cormeta | CLR metadata that indicates that the object file contains managed code | IMAGE\_SCN\_LNK\_INFO |
| .data | Initialized data (free format) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE |
| .debug$F | Generated FPO debug information (object only, x86 architecture only, and now obsolete) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_DISCARDABLE |
| .debug$P | Precompiled debug types (object only) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_DISCARDABLE |
| .debug$S | Debug symbols (object only) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_DISCARDABLE |
| .debug$T | Debug types (object only) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_DISCARDABLE |
| .drective | Linker options | IMAGE\_SCN\_LNK\_INFO |
| .edata | Export tables | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ |
| .idata | Import tables | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE |
| .idlsym | Includes registered SEH (image only) to support IDL attributes. For information, see "IDL Attributes" in "References" at the end of this specification. | IMAGE\_SCN\_LNK\_INFO |
| .pdata | Exception information | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ |
| .rdata | Read-only initialized data | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ |
| .reloc | Image relocations | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_DISCARDABLE |
| .rsrc | Resource directory | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ |
| .sbss | GP-relative uninitialized data (free format) | IMAGE\_SCN\_CNT\_UNINITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE | IMAGE \_SCN\_GPREL The IMAGE\_SCN\_GPREL flag should be set for IA64 architectures only; this flag is not valid for other architectures. The IMAGE\_SCN\_GPREL flag is for object files only; when this section type appears in an image file, the IMAGE\_SCN\_GPREL flag must not be set. |
| .sdata | GP-relative initialized data (free format) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE | IMAGE \_SCN\_GPREL The IMAGE\_SCN\_GPREL flag should be set for IA64 architectures only; this flag is not valid for other architectures. The IMAGE\_SCN\_GPREL flag is for object files only; when this section type appears in an image file, the IMAGE\_SCN\_GPREL flag must not be set. |
| .srdata | GP-relative read-only data (free format) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE \_SCN\_GPREL The IMAGE\_SCN\_GPREL flag should be set for IA64 architectures only; this flag is not valid for other architectures. The IMAGE\_SCN\_GPREL flag is for object files only; when this section type appears in an image file, the IMAGE\_SCN\_GPREL flag must not be set. |
| .sxdata | Registered exception handler data (free format and x86/object only) | IMAGE\_SCN\_LNK\_INFO Contains the symbol index of each of the exception handlers being referred to by the code in that object file. The symbol can be for an UNDEF symbol or one that is defined in that module. |
| .text | Executable code (free format) | IMAGE\_SCN\_CNT\_CODE | IMAGE\_SCN\_MEM\_EXECUTE | IIMAGE\_SCN\_MEM\_READ |
| .tls | Thread-local storage (object only) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE |
| .tls$ | Thread-local storage (object only) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE |
| .vsdata | GP-relative initialized data (free format and for ARM, SH4, and Thumb architectures only) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ | IMAGE\_SCN\_MEM\_WRITE |
| .xdata | Exception information (free format) | IMAGE\_SCN\_CNT\_INITIALIZED\_DATA | IMAGE\_SCN\_MEM\_READ |

Some of the sections listed here are marked “object only” or “image only” to indicate that their special semantics are relevant only for object files or image files, respectively. A section that is marked “image only” might still appear in an object file as a way of getting into the image file, but the section has no special meaning to the linker, only to the image file loader.

## The .debug Section

The **.debug** section is used in object files to contain compiler-generated debug information and in image files to contain all of the debug information that is generated. This section describes the packaging of debug information in object and image files.

The next section describes the format of the debug directory, which can be anywhere in the image. Subsequent sections describe the “groups” in object files that contain debug information.

The default for the linker is that debug information is not mapped into the address space of the image. A .**debug** section exists only when debug information is mapped in the address space.

### Debug Directory (Image Only)

Image files contain an optional debug directory that indicates what form of debug information is present and where it is. This directory consists of an array of debug directory entries whose location and size are indicated in the image optional header.

The debug directory can be in a discardable **.debug** section (if one exists), or it can be included in any other section in the image file, or not be in a section at all.

Each debug directory entry identifies the location and size of a block of debug information. The specified RVA can be zero if the debug information is not covered by a section header (that is, it resides in the image file and is not mapped into the run-time address space). If it is mapped, the RVA is its address.

A debug directory entry has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Characteristics | Reserved, must be zero. |
| 4 | 4 | TimeDateStamp | The time and date that the debug data was created. |
| 8 | 2 | MajorVersion | The major version number of the debug data format. |
| 10 | 2 | MinorVersion | The minor version number of the debug data format. |
| 12 | 4 | Type | The format of debugging information. This field enables support of multiple debuggers. For more information, see section 6.1.2, “Debug Type.” |
| 16 | 4 | SizeOfData | The size of the debug data (not including the debug directory itself). |
| 20 | 4 | AddressOfRawData | The address of the debug data when loaded, relative to the image base. |
| 24 | 4 | PointerToRawData | The file pointer to the debug data. |

### Debug Type

The following values are defined for the Type field of the debug directory entry:

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_DEBUG\_TYPE\_UNKNOWN | 0 | An unknown value that is ignored by all tools. |
| IMAGE\_DEBUG\_TYPE\_COFF | 1 | The COFF debug information (line numbers, symbol table, and string table). This type of debug information is also pointed to by fields in the file headers. |
| IMAGE\_DEBUG\_TYPE\_CODEVIEW | 2 | The Visual C++ debug information. |
| IMAGE\_DEBUG\_TYPE\_FPO | 3 | The frame pointer omission (FPO) information. This information tells the debugger how to interpret nonstandard stack frames, which use the EBP register for a purpose other than as a frame pointer. |
| IMAGE\_DEBUG\_TYPE\_MISC | 4 | The location of DBG file. |
| IMAGE\_DEBUG\_TYPE\_EXCEPTION | 5 | A copy of .pdata section. |
| IMAGE\_DEBUG\_TYPE\_FIXUP | 6 | Reserved. |
| IMAGE\_DEBUG\_TYPE\_OMAP\_TO\_SRC | 7 | The mapping from an RVA in image to an RVA in source image. |
| IMAGE\_DEBUG\_TYPE\_OMAP\_FROM\_SRC | 8 | The mapping from an RVA in source image to an RVA in image. |
| IMAGE\_DEBUG\_TYPE\_BORLAND | 9 | Reserved for Borland. |
| IMAGE\_DEBUG\_TYPE\_RESERVED10 | 10 | Reserved. |
| IMAGE\_DEBUG\_TYPE\_CLSID | 11 | Reserved. |
| IMAGE\_DEBUG\_TYPE\_REPRO | 16 | PE determinism or reproducibility. |

If the Type field is set to IMAGE\_DEBUG\_TYPE\_FPO, the debug raw data is an array in which each member describes the stack frame of a function. Not every function in the image file must have FPO information defined for it, even though debug type is FPO. Those functions that do not have FPO information are assumed to have normal stack frames. The format for FPO information is as follows:

#define FRAME\_FPO 0

#define FRAME\_TRAP 1

#define FRAME\_TSS 2

typedef struct \_FPO\_DATA {

DWORD ulOffStart; // offset 1st byte of function code

DWORD cbProcSize; // # bytes in function

DWORD cdwLocals; // # bytes in locals/4

WORD cdwParams; // # bytes in params/4

WORD cbProlog : 8; // # bytes in prolog

WORD cbRegs : 3; // # regs saved

WORD fHasSEH : 1; // TRUE if SEH in func

WORD fUseBP : 1; // TRUE if EBP has been allocated

WORD reserved : 1; // reserved for future use

WORD cbFrame : 2; // frame type

} FPO\_DATA;

The presence of an entry of type IMAGE\_DEBUG\_TYPE\_REPRO indicates the PE file is built in a way to achieve determinism or reproducibility. If the input does not change, the output PE file is guaranteed to be bit-for-bit identical no matter when or where the PE is produced. Various date/time stamp fields in the PE file are filled with part or all the bits from a calculated hash value that uses PE file content as input, and therefore no longer represent the actual date and time when a PE file or related specific data within the PE is produced. The raw data of this debug entry may be empty, or may contain a calculated hash value preceded by a four-byte value that represents the hash value length.

### .debug$F (Object Only)

The data in this section has been superseded in Visual C++ version 7.0 and later by a more extensive set of data that is emitted into a **.debug$S** subsection.

Object files can contain **.debug$F** sections whose contents are one or more FPO\_DATA records (frame pointer omission information). See “IMAGE\_DEBUG\_TYPE\_FPO” in section 6.1.2, "Debug Type."

The linker recognizes these **.debug$F** records. If debug information is being generated, the linker sorts the FPO\_DATA records by procedure RVA and generates a debug directory entry for them.

The compiler should not generate FPO records for procedures that have a standard frame format.

### .debug$S (Object Only)

This section contains Visual C++ debug information (symbolic information).

### .debug$P (Object Only)

This section contains Visual C++ debug information (precompiled information). These are shared types among all of the objects that were compiled by using the precompiled header that was generated with this object.

### .debug$T (Object Only)

This section contains Visual C++ debug information (type information).

### Linker Support for Microsoft Debug Information

To support debug information, the linker:

* Gathers all relevant debug data from the **.debug$F, debug$S, .debug$P,** and **.debug$T** sections.
* Processes that data along with the linker-generated debugging information into the PDB file, and creates a debug directory entry to refer to it.

## The .drectve Section (Object Only)

A section is a directive section if it has the IMAGE\_SCN\_LNK\_INFO flag set in the section header and has the .drectve section name. The linker removes a **.drectve** section after processing the information, so the section does not appear in the image file that is being linked.

A **.drectve** section consists of a string of text that can be encoded as ANSI or UTF‑8. If the UTF‑8 byte order marker (BOM, a three-byte prefix that consists of 0xEF, 0xBB, and 0xBF) is not present, the directive string is interpreted as ANSI. The directive string is a series of linker options that are separated by spaces. Each option contains a hyphen, the option name, and any appropriate attribute. If an option contains spaces, the option must be enclosed in quotes. The **.drectve** section must not have relocations or line numbers.

## The .edata Section (Image Only)

The export data section, named **.edata**, contains information about symbols that other images can access through dynamic linking. Exported symbols are generally found in DLLs, but DLLs can also import symbols.

An overview of the general structure of the export section is described below. The tables described are usually contiguous in the file in the order shown (though this is not required). Only the export directory table and export address table are required to export symbols as ordinals. (An ordinal is an export that is accessed directly by its export address table index.) The name pointer table, ordinal table, and export name table all exist to support use of export names.

| **Table Name** | **Description** |
| --- | --- |
| Export directory table | A table with just one row (unlike the debug directory). This table indicates the locations and sizes of the other export tables. |
| Export address table | An array of RVAs of exported symbols. These are the actual addresses of the exported functions and data within the executable code and data sections. Other image files can import a symbol by using an index to this table (an ordinal) or, optionally, by using the public name that corresponds to the ordinal if a public name is defined. |
| Name pointer table | An array of pointers to the public export names, sorted in ascending order. |
| Ordinal table | An array of the ordinals that correspond to members of the name pointer table. The correspondence is by position; therefore, the name pointer table and the ordinal table must have the same number of members. Each ordinal is an index into the export address table. |
| Export name table | A series of null-terminated ASCII strings. Members of the name pointer table point into this area. These names are the public names through which the symbols are imported and exported; they are not necessarily the same as the private names that are used within the image file. |

When another image file imports a symbol by name, the Win32 loader searches the name pointer table for a matching string. If a matching string is found, the associated ordinal is identified by looking up the corresponding member in the ordinal table (that is, the member of the ordinal table with the same index as the string pointer found in the name pointer table). The resulting ordinal is an index into the export address table, which gives the actual location of the desired symbol. Every export symbol can be accessed by an ordinal.

When another image file imports a symbol by ordinal, it is unnecessary to search the name pointer table for a matching string. Direct use of an ordinal is therefore more efficient. However, an export name is easier to remember and does not require the user to know the table index for the symbol.

### Export Directory Table

The export symbol information begins with the export directory table, which describes the remainder of the export symbol information. The export directory table contains address information that is used to resolve imports to the entry points within this image.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Export Flags | Reserved, must be 0. |
| 4 | 4 | Time/Date Stamp | The time and date that the export data was created. |
| 8 | 2 | Major Version | The major version number. The major and minor version numbers can be set by the user. |
| 10 | 2 | Minor Version | The minor version number. |
| 12 | 4 | Name RVA | The address of the ASCII string that contains the name of the DLL. This address is relative to the image base. |
| 16 | 4 | Ordinal Base | The starting ordinal number for exports in this image. This field specifies the starting ordinal number for the export address table. It is usually set to 1. |
| 20 | 4 | Address Table Entries | The number of entries in the export address table. |
| 24 | 4 | Number of Name Pointers | The number of entries in the name pointer table. This is also the number of entries in the ordinal table. |
| 28 | 4 | Export Address Table RVA | The address of the export address table, relative to the image base. |
| 32 | 4 | Name Pointer RVA | The address of the export name pointer table, relative to the image base. The table size is given by the Number of Name Pointers field. |
| 36 | 4 | Ordinal Table RVA | The address of the ordinal table, relative to the image base. |

### Export Address Table

The export address table contains the address of exported entry points and exported data and absolutes. An ordinal number is used as an index into the export address table.

Each entry in the export address table is a field that uses one of two formats in the following table. If the address specified is *not* within the export section (as defined by the address and length that are indicated in the optional header), the field is an export RVA, which is an actual address in code or data. Otherwise, the field is a forwarder RVA, which names a symbol in another DLL.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Export RVA | The address of the exported symbol when loaded into memory, relative to the image base. For example, the address of an exported function. |
| 0 | 4 | Forwarder RVA | The pointer to a null-terminated ASCII string in the export section. This string must be within the range that is given by the export table data directory entry. See section 3.4.3, "Optional Header Data Directories (Image Only)." This string gives the DLL name and the name of the export (for example, “MYDLL.expfunc”) or the DLL name and the ordinal number of the export (for example, “MYDLL.#27”). |

A forwarder RVA exports a definition from some other image, making it appear as if it were being exported by the current image. Thus, the symbol is simultaneously imported and exported.

For example, in Kernel32.dll in Windows XP, the export named “HeapAlloc” is forwarded to the string “NTDLL.RtlAllocateHeap.” This allows applications to use the Windows XP–specific module Ntdll.dll without actually containing import references to it. The application’s import table refers only to Kernel32.dll. Therefore, the application is not specific to Windows XP and can run on any Win32 system.

### Export Name Pointer Table

The export name pointer table is an array of addresses (RVAs) into the export name table. The pointers are 32 bits each and are relative to the image base. The pointers are ordered lexically to allow binary searches.

An export name is defined only if the export name pointer table contains a pointer to it.

### Export Ordinal Table

The export ordinal table is an array of 16-bit indexes into the export address table. The ordinals are biased by the Ordinal Base field of the export directory table. In other words, the ordinal base must be subtracted from the ordinals to obtain true indexes into the export address table.

The export name pointer table and the export ordinal table form two parallel arrays that are separated to allow natural field alignment. These two tables, in effect, operate as one table, in which the Export Name Pointer column points to a public (exported) name and the Export Ordinal column gives the corresponding ordinal for that public name. A member of the export name pointer table and a member of the export ordinal table are associated by having the same position (index) in their respective arrays.

Thus, when the export name pointer table is searched and a matching string is found at position *i*, the algorithm for finding the symbol’s address is:

i = Search\_ExportNamePointerTable (ExportName);

ordinal = ExportOrdinalTable [i];

SymbolRVA = ExportAddressTable [ordinal - OrdinalBase];

### Export Name Table

The export name table contains the actual string data that was pointed to by the export name pointer table. The strings in this table are public names that other images can use to import the symbols. These public export names are not necessarily the same as the private symbol names that the symbols have in their own image file and source code, although they can be.

Every exported symbol has an ordinal value, which is just the index into the export address table (plus the Ordinal Base value). Use of export names, however, is optional. Some, all, or none of the exported symbols can have export names. For exported symbols that do have export names, corresponding entries in the export name pointer table and export ordinal table work together to associate each name with an ordinal.

The structure of the export name table is a series of null-terminated ASCII strings of variable length.

## The .idata Section

All image files that import symbols, including virtually all executable (EXE) files, have an **.idata** section. A typical file layout for the import information follows:

|  |
| --- |
| Directory Table |
| Null Directory Entry |

|  |
| --- |
| DLL1 Import Lookup Table |
| Null |

|  |
| --- |
| DLL2 Import Lookup Table |
| Null |

|  |
| --- |
| DLL3 Import Lookup Table |
| Null |

|  |
| --- |
| Hint-Name Table |

Figure 3. Typical Import Section Layout

### Import Directory Table

The import information begins with the import directory table, which describes the remainder of the import information. The import directory table contains address information that is used to resolve fixup references to the entry points within a DLL image. The import directory table consists of an array of import directory entries, one entry for each DLL to which the image refers. The last directory entry is empty (filled with null values), which indicates the end of the directory table.

Each import directory entry has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Import Lookup Table RVA (Characteristics) | The RVA of the import lookup table. This table contains a name or ordinal for each import. (The name “Characteristics” is used in Winnt.h, but no longer describes this field.) |
| 4 | 4 | Time/Date Stamp | The stamp that is set to zero until the image is bound. After the image is bound, this field is set to the time/data stamp of the DLL. |
| 8 | 4 | Forwarder Chain | The index of the first forwarder reference. |
| 12 | 4 | Name RVA | The address of an ASCII string that contains the name of the DLL. This address is relative to the image base. |
| 16 | 4 | Import Address Table RVA (Thunk Table) | The RVA of the import address table. The contents of this table are identical to the contents of the import lookup table until the image is bound. |

### Import Lookup Table

An import lookup table is an array of 32-bit numbers for PE32 or an array of 64-bit numbers for PE32+. Each entry uses the bit-field format that is described in the following table. In this format, bit 31 is the most significant bit for PE32 and bit 63 is the most significant bit for PE32+. The collection of these entries describes all imports from a given DLL. The last entry is set to zero (NULL) to indicate the end of the table.

| **Bit(s)** | **Size** | **Bit field** | **Description** |
| --- | --- | --- | --- |
| 31/63 | 1 | Ordinal/Name Flag | If this bit is set, import by ordinal. Otherwise, import by name. Bit is masked as 0x80000000 for PE32, 0x8000000000000000 for PE32+. |
| 15-0 | 16 | Ordinal Number | A 16-bit ordinal number. This field is used only if the Ordinal/Name Flag bit field is 1 (import by ordinal). Bits 30-15 or 62-15 must be 0. |
| 30–0 | 31 | Hint/Name Table RVA | A 31-bit RVA of a hint/name table entry. This field is used only if the Ordinal/Name Flag bit field is 0 (import by name). For PE32+ bits 62-31 must be zero. |

### Hint/Name Table

One hint/name table suffices for the entire import section. Each entry in the hint/name table has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | Hint | An index into the export name pointer table. A match is attempted first with this value. If it fails, a binary search is performed on the DLL’s export name pointer table. |
| 2 | variable | Name | An ASCII string that contains the name to import. This is the string that must be matched to the public name in the DLL. This string is case sensitive and terminated by a null byte. |
| \* | 0 or 1 | Pad | A trailing zero-pad byte that appears after the trailing null byte, if necessary, to align the next entry on an even boundary. |

### Import Address Table

The structure and content of the import address table are identical to those of the import lookup table, until the file is bound. During binding, the entries in the import address table are overwritten with the 32-bit (for PE32) or 64-bit (for PE32+) addresses of the symbols that are being imported. These addresses are the actual memory addresses of the symbols, although technically they are still called “virtual addresses.” The loader typically processes the binding.

## The .pdata Section

The .pdata section contains an array of function table entries that are used for exception handling. It is pointed to by the exception table entry in the image data directory. The entries must be sorted according to the function addresses (the first field in each structure) before being emitted into the final image. The target platform determines which of the three function table entry format variations described below is used.

For 32-bit MIPS images, function table entries have the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Begin Address | The VA of the corresponding function. |
| 4 | 4 | End Address | The VA of the end of the function. |
| 8 | 4 | Exception Handler | The pointer to the exception handler to be executed. |
| 12 | 4 | Handler Data | The pointer to additional information to be passed to the handler. |
| 16 | 4 | Prolog End Address | The VA of the end of the function’s prolog. |

For the ARM, PowerPC, SH3 and SH4 Windows CE platforms, function table entries have the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Begin Address | The VA of the corresponding function. |
| 4 | 8 bits | Prolog Length | The number of instructions in the function’s prolog. |
| 4 | 22 bits | Function Length | The number of instructions in the function. |
| 4 | 1 bit | 32-bit Flag | If set, the function consists of 32-bit instructions. If clear, the function consists of 16-bit instructions. |
| 4 | 1 bit | Exception Flag | If set, an exception handler exists for the function. Otherwise, no exception handler exists. |

For x64 and Itanium platforms, function table entries have the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Begin Address | The RVA of the corresponding function. |
| 4 | 4 | End Address | The RVA of the end of the function. |
| 8 | 4 | Unwind Information | The RVA of the unwind information. |

## The .reloc Section (Image Only)

The base relocation table contains entries for all base relocations in the image. The Base Relocation Table field in the optional header data directories gives the number of bytes in the base relocation table. For more information, see section 3.4.3, "Optional Header Data Directories (Image Only)." The base relocation table is divided into blocks. Each block represents the base relocations for a 4K page. Each block must start on a 32-bit boundary.

The loader is not required to process base relocations that are resolved by the linker, unless the load image cannot be loaded at the image base that is specified in the PE header.

### Base Relocation Block

Each base relocation block starts with the following structure:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Page RVA | The image base plus the page RVA is added to each offset to create the VA where the base relocation must be applied. |
| 4 | 4 | Block Size | The total number of bytes in the base relocation block, including the Page RVA and Block Size fields and the Type/Offset fields that follow. |

The Block Size field is then followed by any number of Type or Offset field entries. Each entry is a WORD (2 bytes) and has the following structure:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 bits | Type | Stored in the high 4 bits of the WORD, a value that indicates the type of base relocation to be applied. For more information, see section 6.6.2, “Base Relocation Types.” |
| 0 | 12 bits | Offset | Stored in the remaining 12 bits of the WORD, an offset from the starting address that was specified in the Page RVA field for the block. This offset specifies where the base relocation is to be applied. |

To apply a base relocation, the difference is calculated between the preferred base address and the base where the image is actually loaded. If the image is loaded at its preferred base, the difference is zero and thus the base relocations do not have to be applied.

### Base Relocation Types

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMAGE\_REL\_BASED\_ABSOLUTE | 0 | The base relocation is skipped. This type can be used to pad a block. |
| IMAGE\_REL\_BASED\_HIGH | 1 | The base relocation adds the high 16 bits of the difference to the 16-bit field at offset. The 16-bit field represents the high value of a 32-bit word. |
| IMAGE\_REL\_BASED\_LOW | 2 | The base relocation adds the low 16 bits of the difference to the 16-bit field at offset. The 16-bit field represents the low half of a 32-bit word. |
| IMAGE\_REL\_BASED\_HIGHLOW | 3 | The base relocation applies all 32 bits of the difference to the 32-bit field at offset. |
| IMAGE\_REL\_BASED\_HIGHADJ | 4 | The base relocation adds the high 16 bits of the difference to the 16-bit field at offset. The 16-bit field represents the high value of a 32-bit word. The low 16 bits of the 32-bit value are stored in the 16-bit word that follows this base relocation. This means that this base relocation occupies two slots. |
| IMAGE\_REL\_BASED\_MIPS\_JMPADDR | 5 | The relocation interpretation is dependent on the machine type.  When the machine type is MIPS, the base relocation applies to a MIPS jump instruction. |
| IMAGE\_REL\_BASED\_ARM\_MOV32 | 5 | This relocation is meaningful only when the machine type is ARM or Thumb. The base relocation applies the 32-bit address of a symbol across a consecutive MOVW/MOVT instruction pair. |
| IMAGE\_REL\_BASED\_RISCV\_HIGH20 | 5 | This relocation is only meaningful when the machine type is RISC-V. The base relocation applies to the high 20 bits of a 32-bit absolute address. |
|  | 6 | Reserved, must be zero. |
| IMAGE\_REL\_BASED\_THUMB\_MOV32 | 7 | This relocation is meaningful only when the machine type is Thumb. The base relocation applies the 32-bit address of a symbol to a consecutive MOVW/MOVT instruction pair. |
| IMAGE\_REL\_BASED\_RISCV\_LOW12I | 7 | This relocation is only meaningful when the machine type is RISC-V. The base relocation applies to the low 12 bits of a 32-bit absolute address formed in RISC-V I-type instruction format. |
| IMAGE\_REL\_BASED\_RISCV\_LOW12S | 8 | This relocation is only meaningful when the machine type is RISC-V. The base relocation applies to the low 12 bits of a 32-bit absolute address formed in RISC-V S-type instruction format. |
| IMAGE\_REL\_BASED\_MIPS\_JMPADDR16 | 9 | The relocation is only meaningful when the machine type is MIPS. The base relocation applies to a MIPS16 jump instruction. |
| IMAGE\_REL\_BASED\_DIR64 | 10 | The base relocation applies the difference to the 64-bit field at offset. |

## The .tls Section

The **.tls** section provides direct PE and COFF support for static thread local storage (TLS). TLS is a special storage class that Windows supports in which a data object is not an automatic (stack) variable, yet is local to each individual thread that runs the code. Thus, each thread can maintain a different value for a variable declared by using TLS.

Note that any amount of TLS data can be supported by using the API calls **TlsAlloc**, **TlsFree**, **TlsSetValue**, and **TlsGetValue**. The PE or COFF implementation is an alternative approach to using the API and has the advantage of being simpler from the high-level-language programmer’s viewpoint. This implementation enables TLS data to be defined and initialized similarly to ordinary static variables in a program. For example, in Visual C++, a static TLS variable can be defined as follows, without using the Windows API:

\_\_declspec (thread) int tlsFlag = 1;

To support this programming construct, the PE and COFF **.tls** section specifies the following information: initialization data, callback routines for per-thread initialization and termination, and the TLS index, which are explained in the following discussion.

Note

Statically declared TLS data objects can be used only in statically loaded image files. This fact makes it unreliable to use static TLS data in a DLL unless you know that the DLL, or anything statically linked with it, will never be loaded dynamically with the **LoadLibrary** API function.

Executable code accesses a static TLS data object through the following steps:

1. At link time, the linker sets the Address of Index field of the TLS directory. This field points to a location where the program expects to receive the TLS index.

The Microsoft run-time library facilitates this process by defining a memory image of the TLS directory and giving it the special name “\_\_tls\_used” (Intel x86 platforms) or “\_tls\_used” (other platforms). The linker looks for this memory image and uses the data there to create the TLS directory. Other compilers that support TLS and work with the Microsoft linker must use this same technique.

2. When a thread is created, the loader communicates the address of the thread’s TLS array by placing the address of the thread environment block (TEB) in the FS register. A pointer to the TLS array is at the offset of 0x2C from the beginning of TEB. This behavior is Intel x86-specific.

3. The loader assigns the value of the TLS index to the place that was indicated by the Address of Index field.

4. The executable code retrieves the TLS index and also the location of the TLS array.

5. The code uses the TLS index and the TLS array location (multiplying the index by 4 and using it as an offset to the array) to get the address of the TLS data area for the given program and module. Each thread has its own TLS data area, but this is transparent to the program, which does not need to know how data is allocated for individual threads.

6. An individual TLS data object is accessed as some fixed offset into the TLS data area.

The TLS array is an array of addresses that the system maintains for each thread. Each address in this array gives the location of TLS data for a given module (EXE or DLL) within the program. The TLS index indicates which member of the array to use. The index is a number (meaningful only to the system) that identifies the module.

### The TLS Directory

The TLS directory has the following format:

| **Offset**  **(PE32/ PE32+)** | **Size (PE32/ PE32+)** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4/8 | Raw Data Start VA | The starting address of the TLS template. The template is a block of data that is used to initialize TLS data. The system copies all of this data each time a thread is created, so it must not be corrupted. Note that this address is not an RVA; it is an address for which there should be a base relocation in the .reloc section. |
| 4/8 | 4/8 | Raw Data End VA | The address of the last byte of the TLS, except for the zero fill. As with the Raw Data Start VA field, this is a VA, not an RVA. |
| 8/16 | 4/8 | Address of Index | The location to receive the TLS index, which the loader assigns. This location is in an ordinary data section, so it can be given a symbolic name that is accessible to the program. |
| 12/24 | 4/8 | Address of Callbacks | The pointer to an array of TLS callback functions. The array is null-terminated, so if no callback function is supported, this field points to 4 bytes set to zero. For information about the prototype for these functions, see section 6.7.2, “TLS Callback Functions.” |
| 16/32 | 4 | Size of Zero Fill | The size in bytes of the template, beyond the initialized data delimited by the Raw Data Start VA and Raw Data End VA fields. The total template size should be the same as the total size of TLS data in the image file. The zero fill is the amount of data that comes after the initialized nonzero data. |
| 20/36 | 4 | Characteristics | The four bits [23:20] describe alignment info. Possible values are those defined as IMAGE\_SCN\_ALIGN\_\*, which are also used to describe alignment of section in object files. The other 28 bits are reserved for future use. |

### TLS Callback Functions

The program can provide one or more TLS callback functions to support additional initialization and termination for TLS data objects. A typical use for such a callback function would be to call constructors and destructors for objects.

Although there is typically no more than one callback function, a callback is implemented as an array to make it possible to add additional callback functions if desired. If there is more than one callback function, each function is called in the order in which its address appears in the array. A null pointer terminates the array. It is perfectly valid to have an empty list (no callback supported), in which case the callback array has exactly one member—a null pointer.

The prototype for a callback function (pointed to by a pointer of type PIMAGE\_TLS\_CALLBACK) has the same parameters as a DLL entry-point function:

typedef VOID

(NTAPI \*PIMAGE\_TLS\_CALLBACK) (

PVOID DllHandle,

DWORD Reason,

PVOID Reserved

);

The Reserved parameter should be set to zero. The Reason parameter can take the following values:

| **Setting** | **Value** | **Description** |
| --- | --- | --- |
| DLL\_PROCESS\_ATTACH | 1 | A new process has started, including the first thread. |
| DLL\_THREAD\_ATTACH | 2 | A new thread has been created. This notification sent for all but the first thread. |
| DLL\_THREAD\_DETACH | 3 | A thread is about to be terminated. This notification sent for all but the first thread. |
| DLL\_PROCESS\_DETACH | 0 | A process is about to terminate, including the original thread. |

## The Load Configuration Structure (Image Only)

The load configuration structure (IMAGE\_LOAD\_CONFIG\_DIRECTORY) was formerly used in very limited cases in the Windows NT operating system itself to describe various features too difficult or too large to describe in the file header or optional header of the image. Current versions of the Microsoft linker and Windows XP and later versions of Windows use a new version of this structure for 32-bit x86-based systems that include reserved SEH technology. This provides a list of safe structured exception handlers that the operating system uses during exception dispatching. If the handler address resides in an image’s VA range and is marked as reserved SEH-aware (that is, IMAGE\_DLLCHARACTERISTICS\_NO\_SEH is clear in the DllCharacteristics field of the optional header, as described earlier), then the handler must be in the list of known safe handlers for that image. Otherwise, the operating system terminates the application. This helps prevent the “x86 exception handler hijacking” exploit that has been used in the past to take control of the operating system.

The Microsoft linker automatically provides a default load configuration structure to include the reserved SEH data. If the user code already provides a load configuration structure, it must include the new reserved SEH fields. Otherwise, the linker cannot include the reserved SEH data and the image is not marked as containing reserved SEH.

### Load Configuration Directory

The data directory entry for a pre-reserved SEH load configuration structure must specify a particular size of the load configuration structure because the operating system loader always expects it to be a certain value. In that regard, the size is really only a version check. For compatibility with Windows XP and earlier versions of Windows, the size must be 64 for x86 images.

### Load Configuration Layout

The load configuration structure has the following layout for 32-bit and 64-bit PE files:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Characteristics | Flags that indicate attributes of the file, currently unused. |
| 4 | 4 | TimeDateStamp | Date and time stamp value. The value is represented in the number of seconds that have elapsed since midnight (00:00:00), January 1, 1970, Universal Coordinated Time, according to the system clock. The time stamp can be printed by using the C runtime (CRT) time function. |
| 8 | 2 | MajorVersion | Major version number. |
| 10 | 2 | MinorVersion | Minor version number. |
| 12 | 4 | GlobalFlagsClear | The global loader flags to clear for this process as the loader starts the process. |
| 16 | 4 | GlobalFlagsSet | The global loader flags to set for this process as the loader starts the process. |
| 20 | 4 | CriticalSectionDefaultTimeout | The default timeout value to use for this process’s critical sections that are abandoned. |
| 24 | 4/8 | DeCommitFreeBlockThreshold | Memory that must be freed before it is returned to the system, in bytes. |
| 28/32 | 4/8 | DeCommitTotalFreeThreshold | Total amount of free memory, in bytes. |
| 32/40 | 4/8 | LockPrefixTable | [x86 only] The VA of a list of addresses where the LOCK prefix is used so that they can be replaced with NOP on single processor machines. |
| 36/48 | 4/8 | MaximumAllocationSize | Maximum allocation size, in bytes. |
| 40/56 | 4/8 | VirtualMemoryThreshold | Maximum virtual memory size, in bytes. |
| 44/64 | 4/8 | ProcessAffinityMask | Setting this field to a non-zero value is equivalent to calling SetProcessAffinityMask with this value during process startup (.exe only) |
| 48/72 | 4 | ProcessHeapFlags | Process heap flags that correspond to the first argument of the HeapCreate function. These flags apply to the process heap that is created during process startup. |
| 52/76 | 2 | CSDVersion | The service pack version identifier. |
| 54/78 | 2 | Reserved | Must be zero. |
| 56/80 | 4/8 | EditList | Reserved for use by the system. |
| 60/88 | 4/8 | SecurityCookie | A pointer to a cookie that is used by Visual C++ or GS implementation. |
| 64/96 | 4/8 | SEHandlerTable | [x86 only] The VA of the sorted table of RVAs of each valid, unique SE handler in the image. |
| 68/104 | 4/8 | SEHandlerCount | [x86 only] The count of unique handlers in the table. |
| 72/112 | 4/8 | GuardCFCheckFunctionPointer | The VA where Control Flow Guard check-function pointer is stored. |
| 76/120 | 4/8 | GuardCFDispatchFunctionPointer | The VA where Control Flow Guard dispatch-function pointer is stored. |
| 80/128 | 4/8 | GuardCFFunctionTable | The VA of the sorted table of RVAs of each Control Flow Guard function in the image. |
| 84/136 | 4/8 | GuardCFFunctionCount | The count of unique RVAs in the above table. |
| 88/144 | 4 | GuardFlags | Control Flow Guard related flags. |
| 92/148 | 12 | CodeIntegrity | Code integrity information. |
| 104/160 | 4/8 | GuardAddressTakenIatEntryTable | The VA where Control Flow Guard address taken IAT table is stored. |
| 108/168 | 4/8 | GuardAddressTakenIatEntryCount | The count of unique RVAs in the above table. |
| 112/176 | 4/8 | GuardLongJumpTargetTable | The VA where Control Flow Guard long jump target table is stored. |
| 116/184 | 4/8 | GuardLongJumpTargetCount | The count of unique RVAs in the above table. |

The GuardFlags field contains a combination of one or more of the following flags and subfields:

* Module performs control flow integrity checks using system-supplied support.

#define IMAGE\_GUARD\_CF\_INSTRUMENTED 0x00000100

* Module performs control flow and write integrity checks.

#define IMAGE\_GUARD\_CFW\_INSTRUMENTED 0x00000200

* Module contains valid control flow target metadata.

#define IMAGE\_GUARD\_CF\_FUNCTION\_TABLE\_PRESENT 0x00000400

* Module does not make use of the /GS security cookie.

#define IMAGE\_GUARD\_SECURITY\_COOKIE\_UNUSED 0x00000800

* Module supports read only delay load IAT.

#define IMAGE\_GUARD\_PROTECT\_DELAYLOAD\_IAT 0x00001000

* Delayload import table in its own .didat section (with nothing else in it) that can be freely reprotected.

#define IMAGE\_GUARD\_DELAYLOAD\_IAT\_IN\_ITS\_OWN\_SECTION 0x00002000

* Module contains suppressed export information. This also infers that the address taken IAT table is also present in the load config.

#define IMAGE\_GUARD\_CF\_EXPORT\_SUPPRESSION\_INFO\_PRESENT 0x00004000

* Module enables suppression of exports.

#define IMAGE\_GUARD\_CF\_ENABLE\_EXPORT\_SUPPRESSION 0x00008000

* Module contains longjmp target information.

#define IMAGE\_GUARD\_CF\_LONGJUMP\_TABLE\_PRESENT 0x00010000

* Mask for the subfield that contains the stride of Control Flow Guard function table entries (that is, the additional count of bytes per table entry).

#define IMAGE\_GUARD\_CF\_FUNCTION\_TABLE\_SIZE\_MASK 0xF0000000

Additionally, the Windows SDK winnt.h header defines this macro for the amount of bits to right-shift the GuardFlags value to right-justify the Control Flow Guard function table stride:

#define IMAGE\_GUARD\_CF\_FUNCTION\_TABLE\_SIZE\_SHIFT 28

## The .rsrc Section

Resources are indexed by a multiple-level binary-sorted tree structure. The general design can incorporate 2\*\*31 levels. By convention, however, Windows uses three levels:

Type  
Name  
Language

A series of resource directory tables relates all of the levels in the following way: Each directory table is followed by a series of directory entries that give the name or identifier (ID) for that level (Type, Name, or Language level) and an address of either a data description or another directory table. If the address points to a data description, then the data is a leaf in the tree. If the address points to another directory table, then that table lists directory entries at the next level down.

A leaf’s Type, Name, and Language IDs are determined by the path that is taken through directory tables to reach the leaf. The first table determines Type ID, the second table (pointed to by the directory entry in the first table) determines Name ID, and the third table determines Language ID.

The general structure of the **.rsrc** section is:

| **Data** | **Description** |
| --- | --- |
| Resource Directory Tables (and Resource Directory Entries) | A series of tables, one for each group of nodes in the tree. All top-level (Type) nodes are listed in the first table. Entries in this table point to second-level tables. Each second-level tree has the same Type ID but different Name IDs. Third-level trees have the same Type and Name IDs but different Language IDs.  Each individual table is immediately followed by directory entries, in which each entry has a name or numeric identifier and a pointer to a data description or a table at the next lower level. |
| Resource Directory Strings | Two-byte-aligned Unicode strings, which serve as string data that is pointed to by directory entries. |
| Resource Data Description | An array of records, pointed to by tables, that describe the actual size and location of the resource data. These records are the leaves in the resource-description tree. |
| Resource Data | Raw data of the resource section. The size and location information in the Resource Data Descriptions field delimit the individual regions of resource data. |

### Resource Directory Table

Each resource directory table has the following format. This data structure should be considered the heading of a table because the table actually consists of directory entries (described in section 6.9.2, "Resource Directory Entries") and this structure:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Characteristics | Resource flags. This field is reserved for future use. It is currently set to zero. |
| 4 | 4 | Time/Date Stamp | The time that the resource data was created by the resource compiler. |
| 8 | 2 | Major Version | The major version number, set by the user. |
| 10 | 2 | Minor Version | The minor version number, set by the user. |
| 12 | 2 | Number of Name Entries | The number of directory entries immediately following the table that use strings to identify Type, Name, or Language entries (depending on the level of the table). |
| 14 | 2 | Number of ID Entries | The number of directory entries immediately following the Name entries that use numeric IDs for Type, Name, or Language entries. |

### Resource Directory Entries

The directory entries make up the rows of a table. Each resource directory entry has the following format. Whether the entry is a Name or ID entry is indicated by the resource directory table, which indicates how many Name and ID entries follow it (remember that all the Name entries precede all the ID entries for the table). All entries for the table are sorted in ascending order: the Name entries by case-sensitive string and the ID entries by numeric value. Offsets are relative to the address in the IMAGE\_DIRECTORY\_ENTRY\_RESOURCE DataDirectory.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Name Offset | The offset of a string that gives the Type, Name, or Language ID entry, depending on level of table. |
| 0 | 4 | Integer ID | A 32-bit integer that identifies the Type, Name, or Language ID entry. |
| 4 | 4 | Data Entry Offset | High bit 0. Address of a Resource Data entry (a leaf). |
| 4 | 4 | Subdirectory Offset | High bit 1. The lower 31 bits are the address of another resource directory table (the next level down). |

### Resource Directory String

The resource directory string area consists of Unicode strings, which are word-aligned. These strings are stored together after the last Resource Directory entry and before the first Resource Data entry. This minimizes the impact of these variable-length strings on the alignment of the fixed-size directory entries. Each resource directory string has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | Length | The size of the string, not including length field itself. |
| 2 | variable | Unicode String | The variable-length Unicode string data, word-aligned. |

### Resource Data Entry

Each Resource Data entry describes an actual unit of raw data in the Resource Data area. A Resource Data entry has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Data RVA | The address of a unit of resource data in the Resource Data area. |
| 4 | 4 | Size | The size, in bytes, of the resource data that is pointed to by the Data RVA field. |
| 8 | 4 | Codepage | The code page that is used to decode code point values within the resource data. Typically, the code page would be the Unicode code page. |
| 12 | 4 | Reserved, must be 0. | |

## The .cormeta Section (Object Only)

CLR metadata is stored in this section. It is used to indicate that the object file contains managed code. The format of the metadata is not documented, but can be handed to the CLR interfaces for handling metadata.

## The .sxdata Section

The valid exception handlers of an object are listed in the .sxdata section of that object. The section is marked IMAGE\_SCN\_LNK\_INFO. It contains the COFF symbol index of each valid handler, using 4 bytes per index.

Additionally, the compiler marks a COFF object as registered SEH by emitting the absolute symbol “@feat.00” with the LSB of the value field set to 1. A COFF object with no registered SEH handlers would have the “@feat.00” symbol, but no .sxdata section.

# Archive (Library) File Format

The COFF archive format provides a standard mechanism for storing collections of object files. These collections are commonly called libraries in programming documentation.

The first 8 bytes of an archive consist of the file signature. The rest of the archive consists of a series of archive members, as follows:

* The first and second members are “linker members.” Each of these members has its own format as described in section 8.3, "Import Name Type." Typically, a linker places information into these archive members. The linker members contain the directory of the archive.
* The third member is the "longnames" member. This member consists of a series of null-terminated ASCII strings in which each string is the name of another archive member.
* The rest of the archive consists of standard (object-file) members. Each of these members contains the contents of one object file in its entirety.

An archive member header precedes each member. The following figure shows the general structure of an archive:

|  |
| --- |
| Signature :”!<arch>\n” |

|  |
| --- |
| Header |
| 1st Linker Member |

|  |
| --- |
| Header |
| 2nd Linker Member |

|  |
| --- |
| Header |
| Longnames Member |

|  |
| --- |
| Header |
| Contents of OBJ File 1  (COFF format) |

|  |
| --- |
| Header |
| Contents of OBJ File 2  (COFF format) |

.

.

.

|  |
| --- |
| Header |
| Contents of OBJ File N  (COFF format) |

Figure 4. Archive File Structure

## Archive File Signature

The archive file signature identifies the file type. Any utility (for example, a linker) that takes an archive file as input can check the file type by reading this signature. The signature consists of the following ASCII characters, in which each character below is represented literally, except for the newline (\n) character:

!<arch>\n

## Archive Member Headers

Each member (linker, longnames, or object-file member) is preceded by a header. An archive member header has the following format, in which each field is an ASCII text string that is left justified and padded with spaces to the end of the field. There is no terminating null character in any of these fields.

Each member header starts on the first even address after the end of the previous archive member.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 16 | Name | The name of the archive member, with a slash (/) appended to terminate the name. If the first character is a slash, the name has a special interpretation, as described in the following table. |
| 16 | 12 | Date | The date and time that the archive member was created: This is the ASCII decimal representation of the number of seconds since 1/1/1970 UCT. |
| 28 | 6 | User ID | An ASCII decimal representation of the user ID. This field does not contain a meaningful value on Windows platforms because Microsoft tools emit all blanks. |
| 34 | 6 | Group ID | An ASCII decimal representation of the group ID. This field does not contain a meaningful value on Windows platforms because Microsoft tools emit all blanks. |
| 40 | 8 | Mode | An ASCII octal representation of the member’s file mode. This is the ST\_MODE value from the C run-time function \_wstat. |
| 48 | 10 | Size | An ASCII decimal representation of the total size of the archive member, not including the size of the header. |
| 58 | 2 | End of Header | The two bytes in the C string “‘\n” (0x60 0x0A). |

The Name field has one of the formats shown in the following table. As mentioned earlier, each of these strings is left justified and padded with trailing spaces within a field of 16 bytes:

| **Contents of Name field** | **Description** |
| --- | --- |
| name/ | The name of the archive member. |
| / | The archive member is one of the two linker members. Both of the linker members have this name. |
| // | The archive member is the longnames member, which consists of a series of null-terminated ASCII strings. The longnames member is the third archive member and must always be present even if the contents are empty. |
| /n | The name of the archive member is located at offset n within the longnames member. The number n is the decimal representation of the offset. For example: “/26” indicates that the name of the archive member is located 26 bytes beyond the beginning of the longnames member contents. |

## First Linker Member

The name of the first linker member is “\”. The first linker member is included for backward compatibility. It is not used by current linkers, but its format must be correct. This linker member provides a directory of symbol names, as does the second linker member. For each symbol, the information indicates where to find the archive member that contains the symbol.

The first linker member has the following format. This information appears after the header:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Number of Symbols | Unsigned long that contains the number of indexed symbols. This number is stored in big-endian format. Each object-file member typically defines one or more external symbols. |
| 4 | 4 \* n | Offsets | An array of file offsets to archive member headers, in which n is equal to the Number of Symbols field. Each number in the array is an unsigned long stored in big-endian format. For each symbol that is named in the string table, the corresponding element in the offsets array gives the location of the archive member that contains the symbol. |
| \* | \* | String Table | A series of null-terminated strings that name all the symbols in the directory. Each string begins immediately after the null character in the previous string. The number of strings must be equal to the value of the Number of Symbols field. |

The elements in the offsets array must be arranged in ascending order. This fact implies that the symbols in the string table must be arranged according to the order of archive members. For example, all the symbols in the first object-file member would have to be listed before the symbols in the second object file.

## Second Linker Member

The second linker member has the name “\” as does the first linker member. Although both linker members provide a directory of symbols and archive members that contain them, the second linker member is used in preference to the first by all current linkers. The second linker member includes symbol names in lexical order, which enables faster searching by name.

The second member has the following format. This information appears after the header:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Number of Members | An unsigned long that contains the number of archive members. |
| 4 | 4 \* m | Offsets | An array of file offsets to archive member headers, arranged in ascending order. Each offset is an unsigned long. The number m is equal to the value of the Number of Members field. |
| \* | 4 | Number of Symbols | An unsigned long that contains the number of symbols indexed. Each object-file member typically defines one or more external symbols. |
| \* | 2 \* n | Indices | An array of 1-based indexes (unsigned short) that map symbol names to archive member offsets. The number n is equal to the Number of Symbols field. For each symbol that is named in the string table, the corresponding element in the Indices array gives an index into the offsets array. The offsets array, in turn, gives the location of the archive member that contains the symbol. |
| \* | \* | String Table | A series of null-terminated strings that name all of the symbols in the directory. Each string begins immediately after the null byte in the previous string. The number of strings must be equal to the value of the Number of Symbols field. This table lists all the symbol names in ascending lexical order. |

## Longnames Member

The name of the longnames member is “\\”. The longnames member is a series of strings of archive member names. A name appears here only when there is insufficient room in the Name field (16 bytes). The longnames member can be empty, though its header must appear.

The strings are null-terminated. Each string begins immediately after the null byte in the previous string.

# Import Library Format

Traditional import libraries, that is, libraries that describe the exports from one image for use by another, typically follow the layout described in section 7, "Archive (Library) File Format*.*"The primary difference is that import library members contain pseudo-object files instead of real ones, in which each member includes the section contributions that are required to build the import tables that are described in section 6.4, "The .idata Section." The linker generates this archive while building the exporting application.

The section contributions for an import can be inferred from a small set of information. The linker can either generate the complete, verbose information into the import library for each member at the time of the library’s creation or write only the canonical information to the library and let the application that later uses it generate the necessary data on the fly.

In an import library with the long format, a single member contains the following information:

Archive member header  
 File header  
 Section headers  
 Data that corresponds to each of the section headers  
 COFF symbol table  
 Strings

In contrast, a short import library is written as follows:

Archive member header   
 Import header  
 Null-terminated import name string  
 Null-terminated DLL name string  
  
This is sufficient information to accurately reconstruct the entire contents of the member at the time of its use.

## Import Header

The import header contains the following fields and offsets:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | Sig1 | Must be IMAGE\_FILE\_MACHINE\_UNKNOWN. For more information, see section 3.3.1, “Machine Types.” |
| 2 | 2 | Sig2 | Must be 0xFFFF. |
| 4 | 2 | Version | The structure version. |
| 6 | 2 | Machine | The number that identifies the type of target machine. For more information, see section 3.3.1, “Machine Types.” |
| 8 | 4 | Time-Date Stamp | The time and date that the file was created. |
| 12 | 4 | Size Of Data | The size of the strings that follow the header. |
| 16 | 2 | Ordinal/Hint | Either the ordinal or the hint for the import, determined by the value in the Name Type field. |
| 18 | 2 bits | Type | The import type. For specific values and descriptions, see section 8.2, "Import Type." |
|  | 3 bits | Name Type | The import name type. For specific values and descriptions, see section 8.3. Import Name Type." |
|  | 11 bits | Reserved | Reserved, must be 0. |

This structure is followed by two null-terminated strings that describe the imported symbol’s name and the DLL from which it came.

## Import Type

The following values are defined for the Type field in the import header:

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMPORT\_CODE | 0 | Executable code. |
| IMPORT\_DATA | 1 | Data. |
| IMPORT\_CONST | 2 | Specified as CONST in the .def file. |

These values are used to determine which section contributions must be generated by the tool that uses the library if it must access that data.

## Import Name Type

The null-terminated import symbol name immediately follows its associated import header. The following values are defined for the Name Type field in the import header. They indicate how the name is to be used to generate the correct symbols that represent the import:

| **Constant** | **Value** | **Description** |
| --- | --- | --- |
| IMPORT\_ORDINAL | 0 | The import is by ordinal. This indicates that the value in the Ordinal/Hint field of the import header is the import’s ordinal. If this constant is not specified, then the Ordinal/Hint field should always be interpreted as the import’s hint. |
| IMPORT\_NAME | 1 | The import name is identical to the public symbol name. |
| IMPORT\_NAME\_NOPREFIX | 2 | The import name is the public symbol name, but skipping the leading ?, @, or optionally \_. |
| IMPORT\_NAME\_UNDECORATE | 3 | The import name is the public symbol name, but skipping the leading ?, @, or optionally \_, and truncating at the first @. |

# TD32

## TD32FileSignature

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 4 | Signature |  |
| 4 | 4 | Offset |  |

## TD32DirectoryEntry

The directory header structure is followed by the directory entries which specify the subsection type, module index, file offset, and size. The subsection directory gives the location (LFO) and size of each subsection, as well as its type and module number if applicable.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | SubsectionType | Subdirectory type. |
| 2 | 2 | ModuleIndex | Module index. |
| 4 | 4 | Offset | Offset from the base offset lfoBase. |
| 8 | 4 | Size | Number of bytes in subsection. |

## TD32DirectoryHeader

The subsection directory is prefixed with a directory header structure indicating size and number of subsection directory entries that follow.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | Size | Length of this structure. |
| 2 | 2 | DirEntrySize | Length of each directory entry. |
| 4 | 4 | DirEntryCount | Number of directory entries. |
| 8 | 4 | lfoNextDir | Offset from lfoBase of next directory. |
| 12 | 4 | Flags | Flags describing directory and subsection tables. |
| 16 | sizeof(TD32DirectoryEntry)\* DirEntryCount | Entries |  |

# Symbol and Type Information

This document describes the format and meaning of Microsoft symbol and type debugging information. The information is contained within two tables emitted by the language processor into the object file. Each table is treated as a stream of variable length records. The first table is called $$SYMBOLS and describes the symbols in the object file. The record for each symbol contains the symbol name, the symbol address and other information needed to describe the symbol. The second table is called $$TYPES and contains information about symbol typing. There are fields in the records contained in $$SYMBOLS that index into the records contained in $$TYPES. Records in $$TYPES can also index into the records contained in the $$TYPES table.

The records for $$SYMBOLS and $$TYPES are accumulated by the linker and are written into the executable file. There is a third table of symbol information for each object file that is generated by the linker and written into the executable file called the PUBLICS table. This table contains symbol records for each public symbol definition encountered in the object file.

Field sizes and arrangement in $$SYMBOLS and $$TYPES are arranged to maintain "natural alignment" to improve performance. Natural alignment indicates that a field begins on an address that is divisible by the size of the field. For example, a four byte (long) value begins on an address that is evenly divisible by four. Some architectures, such as the MIPS R4000, impose a severe penalty for loading data that is not in natural alignment. Even for Intel386TM and Intel486TM processors, there is a significant improvement when processing data that is in natural alignment.

Compilers that emit Symbol and Type OMF (object module formats) according to this specification indicate so by placing a signature of 0x00000001 at the beginning of the $$SYMBOLS and $$TYPES tables.

In all structure descriptions and value enumerations, all values not specified in this document are reserved for future use. All values should be referenced by the symbolic descriptions.

The CVPACK utility must be run on a linked executable file before the Microsoft debugger can process the file. This utility removes duplicate symbol and type information and rewrites the remaining information in a format optimized for processing by the debugger. CVPACK will recognize old Symbol and Type OMF and rewrite it to this format during packing.

## Logical Segments

When the linker emits address information about a symbol, it is done in *segment*:*offset* format. The *segment* is a logical segment index assigned by the linker and the *offset* is the offset from the beginning of the logical segment. The physical address is assigned by the operating system when the program is loaded.

For PE-formatted executables, the *segment* field is interpreted as the PE section number.

## Lexical Scope Linkage

The model of a program envisioned by this document is that programs have nested scopes. The outermost scope is module scope which encompasses all of the symbols not defined within any inner (lexical) scope. Symbols and types defined at one scoping level are visible to all scopes nested within it. Symbols and types defined at module scope are visible to all inner scopes.

The next level of scoping is "function" scope, which in turn contains lexical blocks (including other functions scopes) that can be further nested. Nested lexical scopes are opened by a procedure, method, thunk, with, or block start symbol. They are closed by the matching blockend symbol.

In general, symbol searching within a module's symbol table is performed in the following manner. The lexical scope that contains the current program address is searched for the symbol. If the symbol is not found within that scope, the enclosing lexical scope is searched. This search is repeated outward until the symbol is found or the module scope is searched unsuccessfully. Note that lexical scopes at the same depth level are not searched. As an optimization for the debugger, symbols that open a lexical scope have fields that contain offsets from the beginning of the symbols for the module, which point to the parent of the scope, the next lexical scope that is at the same scoping level, and the S\_END symbol that closes this lexical scope.

The *pParent*, *pNext* and *pEnd* fields described below are filled in by the CVPACK utility and should be emitted as zeroes by the language processor.

| **Field** | **Linkage** |
| --- | --- |
| *pParent* | Used in local procedures, global procedures, thunk start, with start, and block start symbols. If the scope is not enclosed by another lexical scope, then *pParent* is zero. Otherwise, the parent of this scope is the symbol within this module that opens the outer scope that encloses this scope but encloses no other scope that encloses this scope. The *pParent* field contains the offset from the beginning of the module's symbol table of the symbol that opens the enclosing lexical scope. |
| *pNext* | Used in start search local procedures, global procedures, and thunk start symbols. The *pNext* field, along with the start search symbol, defines a group of lexically scoped symbols within a symbol table that is contained within a code segment or PE section. For each segment or section represented in the symbol table, there is a start search symbol that contains the offset from the start of the symbols for this module to the first procedure or thunk contained in the segment. Each outermost lexical scope symbol has a next field containing the next outermost scope symbol contained in the segment. The last outermost scope in the symbol table for each segment has a next field of zero. |
| *pEnd* | This field is defined for local procedures, global procedures, thunk, block, and with symbols. The end field contains the offset from the start of the symbols for this module to the matching block end symbol that terminates the lexical scope. |

## Numeric Leaves

When the symbol or type processor knows that a numeric leaf is next in the symbol or type record, the next two bytes of the symbol or type string are examined. If the value of these two bytes is less than LF\_NUMERIC (0x8000), then the two bytes contain the actual numeric value. If the value is greater than or equal to LF\_NUMERIC (0x8000), then the numeric data follows the two-byte leaf index in the format specified by the numeric leaf index. It is the responsibility of routines reading numeric fields to handle the potential non alignment of the data fields. See Section 4 entitled Numeric Leaves for details.

## Types Indices

All Symbol and Type OMF records which reference records in the $$TYPES table must use valid non-zero type indices. For public symbols a type index of 0x0000 (T\_NOTYPE) is permitted.

Since many types (relating to hardware and language primitives) are common, type index values less than 0x1000 (CV\_FIRST\_NONPRIM) are reserved for a set of predefined primitive types. A list of predefined types and their indices are defined in this document in Section 5. Type indices of 0x1000 and higher are used to index into the set of non-primitive type definitions in the module's $$TYPES segment. Thus 0x1000 is the first type, 0x1001 the second, and so on. Non-primitive type indices must be sequential and cannot contain gaps in the numbering.

## $$SYMBOLS and $$TYPES Definitions

### $$TYPES Definition

#### OMF

Type information appears in OMF TYPDEF format as LEDATA records that contribute to the special $$TYPES debug segment. A SEGDEF or SEGDEF32 record for this segment must be produced in each module that contains Symbol and Type OMF type information and have the attributes:

Name: $$TYPES

Combine type: private

Class: DEBTYP

The first four bytes of the $$TYPES table is used as a signature to specify the version of the Symbol and Type OMF contained in the $$TYPES segment. If the first two bytes of the $$TYPES segment are not 0x0000, the signature is invalid and the version is assumed to be that emitted for an earlier version of the Microsoft CodeView debugger (version 3.x and earlier). If the signature is 0x00000001, the Symbol and Type OMF has been written to conform to the later version of the Microsoft debugger (version 4.0) specification. All other values for the signature are reserved. The CVPACK utility rewrites previous versions of the Symbol and Type OMF to conform to this specification. The signatures of the $$TYPES and $$SYMBOLS tables must agree.

#### COFF

Type information appears in a COFF (common object file format) as initialized data sections. The attributes for the sections are:

NAME: .debug$T

Attribute: Read Only, Discardable, Initialized Data

As with OMF, the first four bytes in the types section must contain a valid signature and agree with the signature in the symbol table.

### $$SYMBOLS Definition

#### OMF

Symbol information appears in OMF TYPDEF format as LEDATA records that contribute to the special $$SYMBOLS debug segment. A SEGDEF or SEGDEF32 record for this segment must be produced in each module that contains Symbol and Type OMF symbol information and have these attributes:

Name: $$SYMBOLS

Combine type: private

Class: DEBSYM

The first four bytes of the $$SYMBOLS segment is used as a signature to specify the version of the Symbol and Type OMF contained in the $$SYMBOLS segment. If the first two bytes of the $$SYMBOLS segment are not 0x0000, the signature is invalid and the version is assumed to be that emitted for an earlier version of the Microsoft CodeView debugger, version 3.x and earlier. If the signature is 0x00000001, the Symbol and Type OMF has been written to conform to the version 4.0 specification of the Microsoft CodeView debugger. All other values for the signature are reserved. The CVPACK utility rewrites previous versions of the Symbol and Type OMF to conform to this specification. The signatures for the $$TYPES and $$SYMBOLS tables must agree.

#### COFF

Symbol information appears in separate sections. The attributes of the section are:

Name: .debug$S

Attributes: Read Only, Discardable, Initialized Data

There may be multiple symbol sections in an object. The first symbol section to appear in the object file must NOT be associated with a comdat section and must contain a valid signature. If a comdat section is present in the object then the symbol information for that comdat should be in a separate symbol section associated with the text comdat section. Symbol sections associated with comdats must not contain a signature.

# Symbols

## General

### Format of Symbol Records

Data in the $$SYMBOLS segment is a stream of variable length records with the general format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | length | Length of record, excluding the length field. |
|  | 2 | index | Type of symbol. |
|  | \* | data... | Data specific to each symbol format. |

The symbol records are described below. Numbers above the fields indicate the length in bytes, and \* means variable length for that field.

Symbol indices are broken into five ranges. The first range is for symbols whose format does not change with the compilation model of the program or the target machine. These include register symbols, user-defined type symbols, and so on. The second range of symbols are those that contain 16:16 segmented addresses. The third symbol range is for symbols that contain 16:32 addresses. Note that for flat model programs, the segment is replaced with the section number for PE format .exe files. The fourth symbol range is for symbols that are specific to the MIPS architecture/compiler. The fifth range is for Microsoft CodeView optimization.

The symbol records are formatted such that most fields fall into natural alignment if the symbol length field is placed on a long word boundary. For all symbols, the variable length data is at the end of the symbol structure. Note specifically that fields that contain data in potentially nonaligned numeric fields must either pay the load penalty or first do a byte wise copy of the data to a memory that is in natural alignment. Refer to Section 4 for details about numeric leaves.

16:16 compilers do not have to emit padding bytes between symbols to maintain natural alignment. The CVPACK utility places the symbols into the executable files in natural alignment and zero pads the symbol to force alignment. The length of each symbol is adjusted to account for the pad bytes. 16:32 compilers must align symbols on a long word boundary.

Provisions for enabling future implementation of register tracking and a stack machine to perform computation on symbol addresses are provided in the symbols. When the symbol processor is examining a symbol, the length field of the symbol is compared with the offset of the byte following the end of the symbol name field. If these are the same, there is no stack machine code at the end of the symbol. If the length and offset are different, the byte following the end of the symbol name is examined. If the byte is zero, there is no stack machine code following the symbol. If the byte is not zero, then the byte indexes into the list of stack machine implementations and styles of register tracking. If stack machine code is present, the address field of the symbol becomes the initial value of the stack machine. Microsoft does not currently emit or process stack machine code or register tracking information. The opcodes and operation of the stack machine have not been defined.

### Symbol Indices

|  |  |  |
| --- | --- | --- |
| 0x0001 | S\_COMPILE | Compile flags symbol |
| 0x0002 | S\_REGISTER16 | Register variable |
| 0x0003 | S\_CONSTANT16 | Constant symbol |
| 0x0004 | S\_UDT16 | User-defined Type |
| 0x0005 | S\_SSEARCH | Start search. |
| 0x0006 | S\_END | End block, procedure, with, or thunk |
| 0x0007 | S\_SKIP | Skip - Reserve symbol space |
| 0x0008 | S\_CVRESERVE | Reserved for internal use by the Microsoft debugger |
| 0x0009 | S\_OBJNAME\_ST | Specify name of object file |
| 0x000a | S\_ENDARG | Specify end of arguments in function symbols |
| 0x000b | S\_COBOLUDT16 | Microfocus COBOL user-defined type |
| 0x000c | S\_MANYREG16 | Many register symbol |
| 0x000d | S\_RETURN | Function return description |
| 0x000e | S\_ENTRYTHIS | Description of this pointer at entry |
| 0x0024 | S\_USES |  |
| 0x0025 | S\_NAMESPACE |  |
| 0x0026 | S\_USING |  |
| 0x0027 | S\_PCONSTANT |  |
| 0x0100 | S\_BPREL16 | BP relative 16:16 |
| 0x0101 | S\_LDATA16 | Local data 16:16 |
| 0x0102 | S\_GDATA16 | Global data 16:16 |
| 0x0103 | S\_PUB16 | Public symbol 16:16 |
| 0x0104 | S\_LPROC16 | Local procedure start 16:16 |
| 0x0105 | S\_GPROC16 | Global procedure start 16:16 |
| 0x0106 | S\_THUNK16 | Thunk start 16:16 |
| 0x0107 | S\_BLOCK16 | Block start 16:16 |
| 0x0108 | S\_WITH16 | With start 16:16 |
| 0x0109 | S\_LABEL16 | Code label 16:16 |
| 0x010a | S\_CEXMODEL16 | Change execution model 16:16 |
| 0x010b | S\_VFTPATH16 | Virtual function table path descriptor 16:16 |
| 0x010c | S\_REGREL16 | Specify 16:16 offset relative to arbitrary register |
| 0x0200 | S\_BPREL32\_16 | BP relative 16:32 |
| 0x0201 | S\_LDATA32\_16 | Local data 16:32 |
| 0x0202 | S\_GDATA32\_16 | Global data 16:32 |
| 0x0203 | S\_PUB32\_16 | Public symbol 16:32 |
| 0x0204 | S\_LPROC32\_16 | Local procedure start 16:32 |
| 0x0205 | S\_GPROC32\_16 | Global procedure start 16:32 |
| 0x0206 | S\_THUNK32\_ST | Thunk start 16:32 |
| 0x0207 | S\_BLOCK32\_ST | Block start 16:32 |
| 0x0208 | S\_WITH32\_ST |  |
| 0x0209 | S\_LABEL32\_ST |  |
| 0x020a | S\_CEXMODEL32 |  |
| 0x020b | S\_VFTPATH32 | Virtual function table path descriptor 16:32 |
| 0x020c | S\_REGREL32\_16 | 16:32 offset relative to arbitrary register |
| 0x020d | S\_LTHREAD32\_16 | Local Thread Storage data |
| 0x020e | S\_GTHREAD32\_16 | Global Thread Storage data |
| 0x020f | S\_SLINK32 |  |
| 0x0211 | S\_OPTVAR32 |  |
| 0x0300 | S\_LPROCMIPS16 | Local procedure start MIPS |
| 0x0301 | S\_GPROCMIPS16 | Global procedure start MIPS |
| 0x0400 | S\_PROCREF\_ST | Reference to a procedure |
| 0x0401 | S\_DATAREF\_ST | Reference to data |
| 0x0402 | S\_ALIGN | Page align symbols |
| 0x0403 | S\_LPROCREF\_ST |  |
| 0x0404 | S\_OEM |  |
| 0x1000 | S\_TI16\_MAX |  |
| 0x1001 | S\_REGISTER\_ST | Register variable |
| 0x1002 | S\_CONSTANT\_ST | Constant symbol |
| 0x1003 | S\_UDT\_ST | User defined type |
| 0x1004 | S\_COBOLUDT\_ST | Special UDT for cobol that does not symbol pack |
| 0x1005 | S\_MANYREG\_ST | Multiple register variable |
| 0x1006 | S\_BPREL32\_ST | BP relative |
| 0x1007 | S\_LDATA32\_ST | Module-local symbol |
| 0x1008 | S\_GDATA32\_ST | Global data symbol |
| 0x1009 | S\_PUB32\_ST | Public symbol (CV internal reserved) |
| 0x100a | S\_LPROC32\_ST | Local procedure start |
| 0x100b | S\_GPROC32\_ST | Global procedure start |
| 0x100c | S\_VFTABLE32 | Address of virtual function table |
| 0x100d | S\_REGREL32\_ST | Register relative address |
| 0x100e | S\_LTHREAD32\_ST | Local thread storage |
| 0x100f | S\_GTHREAD32\_ST | Global thread storage |
| 0x1010 | S\_LPROCMIPS\_ST | Local procedure start |
| 0x1011 | S\_GPROCMIPS\_ST | Global procedure start |
| 0x1012 | S\_FRAMEPROC | Extra frame and proc information |
| 0x1013 | S\_COMPILE2\_ST | Extended compile flags and info |
| 0x1014 | S\_MANYREG2\_ST | Multiple register variable |
| 0x1015 | S\_LPROCIA64\_ST | Local procedure start (IA64) |
| 0x1016 | S\_GPROCIA64\_ST | Global procedure start (IA64) |
| 0x1017 | S\_LOCALSLOT\_ST | Local IL sym with field for local slot index |
| 0x1018 | S\_PARAMSLOT\_ST | Local IL sym with field for parameter slot index |
| 0x1019 | S\_ANNOTATION | Annotation string literals |
| 0x101a | S\_GMANPROC\_ST | Global proc |
| 0x101b | S\_LMANPROC\_ST | Local proc |
| 0x101c | S\_RESERVED1 | Reserved |
| 0x101d | S\_RESERVED2 | Reserved |
| 0x101e | S\_RESERVED3 | Reserved |
| 0x101f | S\_RESERVED4 | Reserved |
| 0x1020 | S\_LMANDATA\_ST |  |
| 0x1021 | S\_GMANDATA\_ST |  |
| 0x1022 | S\_MANFRAMEREL\_ST |  |
| 0x1023 | S\_MANREGISTER\_ST |  |
| 0x1024 | S\_MANSLOT\_ST |  |
| 0x1025 | S\_MANMANYREG\_ST |  |
| 0x1026 | S\_MANREGREL\_ST |  |
| 0x1027 | S\_MANMANYREG2\_ST |  |
| 0x1028 | S\_MANTYPREF | Index for type referenced by name from metadata |
| 0x1029 | S\_UNAMESPACE\_ST | Using namespace |
| 0x1100 | S\_ST\_MAX | Starting point for SZ name symbols |
| 0x1101 | S\_OBJNAME | Path to object file name |
| 0x1102 | S\_THUNK32 | Thunk Start |
| 0x1103 | S\_BLOCK32 | Block start |
| 0x1104 | S\_WITH32 | With start |
| 0x1105 | S\_LABEL32 | Code label |
| 0x1106 | S\_REGISTER | Register variable |
| 0x1107 | S\_CONSTANT | Constant symbol |
| 0x1108 | S\_UDT | User defined type |
| 0x1109 | S\_COBOLUDT | Special UDT for cobol that does not symbol pack |
| 0x110a | S\_MANYREG | Multiple register variable |
| 0x110b | S\_BPREL32 | BP relative |
| 0x110c | S\_LDATA32 | Module-local symbol |
| 0x110d | S\_GDATA32 | Global data symbol |
| 0x110e | S\_PUB32 | Public symbol (CV internal reserved) |
| 0x110f | S\_LPROC32 | Local procedure start |
| 0x1110 | S\_GPROC32 | Global procedure start |
| 0x1111 | S\_REGREL32 | Register relative address |
| 0x1112 | S\_LTHREAD32 | Local thread storage |
| 0x1113 | S\_GTHREAD32 | Global thread storage |
| 0x1114 | S\_LPROCMIPS | Local procedure start |
| 0x1115 | S\_GPROCMIPS | Global procedure start |
| 0x1116 | S\_COMPILE2 | Extended compile flags and info |
| 0x1117 | S\_MANYREG2 | Multiple register variable |
| 0x1118 | S\_LPROCIA64 | Local procedure start (IA64) |
| 0x1119 | S\_GPROCIA64 | Global procedure start (IA64) |
| 0x111a | S\_LOCALSLOT | Local IL sym with field for local slot index |
| 0x111b | S\_PARAMSLOT | Local IL sym with field for parameter slot index |
| 0x111c | S\_LMANDATA |  |
| 0x111d | S\_GMANDATA |  |
| 0x111e | S\_MANFRAMEREL |  |
| 0x111f | S\_MANREGISTER |  |
| 0x1120 | S\_MANSLOT |  |
| 0x1121 | S\_MANMANYREG |  |
| 0x1122 | S\_MANREGREL |  |
| 0x1123 | S\_MANMANYREG2 |  |
| 0x1124 | S\_UNAMESPACE | Using namespace |
| 0x1125 | S\_PROCREF | Reference to a procedure |
| 0x1126 | S\_DATAREF | Reference to data |
| 0x1127 | S\_LPROCREF | Local Reference to a procedure |
| 0x1128 | S\_ANNOTATIONREF | Reference to an S\_ANNOTATION symbol |
| 0x1129 | S\_TOKENREF | Reference to one of the many MANPROCSYM's |
| 0x112a | S\_GMANPROC | Global proc |
| 0x112b | S\_LMANPROC | Local proc |
| 0x112c | S\_TRAMPOLINE | Trampoline thunks |
| 0x112d | S\_MANCONSTANT | Constants with metadata type info |
| 0x112e | S\_ATTR\_FRAMEREL | Relative to virtual frame ptr |
| 0x112f | S\_ATTR\_REGISTER | Stored in a register |
| 0x1130 | S\_ATTR\_REGREL | Relative to register (alternate frame ptr) |
| 0x1131 | S\_ATTR\_MANYREG | Stored in >1 register |
| 0x1132 | S\_SEPCODE |  |
| 0x1133 | S\_LOCAL\_2005 | Defines a local symbol in optimized code |
| 0x1134 | S\_DEFRANGE\_2005 | Defines a single range of addresses in which symbol can be evaluated |
| 0x1135 | S\_DEFRANGE2\_2005 | Defines ranges of addresses in which symbol can be evaluated |
| 0x1136 | S\_SECTION | A COFF section in a PE executable |
| 0x1137 | S\_COFFGROUP | A COFF group |
| 0x1138 | S\_EXPORT | A export |
| 0x1139 | S\_CALLSITEINFO | Indirect call site information |
| 0x113a | S\_FRAMECOOKIE | Security cookie information |
| 0x113b | S\_DISCARDED | Discarded by LINK /OPT:REF (experimental, see richards) |
| 0x113c | S\_COMPILE3 | Replacement for S\_COMPILE2 |
| 0x113d | S\_ENVBLOCK | Environment block split off from S\_COMPILE2 |
| 0x113e | S\_LOCAL | Defines a local symbol in optimized code |
| 0x113f | S\_DEFRANGE | Defines a single range of addresses in which symbol can be evaluated |
| 0x1140 | S\_DEFRANGE\_SUBFIELD | Ranges for a subfield |
| 0x1141 | S\_DEFRANGE\_REGISTER | Ranges for en-registered symbol |
| 0x1142 | S\_DEFRANGE\_FRAMEPOINTER\_REL | Range for stack symbol. |
| 0x1143 | S\_DEFRANGE\_SUBFIELD\_REGISTER | Ranges for en-registered field of symbol |
| 0x1144 | S\_DEFRANGE\_FRAMEPOINTER\_REL\_FULL\_SCOPE | Range for stack symbol span valid full scope of function body, gap might apply. |
| 0x1145 | S\_DEFRANGE\_REGISTER\_REL | Range for symbol address as register + offset. |
| 0x1146 | S\_LPROC32\_ID |  |
| 0x1147 | S\_GPROC32\_ID |  |
| 0x1148 | S\_LPROCMIPS\_ID |  |
| 0x1149 | S\_GPROCMIPS\_ID |  |
| 0x114a | S\_LPROCIA64\_ID |  |
| 0x114b | S\_GPROCIA64\_ID |  |
| 0x114c | S\_BUILDINFO | Build information. |
| 0x114d | S\_INLINESITE | Inlined function callsite. |
| 0x114e | S\_INLINESITE\_END |  |
| 0x114f | S\_PROC\_ID\_END |  |
| 0x1150 | S\_DEFRANGE\_HLSL |  |
| 0x1151 | S\_GDATA\_HLSL |  |
| 0x1152 | S\_LDATA\_HLSL |  |
| 0x1153 | S\_FILESTATIC |  |
| 0x1154 | S\_LOCAL\_DPC\_GROUPSHARED | DPC groupshared variable |
| 0x1155 | S\_LPROC32\_DPC | DPC local procedure start |
| 0x1156 | S\_LPROC32\_DPC\_ID |  |
| 0x1157 | S\_DEFRANGE\_DPC\_PTR\_TAG | DPC pointer tag definition range |
| 0x1158 | S\_DPC\_SYM\_TAG\_MAP | DPC pointer tag value to symbol record map |
| 0x1159 | S\_ARMSWITCHTABLE |  |
| 0x115a | S\_CALLEES |  |
| 0x115b | S\_CALLERS |  |
| 0x115c | S\_POGODATA |  |
| 0x115d | S\_INLINESITE2 | Extended inline site information |
| 0x115e | S\_HEAPALLOCSITE | Heap allocation site |
| 0x115f | S\_MOD\_TYPEREF | Only generated at link time |
| 0x1160 | S\_REF\_MINIPDB | Only generated at link time for mini PDB |
| 0x1161 | S\_PDBMAP | Only generated at link time for mini PDB |
| 0x1162 | S\_GDATA\_HLSL32 |  |
| 0x1163 | S\_LDATA\_HLSL32 |  |
| 0x1164 | S\_GDATA\_HLSL32\_EX |  |
| 0x1165 | S\_LDATA\_HLSL32\_EX |  |

## Non-modal Symbols

### (0x0001) Compile Flag

### {S\_REGISTER16} Register

This symbol record describes a symbol that has been placed in a register. Provisions for enabling future implementation tracking of a symbol into and out of registers is provided in this symbol. When the symbol processor is examining a register symbol, the length field of the symbol is compared with the offset of the byte following the symbol name field. If these are the same, there is no register tracking information. If the length and offset are different, the byte following the end of the symbol name is examined. If the byte is zero, there is no register tracking information following the symbol. If the byte is not zero, then the byte is the index into the list of stack machine implementations and styles of register tracking. Microsoft does not currently emit or process register-tracking information.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | length | Length of record, excluding the length field. |
| 2 | 2 | index | S\_REGISTER |
|  |  | @type | Type of symbol. |
|  |  | register | Enumeration of the registers in which the symbol value is stored. This field is treated as two bytes. The high order byte specifies the register in which the high order part of the value is stored. The low byte specifies the register for the low order part of the value. If the value is not stored in two registers then high order register field contains the enumeration value for no register. For register enumeration values, see Section 6. The register index enumeration is specific to the processor model for the module. |
|  |  | name | Length-prefixed name of the symbol stored in the register. |
|  |  | tracking | Register-tracking information. Format unspecified. |

### (0x0003) Constant

### (0x0004) User-defined Type

### {S\_SSEARCH} Start Search

These records are always the first symbol records in a module's $$SYMBOL section. There is **one** Start Search symbol for each segment (PE section) to which the module contributes code. Each Start Search symbol contains the segment (PE section) number and $$SYMBOL offset of the record of the outermost lexical scope in this module that physically appears first in the specified segment of the load image. This referenced symbol is the symbol used to initiate context searches within this module. The Start Search symbols are inserted into the $$SYMBOLS table by the CVPACK utility and must not be emitted by the language processor.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | length | Length of record, excluding the length field. |
| 2 | 2 | index | S\_SSEARCH |
| 4 | 4 | sym off | $$SYMBOL offset of the procedure or thunk record for this module that has the lowest offset for the specified segment. See Section 1.2 on lexical scope linking. |
| 8 | 2 | segment | Segment (PE section) to which this Start Search refers. |

**FB09**

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | length | Length of record, excluding the length field. |
| 2 | 2 | index | S\_SSEARCH |
| 4 | 4 | sym off | $$SYMBOL offset of the procedure or thunk record for this module that has the lowest offset for the specified segment. See Section 1.2 on lexical scope linking. |
| 8 | 2 | segment | Segment (PE section) to which this Start Search refers. |
| 10 | 2 | CodeSyms |  |
| 12 | 2 | DataSyms |  |
| 14 | 4 | FirstData |  |

### {S\_END} End of Block

Closes the scope of the nearest preceding Block Start, Global Procedure Start, Local Procedure Start, With Start, or Thunk Start definition.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | length | Length of record, excluding the length field. |
| 2 | 2 | index | S\_END |

### (0x0007) Skip Record

### (0x0008) Microsoft Debugger Internal

### (0x0009) Object File Name

### (0x000a) End of Arguments

### (0x000b) COBOL User-defined Type

### (0x000c) Many Registers

### (0x000d) Function Return

### (0x000e) this at Method Entry

## Symbols for 16:16 Segmented Architectures

### (0x0100) BP Relative 16:16

### (0x0101) Local Data 16:16

### (0x0102) Global Data Symbol 16:16

### (0x0103) Public Symbol 16:16

### (0x0104) Local Start 16:16

### (0x0105) Global Procedure Start 16:16

### (0x0106) Thunk Start 16:16

### (0x0107) Block Start 16:16

### (0x0108) With Start 16:16

### (0x0109) Code Label 16:16

### (0x010a) Change Execution Model 16:16

### (0x010b) Virtual Function Table Path 16:16

### (0x010c) Register Relative 16:16

## Symbols for 16:32 Segmented Architectures

### (0x0200) BP Relative 16:32

### (0x0201) Local Data 16:32

### (0x0202) Global Data Symbol 16:32

### (0x0203) Public 16:32

### {S\_LPROC32\_16} Local Procedure Start 16:32

This symbol record defines local (file static) procedure definition. For C and C++, functions that are declared static to a module are emitted as Local Procedure symbols. Functions not specifically declared static are emitted as Global Procedures (see below.)

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
| 0 | 2 | length | Length of record, excluding the length field. |
| 2 | 2 | index | S\_LPROC32\_16 or S\_GPROC32. |
| 4 | 4 | pParent | See the section on lexical scope linking. |
| 8 | 4 | pEnd | See the section on lexical scope linking. |
| 12 | 4 | pNext | See the section on lexical scope linking. |
| 16 | 4 | proc length | Length in bytes of this procedure. |
| 20 | 4 | debug start | Offset in bytes from the start of the procedure to the point where the stack frame has been set up. Parameter and frame variables can be viewed at this point. |
| 24 | 4 | debug end | Offset in bytes from the start of the procedure to the point where the procedure is ready to return and has calculated its return value, if any. Frame and register variables can still be viewed. |
| 28 | 4 | offset | Offset portion of the procedure address. |
| 32 | 2 | segment | Segment portion of the procedure address. |
| 34 | 2 | @proctype | Type of the procedure type record. |
| 36 | 1 | flags | Procedure flags: |
| 37 | \* | name | Length-prefixed name of procedure. |

### {S\_GPROC32\_16} Global Procedure Start 16:32

This symbol is used for procedures that are not specifically declared static to a module. The format is the same as the Local Procedure Start 16:32 symbol (see above.)

### (0x0206) Thunk Start 16:32

### (0x0207) Block Start 16:32

### (0x0208) With Start 16:32

### (0x0209) Code Label 16:32

### (0x020a) Change Execution Model 16:32

### (0x020b) Virtual Function Table Path 16:32

### (0x020c) Register Relative 16:32

### (0x020d) Local Thread Storage 16:32

### (0x020e) Global Thread Storage 16:32

## Symbols for MIPS Architectures

### (0x0300) Local Procedure Start MIPS

### (0x0301) Global Procedure Start MIPS

## Symbols for CVPACK Optimization

### (0x0400) Procedure Reference

### (0x0401) Data Reference

### (0x0402) Symbol Page Alignment

# Types Definition Segment ($$TYPES)

A $$TYPES segment may appear in linkable modules. It provides descriptions of the types of symbols found in the $$PUBLICS and $$SYMBOLS debug section for the module.

## Type Record

A type record has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | length | Length in bytes of the following type string. This count does not include the length field. |
|  | \* | type string |  |

## Type String

A type string is a series of consecutive leaf structures and has the following format:

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| 2 | \* | 2 | \* |  | 2 | \* |
| leaf | data | leaf | data | … | leaf | data |

|  |  |
| --- | --- |
| *leaf* | LF\_... index, as described below. |
| *data* | Data specified to each leaf type. |

No LF\_... index can have a value of 0x0000. The leaf indices are separated into four ranges according to the use of the type record. The first range is for the type records that are directly referenced in symbols. The second range is for type records that are not referenced by symbols, but instead are referenced by other type records. All type records must have a starting leaf index in these first two ranges.

The third range of leaf indices is used to build complex lists, such as the field list of a class type record. No type record can begin with one of the leaf indices in this range.

The fourth ranges of type indices are used to represent numeric data in a symbol or type records. These leaf indices are greater than 0x8000. At the point that the type or symbol processor is expecting a numeric field, the next two bytes in the type record are examined. If the value is less than 0x8000, then the two bytes contain the numeric value. If the value is greater than 0x8000, then the data follows the leaf index in a format specified by the leaf index. See Section 4 for a detailed description of numeric leaf indices.

Because of the method used to maintain natural alignment in complex lists, no leaf index can have a value greater than or equal to 0xf000. Also, no leaf index can have a value such that the least significant 8 bits of the value is greater than or equal to 0xf0.

Leaf indices for type records that can be referenced from symbols are the following:

|  |  |
| --- | --- |
| 0x0001 | LF\_MODIFIER |
| 0x0002 | LF\_POINTER |
| 0x0003 | LF\_ARRAY |
| 0x0004 | LF\_CLASS |
| 0x0005 | LF\_STRUCTURE |
| 0x0006 | LF\_UNION |
| 0x0007 | LF\_ENUM |
| 0x0008 | LF\_PROCEDURE |
| 0x0009 | LF\_MFUNCTION |
| 0x000a | LF\_VTSHAPE |
| 0x000b | LF\_COBOL0 |
| 0x000c | LF\_COBOL1 |
| 0x000d | LF\_BARRAY |
| 0x000e | LF\_LABEL |
| 0x000f | LF\_NULL |
| 0x0010 | LF\_NOTTRAN |
| 0x0011 | LF\_DIMARRAY |
| 0x0012 | LF\_VFTPATH |
| 0x0013 | LF\_PRECOMP |
| 0x0014 | LF\_ENDPRECOMP |
| 0x0015 | LF\_OEM |
| 0x0016 | Reserved |

Leaf indices for type records that can be referenced from other type records are the following:

|  |  |
| --- | --- |
| 0x0200 | LF\_SKIP |
| 0x0201 | LF\_ARGLIST |
| 0x0202 | LF\_DEFARG |
| 0x0203 | LF\_LIST |
| 0x0204 | LF\_FIELDLIST |
| 0x0205 | LF\_DERIVED |
| 0x0206 | LF\_BITFIELD |
| 0x0207 | LF\_METHODLIST |
| 0x0208 | LF\_DIMCONU |
| 0x0209 | LF\_DIMCONLU |
| 0x020a | LF\_DIMVARU |
| 0x020b | LF\_DIMVARLU |
| 0x020c | LF\_REFSYM |

Leaf indices for fields of complex lists are the following:

|  |  |
| --- | --- |
| 0x0400 | LF\_BCLASS |
| 0x0401 | LF\_VBCLASS |
| 0x0402 | LF\_IVBCLASS |
| 0x0403 | LF\_ENUMERATE |
| 0x0404 | LF\_FRIENDFCN |
| 0x0405 | LF\_INDEX |
| 0x0406 | LF\_MEMBER |
| 0x0407 | LF\_STMEMBER |
| 0x0408 | LF\_METHOD |
| 0x0409 | LF\_NESTTYPE |
| 0x040a | LF\_VFUNCTAB |
| 0x040b | LF\_FRIENDCLS |
| 0x040c | LF\_ONEMETHOD |
| 0x040d | LF\_VFUNCOFF |

Leaf indices for numeric fields of symbols and type records are the following:

|  |  |
| --- | --- |
| 0x8000 | LF\_NUMERIC |
| 0x8000 | LF\_CHAR |
| 0x8001 | LF\_SHORT |
| 0x8002 | LF\_USHORT |
| 0x8003 | LF\_LONG |
| 0x8004 | LF\_ULONG |
| 0x8005 | LF\_REAL32 |
| 0x8006 | LF\_REAL64 |
| 0x8007 | LF\_REAL80 |
| 0x8008 | LF\_REAL128 |
| 0x8009 | LF\_QUADWORD |
| 0x800a | LF\_UQUADWORD |
| 0x800b | LF\_REAL48 |
| 0x800c | LF\_COMPLEX32 |
| 0x800d | LF\_COMPLEX64 |
| 0x800e | LF\_COMPLEX80 |
| 0x800f | LF\_COMPLEX128 |
| 0x8010 | LF\_VARSTRING |

Leaf indices for TD32 specific:

|  |  |
| --- | --- |
| 0x0030 | LF\_SET |
| 0x0031 | LF\_SUBRANGE |
| 0x0032 | LF\_PARRAY |
| 0x0033 | LF\_PSTRING |
| 0x0034 | LF\_CLOSURE |
| 0x0035 | LF\_PROPERTY |
| 0x0036 | LF\_LSTRING |
| 0x0037 | LF\_VARIANT |
| 0x0038 | LF\_CLASSREF |
| 0x0039 | LF\_WSTRING |

|  |  |
| --- | --- |
| 0xf0 | LF\_PAD0 |
| 0xf1 | LF\_PAD1 |
| 0xf2 | LF\_PAD2 |
| 0xf3 | LF\_PAD3 |
| 0xf4 | LF\_PAD4 |
| 0xf5 | LF\_PAD5 |
| 0xf6 | LF\_PAD6 |
| 0xf7 | LF\_PAD7 |
| 0xf8 | LF\_PAD8 |
| 0xf9 | LF\_PAD9 |
| 0xfa | LF\_PAD10 |
| 0xfb | LF\_PAD11 |
| 0xfc | LF\_PAD12 |
| 0xfc | LF\_PAD13 |
| 0xfe | LF\_PAD14 |
| 0xff | LF\_PAD15 |

### Member Attribute Field

|  |  |
| --- | --- |
| *access* | :2 Specifies the access protection of the item |
|  | 0 No access protection |
|  | 1 Private |
|  | 2 Protected |
|  | 3 Public |
| *mprop* | :3 Specifies the properties for methods |
|  | 0 Vanilla method |
|  | 1 Virtual method |
|  | 2 Static method |
|  | 3 Friend method |
|  | 4 Introducing virtual method |
|  | 5 Pure virtual method |
|  | 6 Pure introducing virtual method |
|  | 7 Reserved |
| *pseudo* | :1 True if the method is never instantiated by the compiler |
| *noinherit* | :1 True if the class cannot be inherited |
| *noconstruct* | :1 True if the class cannot be constructed |
| *reserved* | :8 |

## Leaf Indices Referenced from Symbols

## Type Records Referenced from Type Records

## Subfields of Complex Lists

# Numeric Leaves

# Predefined Primitive Types

## Format of Reserved Types

## Primitive Type Listing

# Register Enumerations

## Intel 80x86/80x87 Architectures

## Motorola 68000 Architectures

## MIPS Architectures

# Symbol and Type Format for Microsoft Executables

## Introduction

This section describes the format used to embed debugging information into the executable file.

## Debug Information Format

The debug information format encompasses a block of data that goes into the .exe file at a location dependent upon the executable file format. The version of the debug information is specified by a signature that is contained within the debug information. The signature has the format of NB(FB)xx, where xx is the version number and has the following meanings:

|  |  |  |
| --- | --- | --- |
| NB00 | 0x3030424e | 32-bit CodeView debugger format. |
| NB01 | 0x3130424e | AIX debugger format. |
| NB02 | 0x3230424e | Linked by a Microsoft LINK, version 5.10, or equivalent OEM linker (16-bit CodeView debugger format). |
| NB03 | 0x3330424e | Not supported. |
| NB04 | 0x3430424e | 32-bit OS/2 PM debugger (IBM) format. |
| NB05 | 0x3530424e | Emitted by LINK, version 5.20 and later linkers for a file before it has been packed. |
| NB06 | 0x3630424e | Not supported. |
| NB07 | 0x3730424e | Used for Quick C for Windows 1.0 only. |
| NB08 | 0x3830424e | Used by Microsoft CodeView debugger, versions 4.00 through 4.05, for a file after it has been packed. Microsoft CodeView,, version 4.00 through 4.05 will not process a file that does not have this signature. |
| NB09 | 0x3930424e | Used by Microsoft CodeView, version 4.10 for a file after it has been packed. Microsoft CodeView 4.10 will not process a file that does not have this signature. |
| FB09 | 0x39304246 | Borland 32-bit symbol file signature for Delphi |
| FB0A | 0x41304246 | Borland 32-bit symbol file signature for C++ Builder |

The method for finding the debug information depends upon the executable format.

### OMF

For OMF executables, the debug information is at the end of the .exe file, i.e., after the header plus load image, the overlays, and the Windows resource compiler information. The lower portion of the file is unaffected by the additional data. The last eight bytes of the file contain a signature and a long file offset from the end of the file (**lfoBase**). The long offset indicates the position in the file (relative to the end of the file) of the base address.

The value

**lfaBase** = length of the file **lfoBase**

gives the base address of the start of the Symbol and Type OMF information relative to the beginning of the file.

|  |  |
| --- | --- |
| executable header |  |
| executable code + ... |  |
| **NBxx** | Signature at **lfaBase** |
| lfoDirectory | Offset of directory from base address (**lfoDir**) |
| Subsection tables | sstModule, sstType, sstLibraries, ... |
| . |  |
| . |  |
| . |  |
| Subsection Directory | At file offset **lfaBase** + **lfoDir** |
| **NBxx** | Signature |
| lfoBase | Offset of repeated signature from end of file |

### PE Format

For PE format executables, the base address lfaBase is found by examining the executable header. Note, currently Microsoft code uses the same method that is used for OMF format executables to find the debug information.

|  |  |
| --- | --- |
| executable header |  |
| executable code + ... |  |
| **NBxx** | Signature at **lfaBase** |
| lfoDirectory | Offset of directory from base address (**lfoDir**) |
| Subsection tables | sstModule, sstType, sstLibraries, ... |
| . |  |
| . |  |
| . |  |
| Subsection Directory | At file offset **lfaBase** + **lfoDir** |
| other information |  |

All other file offsets in the Symbol and Type OMF are relative to lfaBase. At the base address, the signature is repeated, followed by the long displacement to the subsection directory (lfoDir). All subsections start on a long word boundary and are designed to maintain natural alignment internally in each subsection and within the subsection directory.

## Subsection Directory

The subsection directory has the following format:

|  |
| --- |
| Directory header |
| Directory entry 0 |
| Directory entry 1 |
| . |
| . |
| . |
| Directory entry *n* |

The subsection directory is prefixed with a directory header structure indicating size and number of subsection directory entries that follow.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | cbDirHeader | Length of directory header. |
|  | 2 | cbDirEntry | Length of each directory entry. |
|  | 4 | cDir | Number of directory entries. |
|  | 4 | lfoNextDir | Offset from **lfaBase** of next directory. This field is currently unused, but is intended for use by the incremental linker to point to the next directory containing Symbol and Type OMF information from an incremental link. |
|  | 4 | flags | Flags describing directory and subsection tables. No values have been defined for this field. |

The directory header structure is followed by the directory entries, which specify the subsection type, module index, if applicable, the subsection offset, and subsection size.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | subsection | Subdirectory index. See the table below for a listing of the valid subsection indices. |
|  | 2 | iMod | Module index. This number is 1 based and zero (0) is never a valid index. The index 0xffff is reserved for tables that are not associated with a specific module. These tables include sstLibraries, sstGlobalSym, sstGlobalPub, and sstGlobalTypes. |
|  | 4 | lfo | Offset from the base address lfaBase. |
|  | 4 | cb | Number of bytes in subsection. |

There is no requirement for a particular subsection to exist for a particular module. There is a preferred order for subsections within the Symbol and Type OMF portion and the subsection directory of the file, as emitted by the linker (NB05 signature). The preferred order is the following:

|  |  |
| --- | --- |
| sstModule1 | Module 1 |
| . |  |
| sstModulen | Module *n* |
| sstTypes1 | Module 1 |
| sstPublics1 | Module 1 |
| sstSymbols1 | Module 1 |
| sstSrcModule1 | Module 1 |
| . |  |
| sstTypesn | Module *n* |
| sstPublicsn | Module *n* |
| sstSymbolsn | Module *n* |
| sstSrcModulen | Module *n* |
| sstLibraries |  |
| directory |  |

However, if the tables are not written in this order by the linker, the CVPACK utility will sort the subsection table into this order and read the subsections in this order by seeking the correct location. The net effect is that packing will be less efficient, but it will work.

CVPACK will write the Symbol and Type OMF back to the file in the order listed below. The Microsoft debugger requires that the sstModule entries be first and sequential in the subsection directory. For performance reasons, it is recommended that the order of the subsections in the file match the order of the subsection directory entries.

For signatures prior to NB09, the packed file has the following subsections and ordering:

|  |  |
| --- | --- |
| **NBxx** | Signature |
| lfoDir | Directory offset |
| sstModule1 | Module 1 |
| . |  |
| sstModulen | Module *n* |
| sstAlignSym1 | Module 1 |
| sstSrcModule1 | Module 1 |
| . |  |
| sstAlignSymn | Module *n* |
| sstSrcModulen | Module *n* |
| sstGlobalPub | Global Publics |
| sstGlobalSym | Global Symbols |
| sstLibraries | Libraries |
| sstGlobalTypes | Global Types |
| Directory |  |
| **NBxx** | Signature, if OMF executable |
| lfoBase | Offset of base, if OMF executable |

For NB09 signatures, the packed file has the following subsections and ordering:

|  |  |
| --- | --- |
| **NBxx** | Signature |
| lfoDir | Directory offset |
| sstModule1 | Module 1 |
| . |  |
| sstModulen | Module *n* |
| sstAlignSym1 | Module 1 |
| sstSrcModule1 | Module 1 |
| . |  |
| sstAlignSymn | Module *n* |
| sstSrcModulen | Module *n* |
| sstGlobalPub | Global Publics |
| sstGlobalSym | Global Symbols |
| sstLibraries | Libraries |
| sstGlobalTypes | Global Types |
| sstStaticSym | Static Symbols |
| sstFileIndex | File Index |
| Directory |  |
| **NBxx** | Signature, if OMF executable |
| lfoBase | Offset of base, if OMF executable |

## SubSection Types (sst...)

All values not defined in the following list are reserved for future use:

|  |  |
| --- | --- |
| sstModule | 0x120 |
| sstTypes | 0x121 |
| sstPublic | 0x122 |
| sstPublicSym | 0x123 |
| sstSymbols | 0x124 |
| sstAlignSym | 0x125 |
| sstSrcLnSeg | 0x126 |
| sstSrcModule | 0x127 |
| sstLibraries | 0x128 |
| sstGlobalSym | 0x129 |
| sstGlobalPub | 0x12a |
| sstGlobalTypes | 0x12b |
| sstMPC | 0x12c |
| sstSegMap | 0x12d |
| sstSegName | 0x12e |
| sstPreComp | 0x12f |
| sstTypeNames | 0x130 |
| sstFileIndex | 0x133 |
| sstStaticSym | 0x134 |

### (0x0120) sstModule

This describes the basic information about an object module, including code segments, module name, and the number of segments for the modules that follow. Directory entries for sstModules precede all other subsection directory entries.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | ovlNumber | Overlay number. |
|  | 2 | iLib | Index into sstLibraries subsection if this module was linked from a library |
|  | 2 | cSeg | Count or number of code segments to which this module contributes. |
|  | 2 | Style | Debugging style for this module. Currently only "CV" is defined. A module can have only one debugging style. If a module contains debugging information in an unrecognized style, the information will be discarded. |
|  | \* | SegInfo | Detailed information about each segment to which code is contributed. This is an array of **cSeg** count segment information descriptor structures. |
|  | \* | Name | Length-prefixed name of module |

**SegInfo** is a structure that describes each segment to which a module contributes code. It is formatted as follows:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | Seg | Segment that this structure describes. |
|  | 2 | pad | Padding to maintain alignment This field is reserved for future use and must be emitted as zeroes. |
|  | 4 | offset | Offset in segment where the code starts. |
|  | 4 | cbSeg | Count or number of bytes of code in the segment. |

### (0x0121) sstTypes

The linker emits one of these subsections for every object file that contains a $$TYPES segment. CVPACK combines all of these subsections into an sstGlobalTypes subsection and deletes the sstTypes tables. The sstTypes table contains the contents of the $$TYPES segment, except that addresses within the $$TYPES segment have been fixed by the linker. (See also sstPreComp.)

### (0x0122) sstPublic

The linker fills each subsection of this type with entries for the public symbols of a module. The CVPACK utility combines all of the sstPublics subsections into an sstGlobalPub subsection. This table has been replaced with the sstPublicSym, but is retained for compatibility with previous linkers.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2/4 | offset | Offset of public within segment. This will be a 16-bit offset unless the executable is a 32-bit executable. Note that if any public symbols are 16:32 model, then all publics are emitted as 16:32 addresses. |
|  | 2 | seg | Segment index. |
|  | 2 | type | Type index of the symbol. This will be zero if the module was compiled without Microsoft symbol and type information. |
|  | \* | name | Length-prefixed name of public |

### (0x0123) sstPublicSym

This table replaces the sstPublic subsection. The format of the public symbols contained in this table is that of an S\_PUB16 or S\_PUB32 symbol, as defined in Sections 2.3 and 2.4. This allows an executable to contain both 16:16 and 16:32 public symbols for mixed-mode executable files. As with symbols sections, public section records must start on a 4-byte boundary.

### (0x0124) sstSymbols

The linker emits one of these subsections for every object file that contains a $$SYMBOLS segment. The sstSymbols table contains the contents of the $$SYMBOLS segment, except that addresses within the $$SYMBOLS segment have been fixed by the linker. The CVPACK utility moves global symbols from the sstSymbols subsection to the sstGlobalSum subsection during packing. When the remaining symbols are written executables, the subsection type is changed to sstAlignSym.

### (0x0125) sstAlignSym

CVPACK writes the remaining unpacked symbols for a module back to the executable in a subsection of this type. All symbols have been padded to fall on a long word boundary, and the lexical scope linkage fields have been initialized.

### (0x0126) sstSrcLnSeg

The linker fills in each subsection of this type with information obtained from any LINNUM records in the module. This table has been replaced by the sstSrcModule, but is retained for compatibility with previous linkers. CVPACK rewrites sstSrcLnSeg tables to sstSrcModule tables.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | \* | name | Length-prefixed name of source file. |
|  | 2 | seg | Segment. |
|  | 2 | cPair | Count or number of line number offset pairs to follow. |
|  | \* | line/offset | Line/offset pairs. This pair consists of the line number followed by the offset of the start of the code for that line within the segment. All offsets are relative to the beginning of the segment, not the start of the contribution of the module to the segment. For example, if the module contributes to segment \_TEXT starting at offset 0x0100, and the code offset of the first line number is 0x0010 relative to the module, it will show up in the subsection as 0x0110. The offsets are 16 bits if the executable is a 16:16 executable. If any segment in the executable is 16:32 model, then all offsets in the line/offset pairs are 32-bit offsets. |

### (0x0127) sstSrcModule

The following table describes the source line number for addressing mapping information for a module. The table permits the description of a module containing multiple source files with each source file contributing code to one or more code segments. The base addresses of the tables described below are all relative to the beginning of the sstSrcModule table.

|  |
| --- |
| Module header |
| Information for source file 1 |
| Information for segment 1 |
| Information for segment 2 |
| . |
| Information for source file 2 |
| Information for segment 1 |
| Information for segment 2 |
| . |
| . |

The module header structure describes the source file and code segment organization of the module.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | cFile | Number of source files contributing code to segments. |
|  | 2 | cSeg | Number of code segments receiving code from this module. |
|  | 4\*cFile | baseSrcFile | An array of base offsets from the beginning of the sstSrcModule table. |
|  | 8\*cSeg | start/end | An array of two 32-bit offsets per segment that receives code from this module. The first offset is the offset within the segment of the first byte of code from this module. The second offset is the ending address of the code from this module. The order of these pairs corresponds to the ordering of the segments in the seg array. Zeroes in these entries means that the information is not known, and the file and line tables described below need to be examined to determine if an address of interest is contained within the code from this module. |
|  | 2\*cSeg | seg | An array of segment indices that receive code from this module. If the number of segments is not even, two pad characters are inserted to maintain natural alignment. |

The file table describes the code segments that receive code from each source file.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | cSeg | Number of segments that receive code from this source file. If the source file contributes code multiple times to a segment, it is reflected in this count. |
|  | 2 | pad | Pad field used to maintain alignment. This field is reserved for future use and must be emitted as zero. |
|  | 4\*cSeg | baseSrcLn | An array of offsets for the line/address mapping tables for each of the segments that receive code from this source file. |
|  | 8\*cSeg | start/end | An array of two 32-bit offsets per segment that receives code from this module. The first offset is the offset within the segment of the first byte of code from this module. The second offset is the ending address of the code from this module. The order of these pairs corresponds to the ordering of the segments in the seg array. Zeroes in these entries means that the information is not known, and the file and line tables described below need to be examined to determine if an address of interest is contained within the code from this module. |
|  | 2 | cbName | Count or number of bytes in source file name. |
|  | \* | Name | Source file name. This can be a fully or partially qualified path name. |

The preferred ordering for this table is by offset order. Line number and offsets must be unique. The line number to address mapping information is contained in a table with the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | Seg | Segment index for this table. |
|  | 2 | cPair | Count or number of source line pairs to follow. |
|  | 4\*cPair | offset | An array of 32-bit offsets for the offset within the code segment of the start of the line contained in the parallel array linenumber. |
|  | 2\*cPair | linenumber | An array of 16-bit line numbers for the numbers of the lines in the source file that cause code to be emitted to the code segment. This array is parallel to the offset array. If cPair is not even, then a zero word is emitted to maintain natural alignment in the sstSrcModule table. |

### (0x0128) sstLibraries

There can be at most one sstLibraries SubSection. The format is an array of length-prefixed names, which define all the library files used during linking. The order of this list defines the library index number (seethe sstModules subsection). The first entry should be empty, i.e., a zero-length string, because library indices are 1-based.

### (0x0129) sstGlobalSym

This subsection contains globally compacted symbols. The format of the table is a header specifying the symbol and address hash functions, the length of the symbol information, the length of the symbol hash function data, and the length of address hash function data. This is followed by the symbol information, which followed by the symbol hash tables, and then followed by the address hash tables. When the pack utility writes the sstGlobals subsection, each symbol is zero-padded such that the following symbol starts on a long boundary, and the length field is adjusted by the pad count. Note that symbol and/or address hash data can be discarded and the globally packed symbols are linearly searched. A hash function index 0 means that no hash data exists. See Section 7.5 for more information about the hashing functions.

The header has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | symhash | Index of the symbol hash function. |
|  | 2 | addrhash | Index of the address hash function. |
|  | 4 | cbSymbol | Count or number of bytes in the symbol table. |
|  | 4 | cbSymHash | Count or number of bytes in the symbol hash table. |
|  | 4 | cbAddrHash | Count or number of bytes in the address hashing table. |

Starting with the NB09 signature files, the sstGlobalSym table can contain S\_ALIGN symbols to maintain a 4-K alignment of symbols. Also, starting with NB09 signature files, the sstGlobal can contain S\_PROCREF and S\_DATAREF symbols to global procedures and to global data symbols that would not otherwise have been globally packed because of symbol type mismatches. See Section 2.6 for more information about the S\_PROCREF and S\_DATAREF symbols.

### (0x012a) sstGlobalPub

This subsection contains the globally compacted public symbols from the sstPublics. The format of the table is a header specifying the symbol and address hash functions, the length of the symbol information, the length of the symbol hash function data, and the length of address hash function data. This is followed by symbol information, which is followed by the symbol hash tables, and then followed by the address hash tables. When the pack utility writes the sstGlobals subsection, each symbol is zero-padded such that the following symbol starts on a long boundary, and the length field of the symbol is adjusted by the pad count. Note that symbol and/or address hash data can be discarded and the globally packed symbolscan be linearly searched in low-memory situations. A hash function index 0 means that no hash data exists. See Section 7.5 for more information about the hashing functions.

The header has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | symhash | Index of the symbol hash function. |
|  | 2 | addrhash | Index of the address hash function. |
|  | 4 | cbSymbol | Count or number of bytes in the symbol table. |
|  | 4 | cbSymHash | Count or number of bytes in the symbol hash table. |
|  | 4 | cbAddrHash | Count or number of bytes in the address hashing table. |

Starting with the NB09 signature files, the sstGlobalSym table can contain S\_ALIGN symbols to maintain a 4-K alignment of symbols.

They contain S\_ALIGN symbol records to maintain a 4-K alignment of tables. Note also that sstGlobalPub table contains S\_PROCREF symbols.

### (0x012b) sstGlobalTypes

This subsection contains the packed type records for the executable file. The first long word of the subsection contains the number of types in the table. This count is followed by a count-sized array of long offsets to the corresponding type record. As the sstGlobalTypes subsection is written, each type record is forced to start on a long word boundary. However, the length of the type string is not adjusted by the pad count. The remainder of the subsection contains the type records. This table is invalid for NB05 signatures.

Types are 48-K aligned as well as naturally aligned, so linear traversal of the type table is nontrivial. The 48-K alignment means that no type record crosses a 48-K boundary.

|  |  |
| --- | --- |
| flags | Types table flag |
| cType | Count or number of types |
| offType[cType] | Offset of each type See note below. |
| type string 0 | Type string for type index 0x1000 |
| type string 1 | Type string for type index 0x1001 |
| . |  |
| type string n | Type string for type index 0x1000 + n |

Note that for NB07 and NB08 executables, the type string offset is from the beginning of the subsection table. For NB09 executables, the type string offset is from the first type record of the sstGlobalTypes subsection. Using the offset from the first type record simplifies demand loading of the sstGlobalTypes table.

The types table flags entry has the following format:

|  |  |
| --- | --- |
| 3 | 1 |
| unused | signature |

|  |  |
| --- | --- |
| *unused* | Reserved for future use. Must be emitted as zeroes. |
| *signature* | Global types table signature. |

### (0x012c) sstMPC

This table is emitted by the Pcode MPC program when a segmented executable is processed into a non-segmented executable file. The table contains the mapping from segment indices to frame numbers.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | cSeg | Count or number of segments converted |
|  | 2\*cSeg | mpSegFrame | Segment-to-frame mapping table. A segmented address segment:offset is converted to a frame by mpSegFrame[segment-1]\*16 + offset |

### (0x012d) sstSegMap

This table contains the mapping between the logical segment indices used in the symbol table and the physical segments where the program was loaded.

There is one **sstSegMap** per executable or DLL.

|  |  |  |
| --- | --- | --- |
| 2 | cSeg | Count or number of segment descriptors in table |
| 2 | cSegLog | Count or number of logical segment descriptors |
| 20 | SegDesc 0 | First segment descriptor |
|  | . |  |
|  | . |  |
| 20 | SegDesc N | cSeg'th segment descriptor |

|  |  |
| --- | --- |
| cSeg | Total number of segment descriptors. |
| cSegLog | Total number of logical segments. All group descriptors follow the logical segment descriptors. The number of group descriptors is given by cSeg - cSegLog. |
| SegDescN | Array of segment descriptors. Information about a logical segment can be found by using logical segment number - 1 as an index into this array. Subtract 1 because the logical segment number is 1 based. |

Each element of the segment descriptor array has the following format:

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | flags | Descriptor flags bit field. See below for details. |
|  | 2 | ovl | Logical overlay number. |
|  | 2 | group | Group index into the descriptor array. The group index must either be 0 or cSegLog <= group < cSeg. |
|  | 2 | frame | This value has the following different meanings depending upon the values of fAbs and fSel in the flags bit array and ovl:   |  |  |  |  | | --- | --- | --- | --- | | fAbs | fSel | ovl | Operation | | 0 | 0 | 0 | Frame is added to PSP + 0x10 if not a .com file | | 0 | 0 | 0 | Frame is added to PSP if it is a .com file | | 0 | 0 | != 0 | Frame is added to current overlay base | | 1 | 0 | x | Frame is absolute address | | 0 | 0 | x | Frame contains a selector | |
|  | 2 | iSegName | Byte index of the segment or group name in the sstSegName table. A value of 0xffff indicates that there is no name. |
|  | 2 | iClassName | Byte index of the class name in the sstSegName table. A value of 0xffff indicates that there is no name. |
|  | 4 | offset | Byte offset of the logical segment within the specified physical segment. If fGroup is set in flags, offset is the offset of the group in the physical segment. Currently all groups define physical segments, so offset will be zero for groups. |
|  | 4 | cbSeg | Byte count of the logical segment or group. |

The descriptor flags bit field flags has the following format:

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| :3 | :1 | :2 | :1 | :1 | :4 | :1 | :1 | :1 | :1 |
| res | fGroup | res | fAbs | fSel | res | f32Bit | fExecute | fWrite | fRead |

|  |  |
| --- | --- |
| res | Reserved and set to zero. |
| fGroup | If set, the descriptor represents a group. Because groups are not assigned logical segment numbers, these entries are placed after the logcial segment descriptors in the descriptor array. |
| fAbs | frame represents an absolute address. |
| fSel | frame represents a selector. |
| f32Bit | The descriptor describes a 32-bit linear address. |
| fExecute | The segment is executable. |
| fWrite | The segment is writable. |
| fRead | The segment is readable. |

### (0x012e) sstSegName

The sstSegName table contains all of the logical segment and class names. The table is an array of zero-terminated strings. Each string is indexed by its beginning from the start of the table. See sstSegMap above.

### (0x012f) sstPreComp

The linker emits one of these sections for every OMF object that has the $$TYPES table flagged as sstPreComp and for every COFF object that contains a .debug$P section. During packing, the CVPACK utility processes modules with a types table having the sstPreComp index before modules with types table having the sstTypes index.

### (0x0133) sstFileIndex

This subsection contains a list of all of the sources files that contribute code to any module (compiland) in the executable. File names are partially qualified relative to the compilation directory.

| **Offset** | **Size** | **Field** | **Description** |
| --- | --- | --- | --- |
|  | 2 | cMod | Count or number of modules in the executable. |
|  | 2 | cRef | Count or total number of file name references. |
|  | 2\*cMod | ModStart | Array of indices into the NameOffset table for each module. Each index is the start of the file name references for each module. |
|  | 2\*cModules | cRefCnt | Number of file name references per module. |
|  | 4\*cRef | NameRef | Array of offsets into the Names table. For each module, the offset to first referenced file name is at NameRef[ModStart] and continues for cRefCnt entries. |
|  | \* | Names | List of zero-terminated file names. Each file name is partially qualified relative to the compilation directory. |

### (0x0134) sstStaticSym

This subsection is structured exactly like the sstGlobalPub and sstGlobalSym subsections. It contains S\_PROCREF for all static functions, as well as S\_DATAREF for static module level data and non-static data that could not be included (due to type conflicts) in the sstGlobalSym subsection.

## Hash table and sort table descriptions

The NB09 signature Microsoft symbol and type information contains hash/sort tables in the sstGlobalSym, sstGlobalPub, and sstStaticSym subsections.

**Name hash table (symhash == 10):**

The symbol name hash table uses the following checksum algorithm to generate the hash.

byt\_toupper(b) <- (b&0xDF)

dwrd\_toupper(dw) <- (dw&0xDFDFDFDF)

cb = {Number of characters in the name}

lpbName= {pointer to the first character of the name}

ulEnd = 0;

while ( cb & 3 ) {

ulEnd |= byt\_toupper ( lpbName [ cb - 1 ] );

ulEnd <<= 8;

cb -= 1;

}

cul = cb / 4;

lpulName = lpbName;

for ( iul = 0; iul < cul; iul++ ) {

ulSum ^= dwrd\_toupper(lpulName[iul]);

\_lrotl ( ulSum, 4 );

}

ulSum ^= ulEnd;

The hash bucket number is derived from ulSum, by taking the modulo of ulSum with the total number of hash buckets.

The format of the table is as follows:

|  |  |  |
| --- | --- | --- |
| 2 | cHash(n) | Number of hash buckets. |
| 2 | Alignment | Filler to preserve alignment. |
| 4n | Hash Table[n] | Each ulong entry is a file offset from the beginning of the chain table to the first chain item for each hash bucket. |
| 4n | Bucket Counts[n] | Each ulong entry is the count of items in the chain for each hash bucket. |
| 8m | Chain table[m] | Each entry is a pair of dwords. The first dword is the file offset of the referenced symbol from the beginning of the symbols. The second dword is the checksum of the referenced symbol generated by the above algorithm. |

|  |  |
| --- | --- |
| n | the number of hash buckets. |
| m | the number of symbols (with names) = the number of entries in the chain table. |

**Address sort table (addrhash == 12):**

The address sort table is a grouping of logical segments (or sections) in which each symbol reference within the segment/section is sorted by its segment/section relative offset.

The format of the table is as follows:

|  |  |  |
| --- | --- | --- |
| 2 | cSeg(n) | Number of logical segments/sections. |
| 2 | Alignment | Filler to preserve alignment. |
| 4n | Segment Table[n] | Each ulong entry is a file offset from the beginning of the offset table to the first offset item for each segment/section. |
| 4n | Offset Counts[n] | Each ulong entry is the count of items in the offset table for each segment. |
| 8m | Offset Table[m] | Each entry is a pair of dwords. The first dword is the file offset of the referenced symbol from the beginning of the symbols. The second dword is the segment/section relative offset of the referenced symbol in memory. |

|  |  |
| --- | --- |
| n | the number of segments/sections. |
| m | the number of symbols (with addresses) = the number of entries in the offset table. |

# Appendix A: Calculating Authenticode PE Image Hash

Several attribute certificates are expected to be used to verify the integrity of the images. However, the most common is Authenticode signature. An Authenticode signature can be used to verify that the relevant sections of a PE image file have not been altered in any way from the file’s original form. To accomplish this task, Authenticode signatures contain something called a PE image hash.

## A.1 What is an Authenticode PE Image Hash?

The Authenticode PE image hash, or file hash for short, is similar to a file checksum in that it produces a small value that relates to the integrity of a file. A checksum is produced by a simple algorithm and is used primarily to detect memory failures. That is, it is used to detect whether a block of memory on disk has gone bad and the values stored there have become corrupted. A file hash is similar to a checksum in that it also detects file corruption. However, unlike most checksum algorithms, it is very difficult to modify a file so that it has the same file hash as its original (unmodified) form. That is, a checksum is intended to detect simple memory failures that lead to corruption, but a file hash can be used to detect intentional and even subtle modifications to a file, such as those introduced by viruses, hackers, or Trojan horse programs.

In an Authenticode signature, the file hash is digitally signed by using a private key known only to the signer of the file. A software consumer can verify the integrity of the file by calculating the hash value of the file and comparing it to the value of signed hash contained in the Authenticode digital signature. If the file hashes do not match, part of the file covered by the PE image hash has been modified.

## A.2 What is Covered in an Authenticode PE Image Hash?

It is not possible or desirable to include all image file data in the calculation of the PE image hash. Sometimes it simply presents undesirable characteristics (for example, debugging information cannot be removed from publicly released files); sometimes it is simply impossible. For example, it is not possible to include all information within an image file in an Authenticode signature, then insert the Authenticode signature that contains that PE image hash into the PE image, and later be able to generate an identical PE image hash by including all image file data in the calculation again, because the file now contains the Authenticode signature that was not originally there.

This appendix illustrates how a PE image hash is calculated and what parts of the PE image can be modified without invalidating the Authenticode signature.

It is worth noting that the PE image hash for a specific file can be included in a separate catalog file without including an attribute certificate within the hashed file. This is relevant, because it becomes possible to invalidate the PE image hash in an Authenticode-signed catalog file by modifying a PE image that does not actually contain an Authenticode signature.

#### Process for Generating the Authenticode PE Image Hash

All data in sections of the PE image that are specified in the section table are hashed in their entirety except for the following exclusion ranges:

* The file CheckSum field of the Windows-specific fields of the optional header. This checksum includes the entire file (including any attribute certificates in the file). In all likelihood, the checksum will be different than the original value after inserting the Authenticode signature.
* Information related to attribute certificates. The areas of the PE image that are related to the Authenticode signature are not included in the calculation of the PE image hash because Authenticode signatures can be added to or removed from an image without affecting the overall integrity of the image. This is not a problem, because there are user scenarios that depend on re-signing PE images or adding a time stamp. Authenticode excludes the following information from the hash calculation:

The Certificate Table field of the optional header data directories.

The Certificate Table and corresponding certificates that are pointed to by the Certificate Table field listed immediately above.

To calculate the PE image hash, Authenticode orders the sections that are specified in the section table by address range, then hashes the resulting sequence of bytes, passing over the exclusion ranges.

* Information past of the end of the last section. The area past the last section (defined by highest offset) is not hashed. This area commonly contains debug information. Debug information can generally be considered advisory to debuggers; it does not affect the actual integrity of the executable program. It is quite literally possible to remove debug information from an image after a product has been delivered and not affect the functionality of the program. In fact, this is sometimes done as a disk-saving measure. It is worth noting that debug information contained within the specified sections of the PE Image cannot be removed without invaliding the Authenticode signature.

You can use the makecert and signtool tools provided in the Windows Platform SDK to experiment with creating and verifying Authenticode signatures. For more information, see "References" at the end of this specification.

# Appendix B: SizeOf

## B.1 Delphi SizeOf

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  | **Windows** | | | | |
| **DCC:24.0** | | **DCC:34.0** | | **DCC:14.0** |
| **WIN32** | **WIN64** | **WIN32** | **WIN64** | **WIN32** |
| **ShortInt** | 1 | 1 | 1 | 1 | 1 |
| **SmallInt** | 2 | 2 | 2 | 2 | 2 |
| **LongInt** | 4 | 4 | 4 | 4 | 4 |
| **Integer** | 4 | 4 | 4 | 4 | 4 |
| **Word** | 2 | 2 | 2 | 2 | 2 |
| **LongWord** | 4 | 4 | 4 | 4 | 4 |
| **Cardinal** | 4 | 4 | 4 | 4 | 4 |
| **NativeInt** | 4 | 8 | 4 | 8 | - |
| **NativeUInt** | 4 | 8 | 4 | 8 | - |
| **Char** | 2 | 2 | 2 | 2 | 1 |
| **AnsiChar** | 1 | 1 | 1 | 1 | 1 |
| **WideChar** | 2 | 2 | 2 | 2 | 2 |
| **UCS2Char** | 2 | 2 | 2 | 2 | 2 |
| **UCS4Char** | 4 | 4 | 4 | 4 | 4 |
| **Int64** | 8 | 8 | 8 | 8 | 8 |
| **UInt64** | 8 | 8 | 8 | 8 | - |
| **Boolean** | 1 | 1 | 1 | 1 | 1 |
| **ByteBool** | 1 | 1 | 1 | 1 | 1 |
| **WordBool** | 2 | 2 | 2 | 2 | 2 |
| **LongBool** | 4 | 4 | 4 | 4 | 4 |
| **Real48** | 6 | 6 | 6 | 6 | 6 |
| **Single** | 4 | 4 | 4 | 4 | 4 |
| **Double** | 8 | 8 | 8 | 8 | 8 |
| **Real** | 8 | 8 | 8 | 8 | 8 |
| **Extended** | 10 | 8 | 10 | 8 | 10 |
| **Comp** | 8 | 8 | 8 | 8 | 8 |
| **Currency** | 8 | 8 | 8 | 8 | 8 |
| **LargeInt** | 8 | 8 | 8 | 8 | - |

## B.2 C++ SizeOf

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  | MSC:19.00.24215.1 | | BCC:7.30 | | BCC:5.5.1 | BCC:5.2 | | | BCC:3.1 | | GCC:3.2.2 | MSC:8.00c | | | | WATCOM:11.0 | | | GCC:5.4.0:20160609 | |
|  | WIN32 | WIN64 | WIN32 | WIN64 | WIN32 | WIN32 | WIN16 | DOS {LARGE,HUGE} | DOS {LARGE,HUGE, COMPACT,LITE286} | DOS {MEDIUM, SMALL,TINY} | DOS32 | DOS {MEDIUM, SMALL, TINY} | DOS {LARGE, HUGE} | DOS-PCODE {SMALL, MEDIUM} | DOS-PCODE {LARGE, HUGE} | DOS {LARGE, HUGE, COMPACT} | DOS {MEDIUM, SMALL} | DOS4GW {FLAT,SMALL} | X86 | X64 |
| {signed} int | 4 | 4 | 4 | 4 | 4 | 4 | 2 | 2 | 2 | 2 | 4 | 2 | 2 | 2 | 2 | 2 | 2 | 4 | 4 | 4 |
| {signed} long | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 8 |
| {signed} long long | 8 | 8 | 8 | 8 | - | - | - | - | - | - | 8 | - | - | - | - | - | - | - | 8 | 8 |
| {signed} short {int} | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 |
| bool | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - | - | 1 | - | - | - | - | 1 | 1 | 1 | 1 | 1 |
| char | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| double | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| float | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 |
| long double | 8 | 8 | 10 | 8 | 10 | 10 | 10 | 10 | 10 | 10 | 12 | 10 | 10 | 10 | 10 | 8 | 8 | 8 | 12 | 16 |
| signed char | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| unsigned {int} | 4 | 4 | 4 | 4 | 4 | 4 | 2 | 2 | 2 | 2 | 4 | 2 | 2 | 2 | 2 | 2 | 2 | 4 | 4 | 4 |
| unsigned char | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| unsigned long | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 8 |
| unsigned long long | 8 | 8 | 8 | 8 | - | - | - | - | - | - | 8 | - | - | - | - | - | - | - | 8 | 8 |
| unsigned short {int} | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 |
| void\* | 4 | 8 | 4 | 8 | 4 | 4 | 2 | 4 | 4 | 2 | 4 | 2 | 4 | 2 | 4 | 4 | 2 | 4 | 4 | 8 |
| wchar\_t | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 1 | 1 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 4 | 4 |
| {signed} \_\_int16 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | - | - | - | - | - | - | - | - | - | - | - | - |
| {signed} \_\_int32 | 4 | 4 | 4 | 4 | 4 | 4 | 2 | 2 | - | - | - | - | - | - | - | - | - | - | - | - |
| {signed} \_\_int64 | 8 | 8 | 8 | 8 | 8 | 8 | - | - | - | - | - | - | - | - | - | 8 | 8 | 8 | - | - |
| {signed} \_\_int8 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - | - | - | - | - | - | - | - | - | - | - | - |
| unsigned \_\_int16 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | - | - | - | - | - | - | - | - | - | - | - | - |
| unsigned \_\_int32 | 4 | 4 | 4 | 4 | 4 | 4 | 2 | 2 | - | - | - | - | - | - | - | - | - | - | - | - |
| unsigned \_\_int64 | 8 | 8 | 8 | 8 | 8 | 8 | - | - | - | - | - | - | - | - | - | 8 | 8 | 8 | - | - |
| unsigned \_\_int8 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - | - | - | - | - | - | - | - | - | - | - | - |
| void\* \_\_ptr32 | 4 | 4 | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - |
| void\* \_\_ptr64 | 8 | 8 | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - | - |
| void\* far | 4 | 8 | 4 | 8 | - | 4 | 2 | 4 | 4 | 2 | - | 2 | 4 | 2 | 4 | 4 | 2 | 4 | - | - |
| void\* near | 4 | 8 | 4 | 8 | - | 4 | 2 | 4 | 4 | 2 | - | 2 | 4 | 2 | 4 | 4 | 2 | 4 | - | - |

# References

Downloads and tools for Windows (includes the Windows SDK)  
<https://developer.microsoft.com/en-us/windows/downloads>

Creating, Viewing, and Managing Certificates  
<https://msdn.microsoft.com/en-us/library/aa379872.aspx>

Kernel-Mode Code Signing Walkthrough   
<http://www.microsoft.com/whdc/winlogo/drvsign/kmcs_walkthrough.mspx>

SignTool  
<https://msdn.microsoft.com/en-us/library/aa387764.aspx>

Windows Authenticode Portable Executable Signature Format

<http://www.microsoft.com/whdc/winlogo/drvsign/Authenticode_PE.mspx>

ImageHlp Functions  
<https://msdn.microsoft.com/en-us/library/ms680181.aspx>