

**SwapInstructionCache** Enable or disable the instruction cache

#include <Memory.h>

**Memory Manager**

<u>Boolean</u>	<b>SwapInstructionCache</b> ( <i>cacheEnable</i> );
<u>Boolean</u>	<i>cacheEnable</i> ;      the desired state of the instruction cache
	<b>returns</b> the previous state of the instruction cache

The **SwapInstructionCache** function enables or disables the instruction cache, depending on whether the *cacheEnable* parameter is set to TRUE or FALSE. **SwapDataCache** returns the previous state of the instruction cache.

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Notes: Be sure to check that the trap `_HWPriv` is implemented before calling this routine. See **Using the Gestalt Manager** for sample code that shows how to determine whether a trap is implemented. If you call this routine and `_HWPriv` is not implemented, your application will crash.

The MC68020, MC68030 and MC68040 microprocessors have instruction caches.