

SInfoRecord structure

#include <Slots.h>

typedef struct	SInfoRecord		<u>Size</u>	<u>Offset</u>	<u>Description</u>
<u>Ptr</u>	siDirPtr;		4	0	Pointer to directory
<u>short</u>	silnitStatusA;		2	4	Initialization error
<u>short</u>	silnitStatusV;		2	6	Status returned by vendor init code
<u>char</u>	siState;		1	8	Initialization state
<u>char</u>	siCPUByteLanes;		1	9	0=[d0..d7], 1=[d8..d15], ...
<u>char</u>	siTopOfROM;		1	10	Top of ROM = \$FssFFFFx, where x is TopOfROM
<u>char</u>	siStatusFlags;		1	11	Bit 0--card is changed
<u>short</u>	siTOConst;		1	12	Timeout constant for bus error
<u>char</u>	siReserved [2];		2	13	Reserved
<u>Ptr</u>	siROMAddr;		4	15	Address of top of ROM
<u>unsigned char</u>	siSlot;		1	19	Slot number
<u>unsigned char</u>	siPadding[3];		3	20	Reserved
} SInfoRecord ;			22		

typedef SInfoRecord ***SInfoRecPtr**;

Notes: The siDirPtr field of the **SInfoRecord** contains a pointer to the sResource directory in the configuration ROM. The silnitStatusA field indicates the result of efforts to initialize the card. A zero value indicates that the card is installed and operational. A non-zero value is the **Slot Manager** error code indicating why the card could not be used.

The silnitStatusV field contains the value returned by the card's primary initialization code (in the seStatus field of the **seBlock**). Negative values cause the card to fail initialization. Zero or positive values indicate that the card is operational.

The siState field is used internally to indicate what initialization steps have occurred so far.

The siCPUByteLanes field indicates which byte lanes are used by the card.

The siTopOfROM field gives the last nibble of the address of the actual ByteLanes value in the **FHeaderRec** record.

The siStatusFlags field gives status information about the slot. Currently only the fCardIsChanged bit has meaning. A value of 1 indicates that the board ID of the installed card did not match the ID saved in parameter RAM--in other words, the card has been changed.

The siTOConstant field contains the number of retries that will be performed when a bus error occurs while accessing the declaration ROM. It defaults to 100, but may be set to another value with the TimeOut field in the board sResource of the card.

The siReserved field is reserved and should have a value of 0.