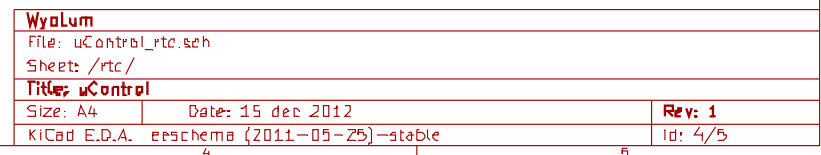
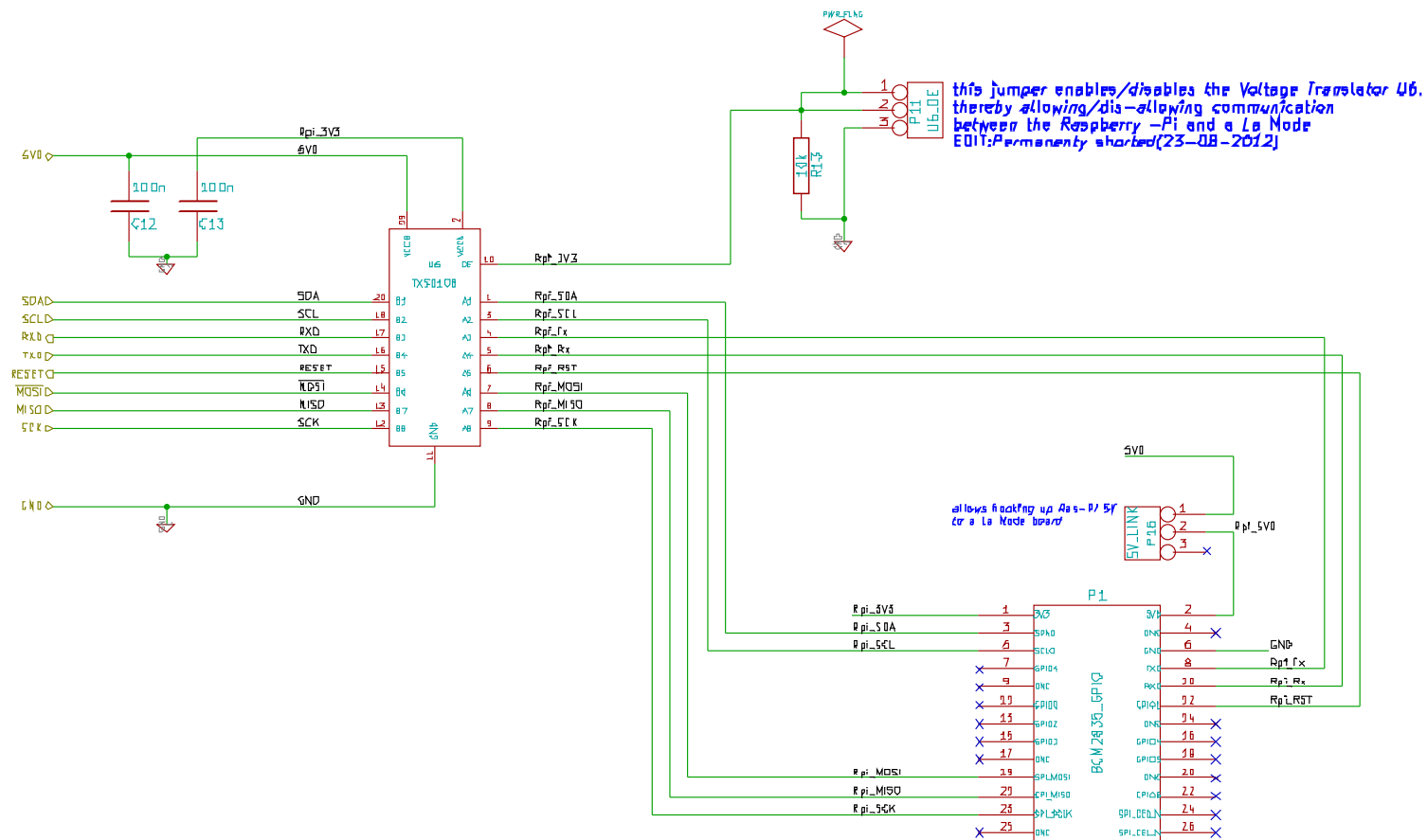


PCB1

<b>WyoLum</b>		PCB1
File: uControl_pwr.sch		
Sheet: /pwr/		
<b>Title: uControl</b>		
Size: A4	Date: 15 dec 2012	Rev: 1
KiCad E.D.A.	eschema (2011-05-25)-stable	Id: 3/5





NOTE : GPIO voltage levels are 3v3 and are not 5v tolerant

Wyalum		
File: uControl_RPi.sch		
Sheet: /RPI/		
Title: uControl		
Size: A4	Date: 15 dec 2012	Rev: 1
KiCad E.D.A.	eeschema (2011-05-25)-stable	Id: 5/5