



P12

USB B

5V USB 1

D- 2

D+ 3

D- 4

GND 5

15

12

13

14

15

16

USB 1 IN

Polyswitch resettable Fuse  
1.5A Hold, 2.2A Trips 8V max.  
non-inductive

F1

5MBU5.0CA-TR  
Bi-directional  
clamping Diode, 5V

D1

C15

1n

5V

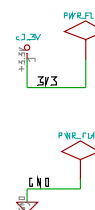
GND

TE - 1981508-3

PCF - 10110993-0003LF

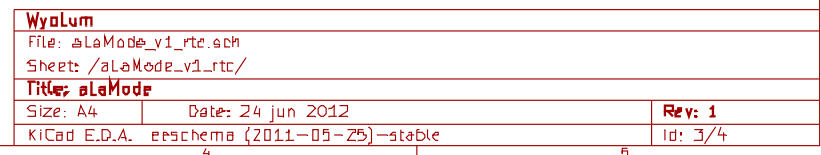
If using Barrel Socket,  
DO NOT INSTALL :  
P12, F1, 07, C15

The diagram shows a two-stage voltage conversion circuit. The first stage is a linear voltage regulator (NCP1117S50T3G) that takes a 5V input (V\_IN) and provides a regulated 3.3V output (3V3). The second stage is a precision centrerail voltage divider (LP2985-330BVR) that takes the 3.3V output and provides a precise 3V3 output (3V3). The circuit includes various passive components like capacitors (C1, C2, C3, C4, C5, C6) and resistors (R1, R2) for stability and precision. The output is labeled 3V3 and is connected to a 3V3 pin on a device.



PCB1

<b>Wyolum</b>		PCB1
File: aLaMode_pwr.sch		
Sheet: /aLaMode_pwr/		
<b>Title: aLaMode</b>		
Size: A4	Date: 24 jun 2012	Rev: 1
KiCad E.D.A.	eeschema (2011-05-25)-stable	Id: 2/4





<b>Wyolum</b>	
File: RPi.ach	
Sheet: /Rpi/	
<b>Title: alaMode</b>	
Size: A4	Date: 24 jun 2012
KiCad E.D.A.	eschema (2011-05-25)-stable
	Id: 4/4