

Computer Architecture HW2 Report

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系級：電機三

1 Screen Shot

```
Inferred memory devices in process
in routine ALU line 144 in file
'/home/raid7_2/userb08/b8502141/CA_HW2/HW2.v'.
=====
| Register Name | Type | Width | Bus | MB | AR | AS | SR | SS | ST |
=====
| shreg_reg | Flip-flop | 64 | Y | N | N | N | N | N | N |
| alu_in_reg | Flip-flop | 32 | Y | N | N | N | N | N | N |
| state_reg | Flip-flop | 3 | Y | N | Y | N | N | N | N |
| counter_reg | Flip-flop | 5 | Y | N | N | N | N | N | N |
=====
Presto compilation completed successfully.
Current design is now '/home/raid7_2/userb08/b8502141/CA_HW2/ALU.db:ALU'
```