

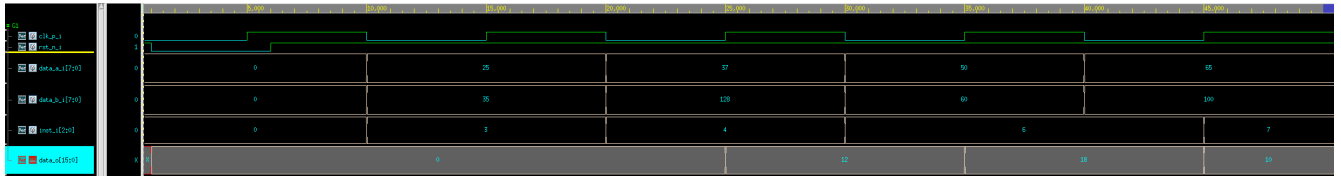
# IC Design LAB Report

學號：B08502141

姓名：石旻翰

系級：電機三

## 1 Waveform:



## 2 Discussion:

As the requirements, I implement signal (rst\_n\_i) to the time. We can easily see the waveform in above graph, when time is 1, rst\_n\_i drops to 0, and raises to 1 when time is 5, and remains 1 further. As for the input signal (data\_a\_i, data\_b\_i, inst\_i) to the ALU, I use "initial" to assign value for those input signals according to the time in requirements. Check the output signal (data\_o), it is correct, and thus this homework has been done.