

IC Design Lab Hw5 Report

學號：B08502141

姓名：石旻翰

系級：電機三

1 LVS/DRC Result

```
REPORT FILE NAME:      lvs_test.rep
LAYOUT NAME:          svdb/CHIP.sp ('CHIP')
SOURCE NAME:          CHIP.spi ('CHIP')
RULE FILE:            G-DF-MIXED_MODE_RFCMOS18-1.8V_3.3V-1P6M-MMC_CALIBRE-LVS-2.1-P8.txt
RULE FILE TITLE:      LVS of UMC 0.18um 1.8V/3.3V 1P6M MMC Mixed Mode/RFCMOS Process
HCELL FILE:           (-automatch)
CREATION TIME:        Mon Mar 21 21:25:48 2021
CURRENT DIRECTORY:    /home/raid7_2/userb08/b8502141/Lab5_LVS
USER NAME:            b8502141
CALIBRE VERSION:      v2021.1_33.19    Mon Mar 1 16:10:01 PST 2021
```

OVERALL COMPARISON RESULTS

```

# #####
# # #
# # CORRECT #
# # #
# #####

```

```

RULECHECK 4.1M ..... TOTAL Result Count = 1
RULECHECK 4.29NOTICE ..... TOTAL Result Count = 1
RULECHECK 4.31A.8KA ..... NOT EXECUTED
RULECHECK 4.31B.a.8KA ..... NOT EXECUTED
RULECHECK 4.31B.b.8KA ..... NOT EXECUTED
RULECHECK 4.31C.a_d.8KA ..... NOT EXECUTED
RULECHECK 4.31C.b_c_e.8KA ..... NOT EXECUTED
RULECHECK 4.31D.8KA ..... NOT EXECUTED
RULECHECK 4.31A.12KA ..... NOT EXECUTED
RULECHECK 4.31B.a.12KA ..... NOT EXECUTED
RULECHECK 4.31B.b.12KA ..... NOT EXECUTED
RULECHECK 4.31C.12KA ..... NOT EXECUTED
RULECHECK 4.31D.12KA ..... NOT EXECUTED
RULECHECK 4.20F ..... TOTAL Result Count = 1
RULECHECK 4.20G ..... TOTAL Result Count = 1
RULECHECK 4.22F ..... TOTAL Result Count = 1
RULECHECK 4.22G ..... TOTAL Result Count = 1
RULECHECK 4.24F ..... TOTAL Result Count = 1
RULECHECK 4.24G ..... TOTAL Result Count = 1
RULECHECK 4.26F ..... TOTAL Result Count = 1
RULECHECK 4.26G ..... TOTAL Result Count = 1
RULECHECK 4.28F ..... TOTAL Result Count = 1
RULECHECK 4.28G ..... TOTAL Result Count = 1
RULECHECK 4.31E ..... TOTAL Result Count = 1
RULECHECK 4.31F ..... TOTAL Result Count = 1
RULECHECK 4.28C ..... TOTAL Result Count = 1
RULECHECK 6Aa.ME1 ..... NOT EXECUTED
RULECHECK 6Ab.ME1 ..... NOT EXECUTED
RULECHECK 6Ba.ME1 ..... NOT EXECUTED
RULECHECK 6Bb.ME1 ..... NOT EXECUTED
RULECHECK 6Aa.ME2 ..... NOT EXECUTED
RULECHECK 6Ab.ME2 ..... NOT EXECUTED
RULECHECK 6Ba.ME2 ..... NOT EXECUTED
RULECHECK 6Bb.ME2 ..... NOT EXECUTED
RULECHECK 6Aa.ME3 ..... NOT EXECUTED

```

2 Discussion

I have no problem in LVS. Yet, I saw some errors in my drc.rep, but I find that those errors were acceptable, by checking those errors on the website that TA has provided.