

lab01

1. Click em new

Entity

Cyclone IV GX: AUTO

lab01

Hierarchy

Files

Design Units

Tasks

Flow: Compilation

Customize...

	Task	Time
▼ ▶	Compile Design	
✓ ▶	▶ Analysis & Synthesis	00:00:02
▶	▶ Fitter (Place & Route)	
▶	▶ Assembler (Generate programming files)	
▶	▶ TimeQuest Timing Analysis	
▶	▶ EDA Netlist Writer	
▶	▶ Program Device (Open Programmer)	

2. Selecionar University Program VWF

3. OK

All <<Search>>

Type	ID	Message
> i	21057	Implemented 4 device resources after synthesis - the final resource count might be different
> i		Quartus II 64-Bit Analysis & Synthesis was successful. 0 errors, 0 warnings

System Processing (10)