



1. New file

Cyclone IV GX: AUTO

lab01

Hierarchy

Files

Design Units

Tasks

Flow: Compilation

Customize...

Task

Compile Design

Analysis & Synthesis

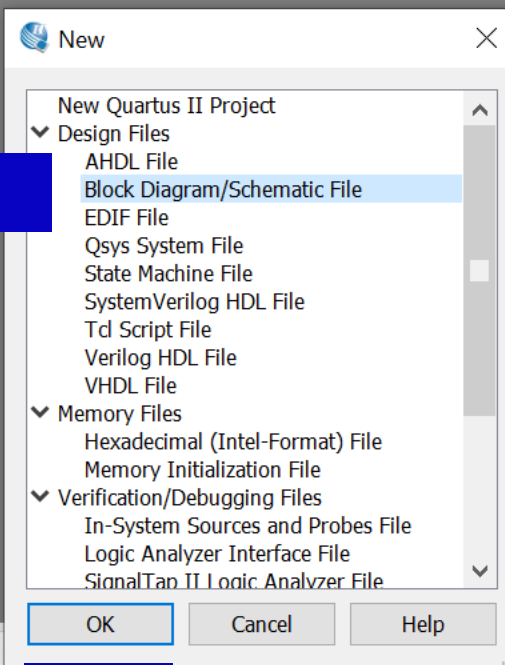
Fitter (Place & Route)

Assembler (Generate programming files)

TimeQuest Timing Analysis

EDA Netlist Writer

2. Block Diagram



3. OK

All <<Search>>

Message

System Processing