

lab01

Project Navigator

Entity

Cyclone IV GX: AUTO

lab01

Hierarchy Files Design Units

Tasks

Flow: Compilation Customize...

Task	Time
Compile Design	
> Analysis & Synthesis	
> Fitter (Place & Route)	
> Assembler (Generate programming files)	
> TimeQuest Timing Analysis	
> EDA Netlist Writer	
Program Device (Open Programmer)	

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vme TD Message

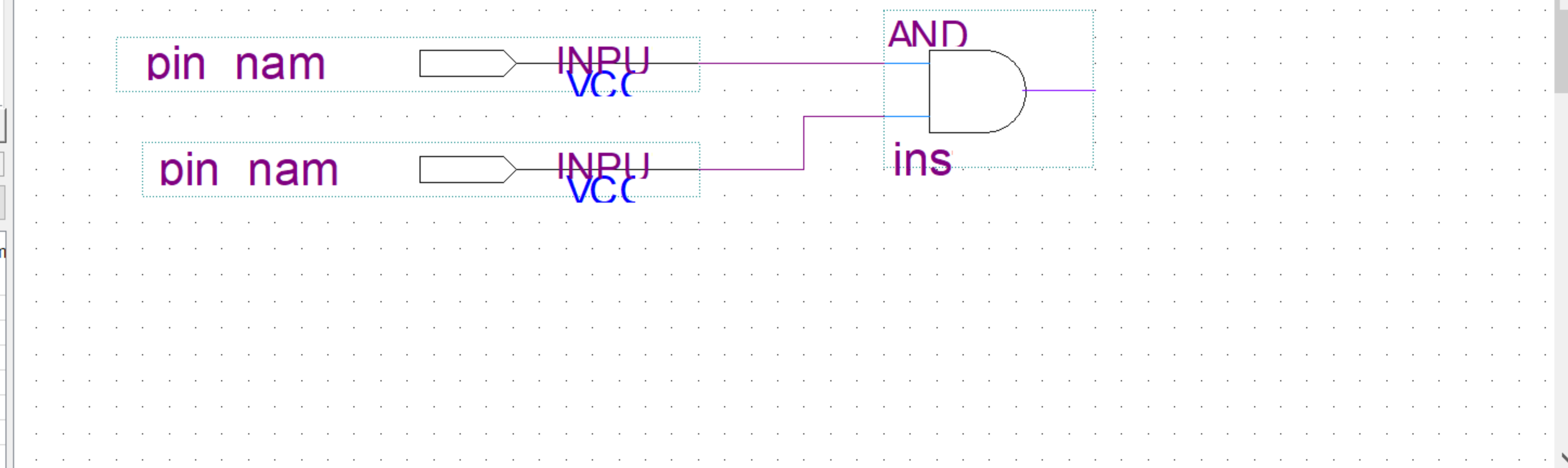
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System Processing

lab01.bdf*

1. Click neste ícone

2. Escolher Output

Input
Output
Bidir

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vme TD Message

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System Processing