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MODULE VectorClocks
EXTENDS Integers, TLC, Sequences
CONSTANTS Procs, MAX

--algorithm VectorClocks
variables
  msgs = [p ∈ Procs ↦ [q ∈ Procs ↦ 0]] ;   defined as Vector Clock

define
    returns the maximum value for each element of two vectors
  Max(v1, v2) ≜ [p ∈ Procs ↦ IF v1[p] > v2[p] THEN v1[p] ELSE v2[p]]
end define ;

fair process VectorClock ∈ Procs
variables
  vc = [p ∈ Procs ↦ 0]  Initially all clocks are zero
begin Main:
  while vc[self] < MAX do
    either Internal:   increments local clock
      vc[self] := vc[self] + 1 ;
    or Send:   increments local clock and sends it to another process
      vc[self] := vc[self] + 1 ;
      with p ∈ Procs \ {self} do   send vc to 'p' via msgs[p]
        msgs[p] := vc ;
      end with ;
    or Receive:   increments local clock and calcs maximum of two clocks
      vc := Max(vc, msgs[self]) ;
      goto Internal ;   vc[self] := vc[self] + 1;
    end either ;
  end while ;
end process ;

end algorithm ;
 BEGIN TRANSLATION
VARIABLES msgs, pc

 define statement
Max(v1, v2) ≜ [p ∈ Procs ↦ IF v1[p] > v2[p] THEN v1[p] ELSE v2[p]]

VARIABLE vc

vars ≜ ⟨msgs, pc, vc⟩

ProcSet ≜ (Procs)

Init ≜  Global variables
        ∧ msgs = [p ∈ Procs ↦ [q ∈ Procs ↦ 0]]
         Process VectorClock
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51          ∧ vc = [self ∈ Procs ↦ [p ∈ Procs ↦ 0]]
52          ∧ pc = [self ∈ ProcSet ↦ "Main"]

54  Main(self) ≜  ∧ pc[self] = "Main"
55                ∧ IF vc[self][self] < MAX
56                      THEN ∧ ∨ ∧ pc' = [pc EXCEPT ![self] = "Internal"]
57                               ∨ ∧ pc' = [pc EXCEPT ![self] = "Send"]
58                               ∨ ∧ pc' = [pc EXCEPT ![self] = "Receive"]
59                      ELSE   ∧ pc' = [pc EXCEPT ![self] = "Done"]
60                ∧ UNCHANGED ⟨msgs, vc⟩

62  Internal(self) ≜  ∧ pc[self] = "Internal"
63                    ∧ vc' = [vc EXCEPT ![self][self] = vc[self][self] + 1]
64                    ∧ pc' = [pc EXCEPT ![self] = "Main"]
65                    ∧ msgs' = msgs

67  Send(self) ≜  ∧ pc[self] = "Send"
68                ∧ vc' = [vc EXCEPT ![self][self] = vc[self][self] + 1]
69                ∧ ∃ p ∈ Procs \ {self} :
70                    msgs' = [msgs EXCEPT ![p] = vc'[self]]
71                ∧ pc' = [pc EXCEPT ![self] = "Main"]

73  Receive(self) ≜  ∧ pc[self] = "Receive"
74                   ∧ vc' = [vc EXCEPT ![self] = Max(vc[self], msgs[self])]
75                   ∧ pc' = [pc EXCEPT ![self] = "Internal"]
76                   ∧ msgs' = msgs

78  VectorClock(self) ≜  Main(self) ∨ Internal(self) ∨ Send(self)
79                                   ∨ Receive(self)

81  Next ≜  (∃ self ∈ Procs : VectorClock(self))
82          ∨  Disjunct to prevent deadlock on termination
83             ((∀ self ∈ ProcSet : pc[self] = "Done") ∧ UNCHANGED vars)

85  Spec ≜  ∧ Init ∧ □[Next]_vars
86          ∧ ∀ self ∈ Procs : WF_vars(VectorClock(self))

88  Termination ≜  ◇(∀ self ∈ ProcSet : pc[self] = "Done")

90  END TRANSLATION

92  Invariants
93  VectorClockOK ≜  (∀ k, l ∈ Procs : vc[k][k] ≥ vc[l][k])

95
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* Modification History
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