

# Breakout RPi V1I1

Variant: Variant name not interpreted

16/05/2014  
V1I1

# PRELIMINARY

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## DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational  
design notes.

DESIGN NOTE:  
Example text for critical  
design notes.

DESIGN NOTE:  
Example text for cautionary  
design notes.

LAYOUT NOTE:  
Example text for critical  
layout guidelines.

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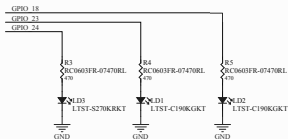
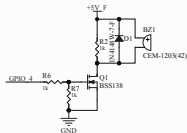
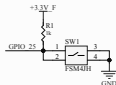
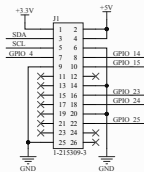
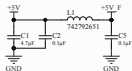
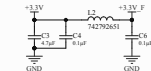
[01] - COVER PAGE SchDoc

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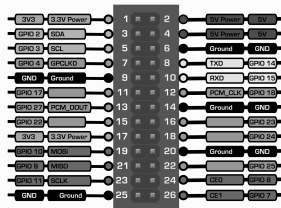
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## BREAKOUT RPI



## Raspberry Pi Rev2 - P1 Connector



- Ⓐ Power 3.3V maximum current draw 50mA
- Ⓑ Power 5V maximum current draw Model A - 500mA, Model B - 300mA
- Ⓒ Ground
- Ⓓ UART
- Ⓔ I2C pulled-up with 1KΩ resistor to 3.3V
- Ⓕ GPIO
- Ⓖ SPI

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[http://combinatorialdesign.com/boards/Raspberry\\_Pi/P1](http://combinatorialdesign.com/boards/Raspberry_Pi/P1)

Rev 1.1 - 4/19/2013

DESIGN NOTE:

GPIO4: output CLK  
GPIO23: output green led  
GPIO24: output red led  
GPIO25: input  
GPIO18: output PWM

GPIO14: TxID  
GPIO15: RxID

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1021 - BREAKOUT RPI SchDoc

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**iElectronic**  
Intelligent Electronics

## DOC: REVISION HISTORY

*16-MAY-2014*    **Started project.**

Breakout RPI V111			 iElectronic Intelligent Electronic
[03] - DOC REVISION HISTORY.SchDoc			
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# MECHANICAL

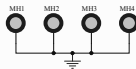
## PCB

PCB1

**PCB**

Breakout RPi V111 PCB

## MOUNTING HOLES



Mounting holes 5mm pad 2.5mm drill  
BOARD MOUNTING HOLES = ONE IN EACH CORNER

## FIDUCIALS



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[04] - MECHANICAL\_SchDoc

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[01] - COVER PAGE.SchDoc

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[02] - BREAKOUT\_RPI.SchDoc

[03] - DOC REVISION HISTORY.SchDoc  
[03] - DOC REVISION HISTORY.SchDoc

[04] - MECHANICAL.SchDoc  
[04] - MECHANICAL.SchDoc

## TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Company, Project and VersionRevision

Mark Not Fitted Components as  
NF

Net Class Example



Differential signal example

TITLE Examples (You can change the color to reflect your company color)

# PAGE TITLE

*Peripheral / Group of component title*

*Smaller Title*

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

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