

Breakout RPi V1I1

Variant: Variant name not interpreted

16/05/2014
V1I1

PRELIMINARY

Page	Index	Page	Index	Page	Index	Page	Index
1	COVER PAGE	11	21	31
2	BREAKOUT RPI	12	22	32
3	DOC REVISION HISTORY	13	23	33
4	MECHANICAL	14	24	34
5	15	25	35
6	16	26	36
7	17	27	37
8	18	28	38
9	19	29	39
10	20	30	40

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

Breakout RPi V1I1

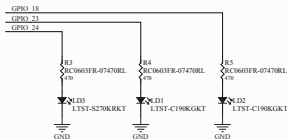
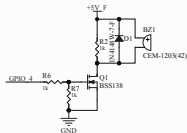
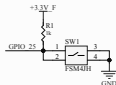
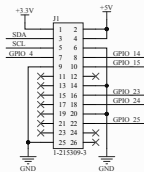
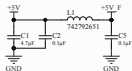
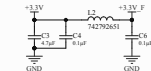
[01] - COVER PAGE SchDoc

Date: 16/05/2014 Rev: V1I1

Sheet: 1 Of 5



BREAKOUT RPI



Raspberry Pi P1 Header

PIN #	NAME	NAME	PIN #
	3.3 VDC Power	5.0 VDC Power	
8	SDA0 (I2C)	DNC	
9	SCL0 (I2C)	0V (Ground)	
7	GPIO 7	TxD	15
	DNC	RxD	16
0	GPIO 0	GPIO1	1
2	GPIO2	DNC	
3	GPIO3	GPIO4	4
	DNC	GPIO5	5
12	MOSI	DNC	
13	MISO	GPIO6	6
14	SCLK	CE0	10
	DNC	CE1	11

<http://www.g4j.com>

DESIGN NOTE:

GPIO4: output CLK
 GPIO23: output green led
 GPIO24: output red led
 GPIO25: input
 GIO18: output PWM
 GPIO14: Tx0
 GPIO15: Rx0

Breakout RPI V111

[02] - BREAKOUT_RPI.SchDoc

Date: 16/05/2014 Rev: V111

Sheet: 2 Of 5



DOC: REVISION HISTORY

16-MAY-2014 **Started project.**

Breakout RPI V111

[03] - DOC REVISION HISTORY.SchDoc

Date: 16/05/2014

Rev: V111

Sheet: 3 Of 5



MECHANICAL

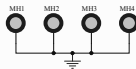
PCB

PCB1

PCB

Breakout RPi V111 PCB

MOUNTING HOLES



Mounting holes 5mm pad 2.5mm drill
BOARD MOUNTING HOLES = ONE IN EACH CORNER

FIDUCIALS



Breakout RPi V111

[04] - MECHANICAL_SchDoc

Date: 16/05/2014 Rev: V111

Sheet: 4 Of 5

[01] - COVER PAGE.SchDoc
[01] - COVER PAGE.SchDoc

[02] - BREAKOUT_RPI.SchDoc
[02] - BREAKOUT_RPI.SchDoc

[03] - DOC REVISION HISTORY.SchDoc
[03] - DOC REVISION HISTORY.SchDoc

[04] - MECHANICAL.SchDoc
[04] - MECHANICAL.SchDoc

TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Company, Project and VersionRevision

Mark Not Fitted Components as
NF

Net Class Example



Differential signal example

TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Title

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

Breakout RPI V111

Breakout_RPI_V111.SchDoc

Date: 16/05/2014 Rev: V111

Sheet: 5 Of 5

