

Breakout RPi V1I1

Variant: Variant name not interpreted

16/05/2014
V1I1

PRELIMINARY

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DESIGN CONSIDERATIONS

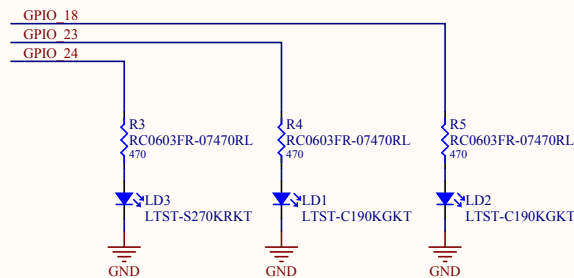
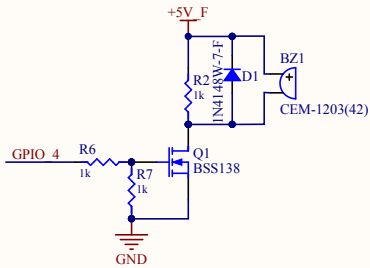
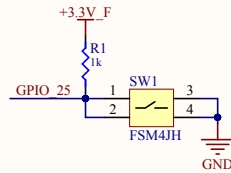
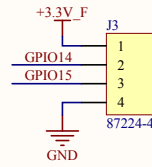
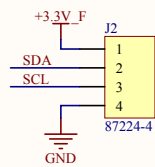
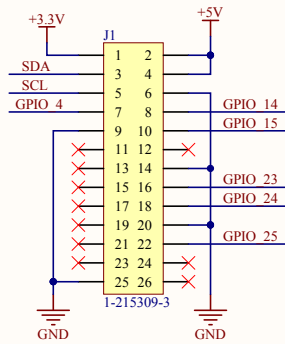
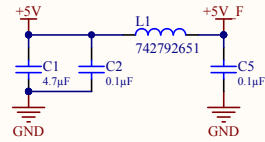
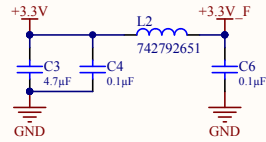
DESIGN NOTE:
Example text for informational
design notes .

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

BREAKOUT



DESIGN NOTE:

GPIO4: output CLK
GPIO23: output green led
GPIO24: output red led
GPIO25: input
GPIO18: output PWM

GPIO14: Tx
GPIO15: Rx

Breakout RPi V111

[02] - BREAKOUT_RPi.SchDoc

Date: 16/05/2014

Rev: V111

Sheet: 2 Of 5

1

2

3

4

DOC: REVISION HISTORY

16-MAY-2014 **Started project.**

CLOCKS(CPU & PCID)

1

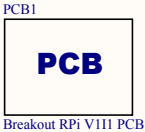
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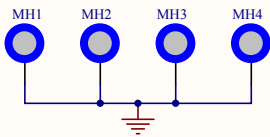
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MECHANICAL

PCB

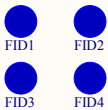


MOUNTING HOLES

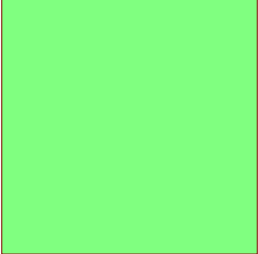


Mounting holes 5mm pad 2.2mm drill
BOARD MOUNTING HOLES - ONE IN EACH CORNER

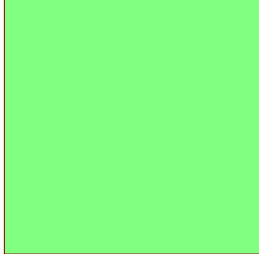
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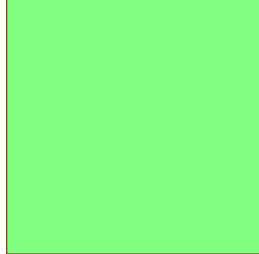
[01] - COVER PAGE.SchDoc
[01] - COVER PAGE.SchDoc



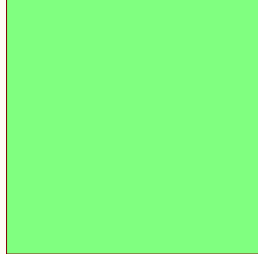
[02] - BREAKOUT_RPI.SchDoc
[02] - BREAKOUT_RPI.SchDoc



[03] - DOC REVISION HISTORY.SchDoc
[03] - DOC REVISION HISTORY.SchDoc



[04] - MECHANICAL.SchDoc
[04] - MECHANICAL.SchDoc



TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Company, Project and VersionRevision

Mark Not Fitted Components as
NF

Net Class Example



Differential signal example

TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Ttitle

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.