





UNIVERSITY OF COLOMBO, SRI LANKA

UNIVERSITY OF COLOMBO SCHOOL OF COMPUTING

DEGREE OF BACHELOR OF INFORMATION TECHNOLOGY (EXTERNAL)

Academic Year 2010/2011 – 1st Year Examination – Semester 1

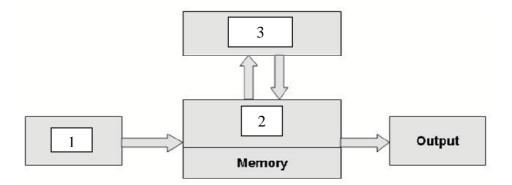
IT1204 - Computer Systems I

05th March 2011
(TWO HOURS)

Important Instructions:

- The duration of the paper is **2** (two) hours.
- The medium of instruction and questions is English.
- The paper has **50** questions and **11** pages.
- All questions are of the MCQ (Multiple Choice Questions) type.
- All questions should be answered.
- Each question will have 5 (five) choices with **one or more** correct answers.
- All questions will carry equal marks.
- There will be a penalty for incorrect responses to discourage guessing.
- The mark given for a question will vary from 0 to +1 (All the correct choices are marked & no incorrect choices are marked).
- Answers should be marked on the special answer sheet provided.
- Note that questions appear on both sides of the paper.
- If a page is not printed, please inform the supervisor immediately.
- Mark the correct choices on the question paper first and then transfer them to the given answer sheet which will be machine marked. Please completely read and follow the instructions given on the other side of the answer sheet before you shade your correct choices.

- 1) Which of the following statements is/are true?
 - (a) Herman Hollerith designed the Analytical Engine.
 - (b) The Pascaline developed by Blaise Pascal could perform addition with carry and subtraction.
 - (c) The Pascaline developed by Blaise Pascal read data from punch cards.
 - (d) Ada Lovelace is considered to be the first computer programmer.
 - e) The Differential Engine was developed by Blaise Pascal.
- 2) Which component(s) an/ are essential part(s) of a basic computing system?
 - (a) A mechanism for transferring data to and from the outside world.
 - (b) Pattern driven computing model to extract unique patterns.
 - (c) A memory to store both data and programs.
 - (d) A processor to interpret and execute programs.
 - (e) Personal Information Manager (PIM) software.
- 3) Consider the following abstract view of the typical components of a computer.



The components marked as 1,2 and 3 respectively represent

(a) Storage, Input, CPU.

(b) Input, CPU, Storage.

(c) ALU, CPU, Storage.

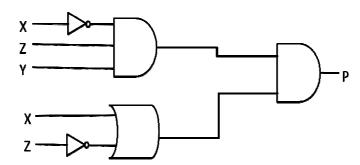
(d) CPU, Storage, Input.

- (e) Input, CPU, Output.
- 4) Which of the following statements about second generation computers is/are true?
 - (a) They are based on transistors.
 - (b) They are based on ICs.
 - (c) They used magnetic drums for primary memory.
 - (d) They used magnetic cores for primary memory.
 - (e) IBM PC is an example for the second generation computers.
- 5) What value(s) is/are equivalent to 256 MB?
 - (a) 256 X 2¹² KB.
- (b) 2²⁸ Bytes.
- (c) 256×10^{-6} TB.

- (d) 2^{20} MB.
- (e) 256 X 10⁹ KB.

(a)	111010 ₂	(b)	0101112	(c) 23 ₈
(d)	17 ₈	(e)	27 ₈	
hich	of the following star	tements ab	out Two's complet	ment representation is/are t
(a)	•	•	•	and 1 respectively.
(b)	Division is done by		•	
(c)	•	•		nal number -42 is 1101011
(d)			itation of the decin	nal number -42 is 1101010
(e)	It is widely used in	ı ALU.		
	s the loss of accurac epresentation with a			al value -0.0235 to 10-bit finantissa?
(a)	0.003512	(b)	-0.031250	(c) 0.023375
(d)	-0.015625	(e)	0.0000625	
(a)	-278	(b)	-234	(c) 349
(a) (d)	-278 -349	(b) (e)	-234 511	(c) 349
(d)		(e)	511	
(d)	-349 of the following sta	(e) tements on	511 Hamming Code is	
(d) /hich (a) (b)	of the following star Minimum distance A Hamming Code	tements on e between with a min	511 Hamming Code is any code words is imum distance of I	/are true ? called Hamming Distance. Vocan correct upto (N-1)/2
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(d) /hich (a) (b) (c) (d) (e)	of the following star Minimum distance A Hamming Code A Hamming d(C ₁ ,C The Hamming Dist Smallest possible o	tements on e between with a min C ₂)Code has tance between	any code words is imum distance of I s C ₁ bit data words yeen 1001 0010 110 etween two distinct	/are true ? called Hamming Distance. N can correct upto (N-1)/2 and C ₂ bit code words. O1 and 1010 0010 0010 is one t code words is 2.
(d) /hich (a) (b) (c) (d) (e)	of the following star Minimum distance A Hamming Code A Hamming d(C ₁ ,C The Hamming Dist Smallest possible of	tements on e between with a min C ₂)Code has tance between distance between	Hamming Code is any code words is imum distance of I is C ₁ bit data words yeen 1001 0010 110 etween two distinct oth input and output	/are true ? called Hamming Distance. I can correct upto (N-1)/2 and C ₂ bit code words. O1 and 1010 0010 0010 is of tode words is 2. t devices?
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(d) /hich (a) (b) (c) (d) (e) /hich (a) (d)	of the following star Minimum distance A Hamming Code A Hamming d(C ₁ ,C The Hamming Dist Smallest possible of of the following dev Mouse Touch Screen	tements on e between with a min c ₂)Code has tance between distance between vices are be (b)	any code words is imum distance of I is C ₁ bit data words ween 1001 0010 110 etween two distinct oth input and output DVD Burner Floppy Driver	/are true ? called Hamming Distance. N can correct upto (N-1)/2 and C ₂ bit code words. O1 and 1010 0010 0010 is of t code words is 2. t devices? (c) Plotter

13) What is the output P of the following digital circuit?



- (a) $(\bar{x} + z + y) + (x \bar{z})$
- (b) (z + y) + (x)
- (c) $\bar{x}zy(x+\bar{z})$

(d) 1

- (e) 0
- 14) Which of the following K-Maps represent(s) the Boolean expression $Q = A + C \oplus B$?

(b)

(a)					
	BA C	00	01	11	10
	0	1	0	0	1
	1	1	1	1	1

 BA
 00
 01
 11
 10

 C
 0
 0
 1
 1
 1

 1
 1
 1
 1
 0

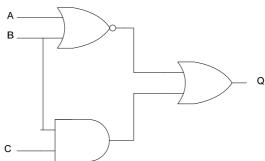
(c) βA \mathbf{C}

(d) βA \mathbf{C}

(e)

BA C	00	01	11	10	
0	0	1	1	1	
1	1	0	0	1	

How many NAND gates are required if each of the gates in the following logic circuit is to be replaced directly by a combination of NAND gates only?



- (b) 7. (a) 6. (c) 8. (d) 9. 10.
- 16 Consider the following truth table.

A	В	С	F(A,B,C)
1	1	1	1
1	1	0	0
1	0	1	1
1	0	0	1
0	1	1	0
0	1	0	0
0	0	1	0
0	0	0	1

Which of the following expressions is/are represented by the Boolean function F(A,B,C)?

(a) $B.\overline{C} + \overline{A}.C$ (b) $A.C + \overline{B.C}$

(c) $A.C + A + A.\overline{B}.\overline{C}$ (d) $A.C + \overline{(B+C)}$

- (e) $A.\overline{C} + \overline{B}.C$
- 17) If any byte of a particular memory space can be addressed by using a 20-bit address and each location can hold one byte, what should be the size of the memory space?
 - 4096 Bytes (a)
- (b) 10 KB

1 MB (c)

- (d) 1000 KB
- (e) 2048 Bytes
- 18) Suppose that any byte of a particular memory space can be addressed by using a 20-bit address and each location can hold a byte. If a 6-byte variable is stored starting at location 1100 1110 0011 0010 0100, what is the address of next available storage location?
 - 1100 1110 0011 0010 0111. (a)
- 1100 1110 0011 0010 1001. (b)
- (c) 1100 1110 0011 0011 1010.
- (d) 1100 1110 0011 0010 1011.
- (e) 1100 1110 0011 0010 1010.
- 19) Which of the following memory types is/are having a direct data path to the processor?
 - **SRAM** (a)

- **MPDRAM** (b)
- **EDORAM** (c)

- (d) DDR2 SDRAM
- (e) **RDRAM**

20)	Which of the following is a/are Network Interface Card(NIC) connectors?				
	(a) SCSI	(b) BNC	(c) DB9		

21) A stack-based processor executes the following set of machine instructions sequentially.

RJ45

(e)

PUSH	100
PUSH	101
ADD	
PUSH	102
SUB	
POP	103

Assuming that:

(d) USB 3.0

- I. Variables 0000 1010, 0000 0101, 0000 0001 and 0000 0111 are stored at memory locations 100, 101, 102 and 103 respectively,
- II. The stack is byte organized and stack pointer is at 00H, and
- III. All PUSH and POP instructions have a memory operand,

which of the following could be the final result?

- (a) Memory location 01H contains the value 0000 0111.
- (b) Memory location 02H contains the value 103.
- (c) Memory location 00H contains the value 103.
- (d) Memory location 103 contains the value 0000 1110.
- (e) Memory location 103 contains the value 0000 0111.
- 22) Which of the following statements is/are true with respect to SCSI interface?
 - (a) SCSI is a hardware bus similar in function to the ATA controller.
 - (b) SCSI interface does not support connections to multiple devices.
 - (c) SCSI is mainly used to connect high speed disk drives to high end servers.
 - (d) SCSI interface can be used to enhance the performance of high-end PCs using RAID.
 - (e) SCSI support RAID, but not IDE interface controllers.
- 23) Consider the following three statements about Fire-Wire and USB interfaces.
 - I. The data transfer rate of USB 2.0 is 480Mbps and Fire-Wire 1394a provides up to 400Mbps.
 - II. USB allows up to 128 devices to run simultaneously on a single bus and a maximum of 63 devices can be connected to a single IEEE 1394 Fire-Wire adapter card
 - III. Fire-Wire 1394b is substantially faster than Hi-Speed USB.

Which of the following statements is/are correct about Fire-Wire and USB interfaces?

(a)	Only (I).	(b)	Only (II).	(c)	Only (III).
(d)	Only (I) and (III).	(e)	All are correct.		

24)	Which of the following is a/are System Software?								
	(a) (d)	BIOS. (b) Emulators. (e)	Web Browsers. (c) Compilers. Utility Software.						
25)	Which of the following is a/are functionality(ies) of an Operating System?								
	(a)		Translates user commands to a form that can be understood by the relavent						
	(b)	computer component. Creates a file structure on th and retrieved.	e computer hard disk where user data can be	stored					
	(c)	Allocates portions of memor memory for reuse when no I	y to programs at their request and frees the a onger needed.	allowed					
	(d)	•	gh quality images to the required level when	the file					
	(e)	Executes special programs to	repair damaged files and backup data.						
26)	Which	of the following statements is/	are true with respect to routers?						
	(a)	Routers are used to join simi segments.	lar topologies together and to divide networl	k					
	(b)	_	nerate transmission signals between similar n	etwork					
	(c)	The advantage of using a rou best path that data can take	ter over a bridge is that routers can determing to reach the destination.	ne the					
	(d) (e)		Bridges can segment large networks and can filter out noise but routers cannot. When a typical WAN is set up, there should be at least two routers.						
27)	Which	of the following Wi-Fi standar	ds has/have a maximum of 54 Mbps bandwid	th?					
	(a) (d)	IEEE 802.11 (b) IEEE 802.11g (e)	IEEE 802.11a (c) IEEE 802.11b IEEE 802.11n						
28)	Which of the following technologies is/are used to set up a short range Personal Area Network?								
	(a)	Wi-Fi	(b) Bluetooth (d) Wi-Max						
	(c) (e)	ZigBee Microwave Access Communi	• •						
29)	Which	of the following is/are not con	sidered as a magnetic storage device?						
	(a) (c)	Compact Disks Zip Disk	(b) Castlewood Orb (d) Floppy Disks						
	(e)	Punch Card							

30)	Which of the following statements is/are correct about a hard disk drive?					
	 (a) The hard disk drive speed is measured in cylinder rounds per minute. (b) The read/write speed of a hard disk drive is much slower than RAM. (c) The number of tracks per disk is equal to the number of cylinders in a hard disk. (d) Data transfer rates of the Serial-ATA hard disks are greater than those of ATA hard disks. 					
	(e) The CPU directly accesses hard disk drive data when primary memory space is not enough.					
31)	Which of the following parts is/are on a typical computer motherboard?					
	(a) Hard Disk. (b) Power Supply. (c) Processor Socket. (d) I/O Controller Hub. (e) RAM cards					
32)	What is/are the components of a typical system bus of a computer motherboard?					
	(a) Control Bus (b) ROM Bus (c) Address Bus (d) Data Bus (e) Program Bus					
33)	Which of the following statements is/are correct about a Central Processing Unit (CPU)?					
	 (a) ALU, Control Unit and Memory are principal parts of a typical CPU. (b) ALU executes arithmetic and logical instructions. (c) ALU operations are controlled by the Control Unit. (d) Both CPU Registers and the Control Unit of the CPU are called the Data-path. (e) Address Bus is used to determine the location of the source or destination of the data. 					
34)	The first two bytes of a main memory with 16-bit addresses have the following hex values: Byte $0 = FFH$ Byte $1 = 01H$					
	If these two bytes hold a 16-bit two's complement integer value, what is its decimal value if the bytes are organized as big-endian?					
	(a) +255 (b) -254 (c) +511 (d) -255 (e) -256					
35)	Which of the following statements about Video Input Devices is/are correct?					
	 (a) A DV camera stores the input audio and video on a DVD in digital format. (b) A DV camera uses FireWire ports for digital video transfer. (c) DVD cameras directly write the video input on to a mini-DVD. (d) The webcam cannot be considered as a basic video camera. (e) Total video transferring time in using FireWire port is equal to the recorded period of the video. 					

- 36) Which of the following statements is/are true with respect to Touchpad?
 - (a) Touchpad was invented by Cirque in 1994.
 - (b) Touchpad must be locked when an external mouse is connected.
 - (c) Touchpad is "Strike-sensitive".
 - (d) Touchpad has a square area about 2 inches by 1.5 inches in laptops.
 - e) Touchpads can be found only on laptops.
- Which of the following statements is/are true with respect to CRT and Flat-Panel LCD monitors?
 - (a) A CRT monitor tube is a glass vacuum tube with one-end having an electron gun and the other a display surface coated with phosphors.
 - (b) A CRT monitor tube uses four electron beams to generate red, green, UV and blue light.
 - (c) The electron beams are used to determine the refresh rate a of the CRT monitor screen.
 - (d) The LCD Flat-Panel first worked in three-colour mode as RGB.
 - (e) A backlight mechanism was introduced to illuminate what was displayed on LCD screen.
- 38) Consider the following three statements about Multimedia Projectors.
 - I. The images projected by the LCD multimedia projectors are bright and sharp but blured.
 - II. The images projected by the DLP multimedia projectors are sharp not blured and brighter.
 - III. DLP multimedia projectors take very little time to cool down when compared with LCD projectors.

Which of the above statements is/are correct?

- (a) Only (I). (b) Only (II). (c) Only (III). (d) Only (II) and (III). (e) All are correct.
- 39) Which of the following features is/are included in Advanced BIOS?
 - (a) SWAP Floppy Drive.
- (b) Anti-Virus Program enable/disable.

(c) DRAM Clock.

- (d) PCI/VGA Palette Snoop.
- (e) Drive A and B enable/disable.
- 40) Which of the following statements is/are always true with respect to the BIOS ROM chip?
 - (a) True-ROM chip does the writing into the chip during the manufacturing phase.
 - (b) EEPROM chip does the writing after manufacture phase and the chip can be erased using UV light.
 - (c) EEPROM chip can be erased electronically and re-written.
 - (d) PROM chip does the writing after manufacture and the chip can be written only once
 - (e) Most modern day motherboard BIOS chips are EPROMs.

41)	Which	of the following softv	vare is/ar	e not utility soft	ware?	
	(a) (d)	Anti-Virus Backup Software	(b) (e)	Scan Disk Disk Defragme	(c)	Organizer Notes
42)	Which	of the following Oper	ating Sy	stems is/are used	l in embedded	l systems?
	(a) (d)	Linux Symbian OS	(b) (e)	Windows NT Windows ME	(c)	Android
43)	Which o	f the following applic	cations is		"windows + I	E" keys are pressed on
	104-key	Windows Keyboard?				
	(a)	Windows Internet E		rogram.		
	(b) (c)	Windows Explorer p Windows Applicatio	_	Лenu.		
	(d)	E-drive partition of t		PP		
	(e)	Windows My Comp	uter App	lication program	1	
44)		of the following softv and controlling resou		e developed for	planning, sch	eduling, allocating
	(a) (c) (e)	Enterprise Application Workgroup Application Manufacturing Cont	ion Soft	ware. (d)		nagement Software. gement Software.
	(6)	Wandracturing Cont	.101 3011	vare.		
45)	Which	of the following state	ments is/	are true about C	ache Memory	?
	(a)	The L1 cache is man		d using SRAM te	echnology sind	ce it is fast and
	(b)	In modern compute	rs, the L			
	(c)	The L2 cache is man cycles per second.	ufacture	d using SRAM si	nce it goes th	rough many refresh
	(d)	When a memory red memory.	quest is g	generated, the re	equest is first	presented to the cache
	(e)	We define a cache r memory.	niss to b	e a reference to	an item that i	s not resident in main
46)		of the following netw the flow of data?	vork peri	pherals can be u	sed to prepare	data, send data and
	(a) (d)	Optical Connector Router	(b) (e)	Bridge Switch	(c) Netwo	rk Interface Card
	<u> (u)</u>	Noutel	(e)	SWITCH		

- Which of the following statements is/are always true about a bus system available in a computer system?
 - (a) PCI bus is in the form of 16-bit slots mounted on the mother board and is in white colour.
 - (b) The fastest bus in the computer system is the processor bus and is used to transfer data between the processor and cache or main memory.
 - (c) ISA bus could handle maximum of only 16-bits, and ISA cards cannot be pluggedin to the EISA slots.
 - (d) AGP slots are designed particularly to be used with video cards and have high performance connectivity.
 - (e) SCSI adapters cannot be plugged-in to the PCI-X slots.
- 48) Which of the following statements is/are correct with respect to RAM types?
 - (a) SDRAM removes the drawback of latency in synchronizing with the CPU clock.
 - (b) The data Transfer on an SDRAM occurs on the rising edge of the motherboard clock cycle.
 - (c) DDR2 SDRAMs increase the speed of memory twofold in sending data on both edges of the CPU clock.
 - (d) DDR SDRAM memory modules have 184 pins.
 - (e) DDR2 SDRAMs consume more power than DDR SDRAMs and generate high heat.
- 49) Which of the following statements is/are correct about POST?
 - (a) The main devices tested by POST are the CPU, motherboard support circuits, ROM, RAM, video graphic adapter and Hard disk drive.
 - (b) The POST program is loaded just after locating the first boot sector to start up the computer system.
 - (c) The POST program indicates errors in the form of audio codes, on screen messages or check point codes.
 - (d) The POST program protects the bootstrapped code from being interrupted by faulty hardware.
 - (e) The BIOS program handles the main duties of POST.
- Which of the following components might need to be considered when upgrading the processor of the computer system?

(a) Main Storage Device(b) Main Memory(c) Video Graphic Adapter(d) Motherboard

(e) Power Supply
